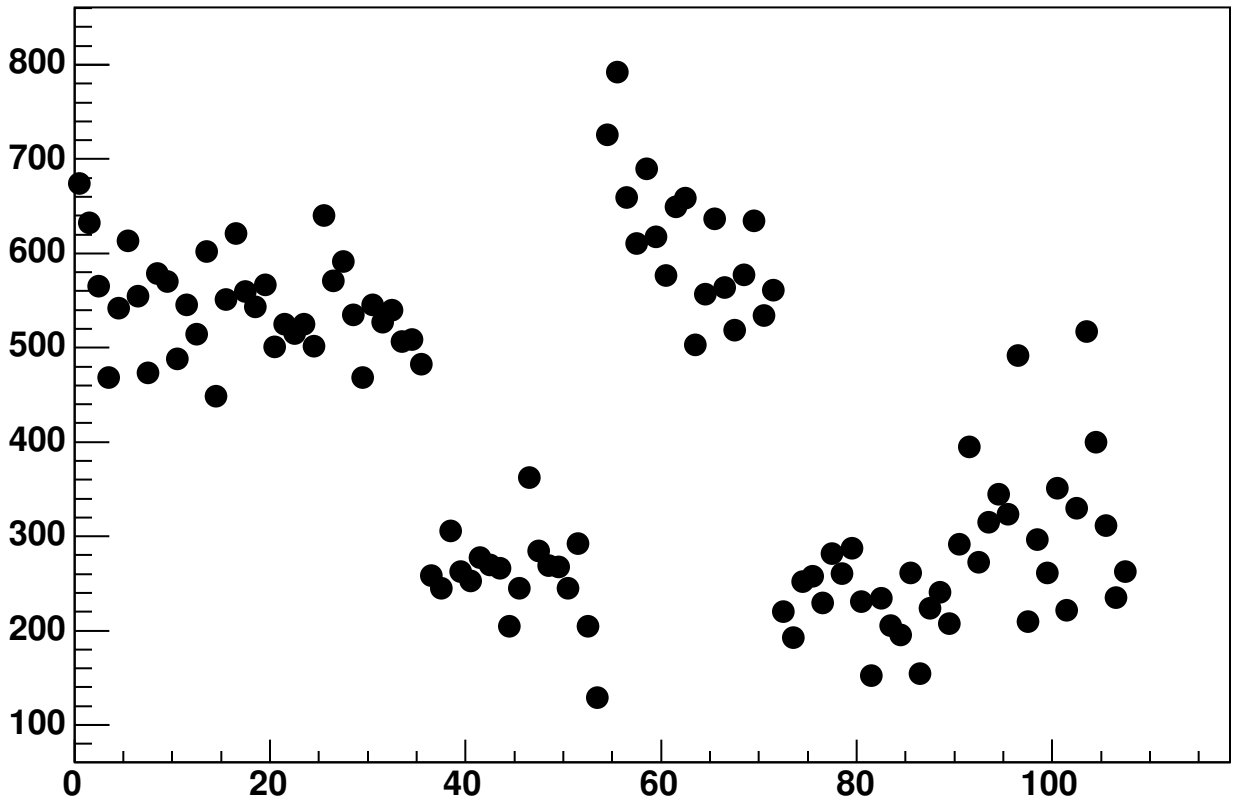
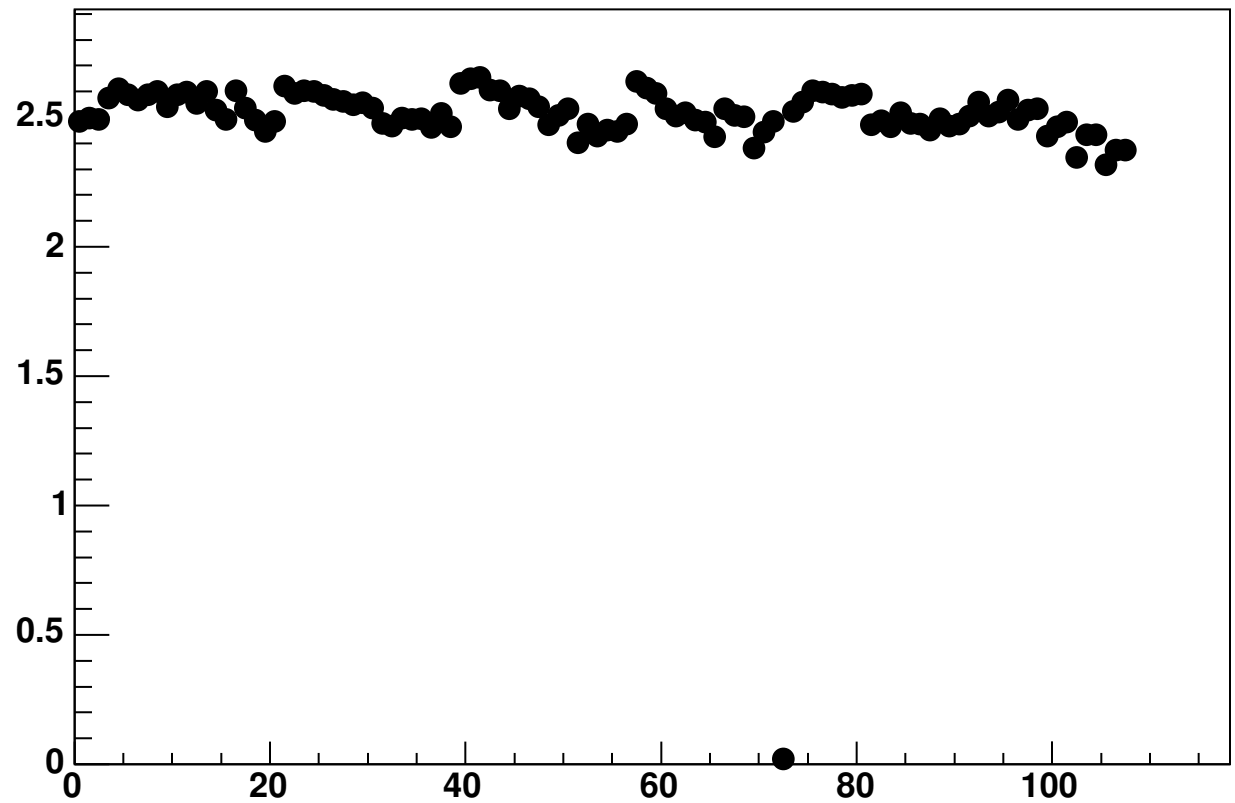
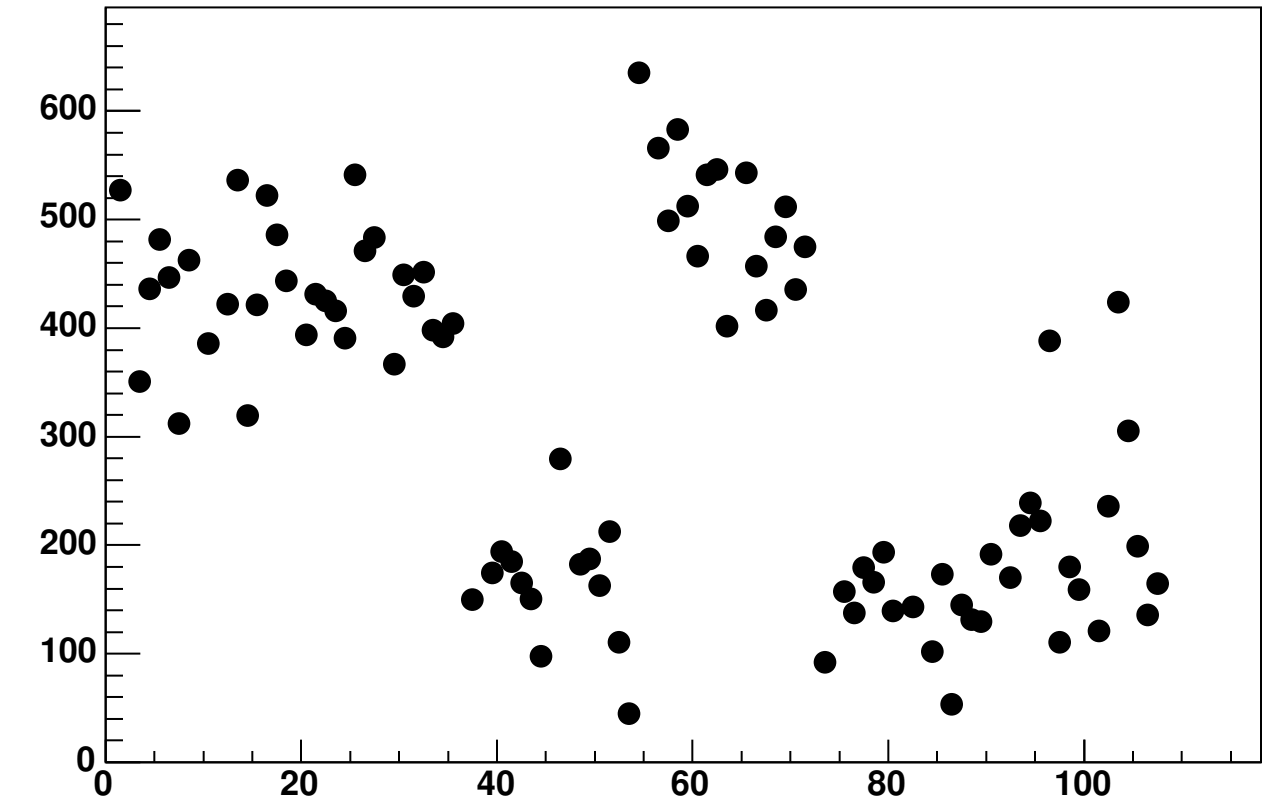
Channel Enabled, Hold=35, Fit Intercept vs 18*Chip+Chan



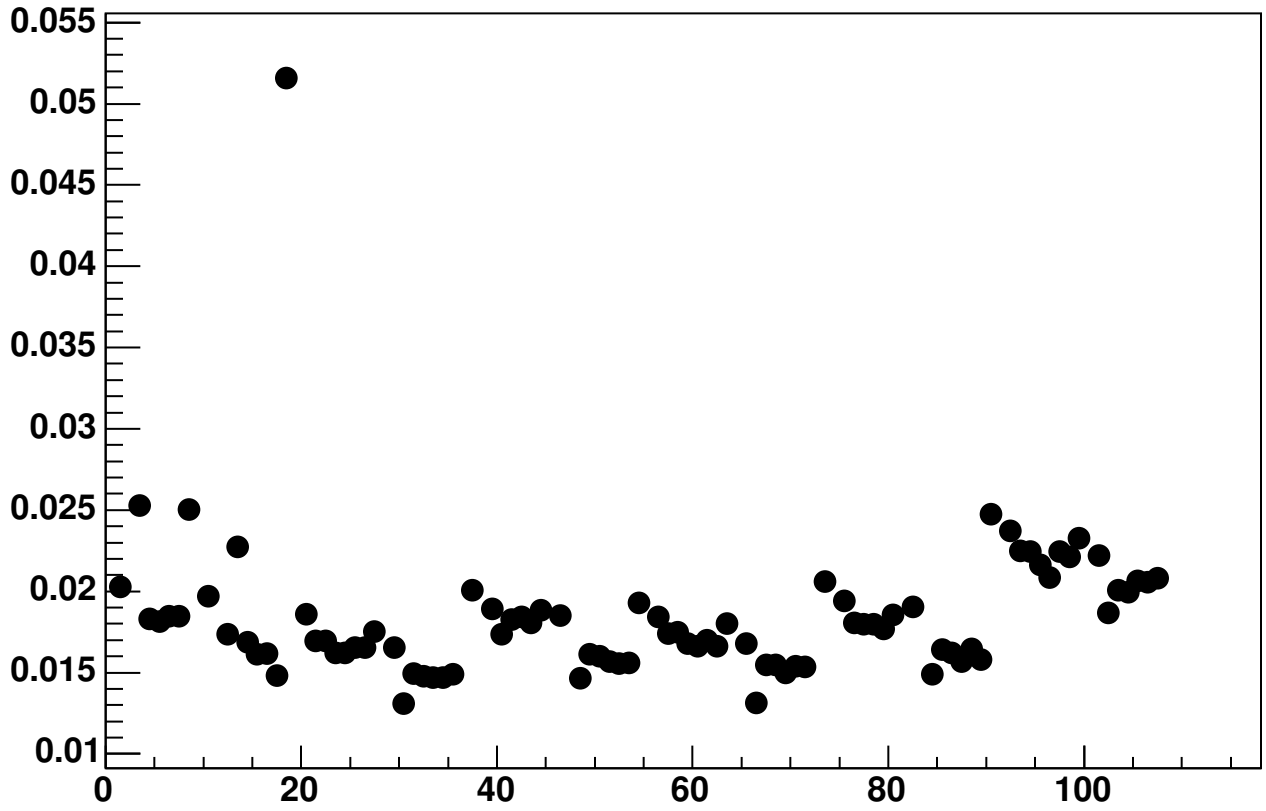
Channel Enabled, Hold=35, Fit Slope vs 18*Chip+Chan



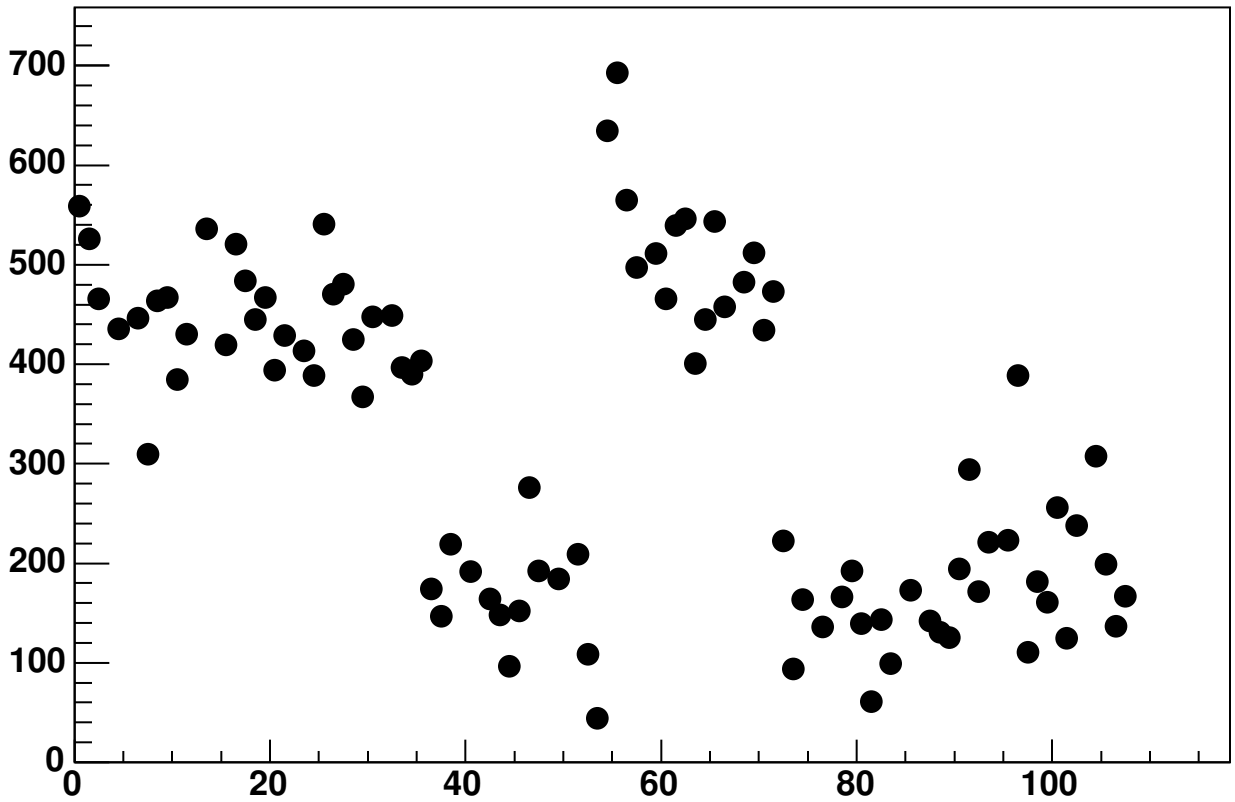
Disabled 0, Hold=35, Fit Intercept vs 18*Chip+Chan



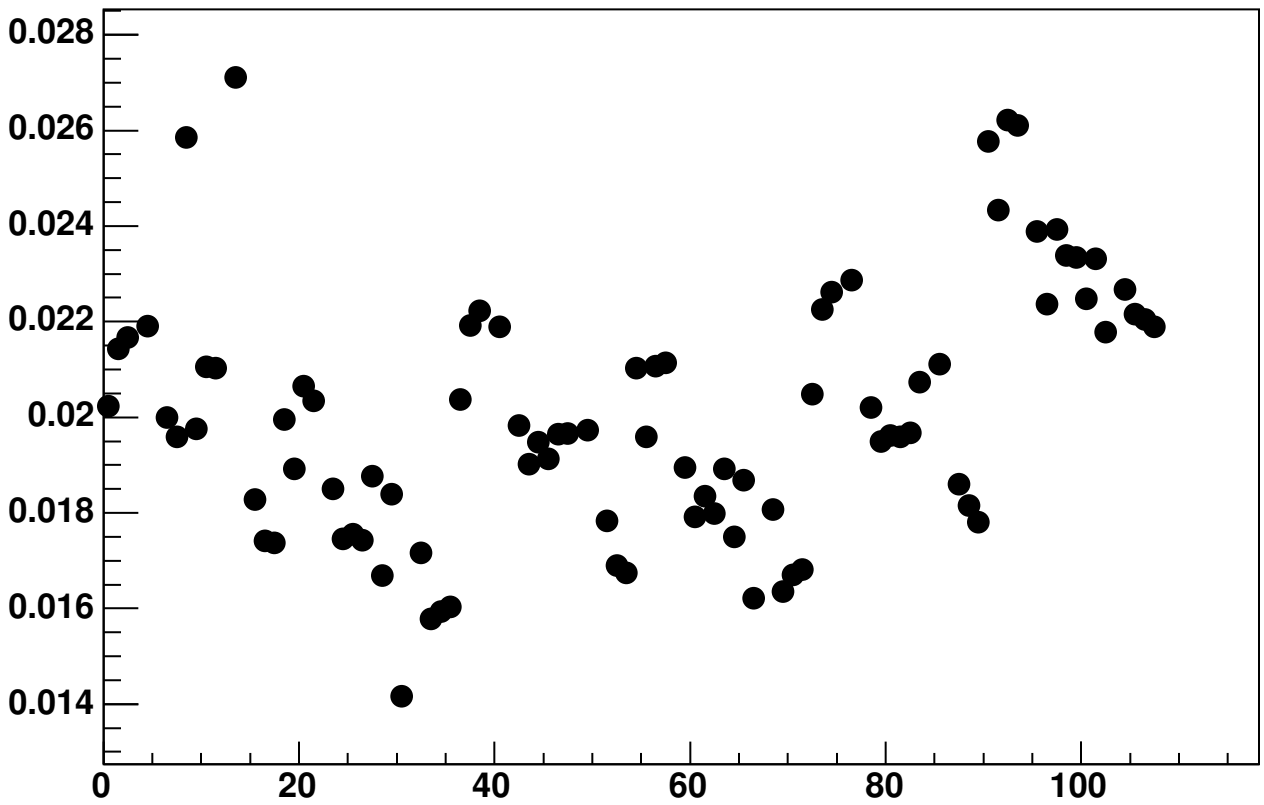
Disabled 0, Hold=35, Fit Slope vs 18*Chip+Chan



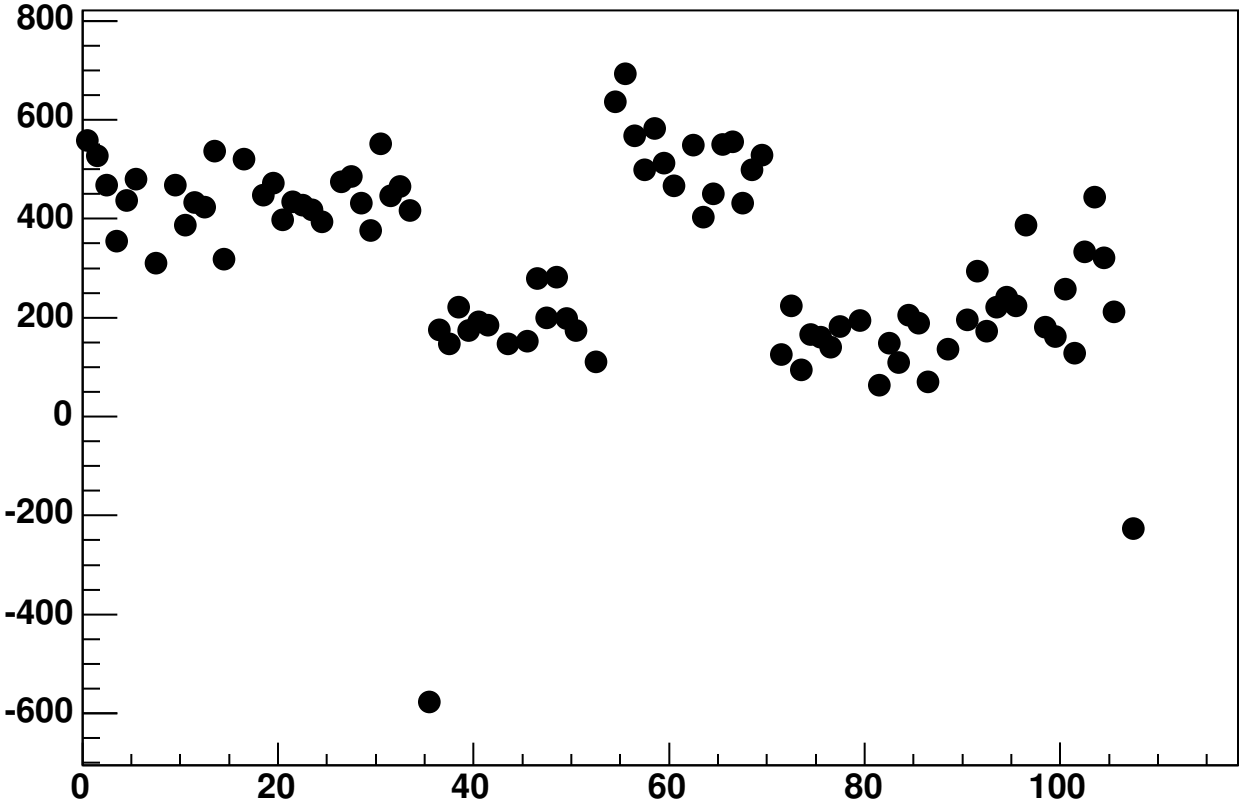
Disabled 1, Hold=35, Fit Intercept vs 18*Chip+Chan



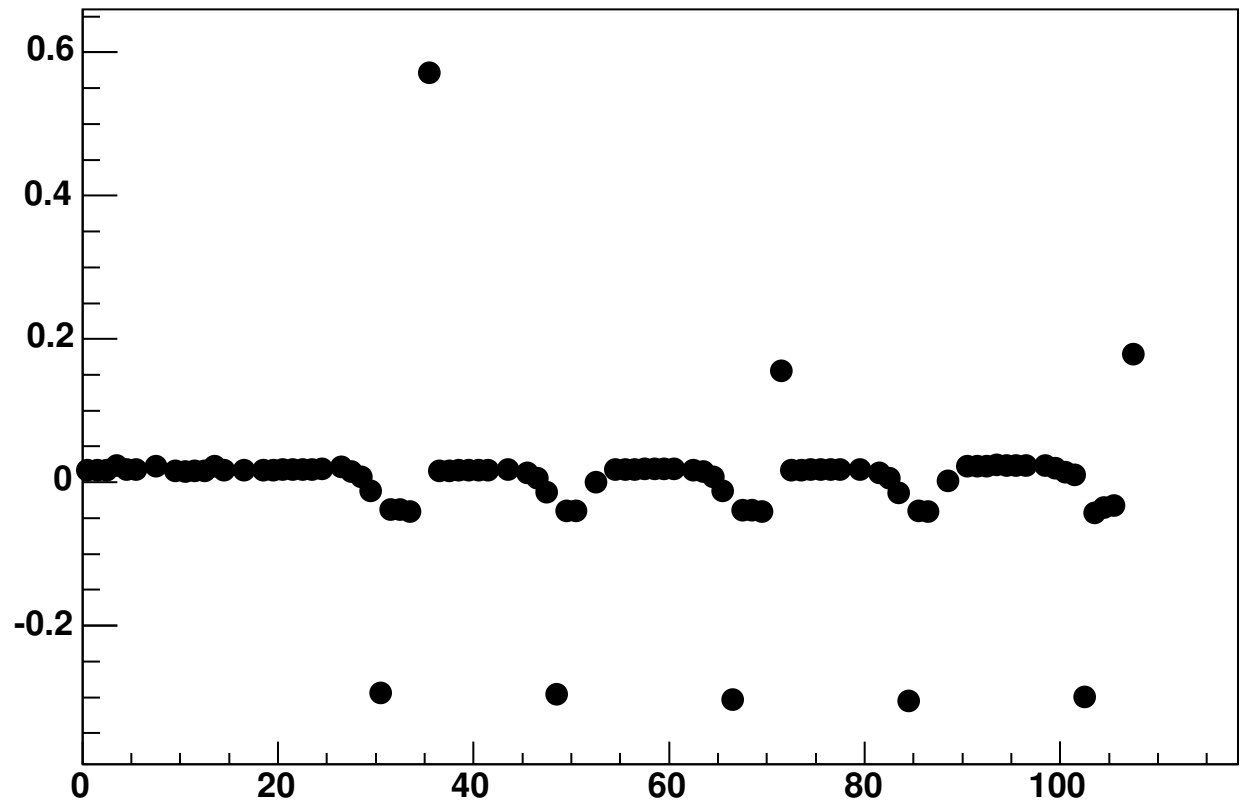
Disabled 1, Hold=35, Fit Slope vs 18*Chip+Chan



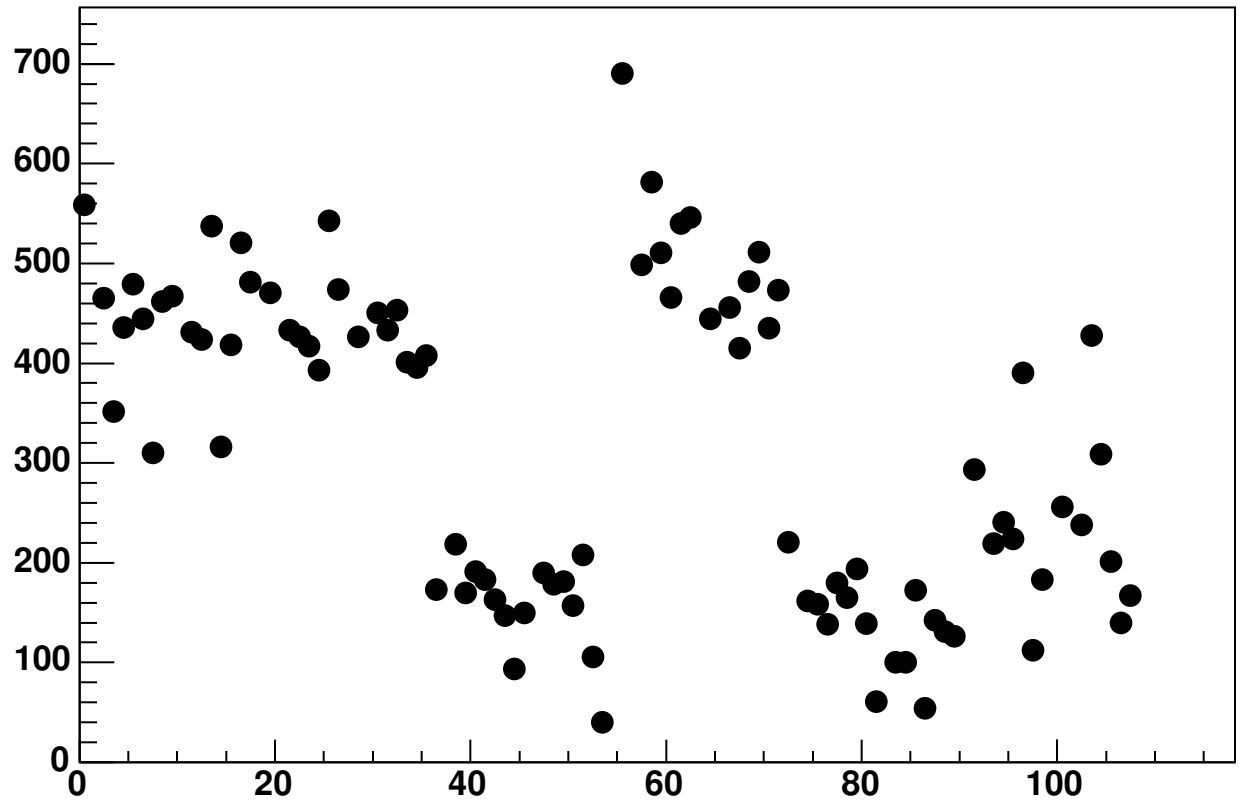
Disabled 2, Hold=35, Fit Intercept vs 18*Chip+Chan



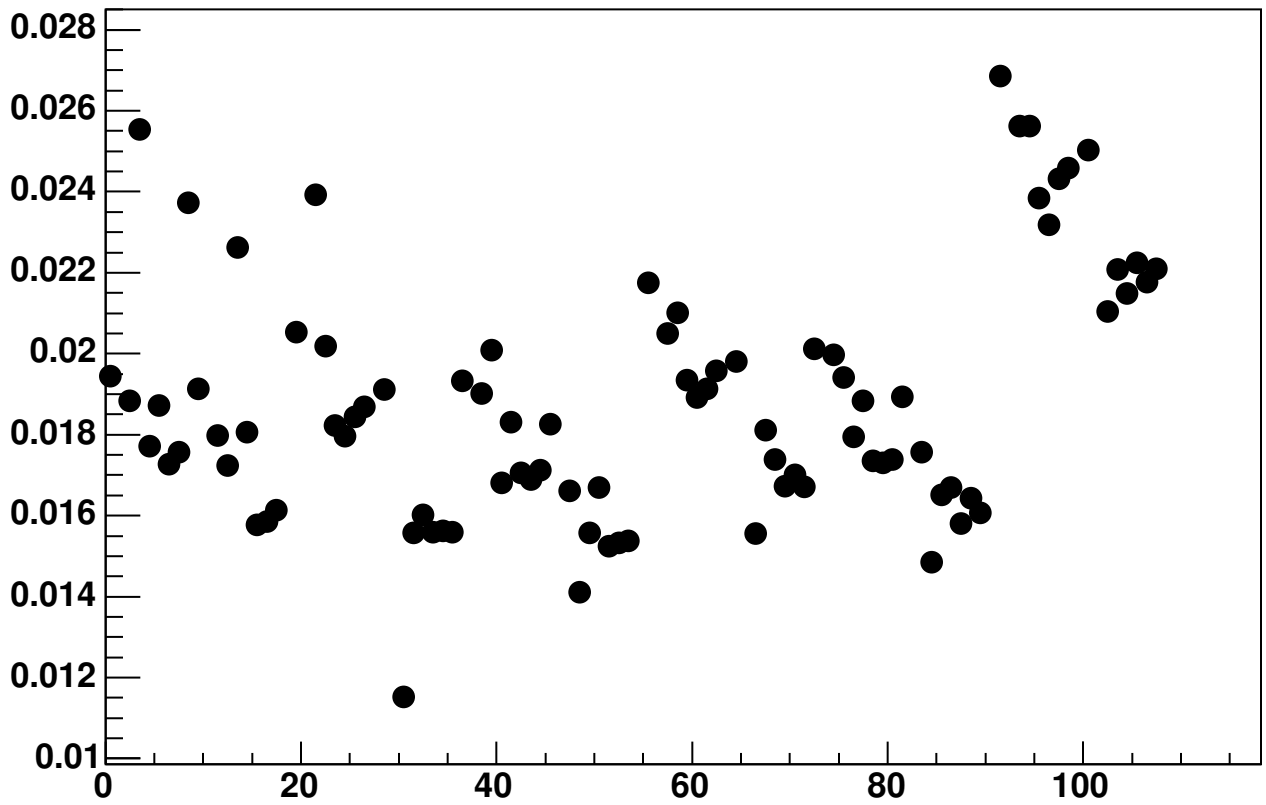
Disabled 2, Hold=35, Fit Slope vs 18*Chip+Chan



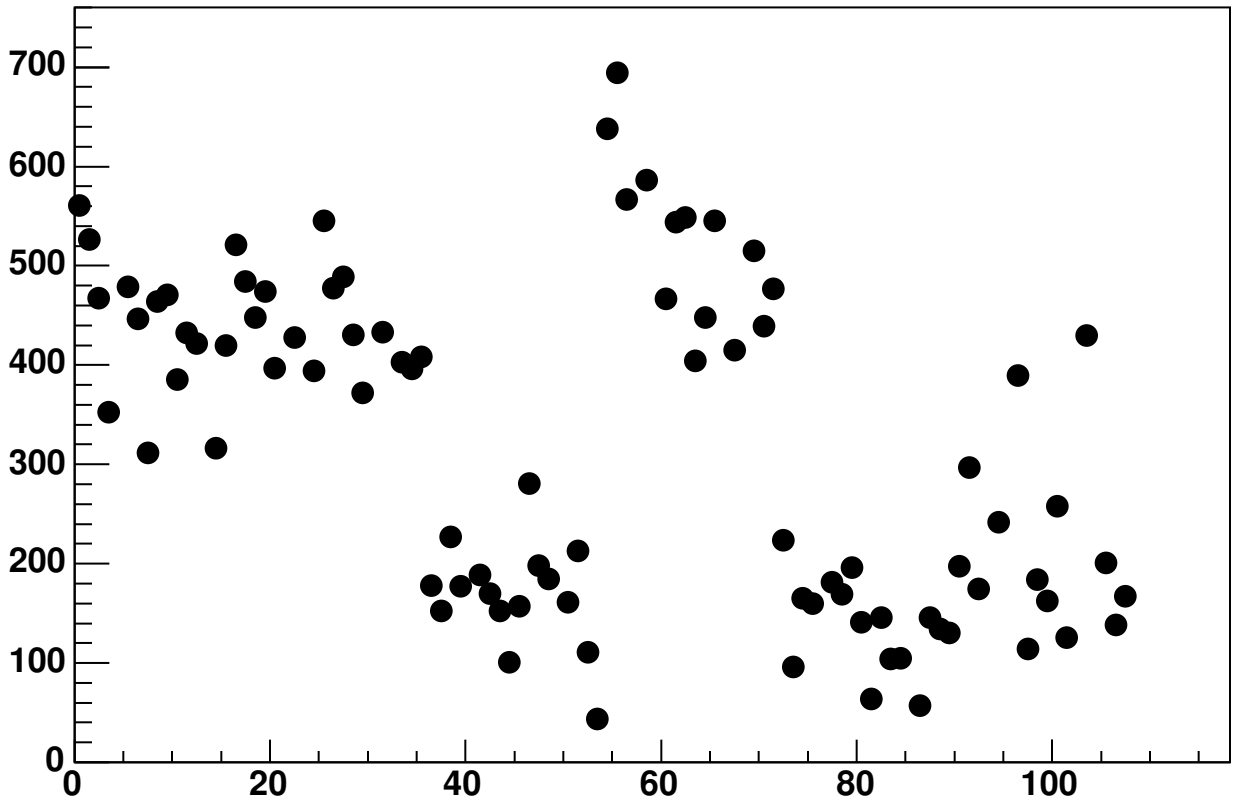
Disabled 3, Hold=35, Fit Intercept vs 18*Chip+Chan



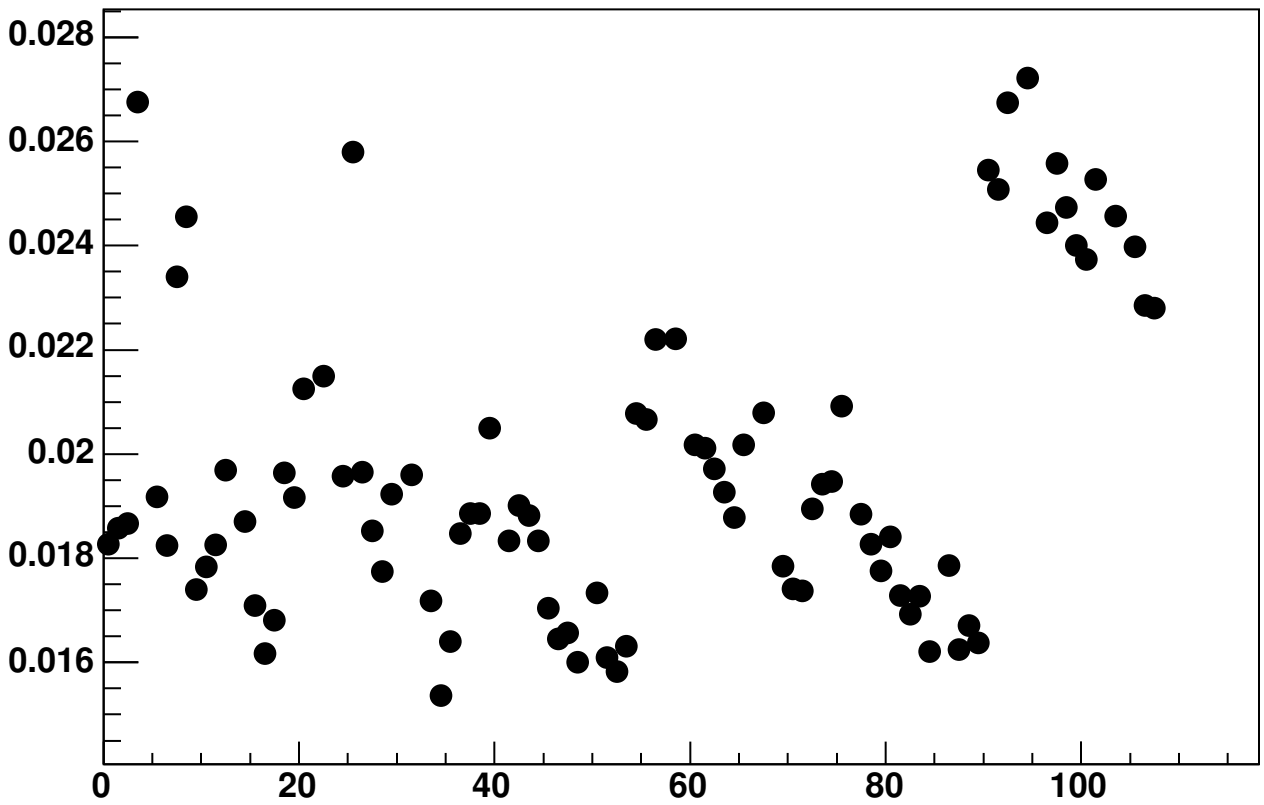
Disabled 3, Hold=35, Fit Slope vs 18*Chip+Chan



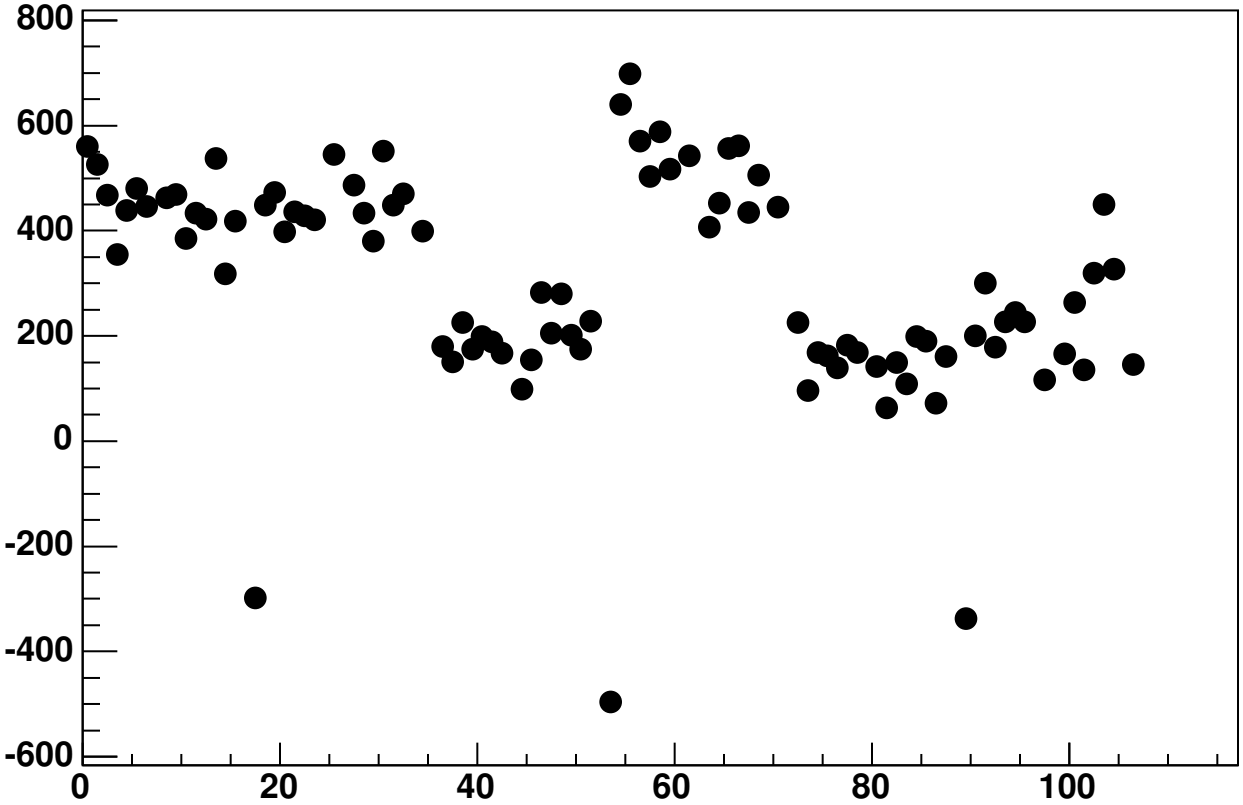
Disabled 4, Hold=35, Fit Intercept vs 18*Chip+Chan



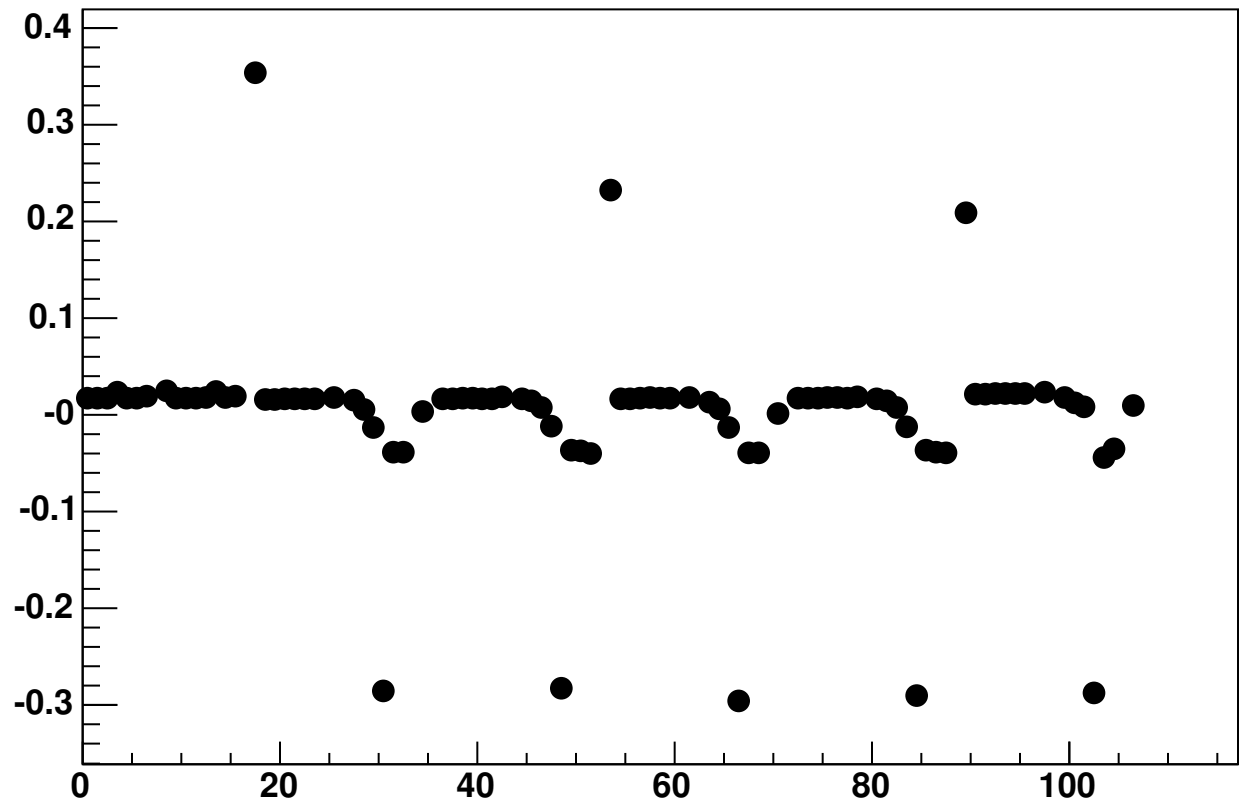
Disabled 4, Hold=35, Fit Slope vs 18*Chip+Chan



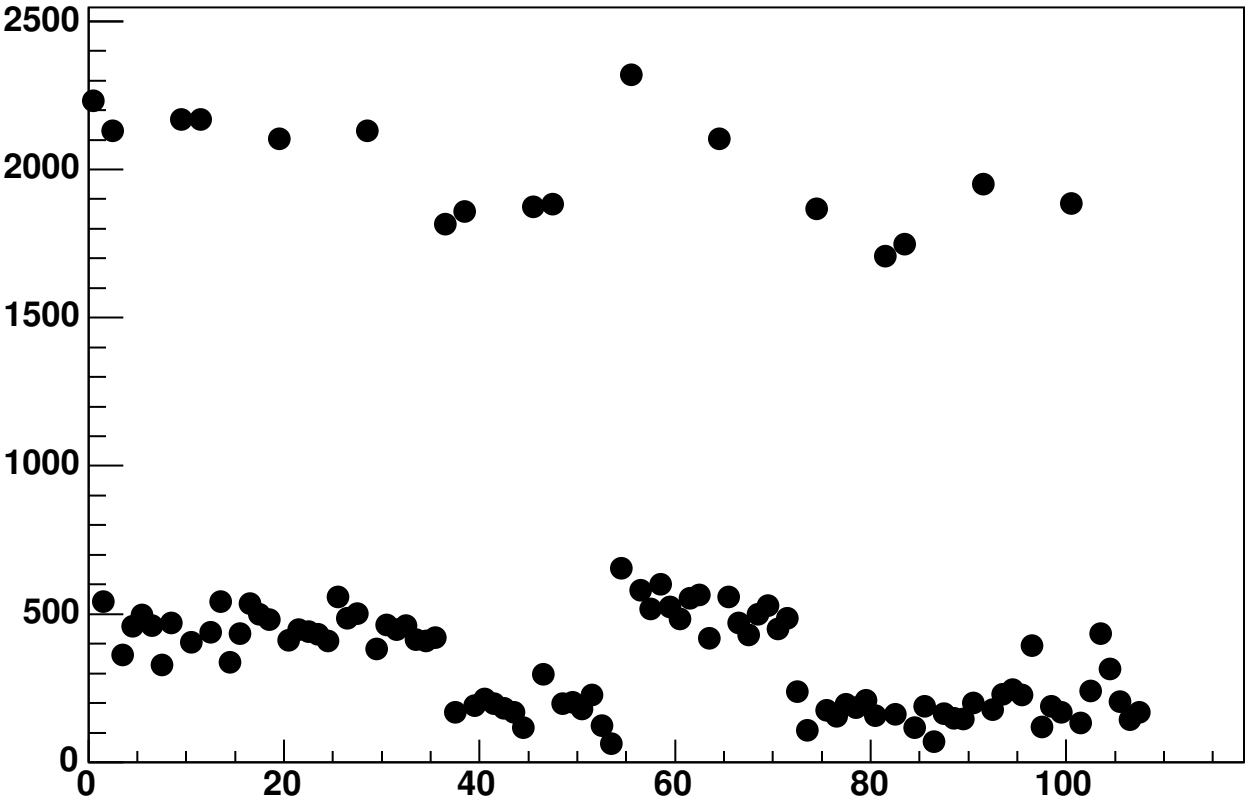
Disabled 5, Hold=35, Fit Intercept vs 18*Chip+Chan



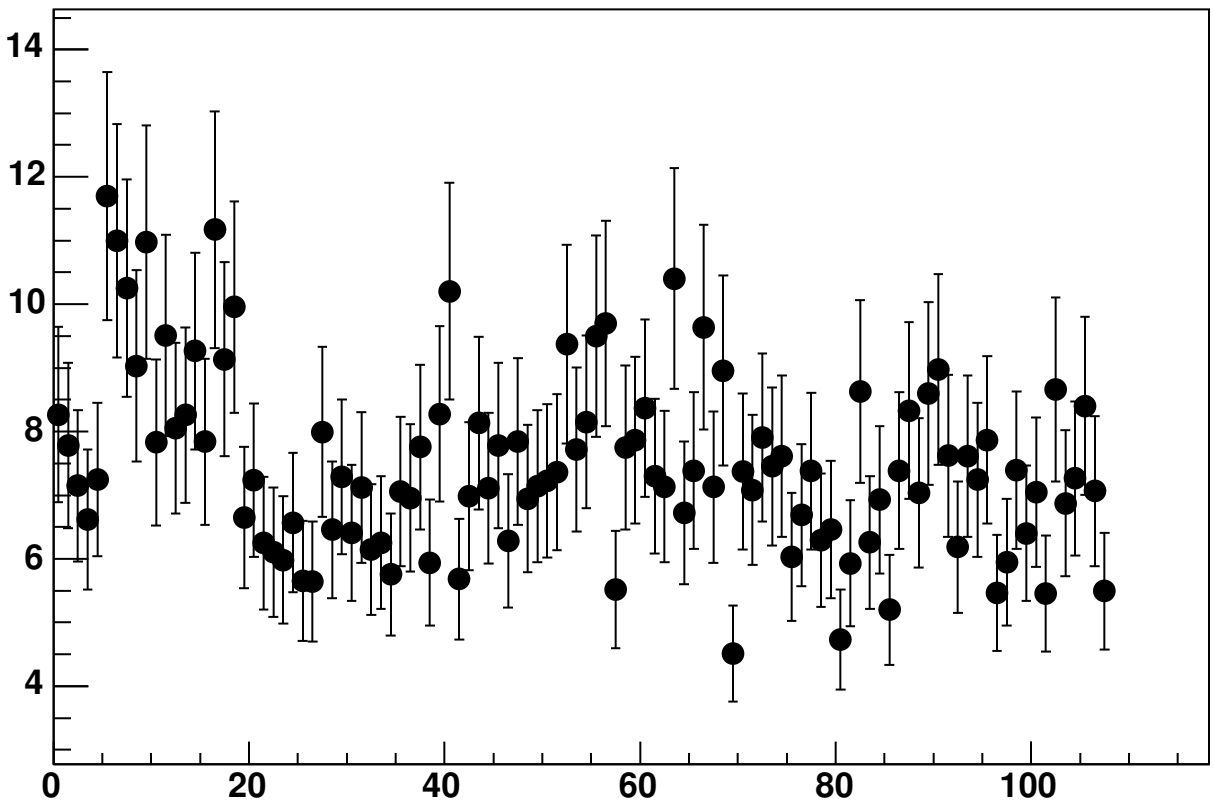
Disabled 5, Hold=35, Fit Slope vs 18*Chip+Chan



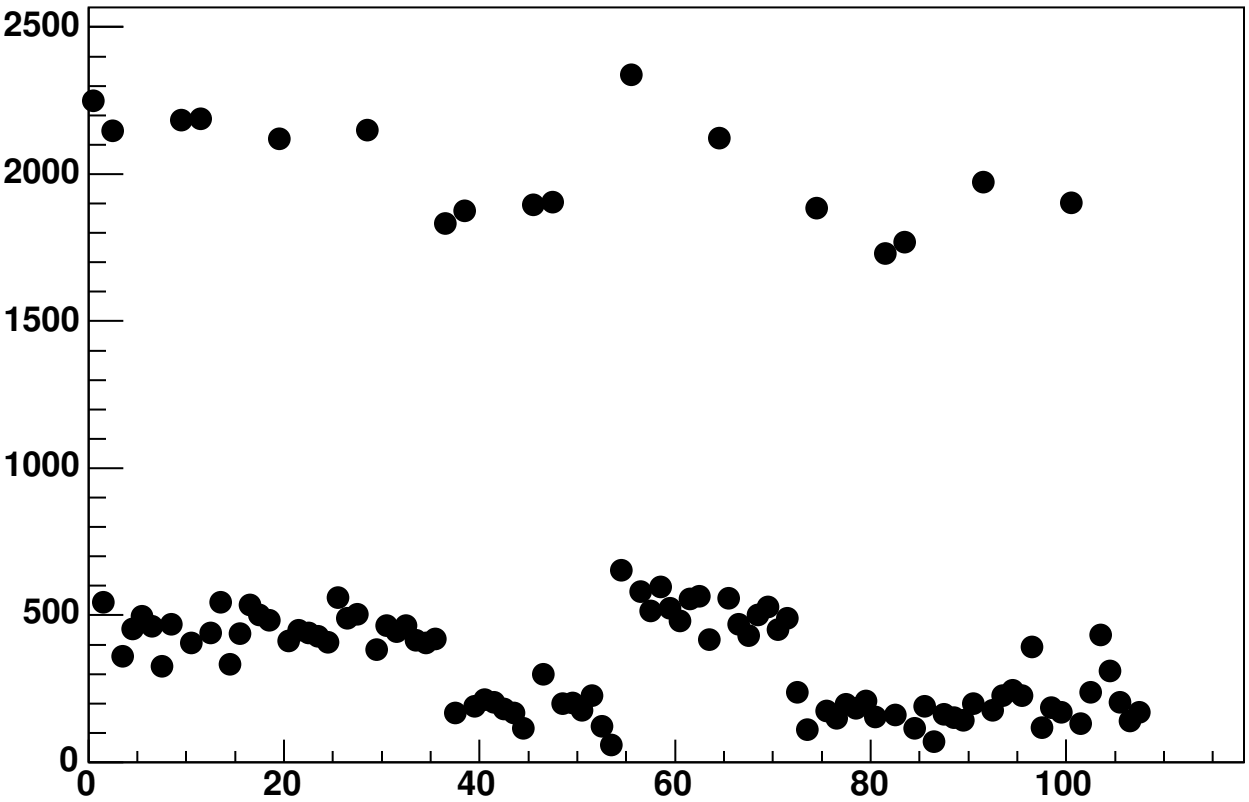
Enable 0, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



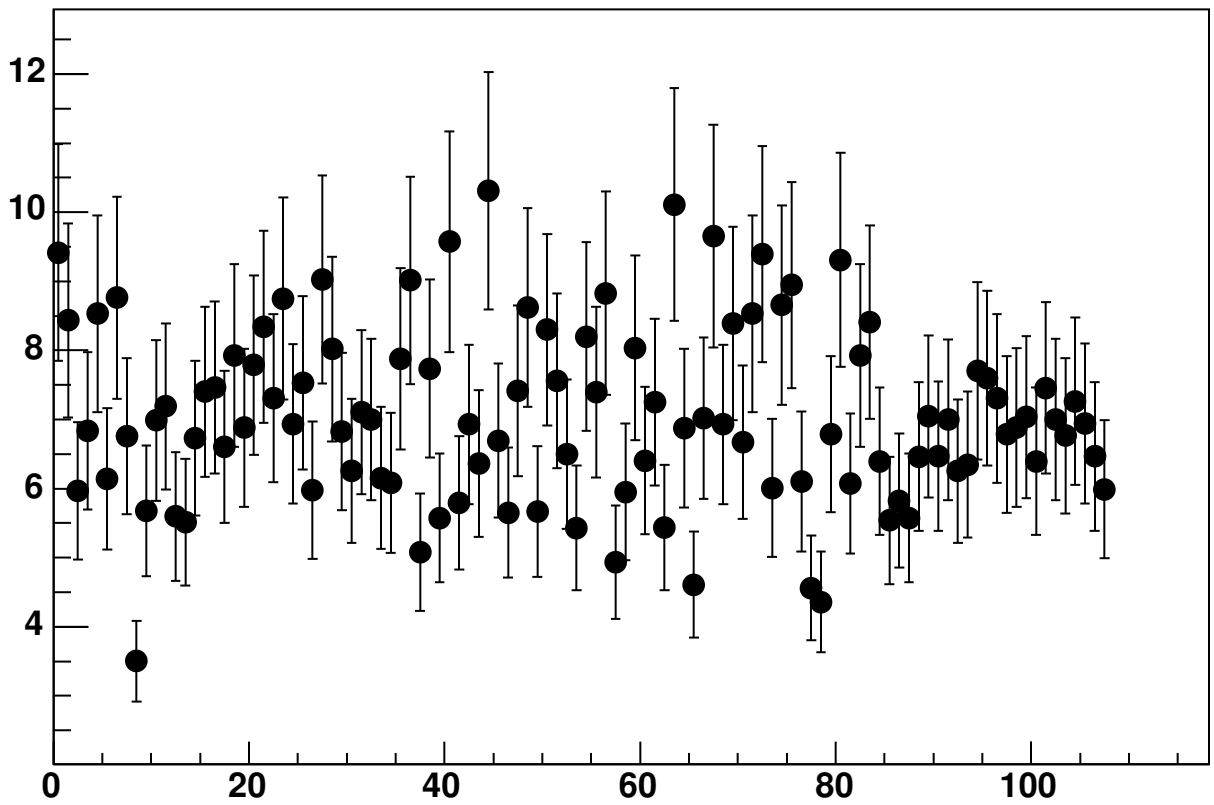
Enable 0, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



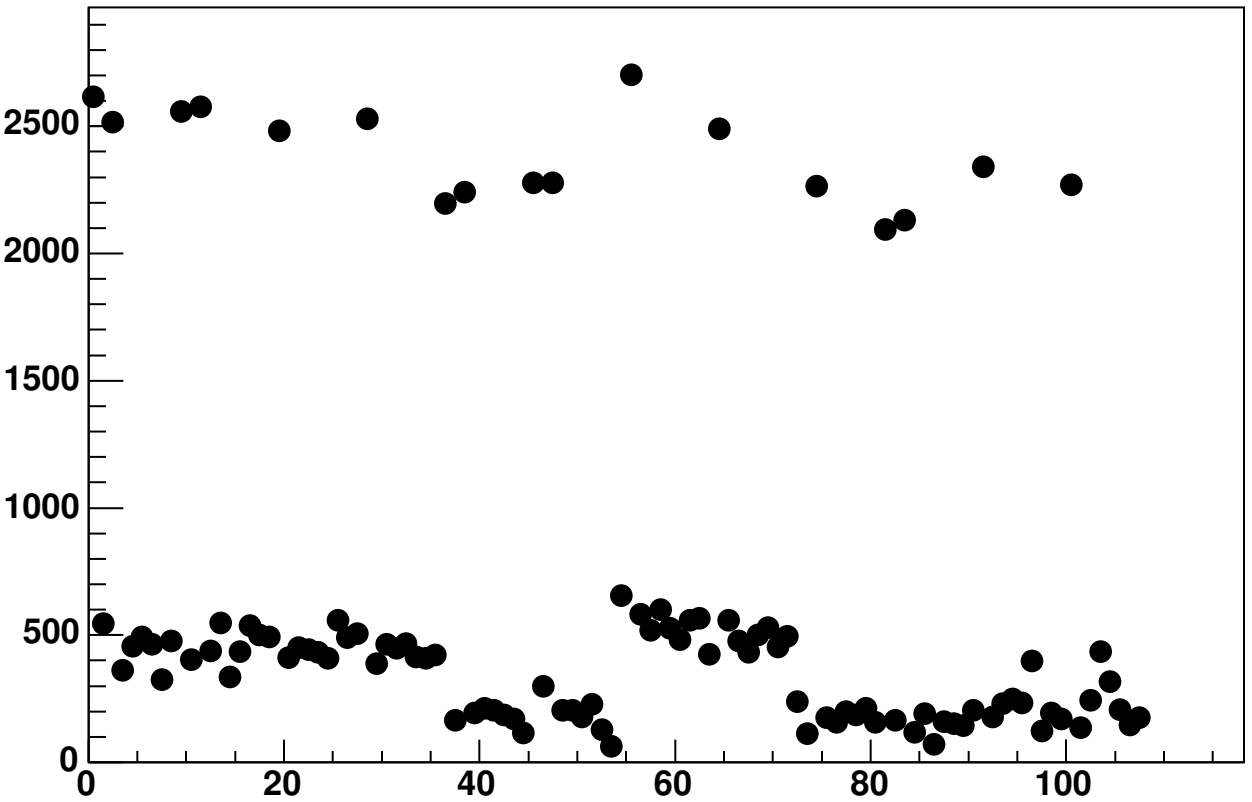
Enable 0, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



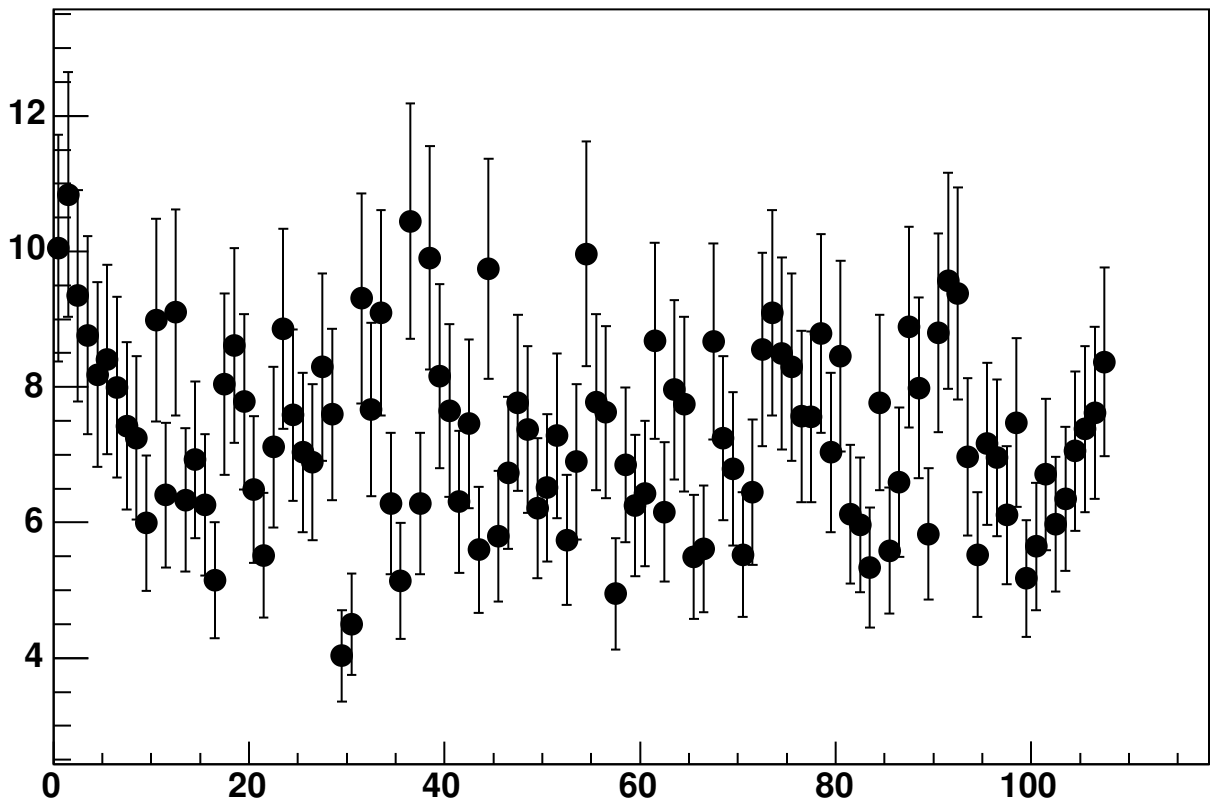
Enable 0, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



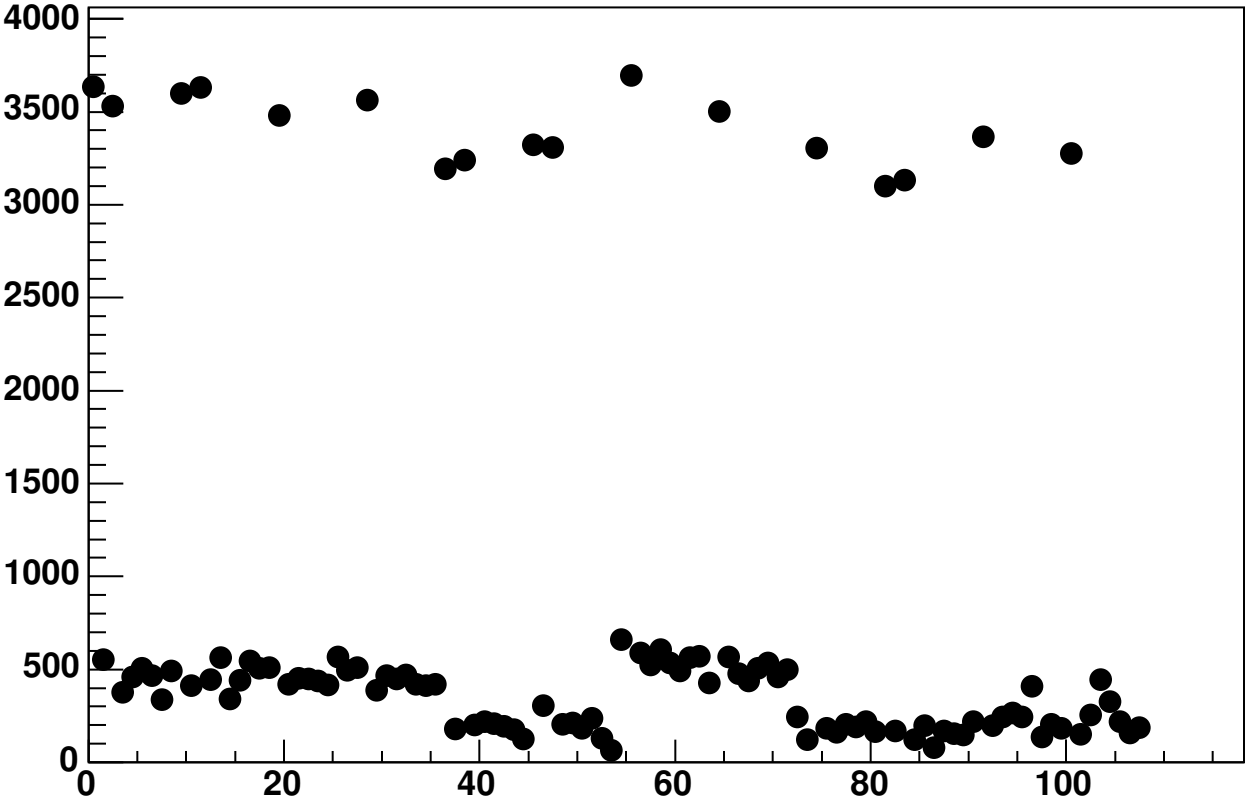
Enable 0, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



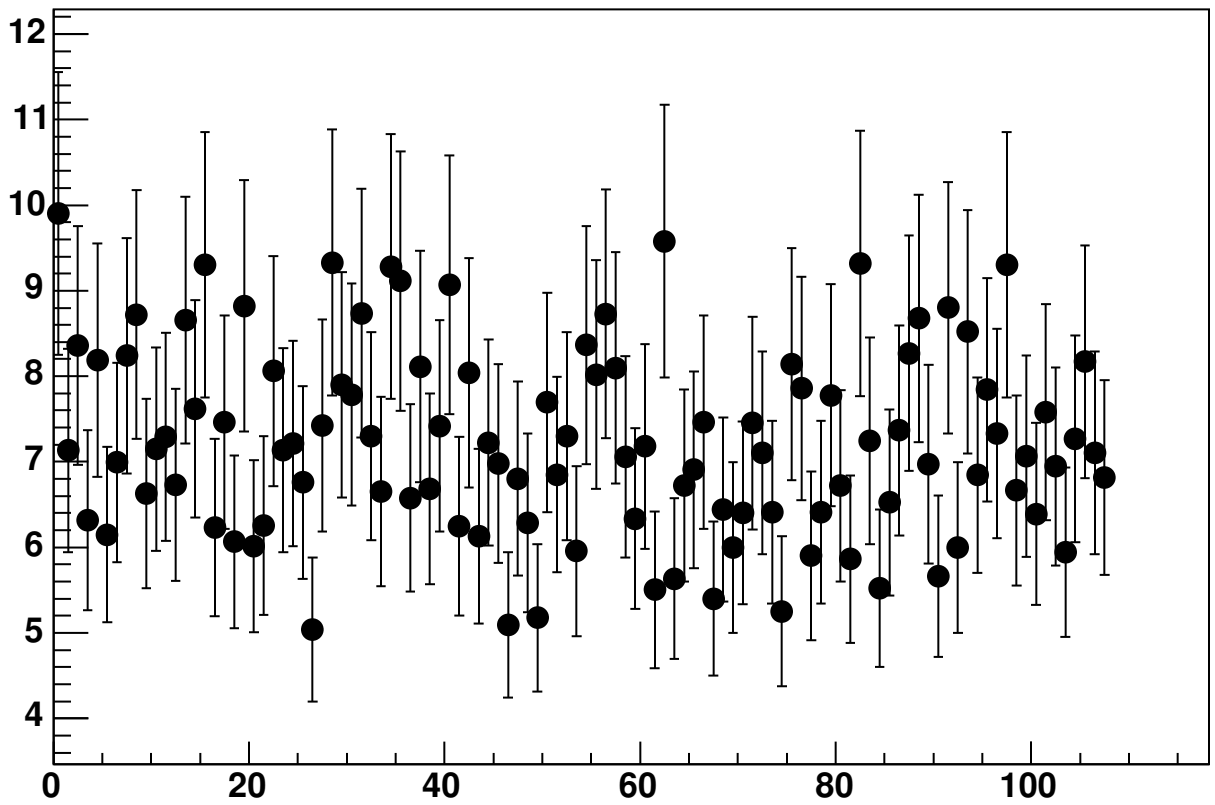
Enable 0, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



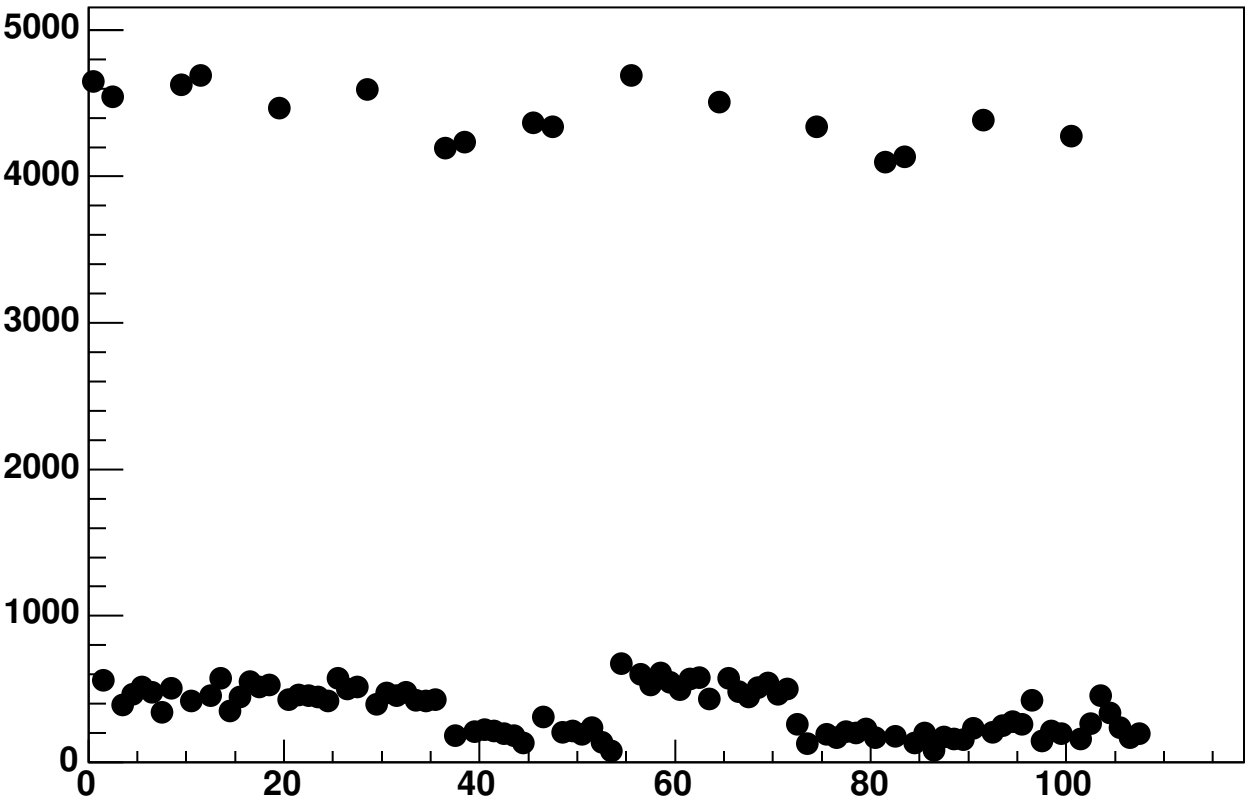
Enable 0, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



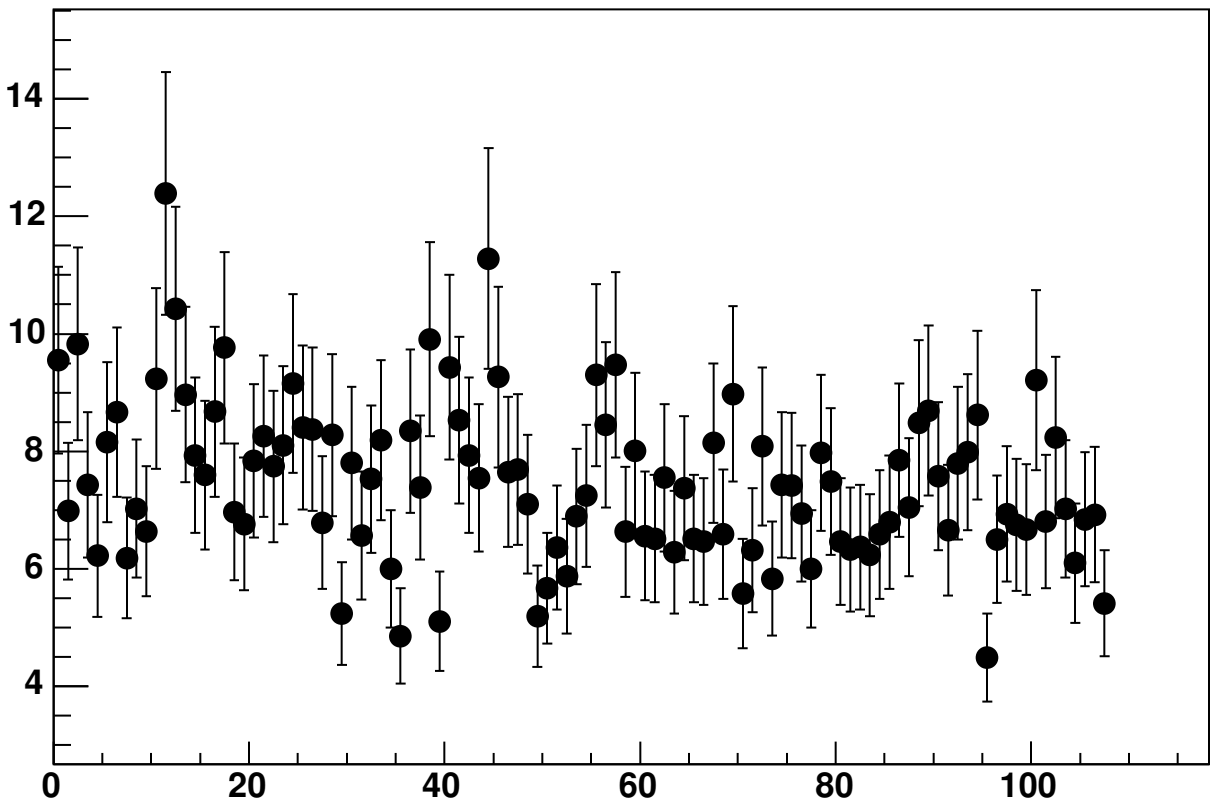
Enable 0, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



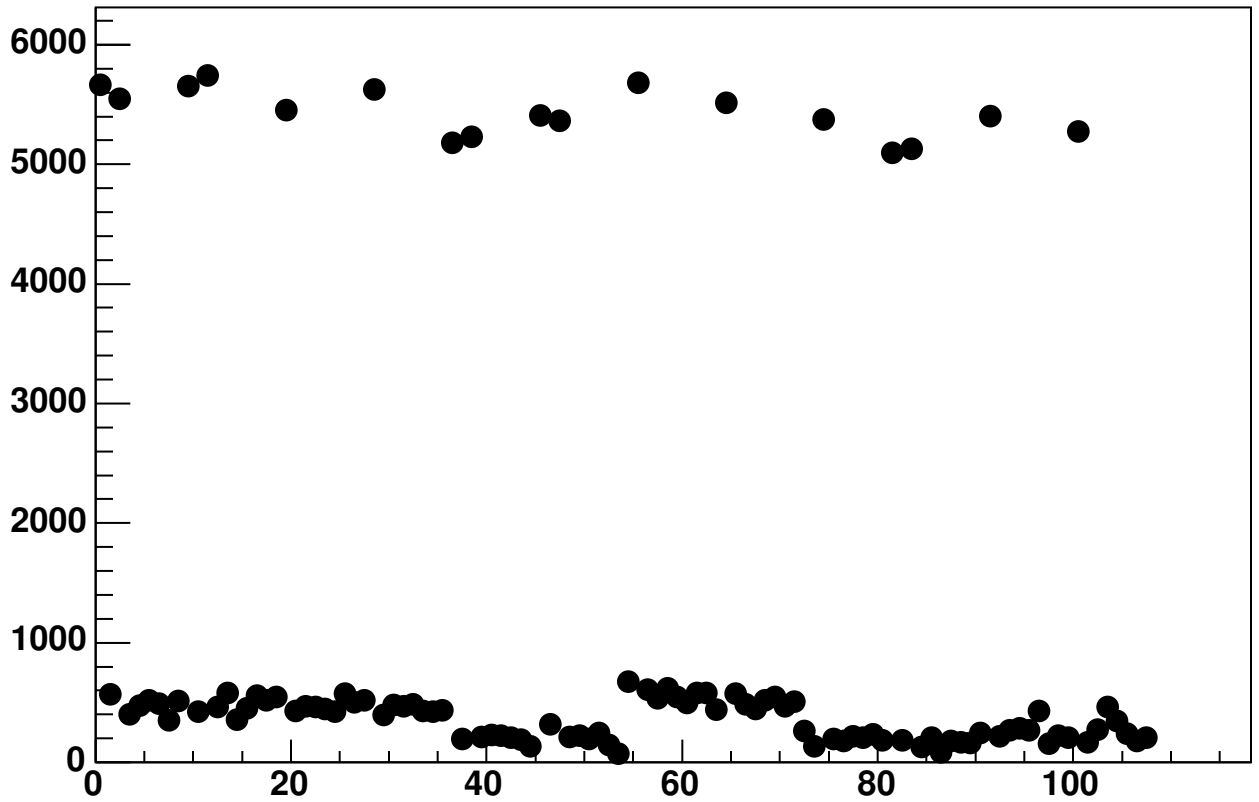
Enable 0, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



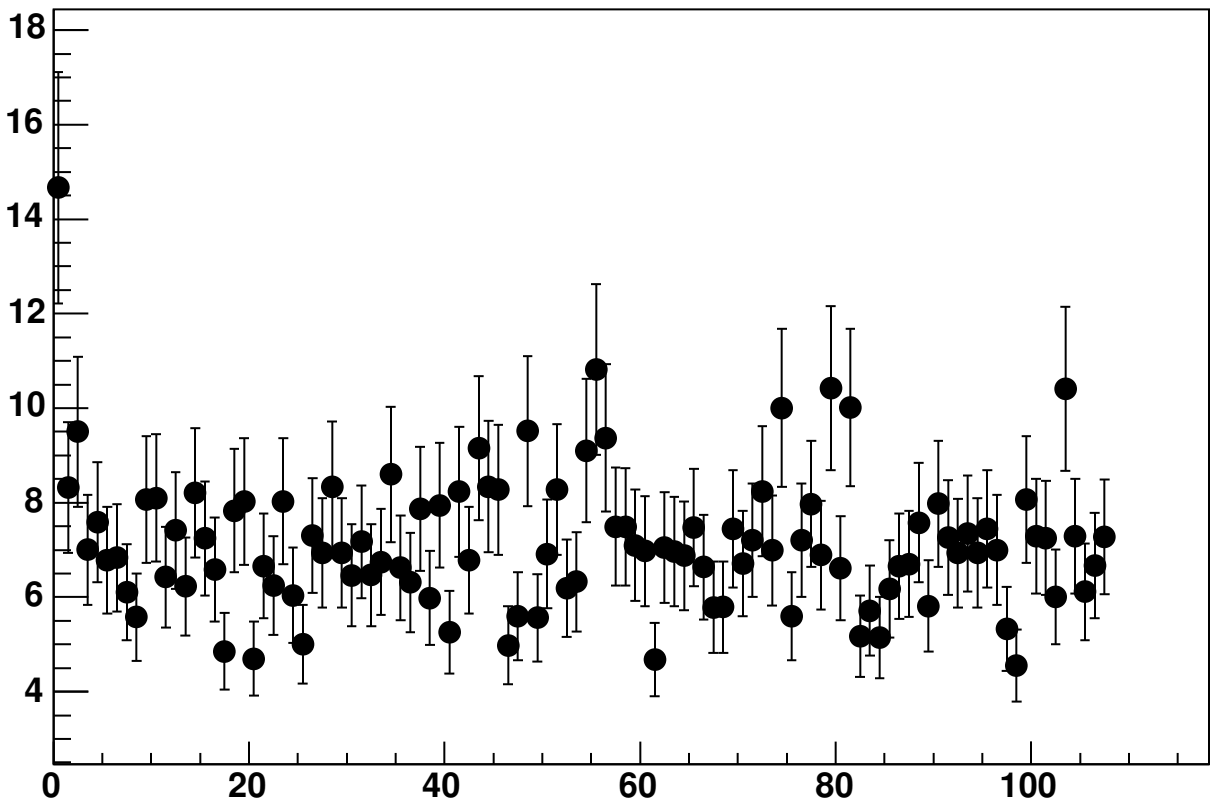
Enable 0, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



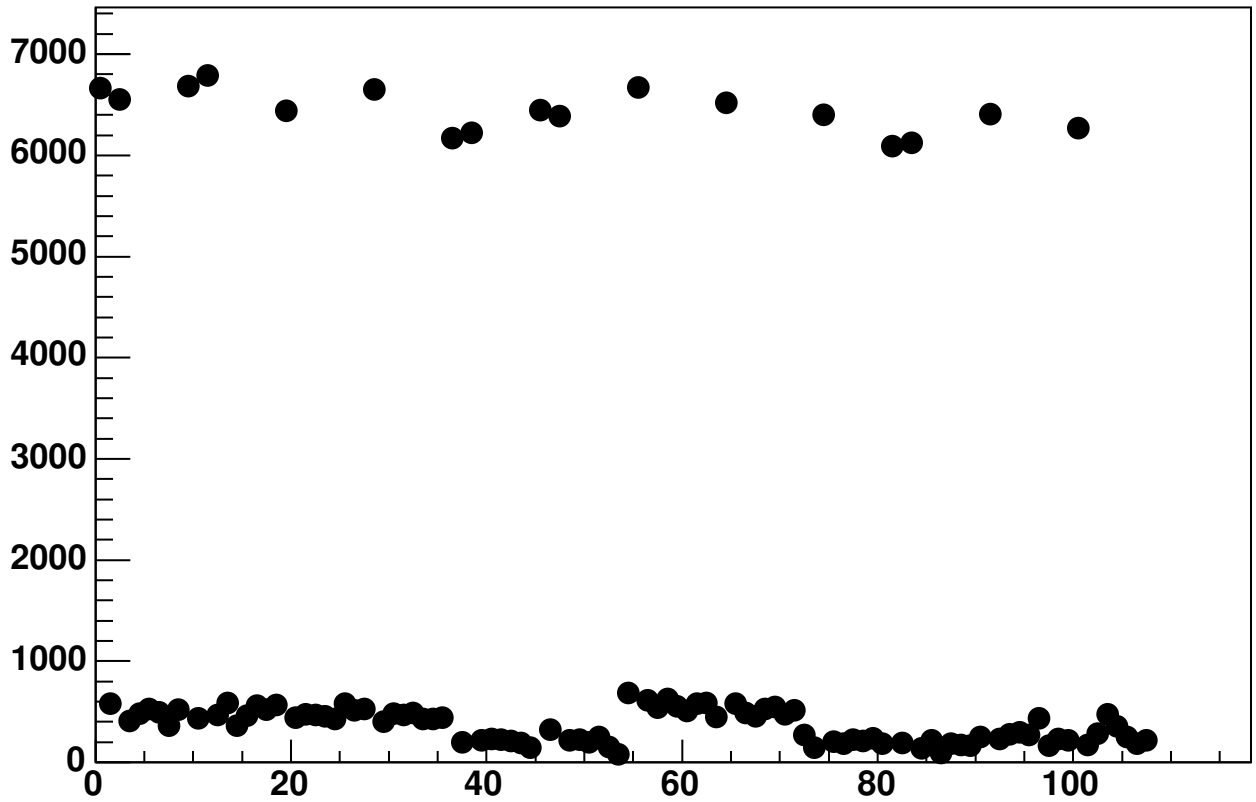
Enable 0, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



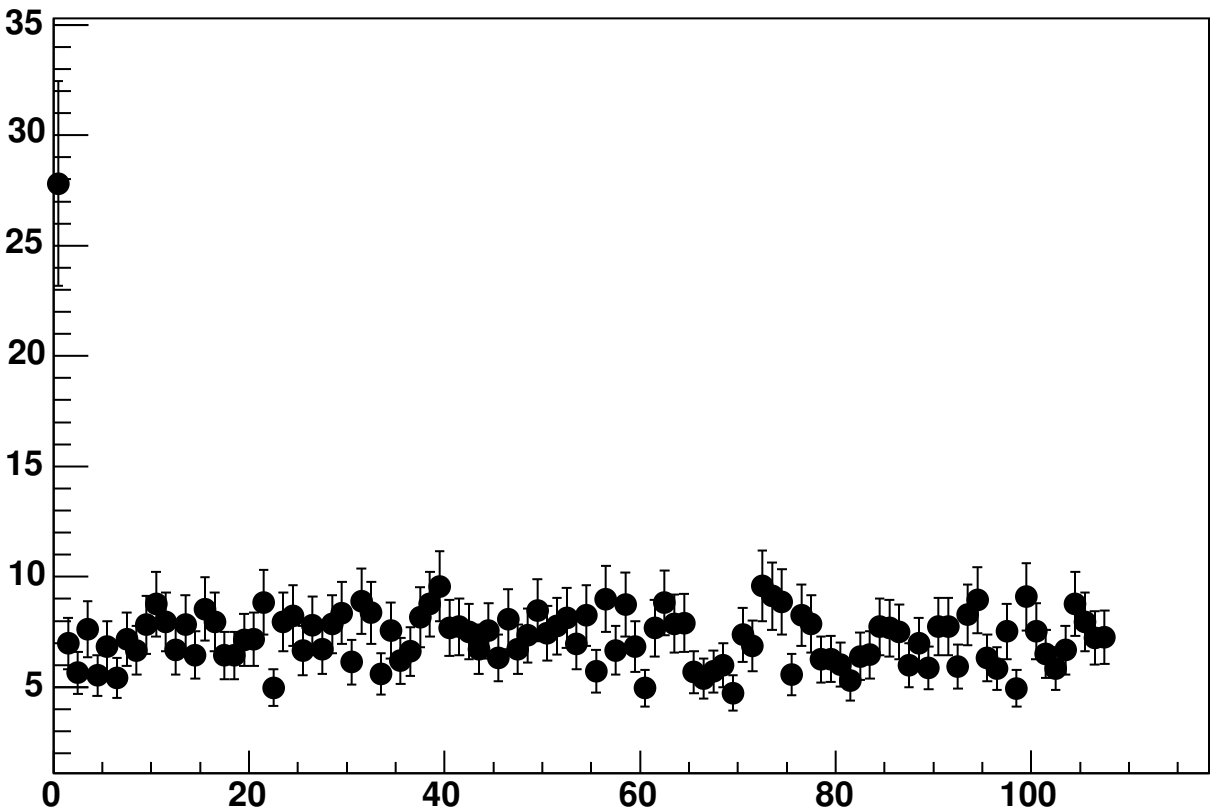
Enable 0, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



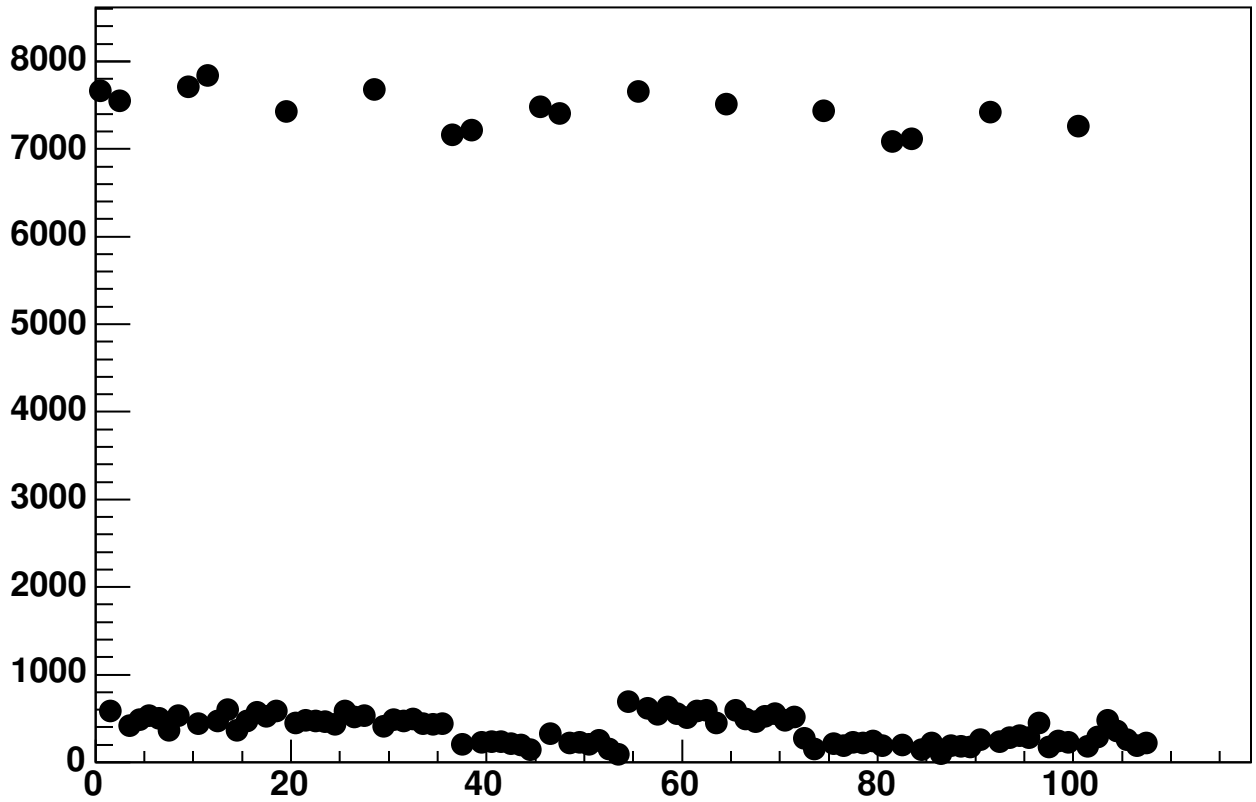
Enable 0, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



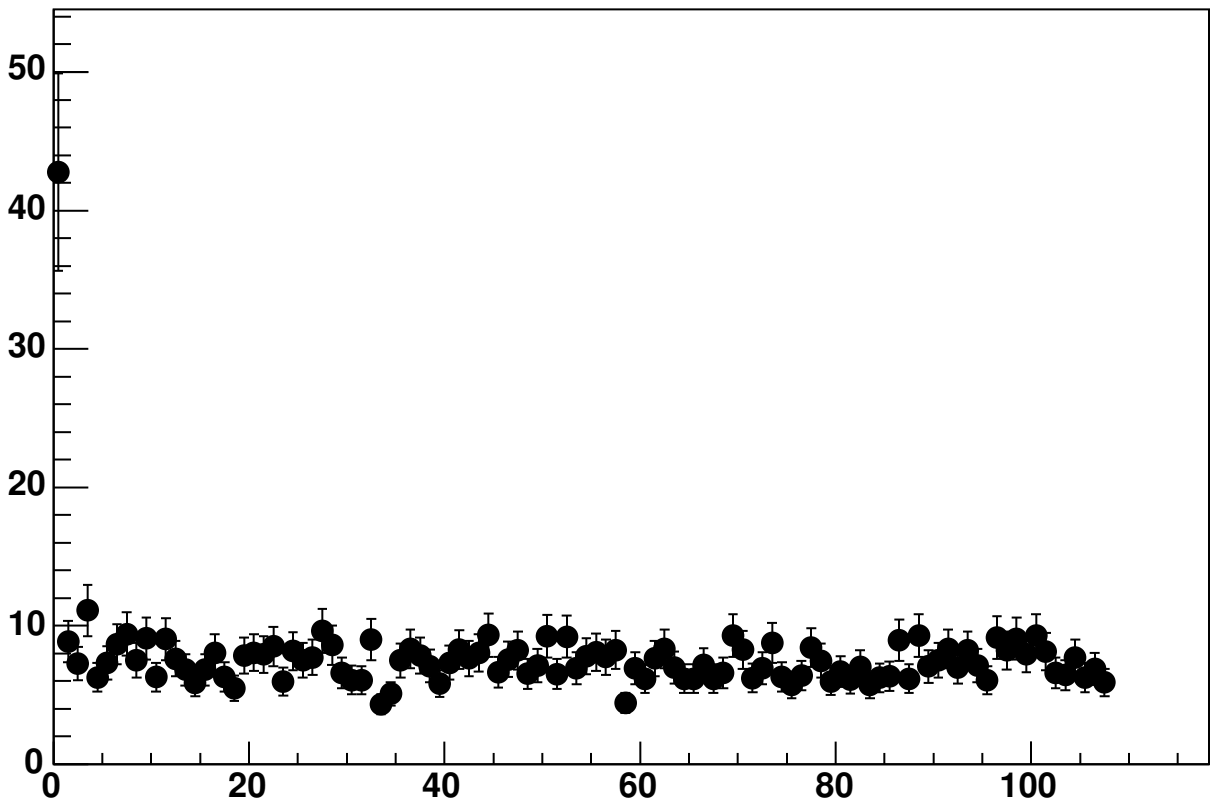
Enable 0, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



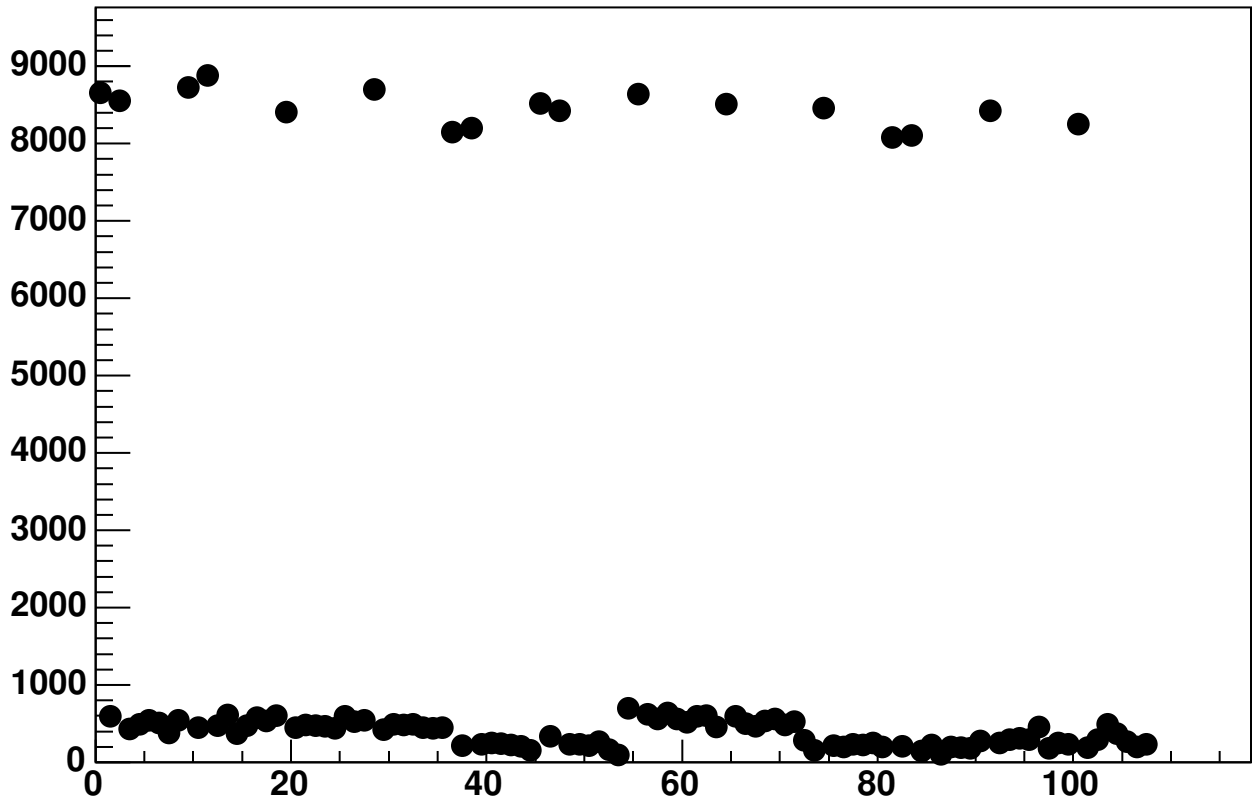
Enable 0, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



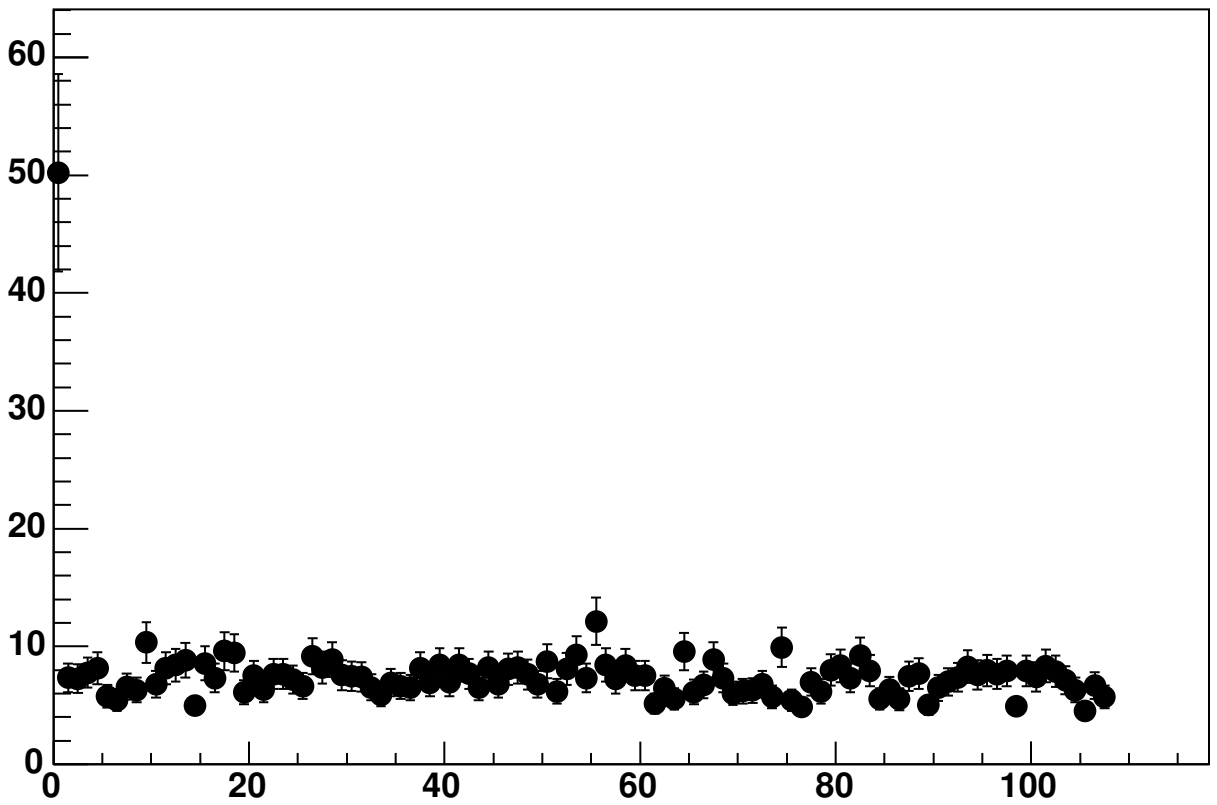
Enable 0, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



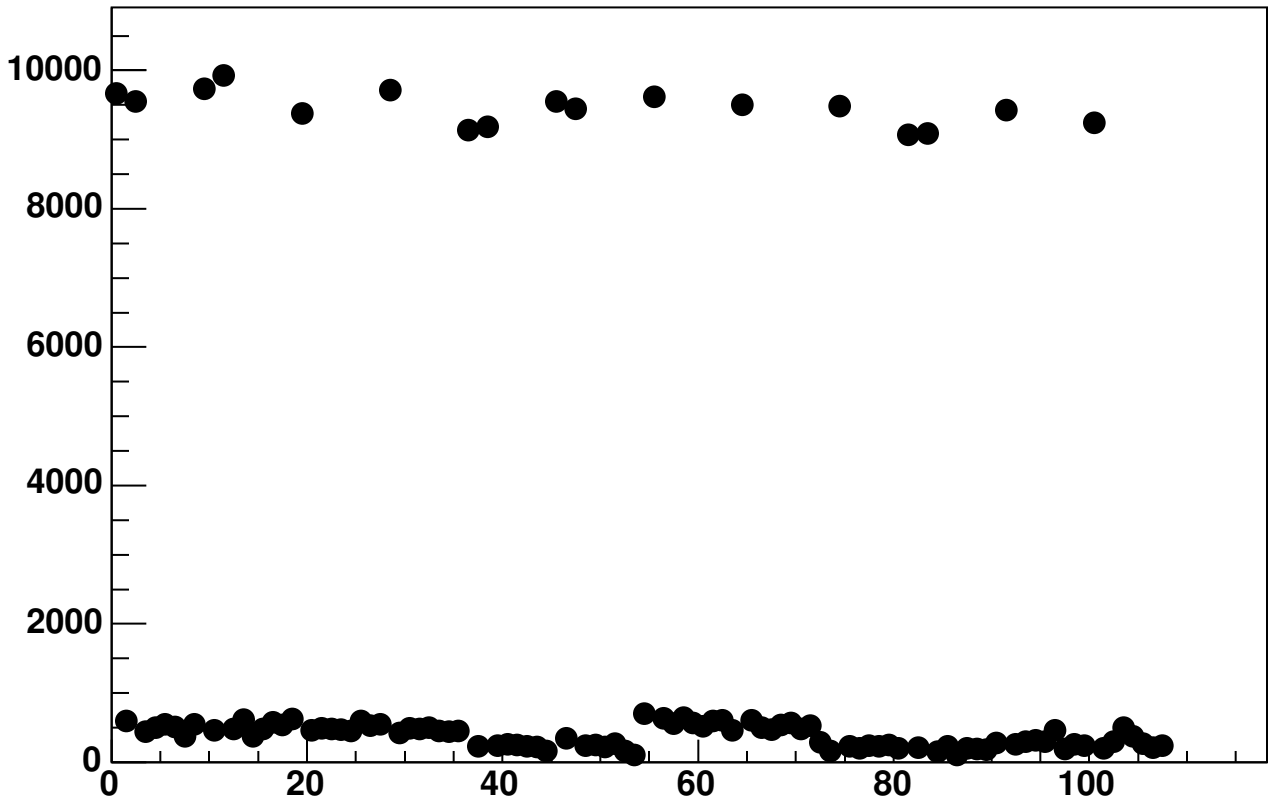
Enable 0, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



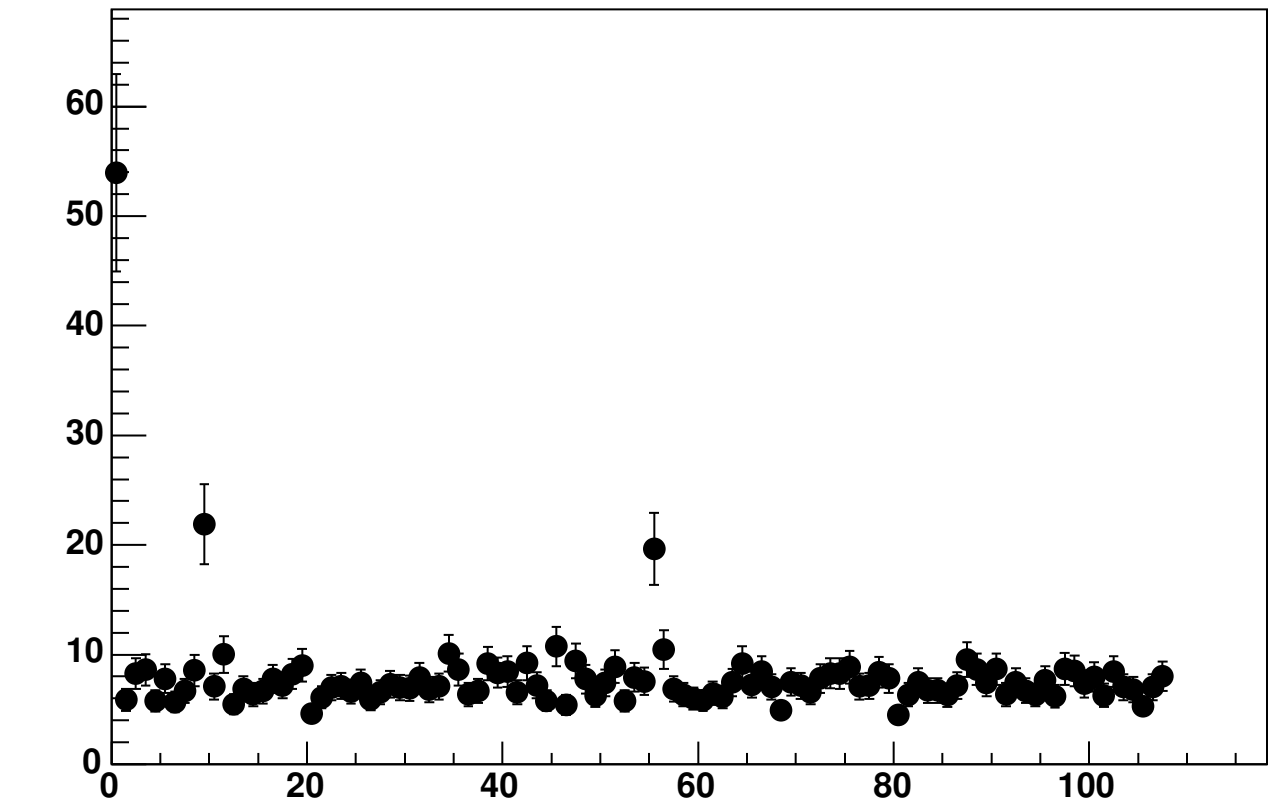
Enable 0, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



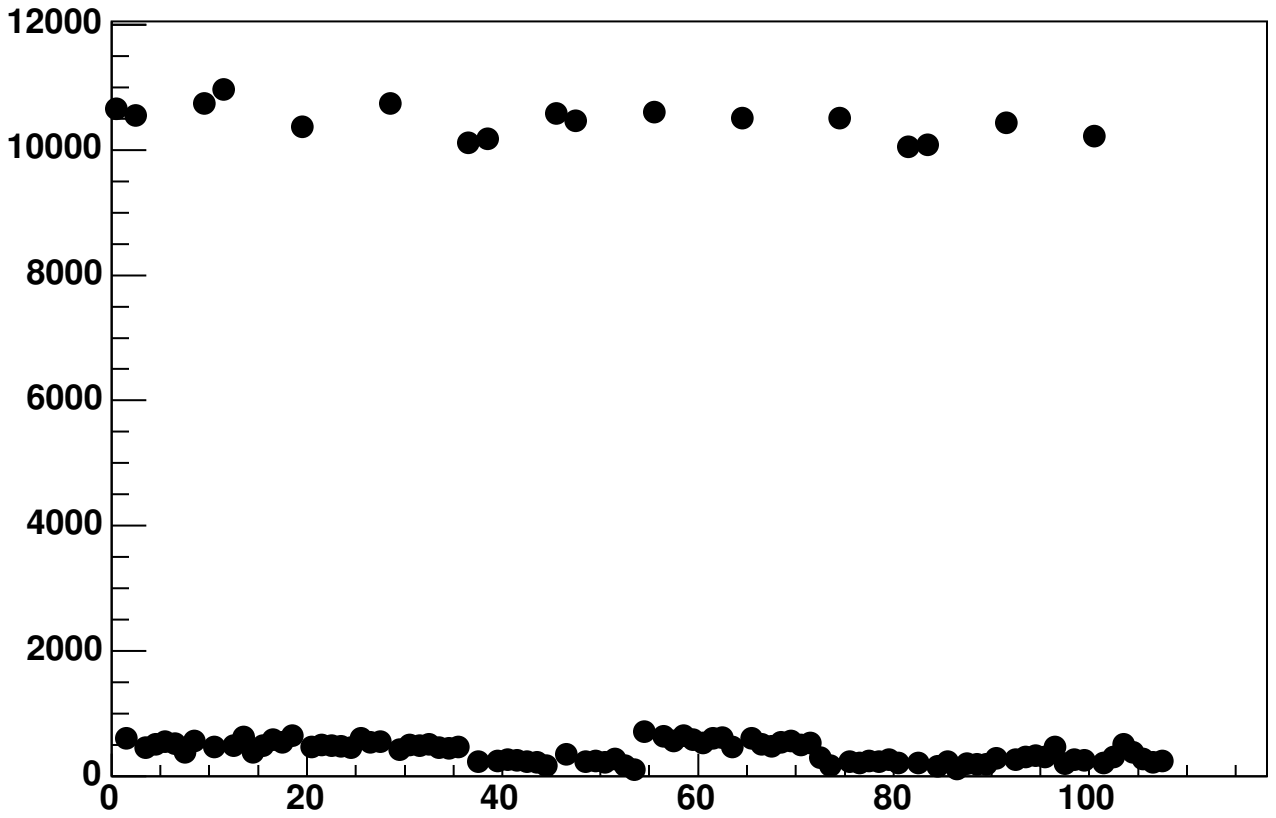
Enable 0, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



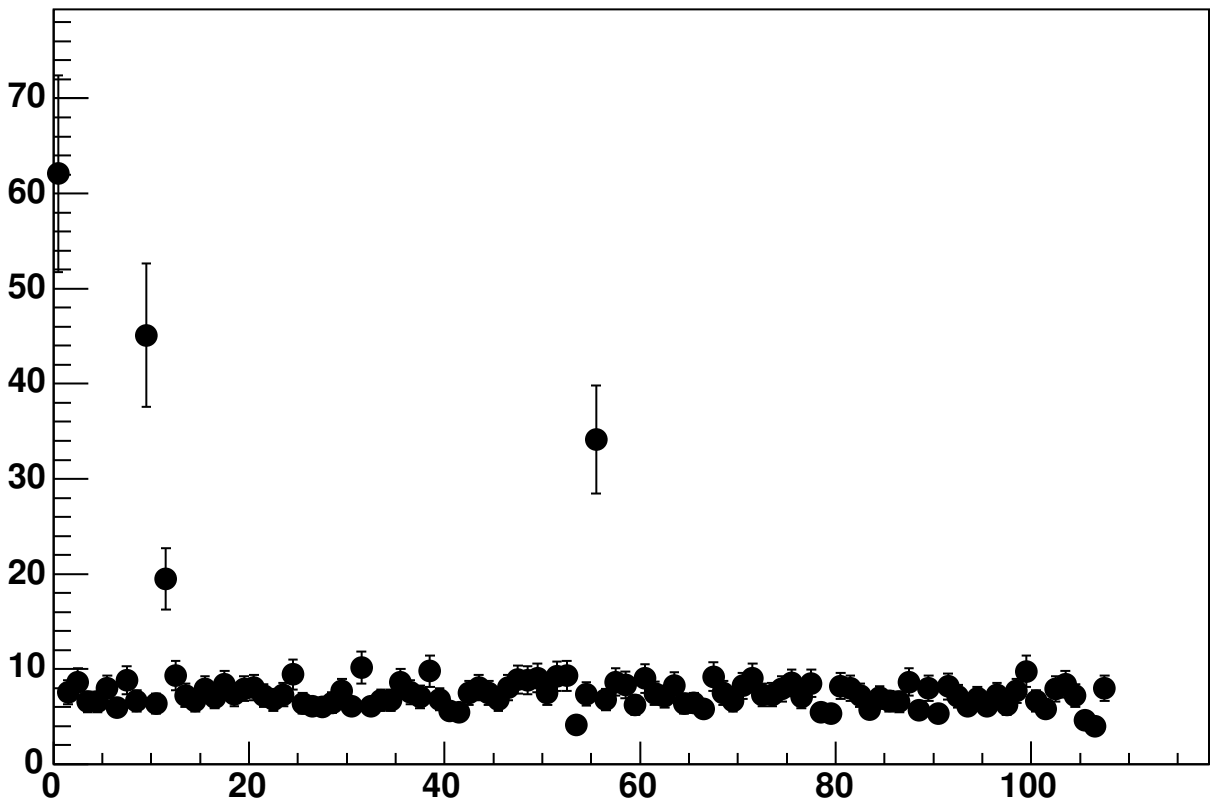
Enable 0, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



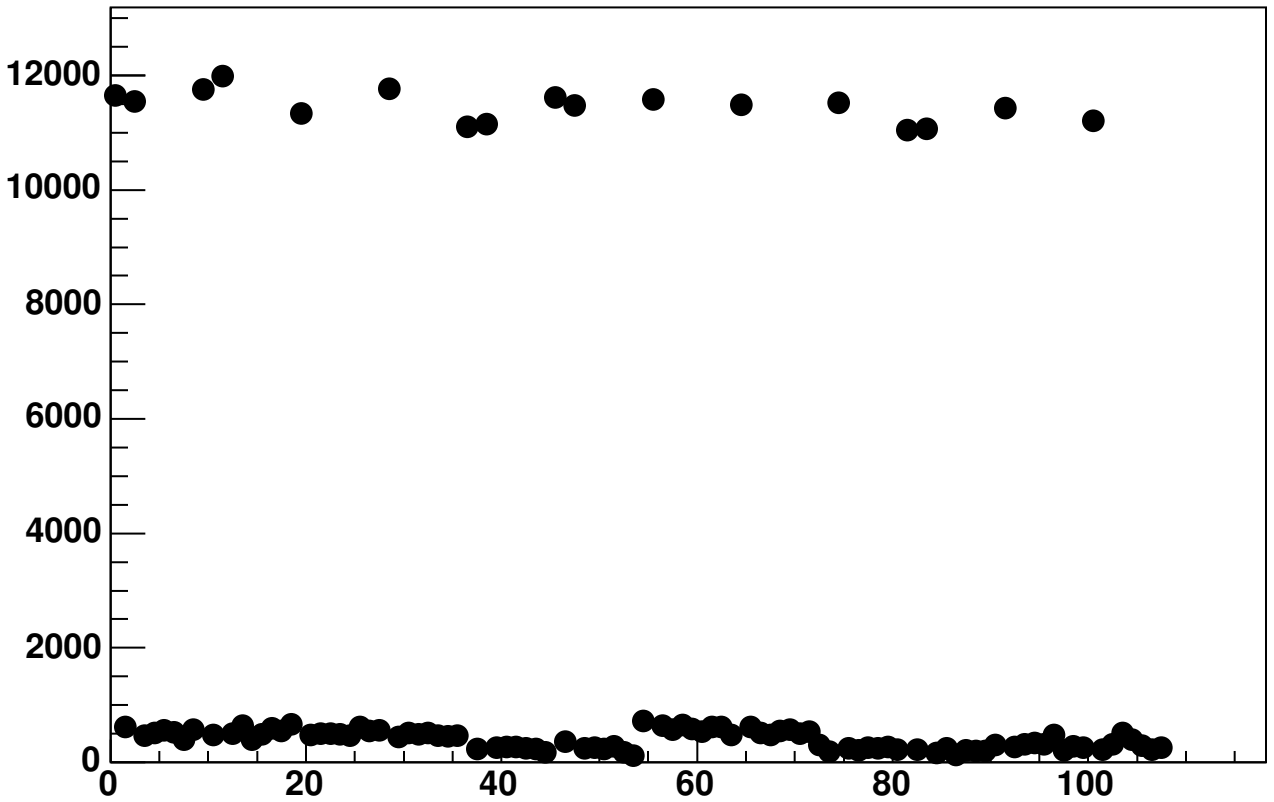
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



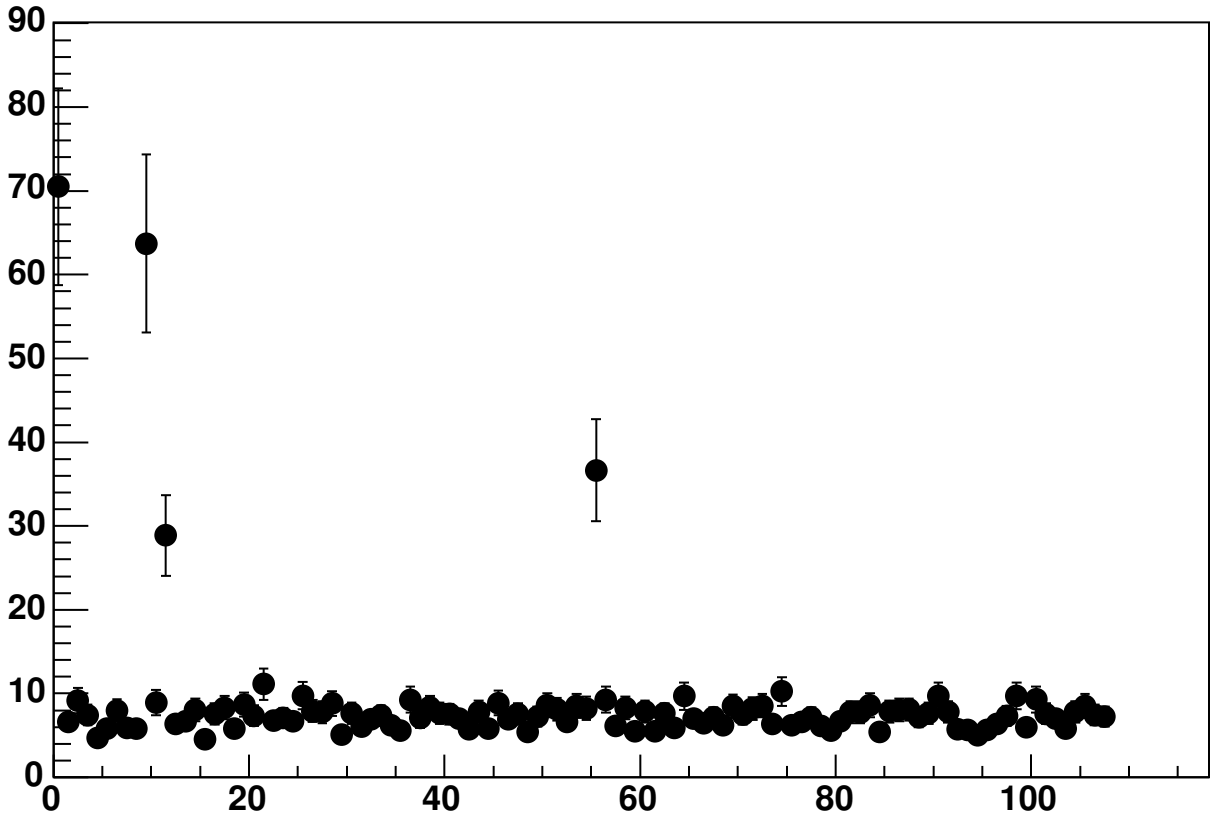
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



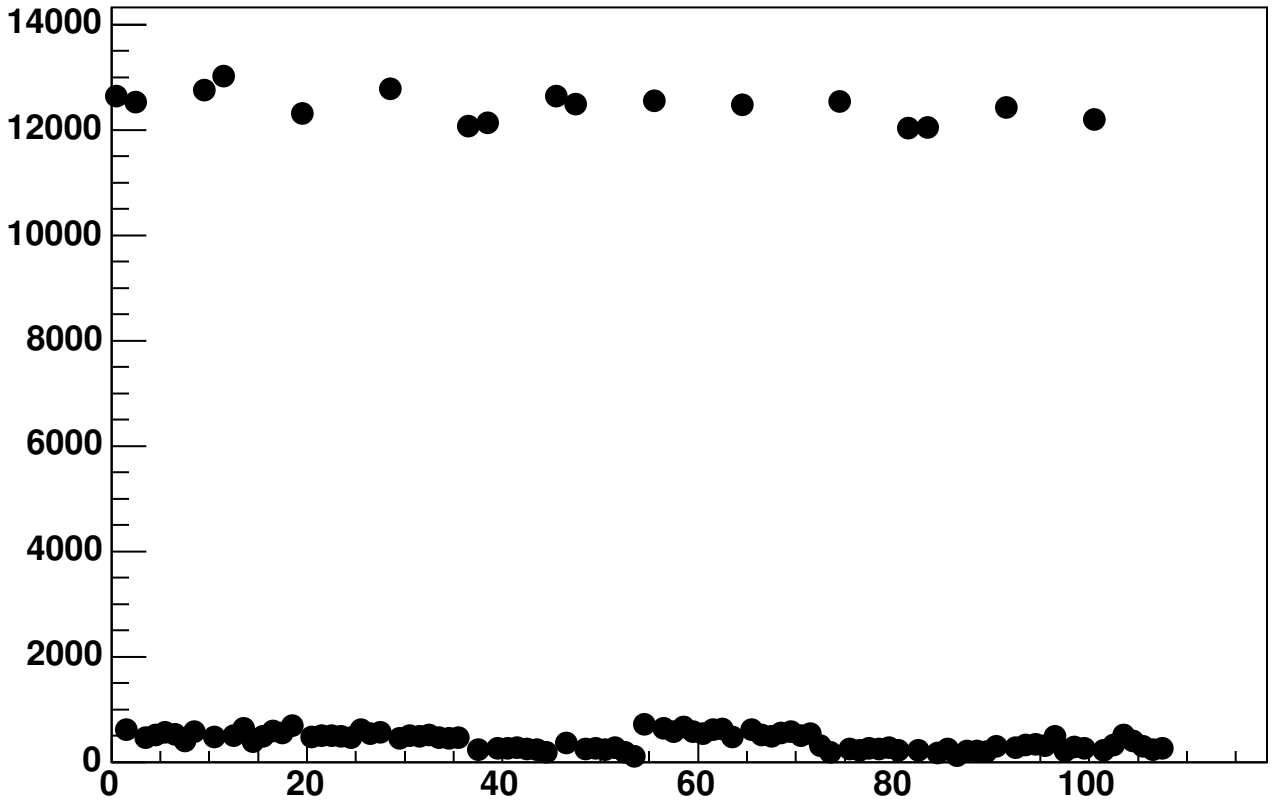
Enable 0, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



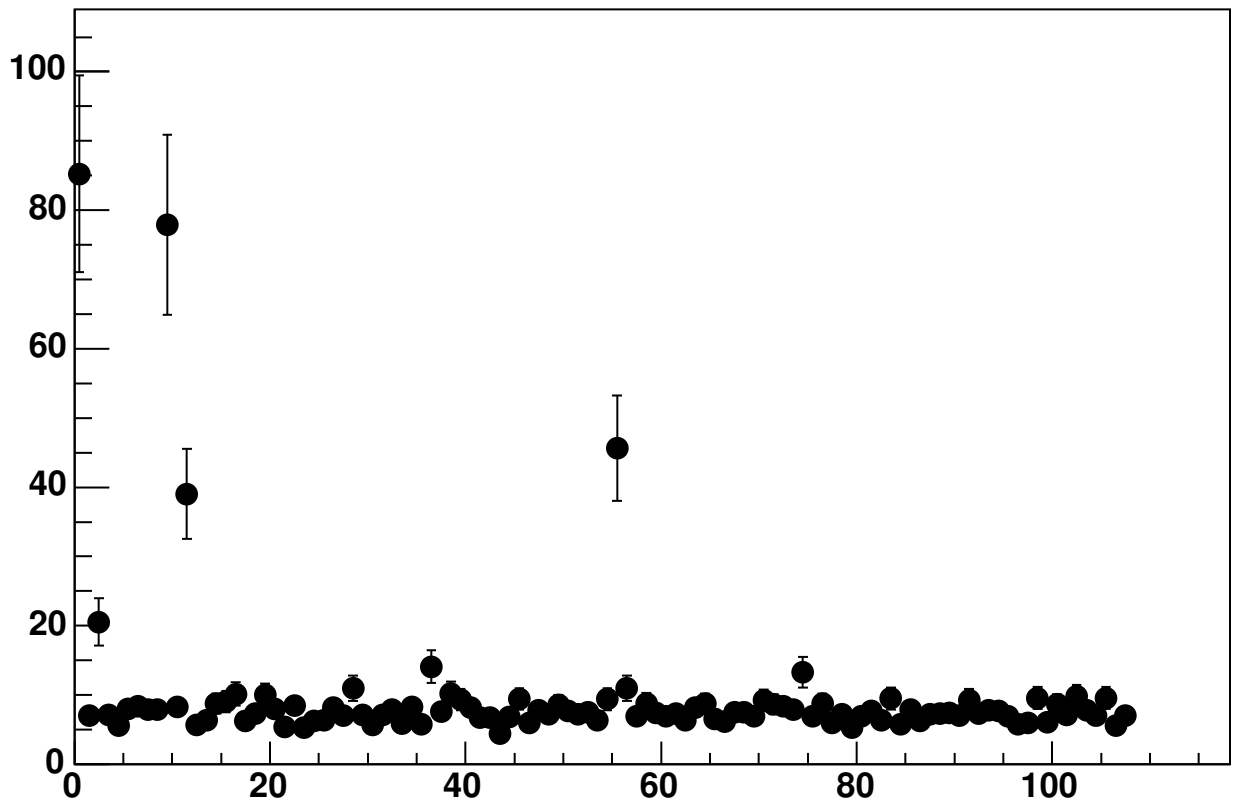
Enable 0, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



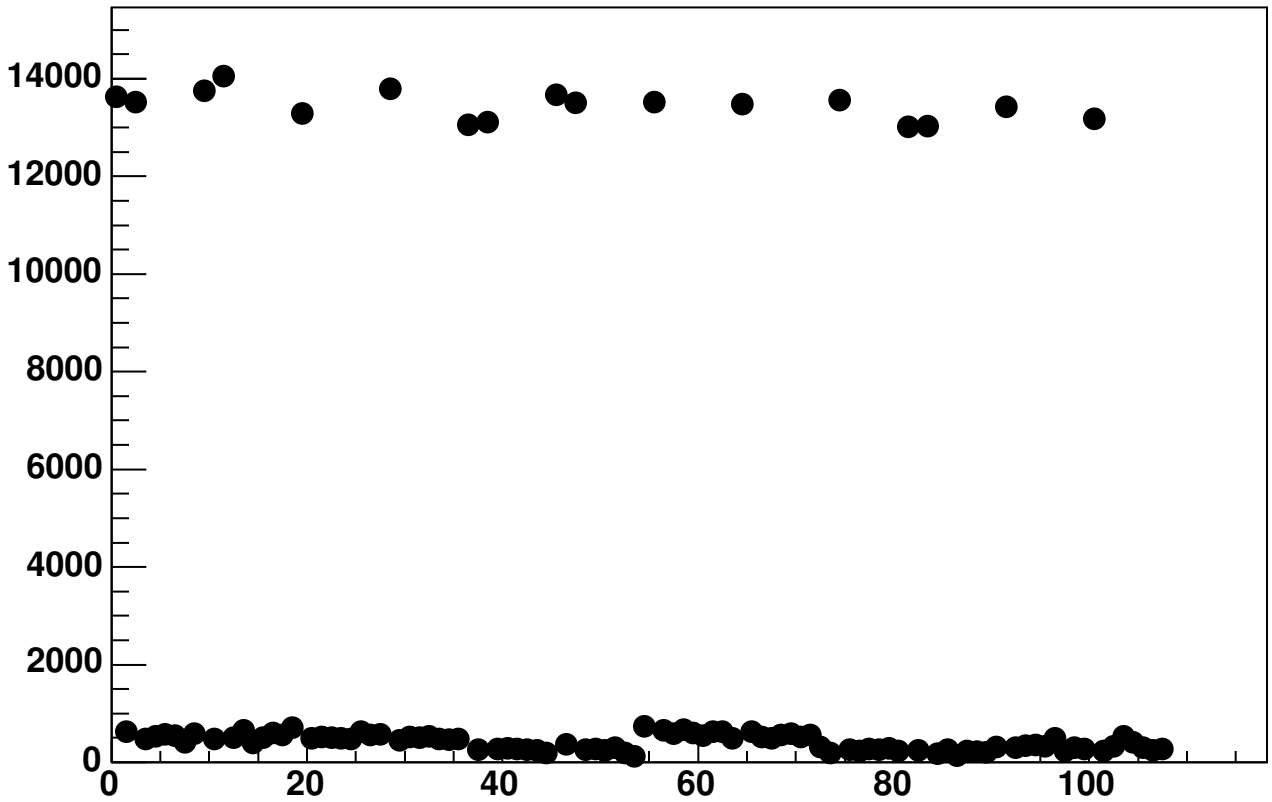
Enable 0, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



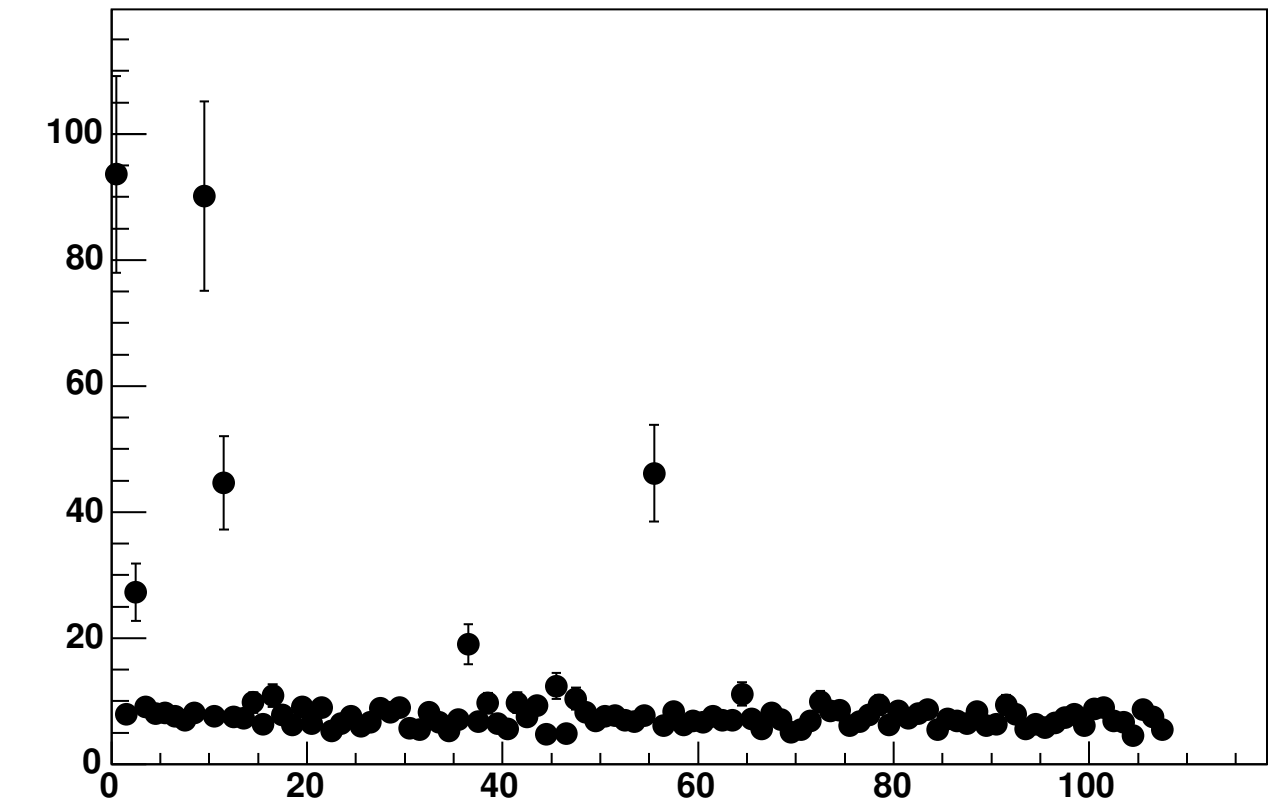
Enable 0, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



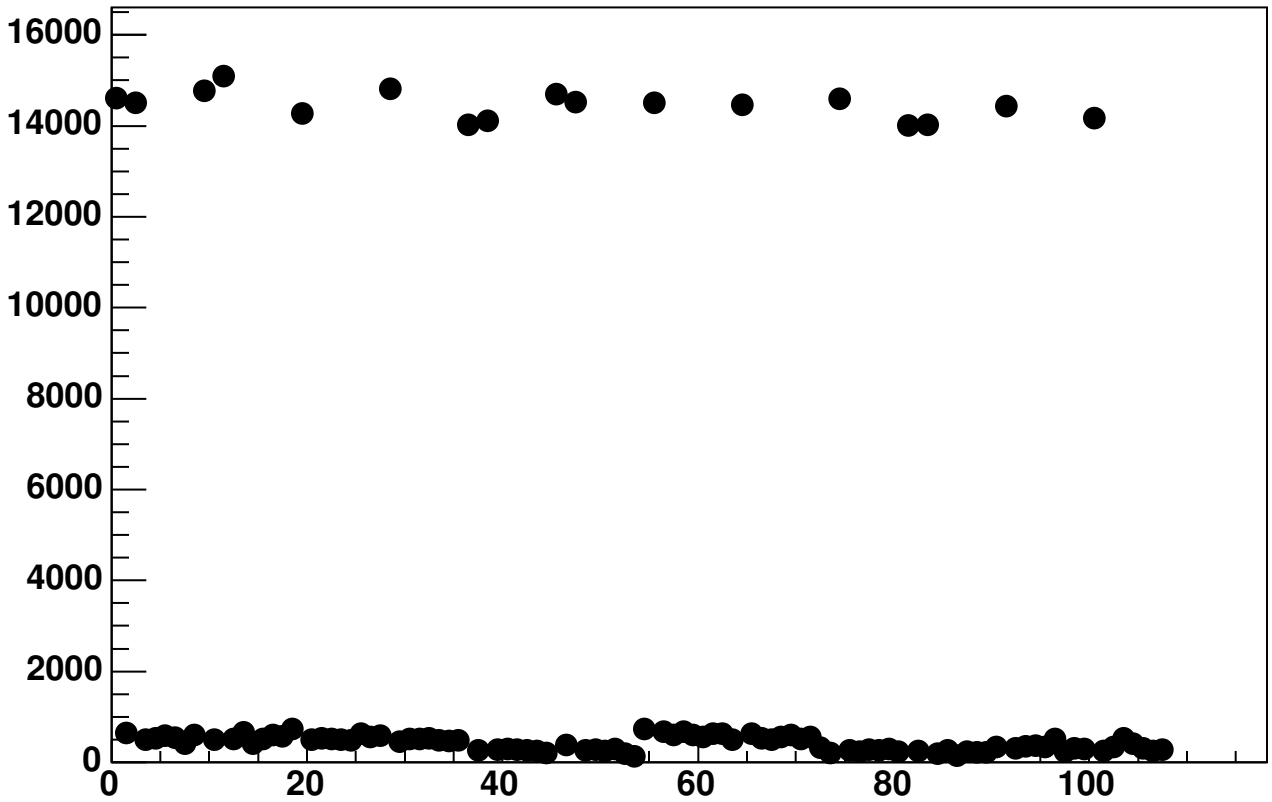
Enable 0, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



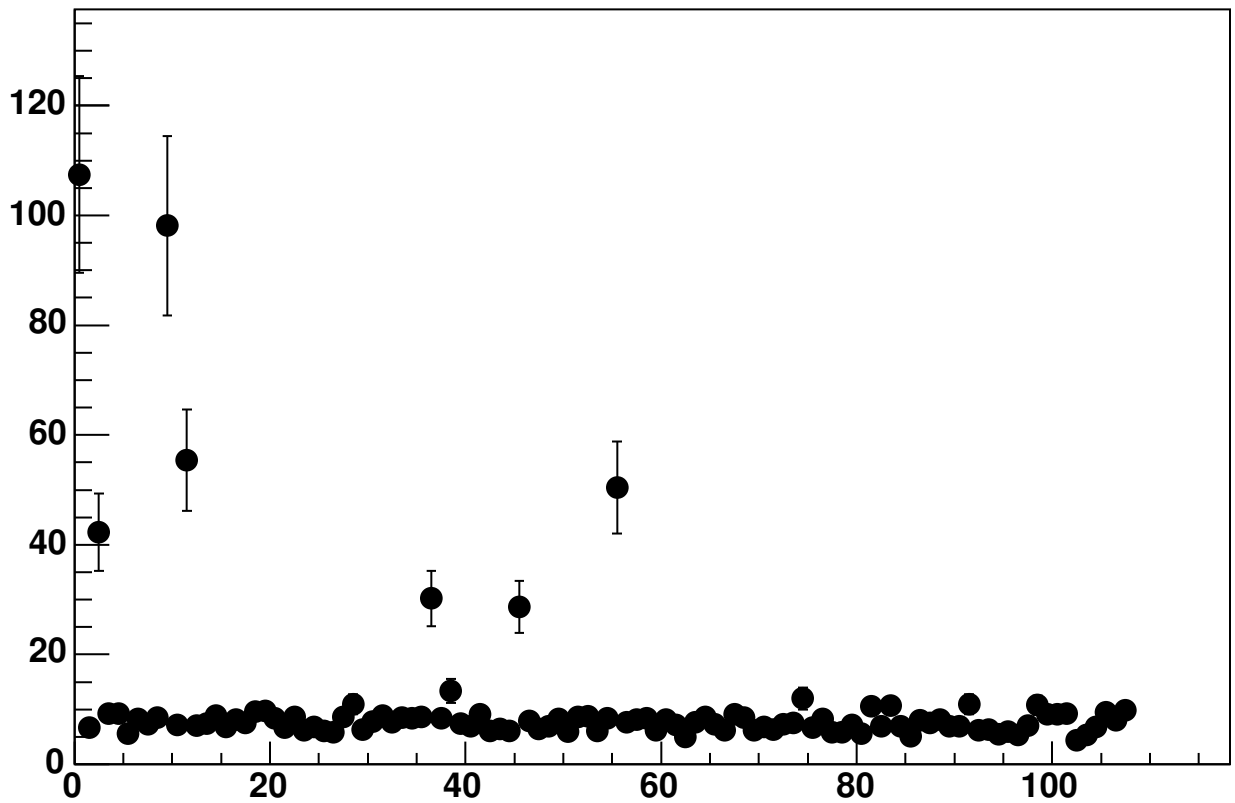
Enable 0, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



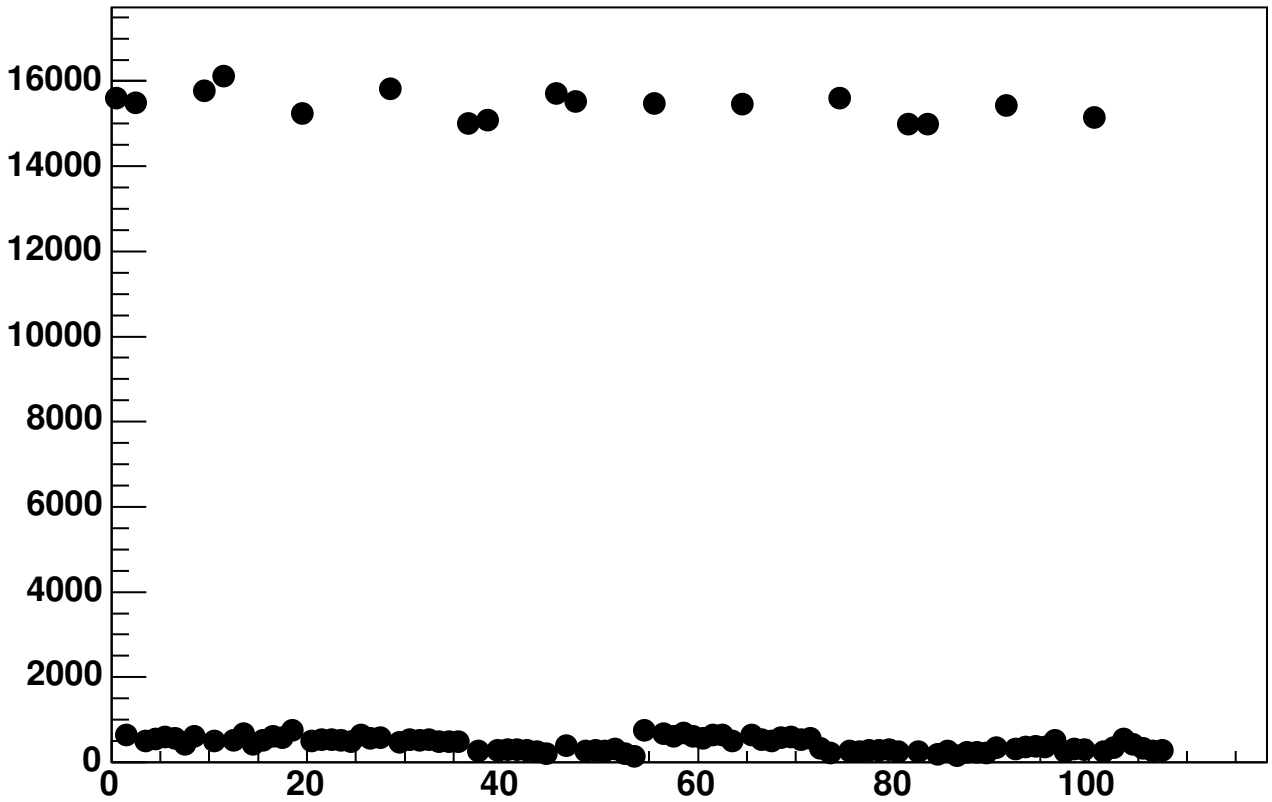
Enable 0, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



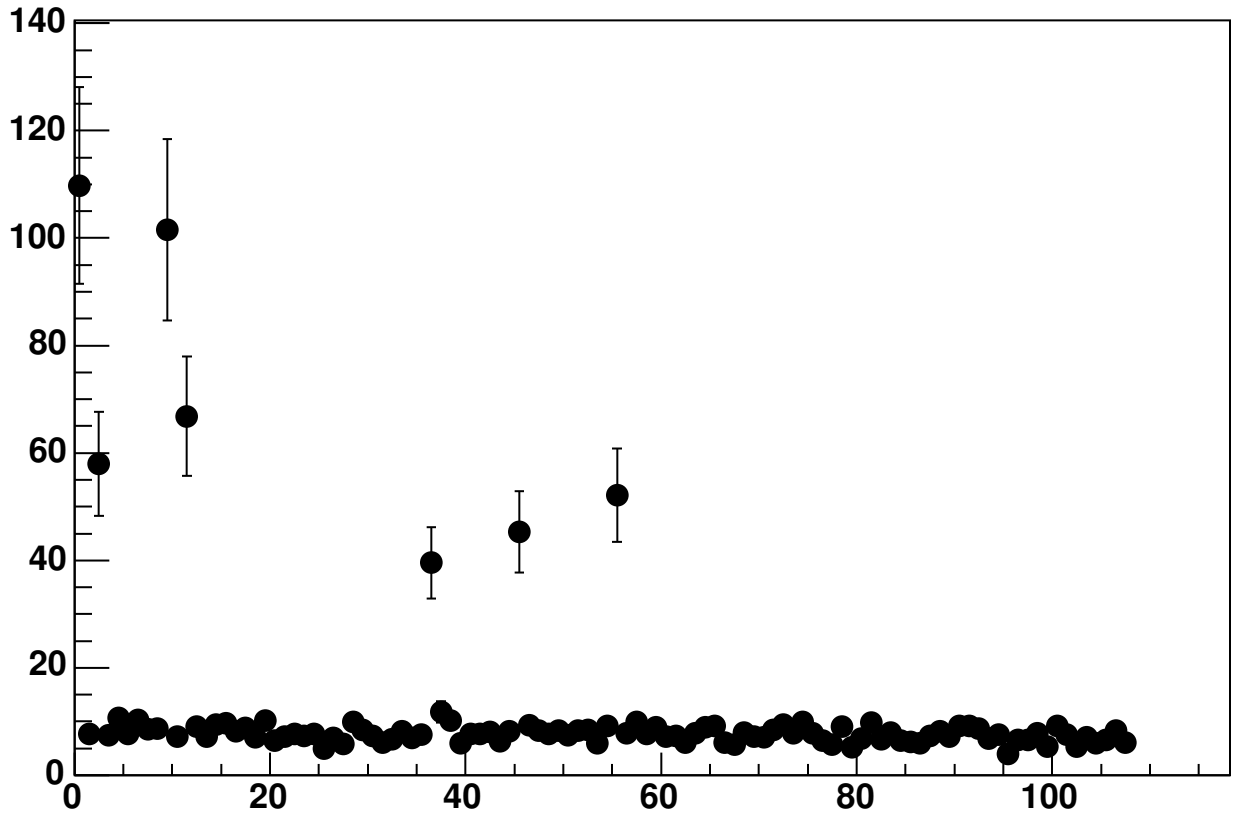
Enable 0, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



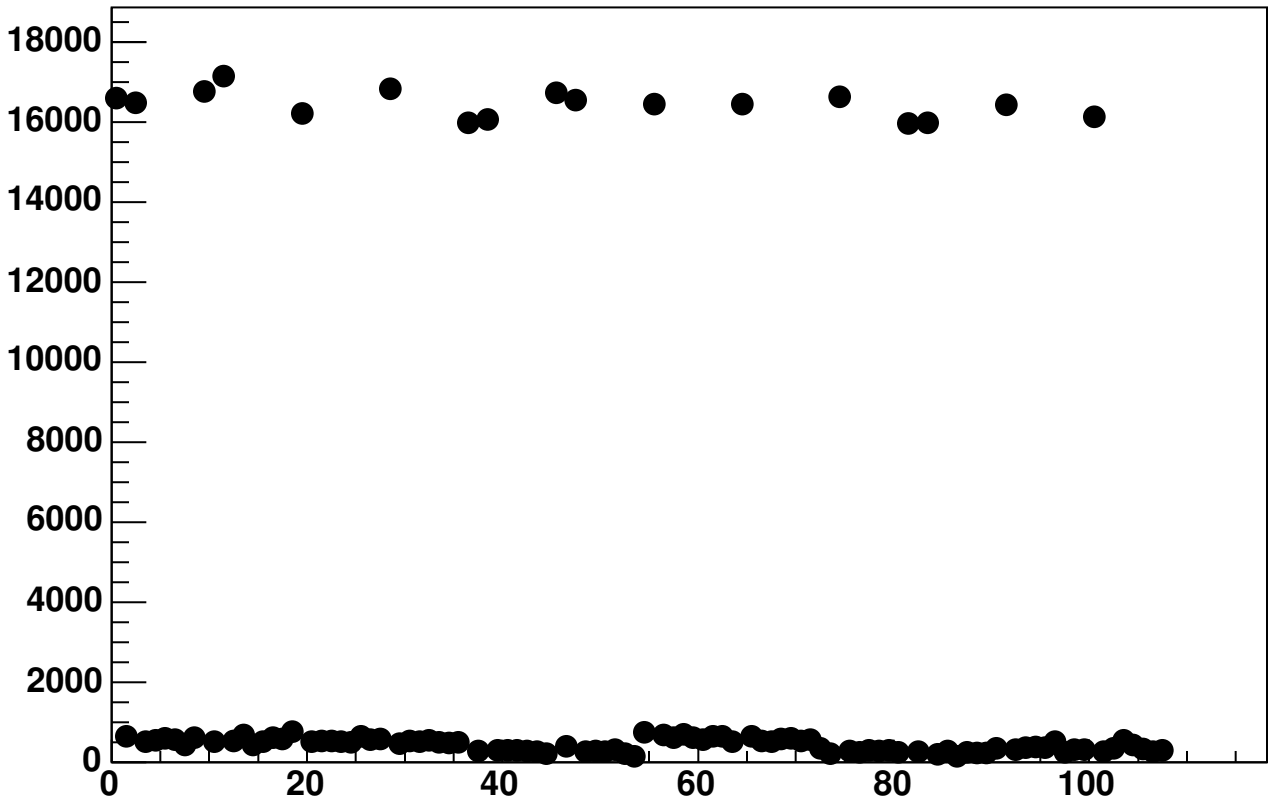
Enable 0, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



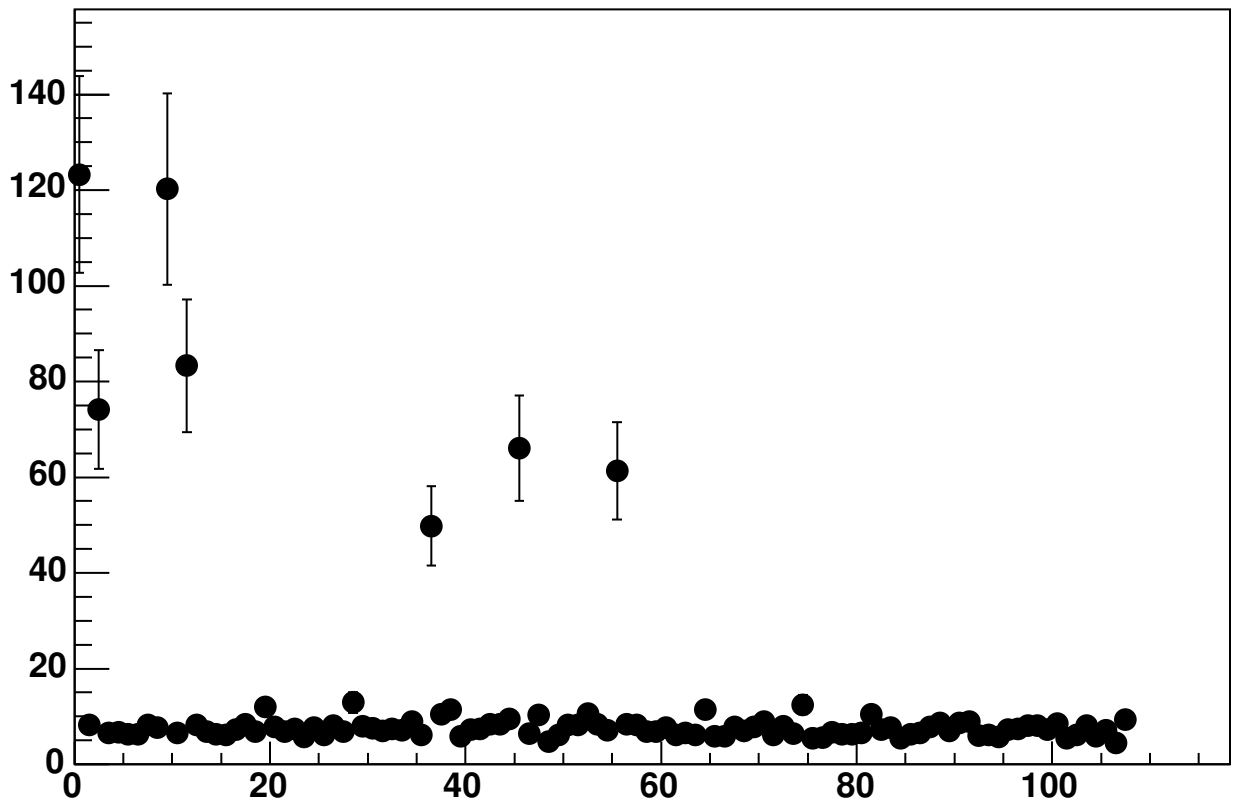
Enable 0, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



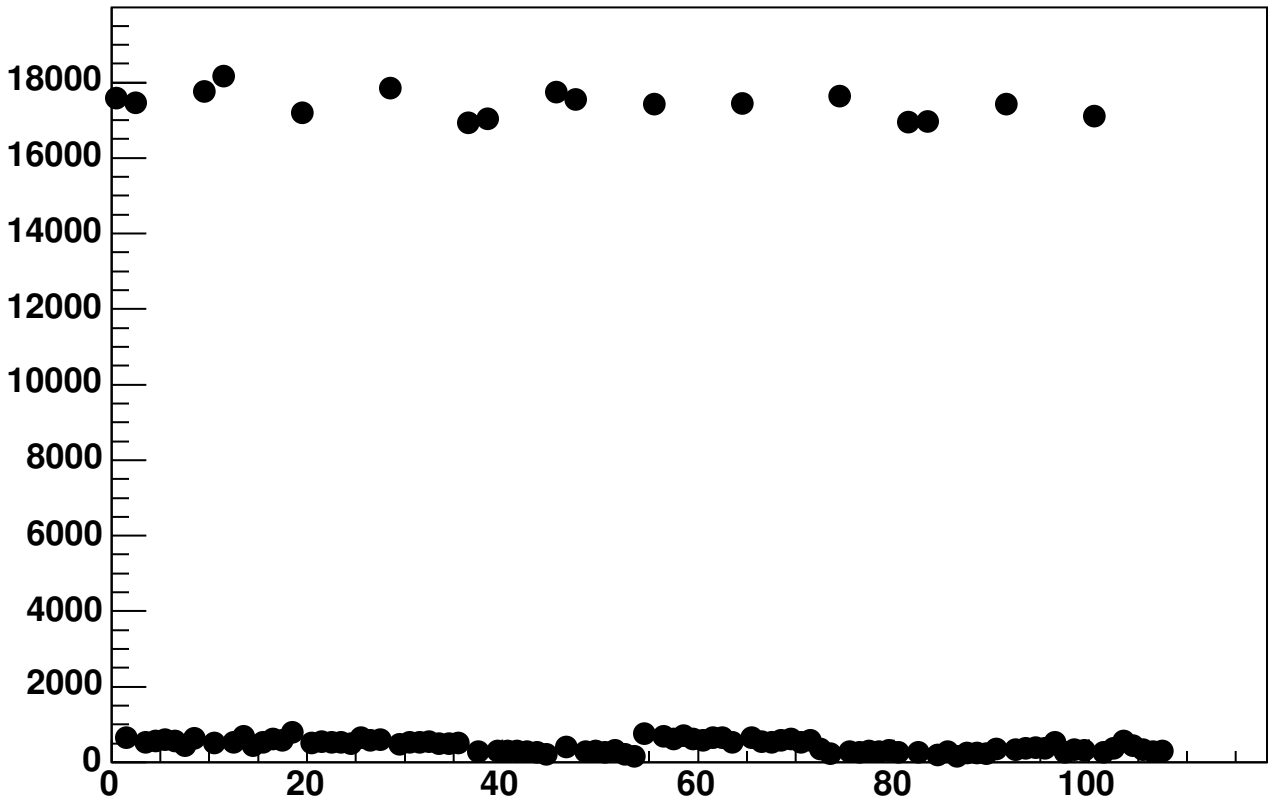
Enable 0, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



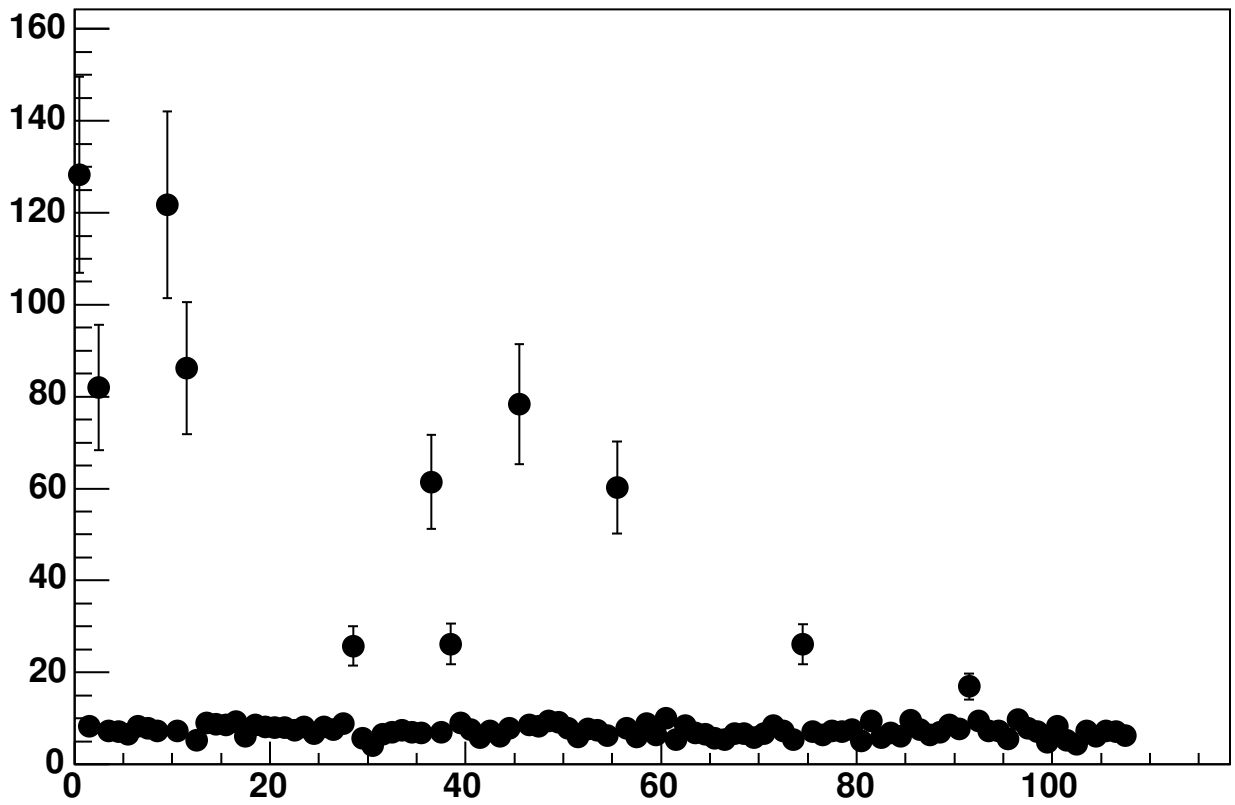
Enable 0, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



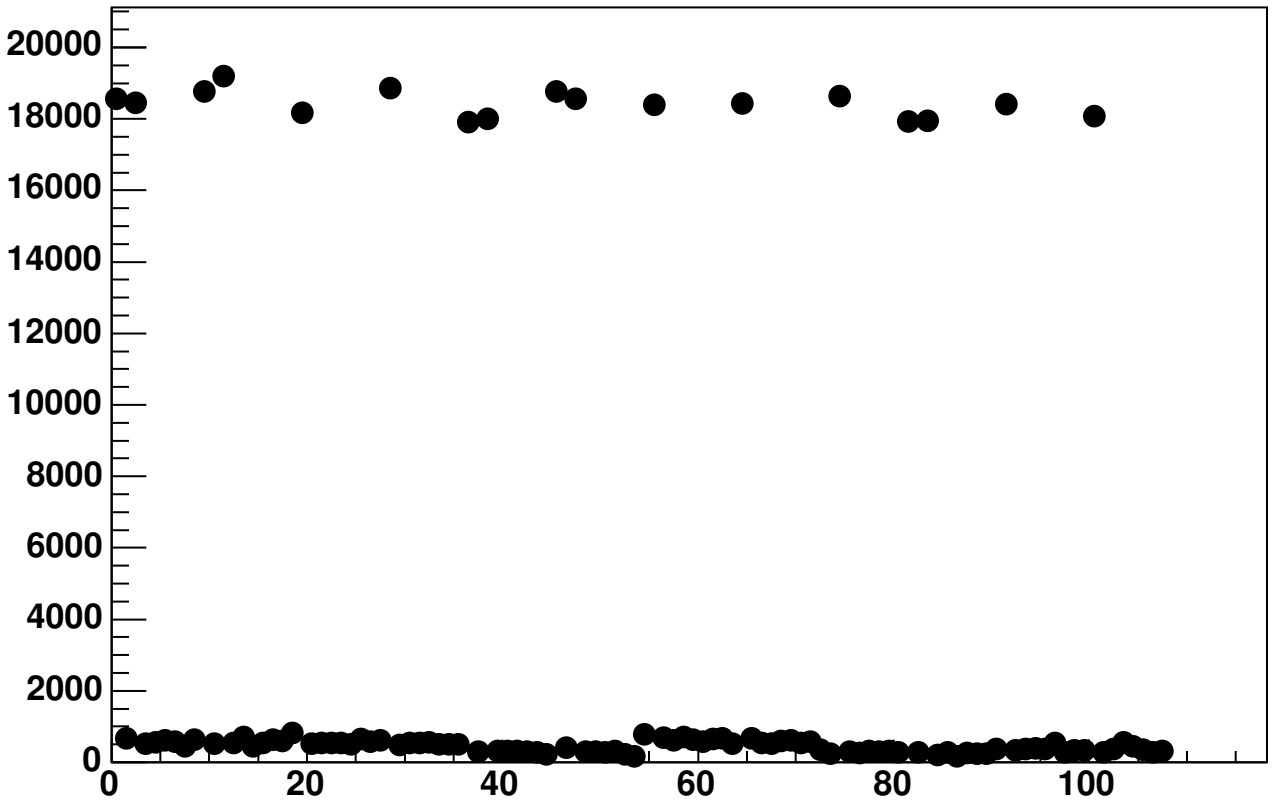
Enable 0, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



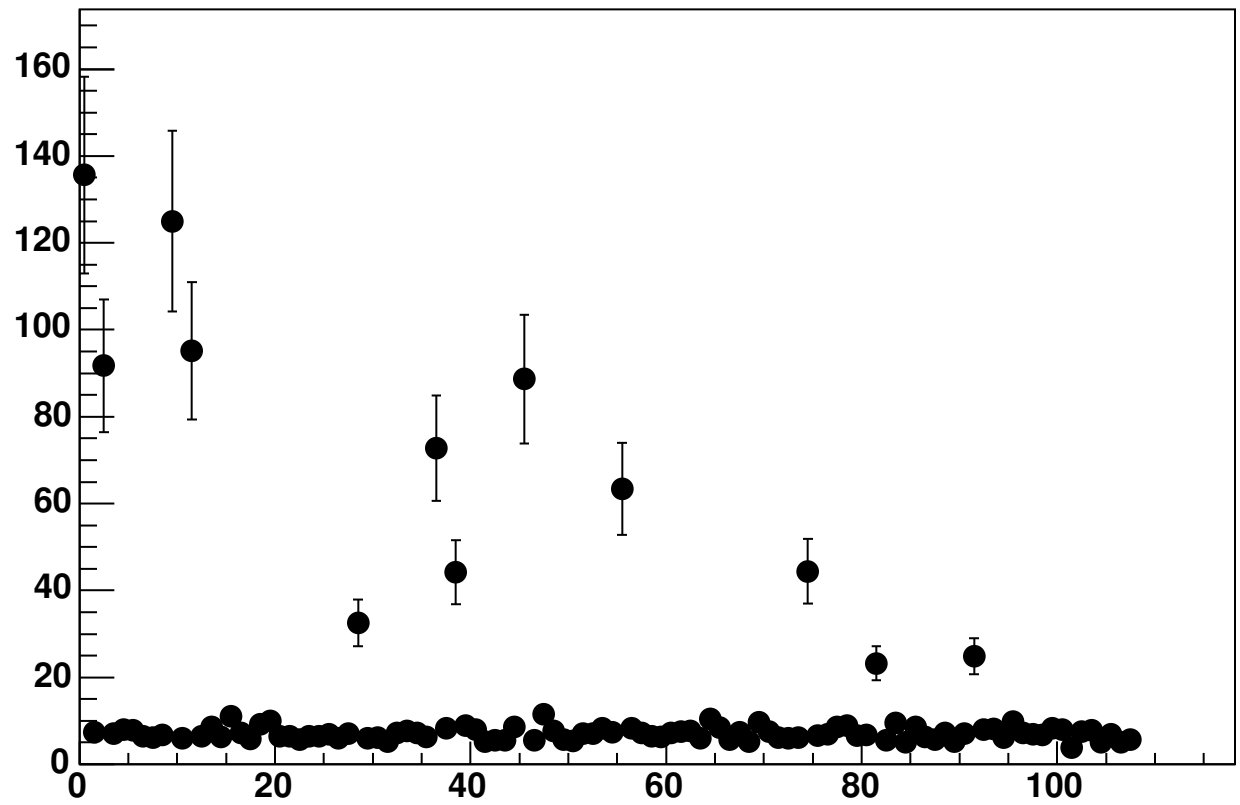
Enable 0, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



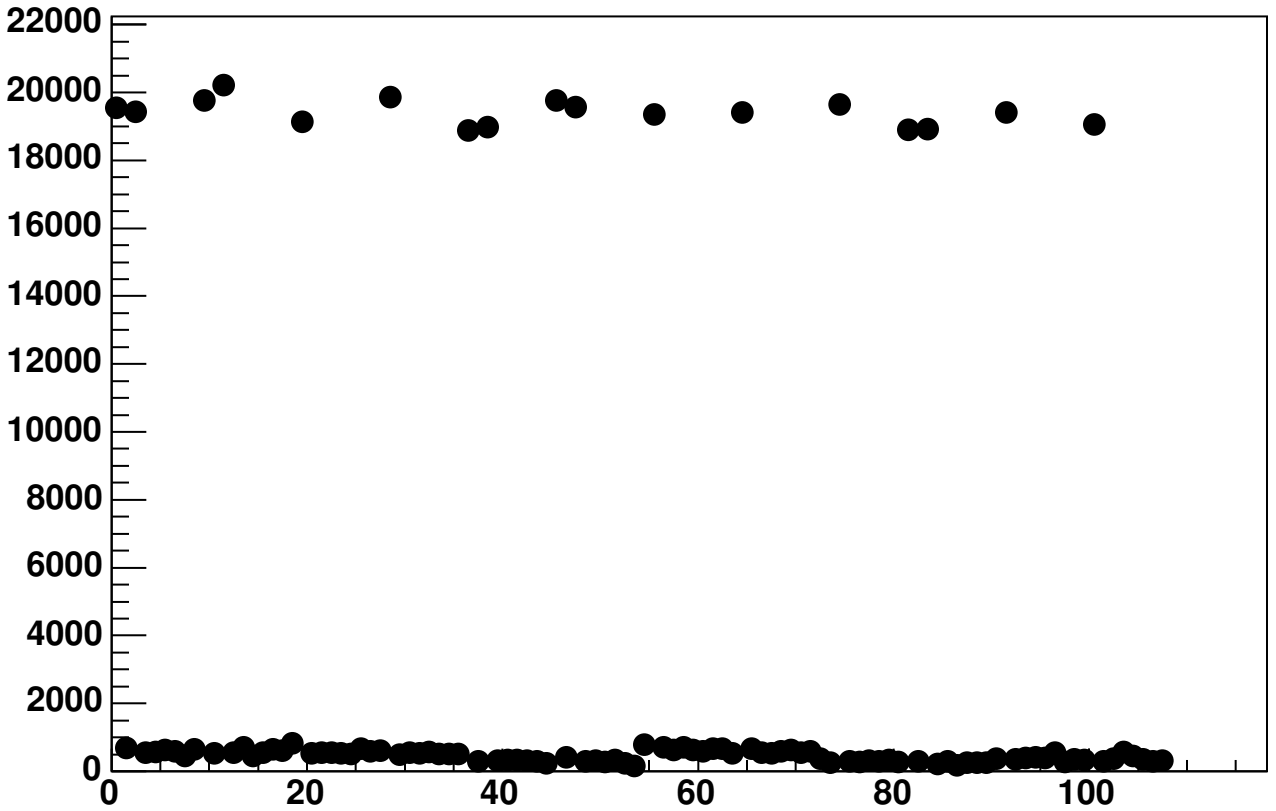
Enable 0, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



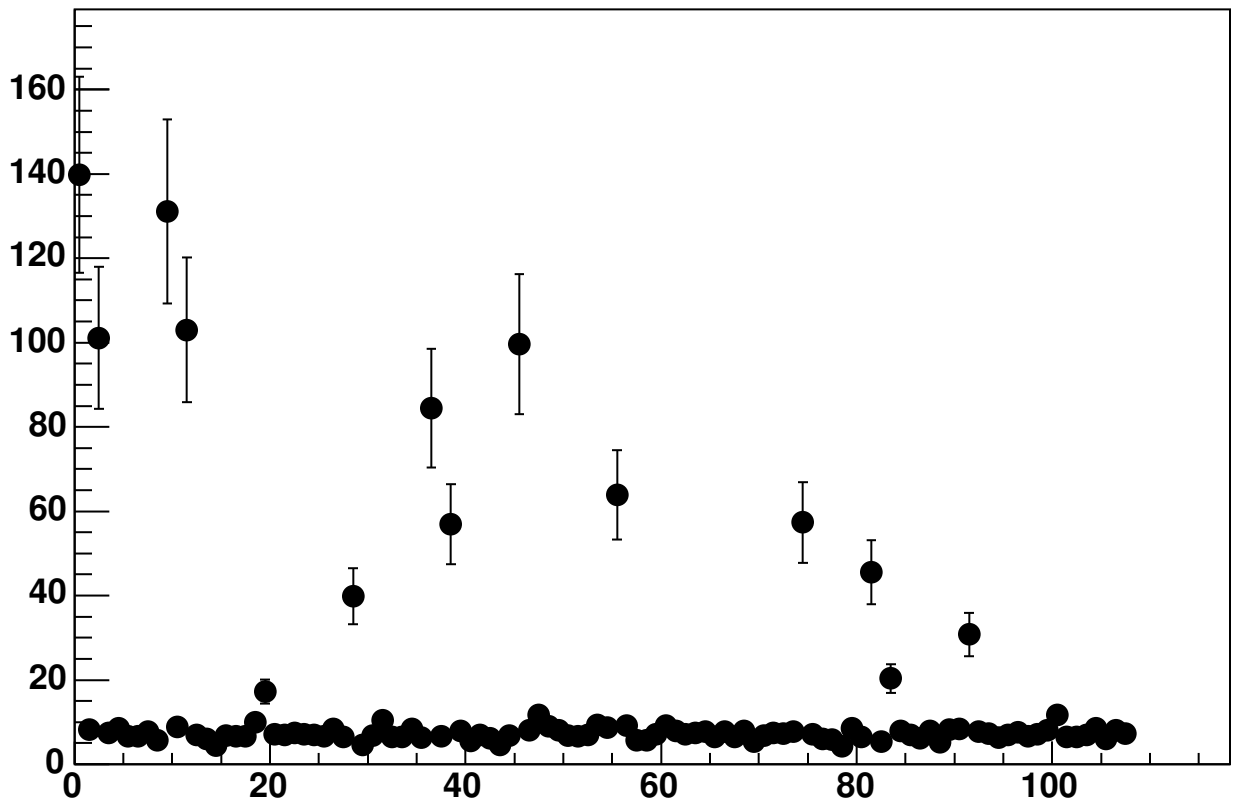
Enable 0, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



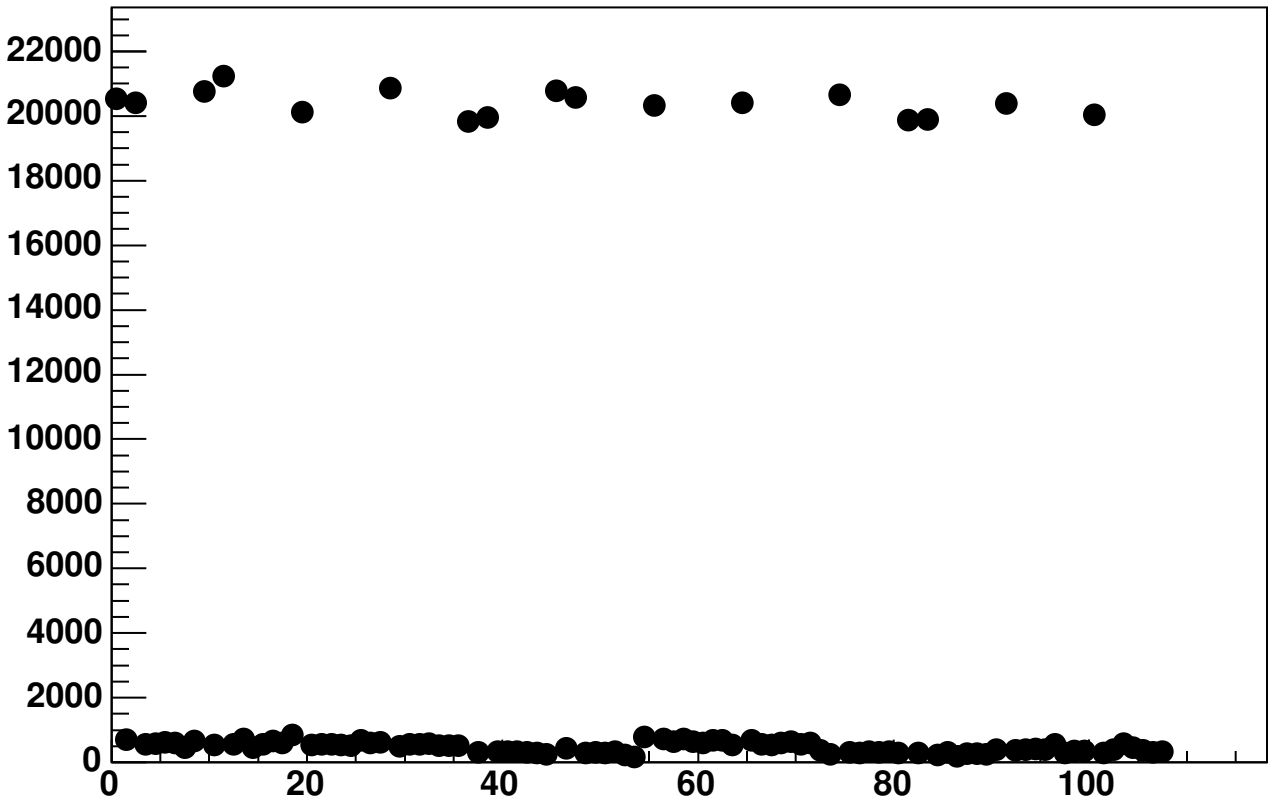
Enable 0, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



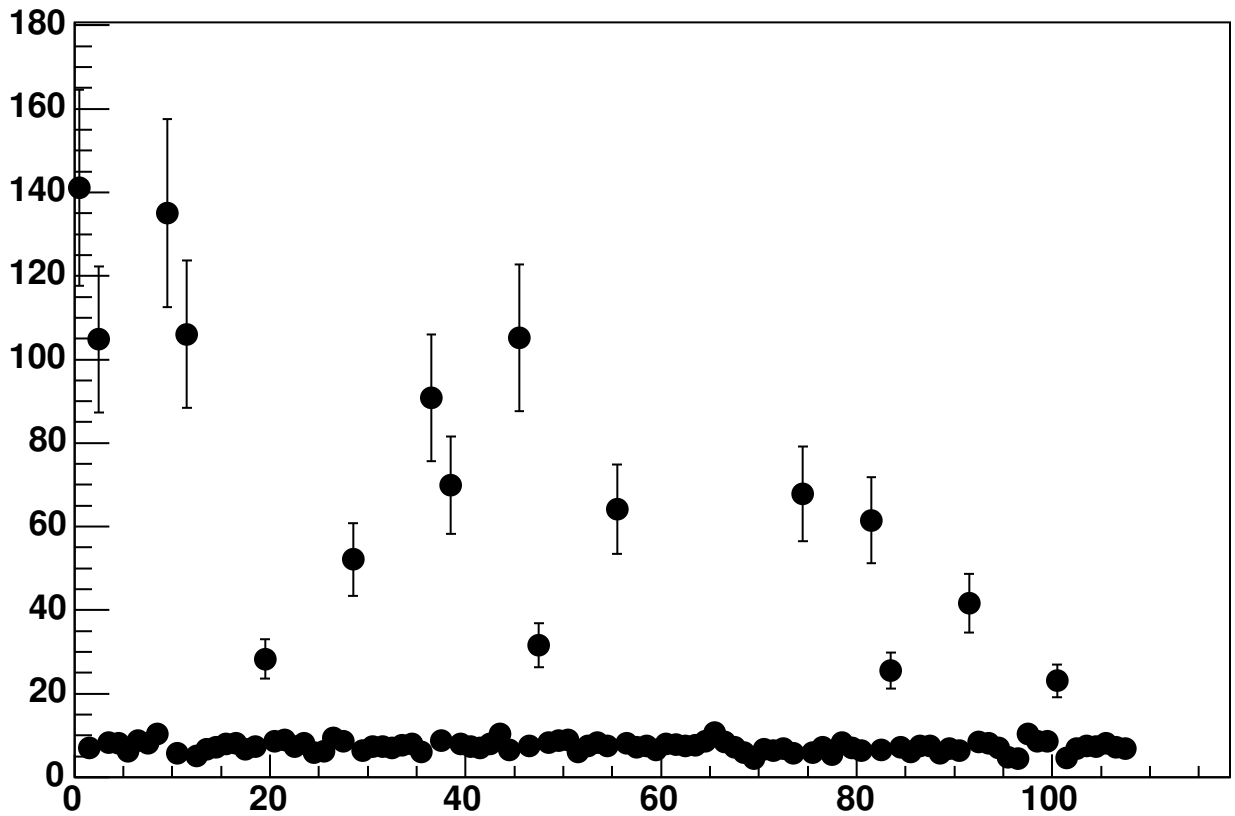
Enable 0, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



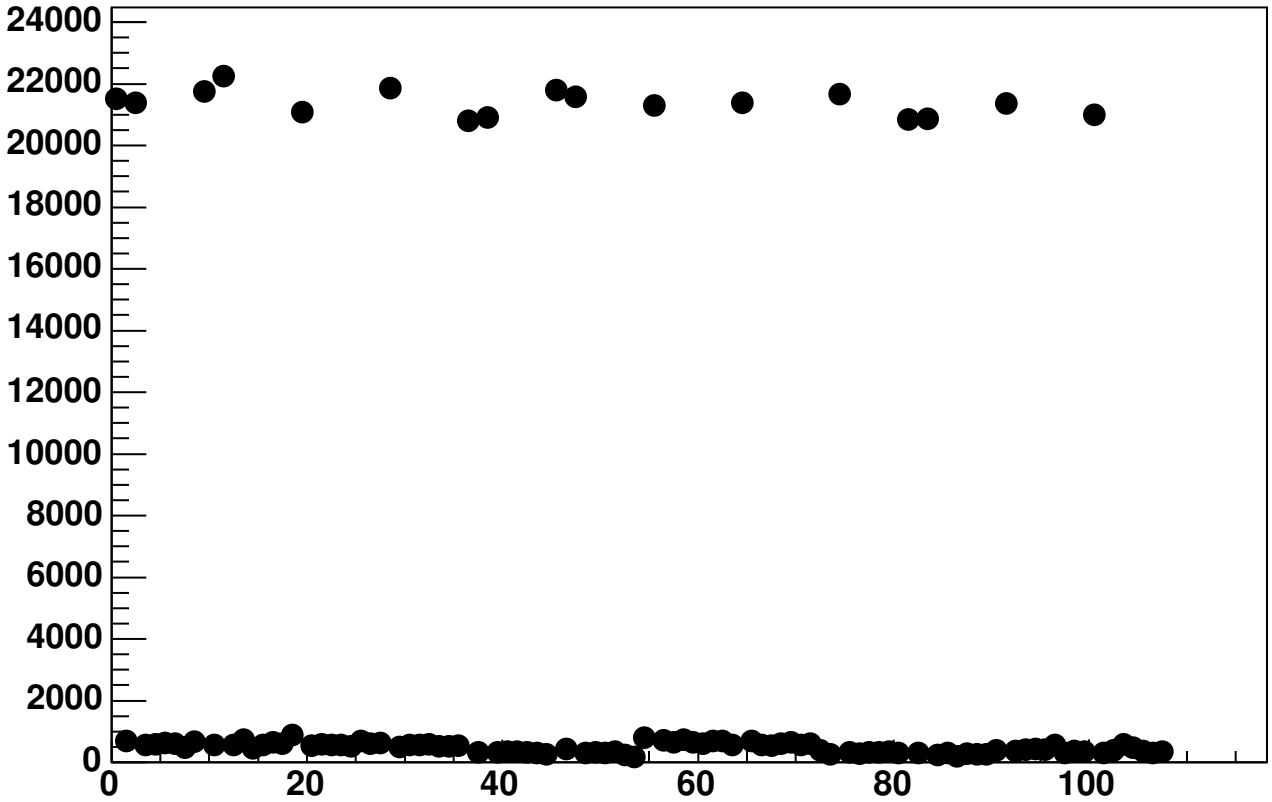
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



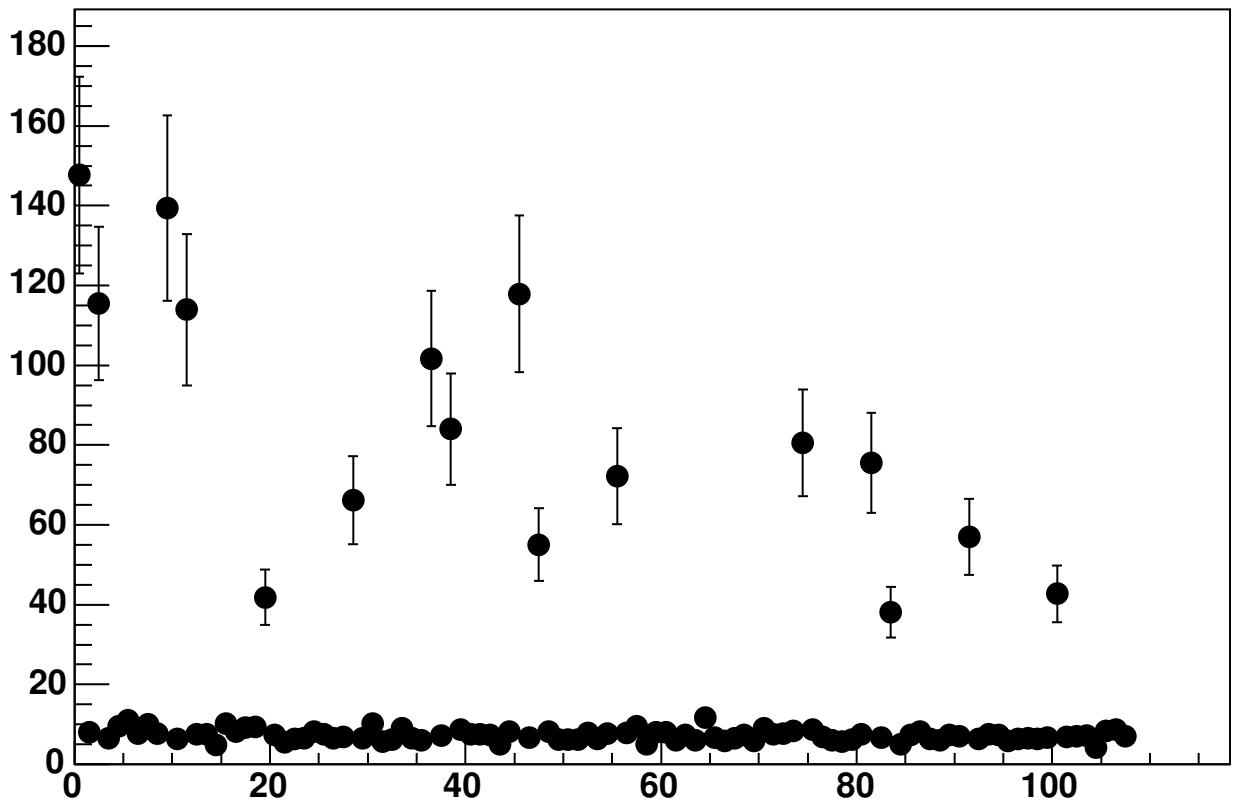
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



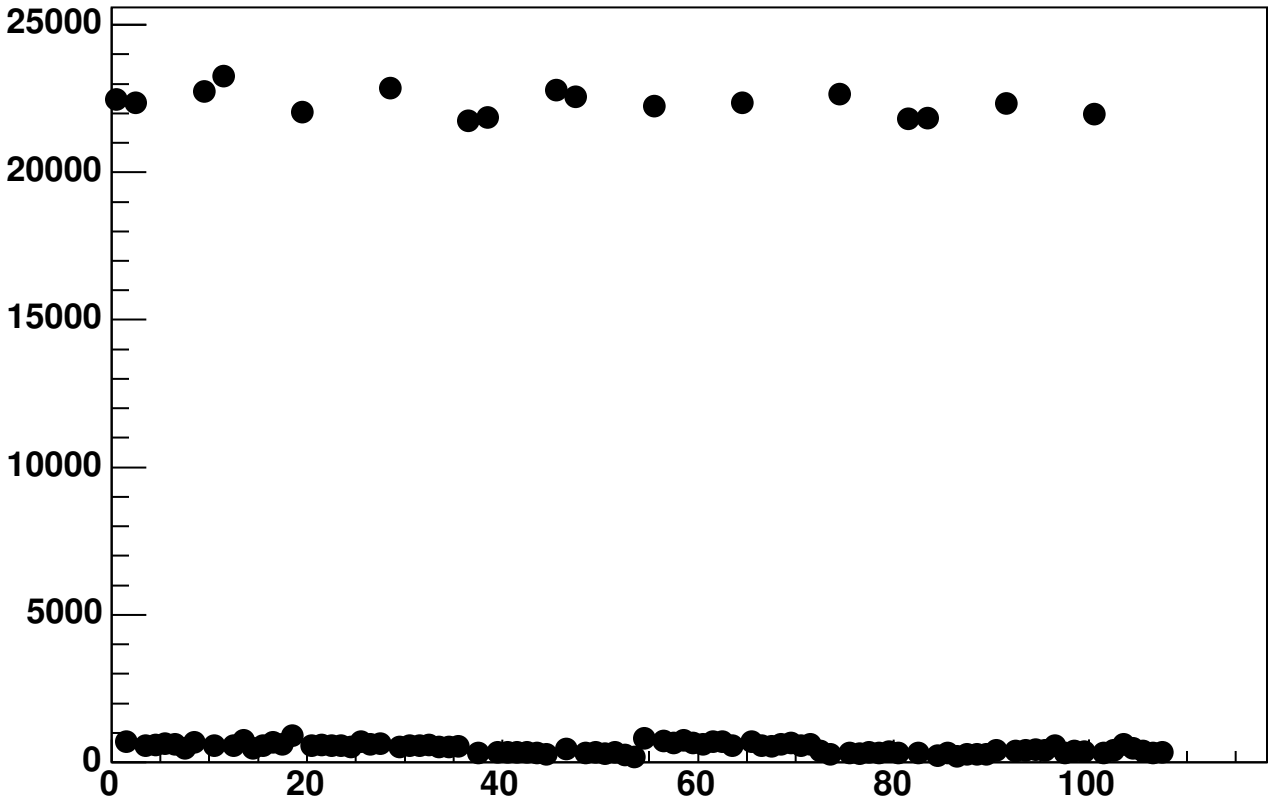
Enable 0, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



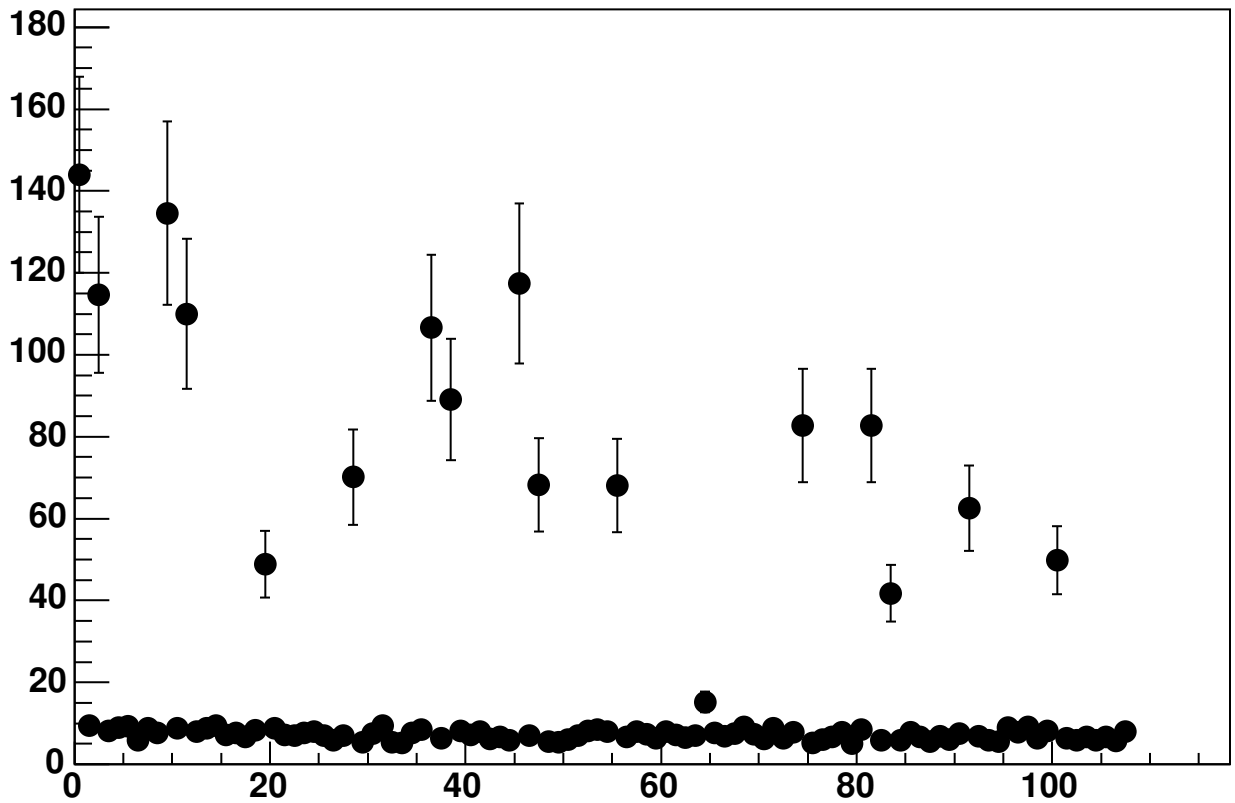
Enable 0, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



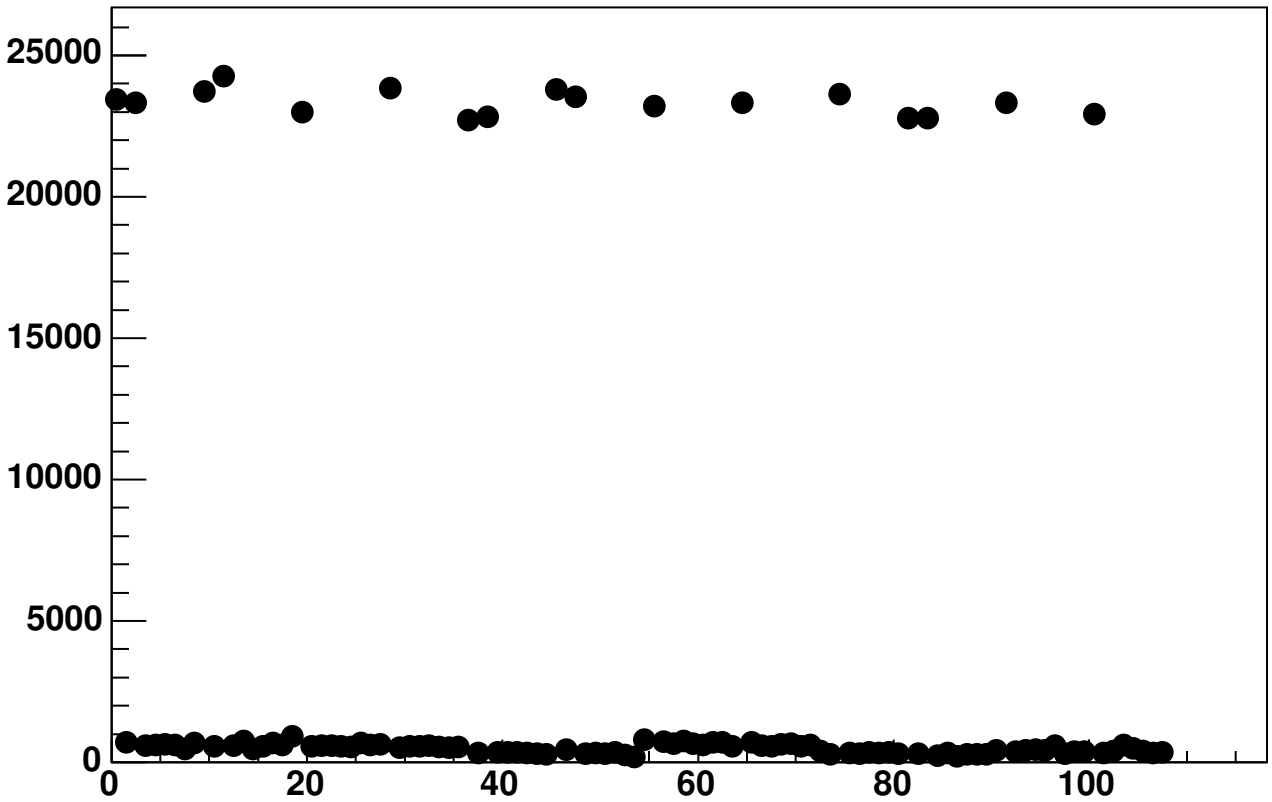
Enable 0, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



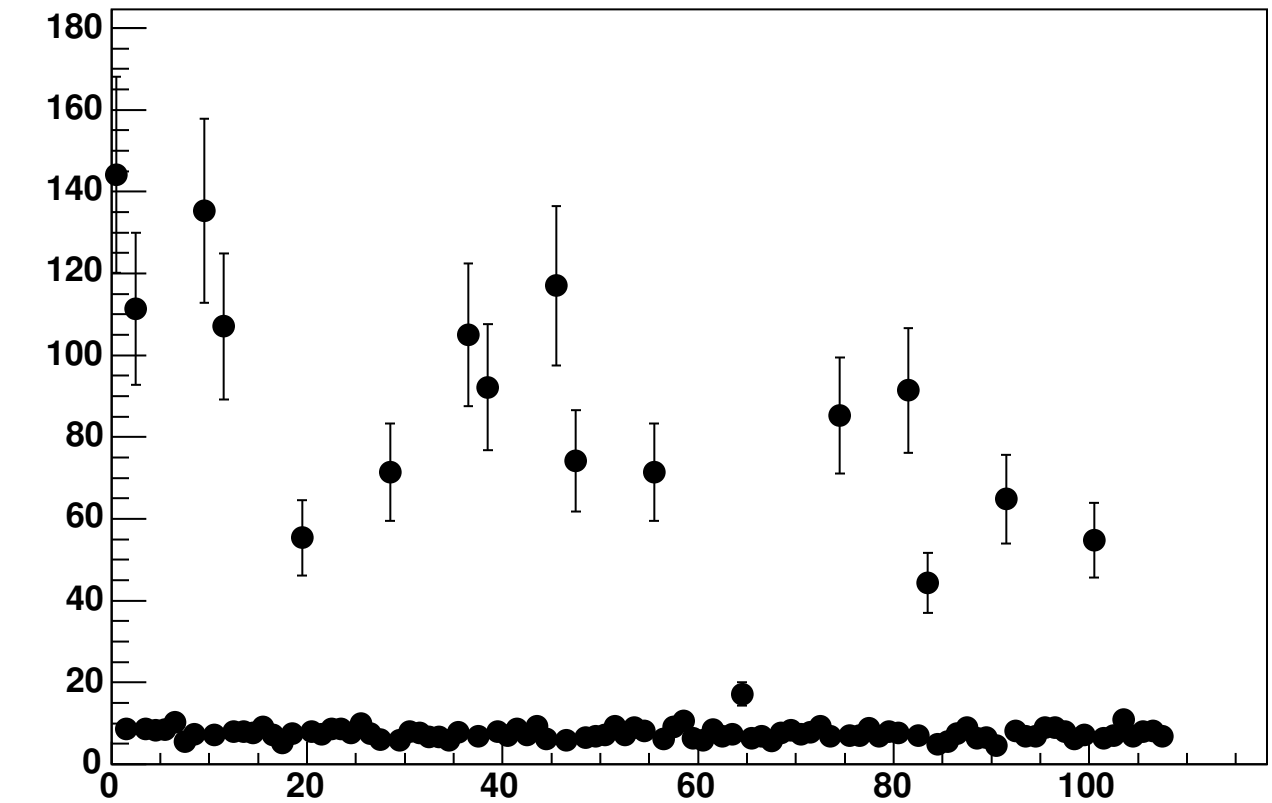
Enable 0, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



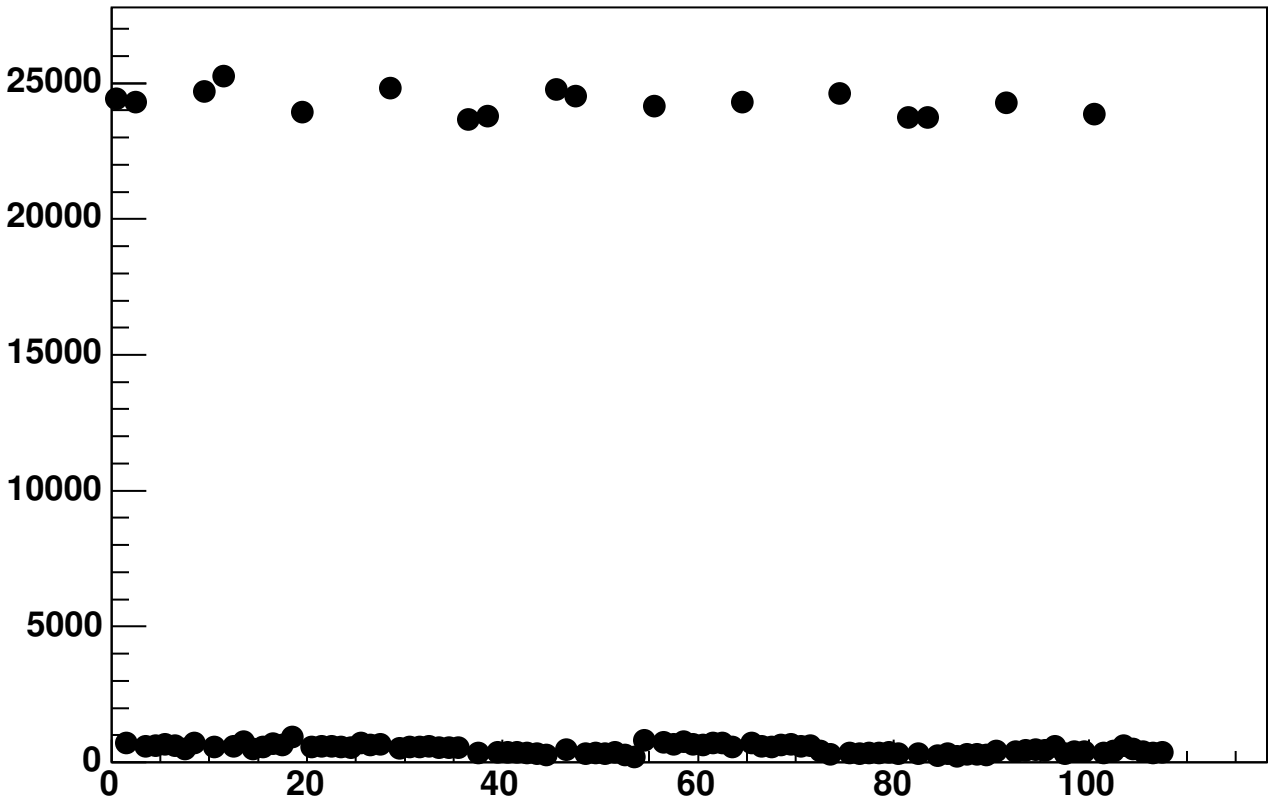
Enable 0, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



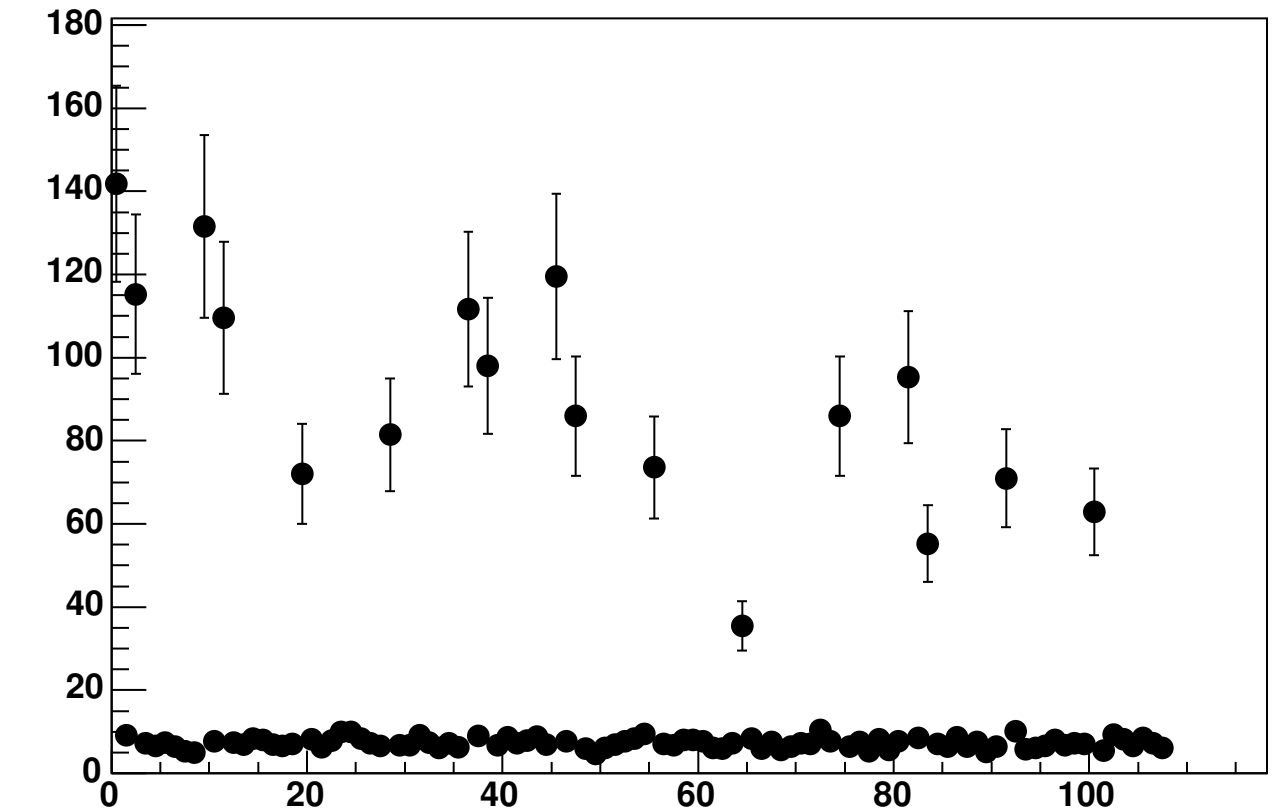
Enable 0, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



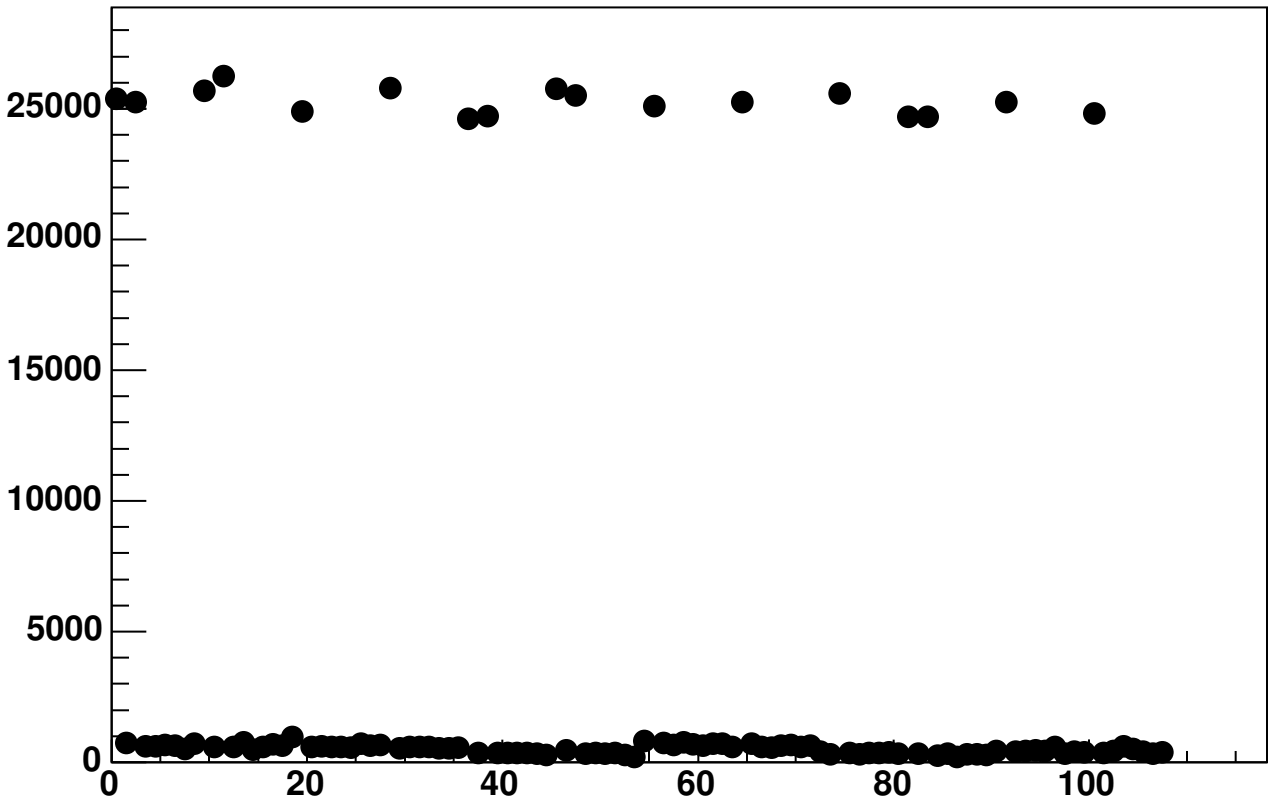
Enable 0, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



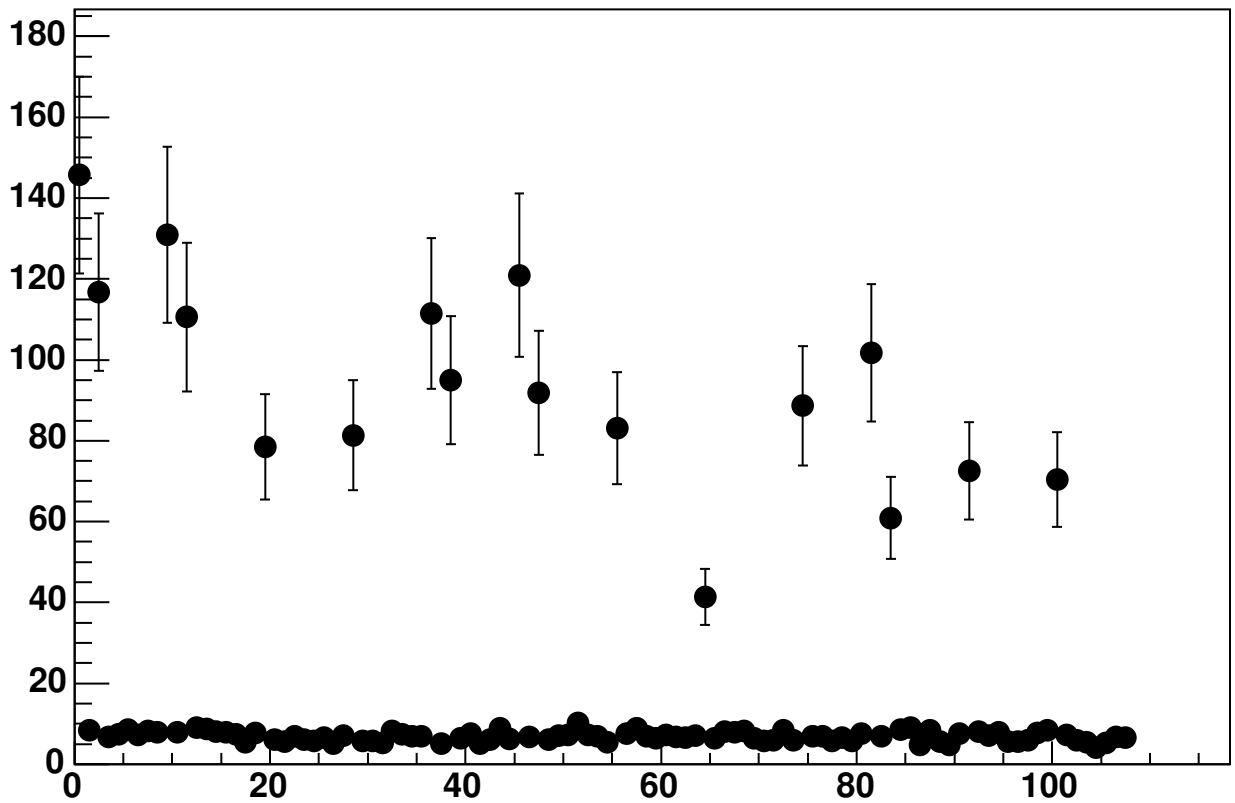
Enable 0, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



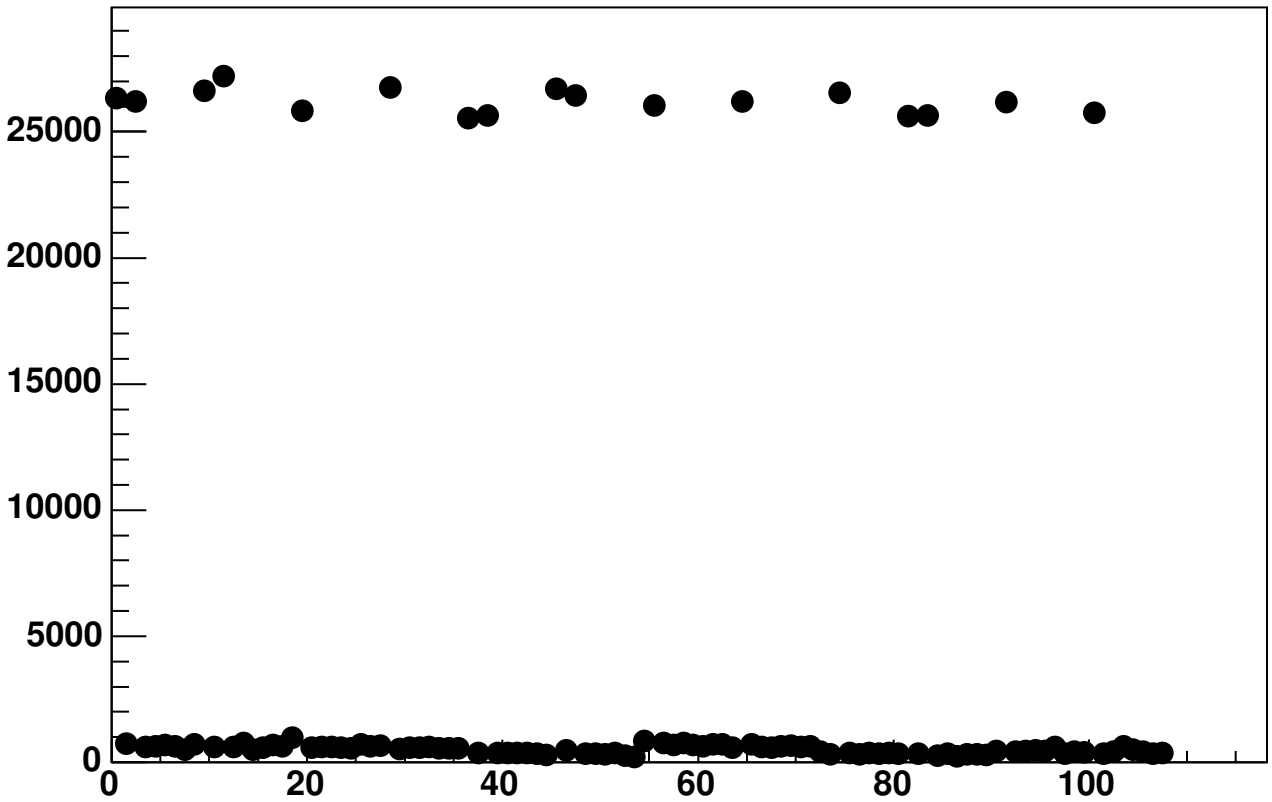
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



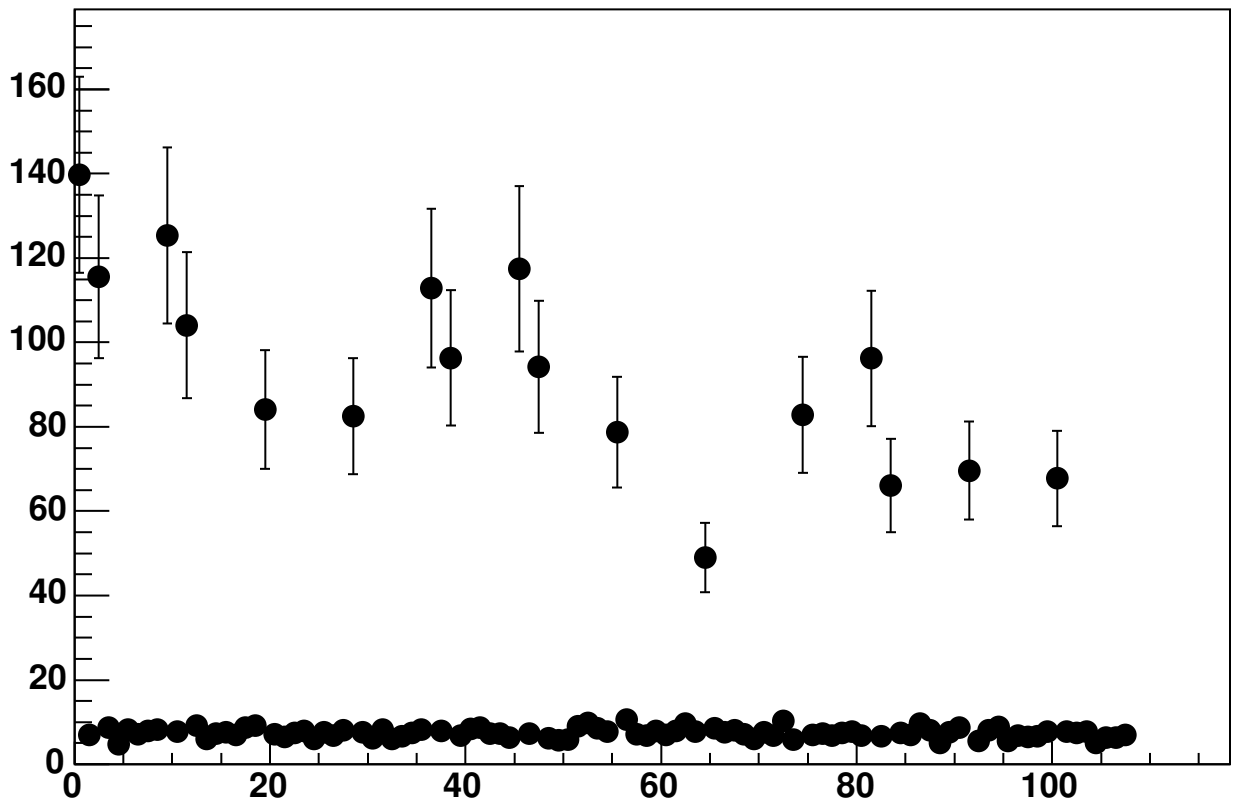
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



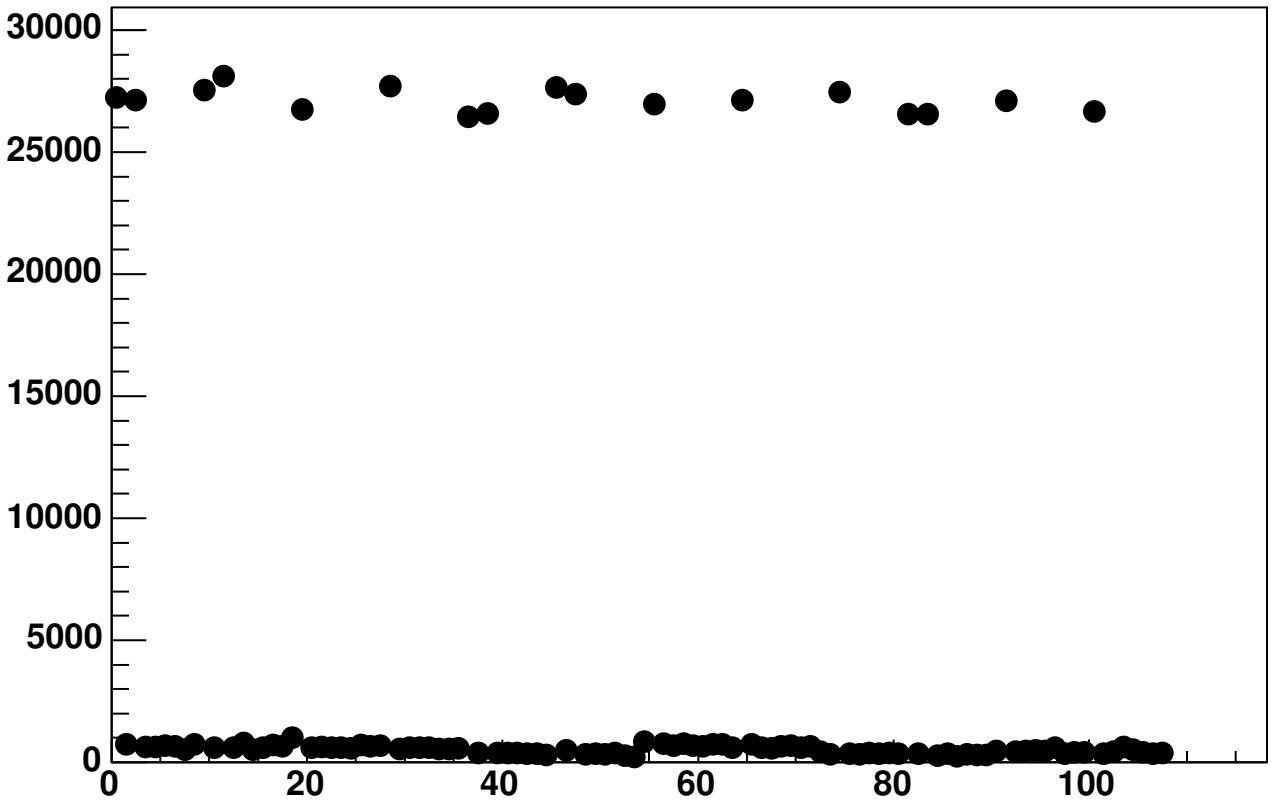
Enable 0, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



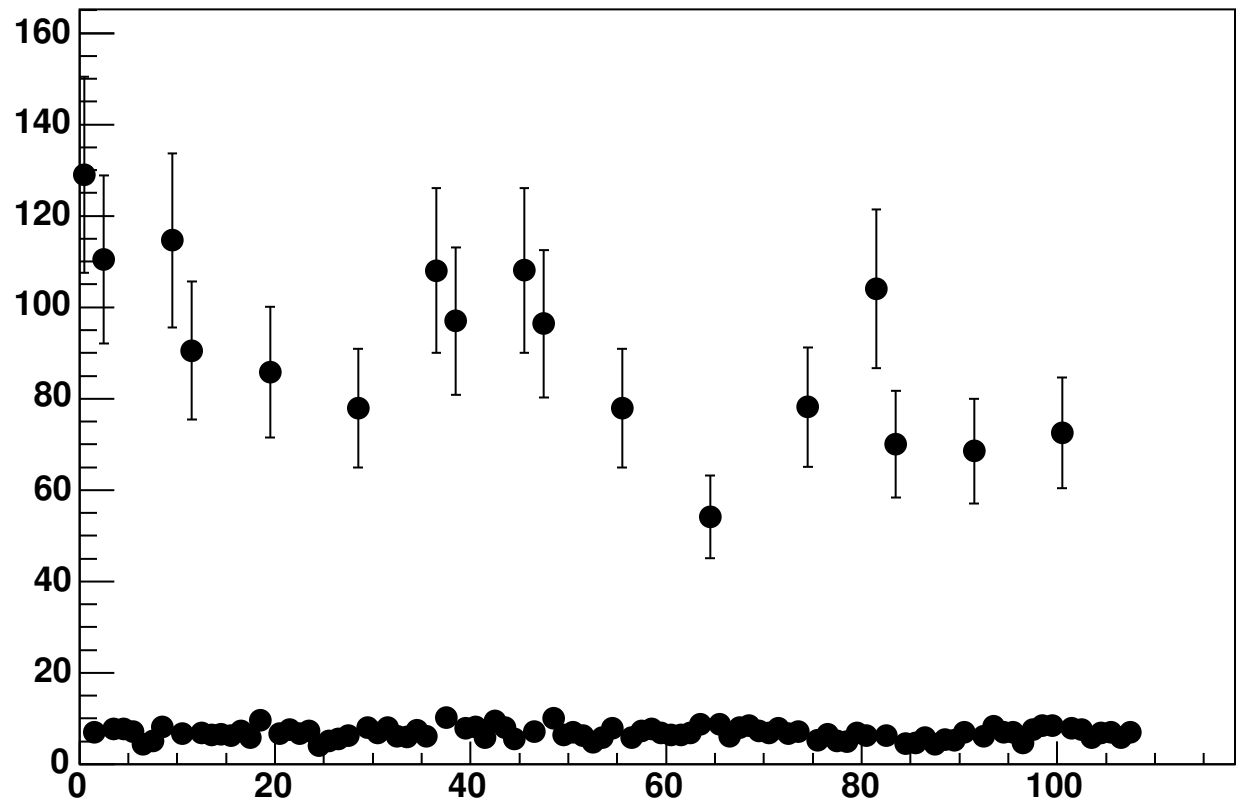
Enable 0, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



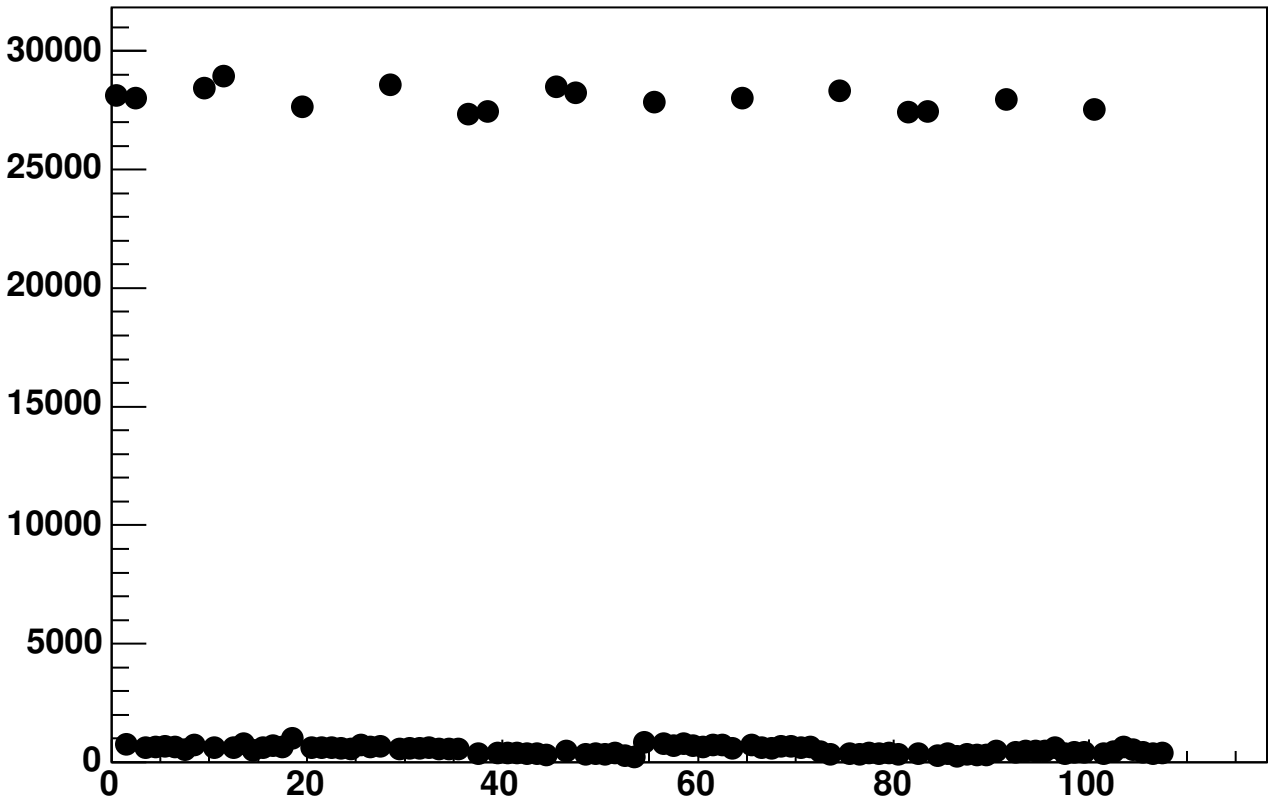
Enable 0, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



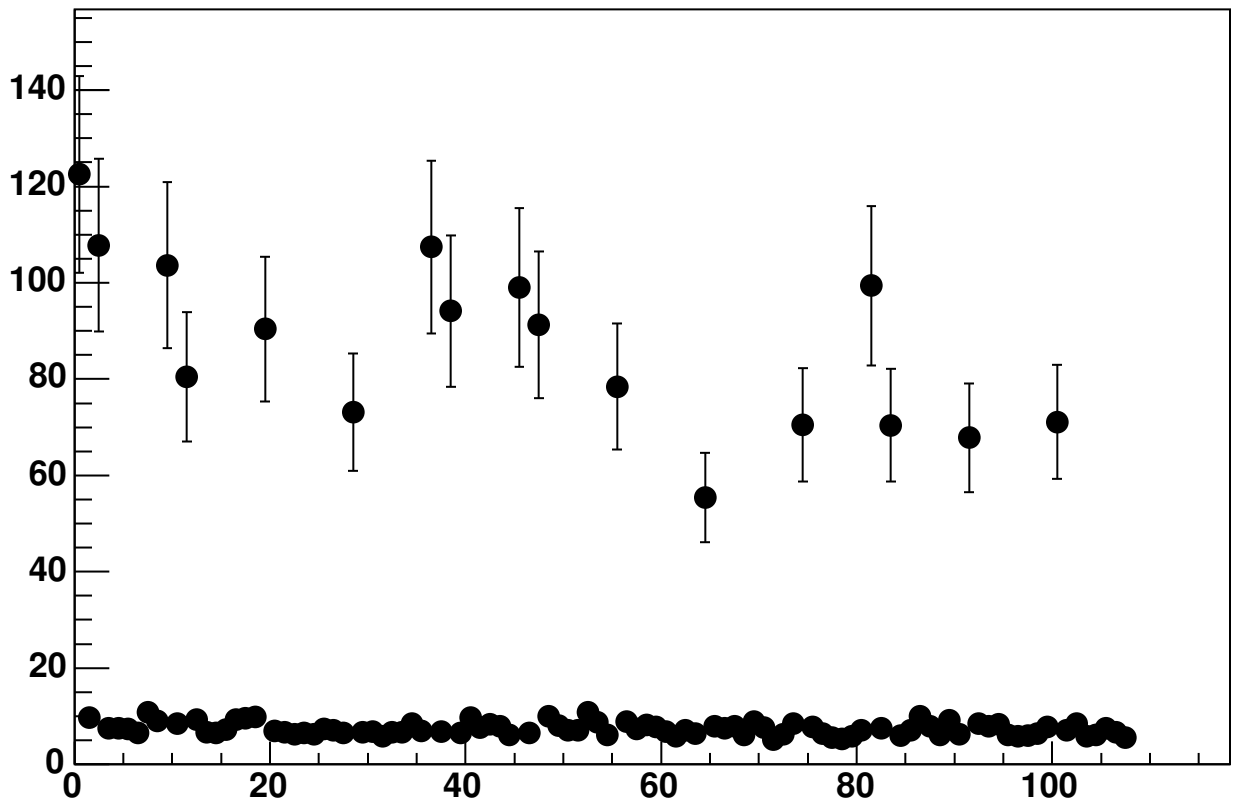
Enable 0, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



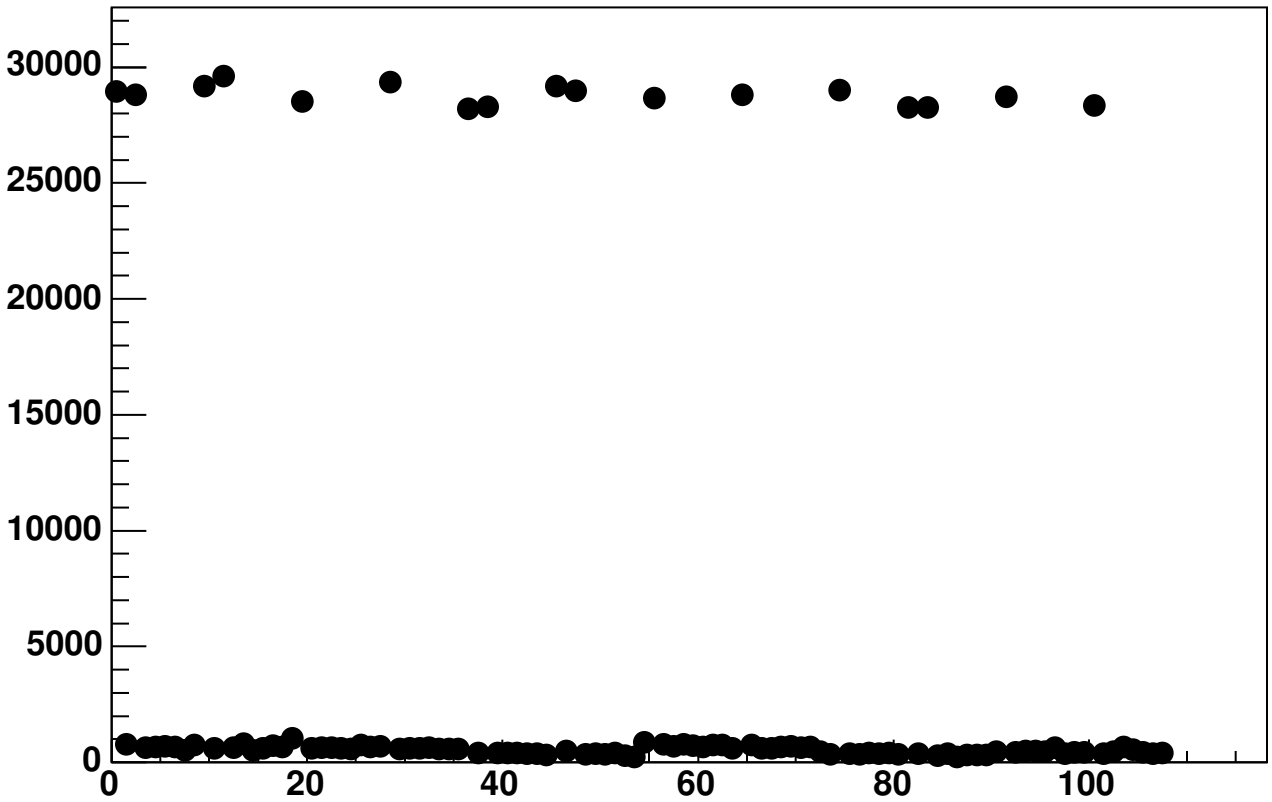
Enable 0, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



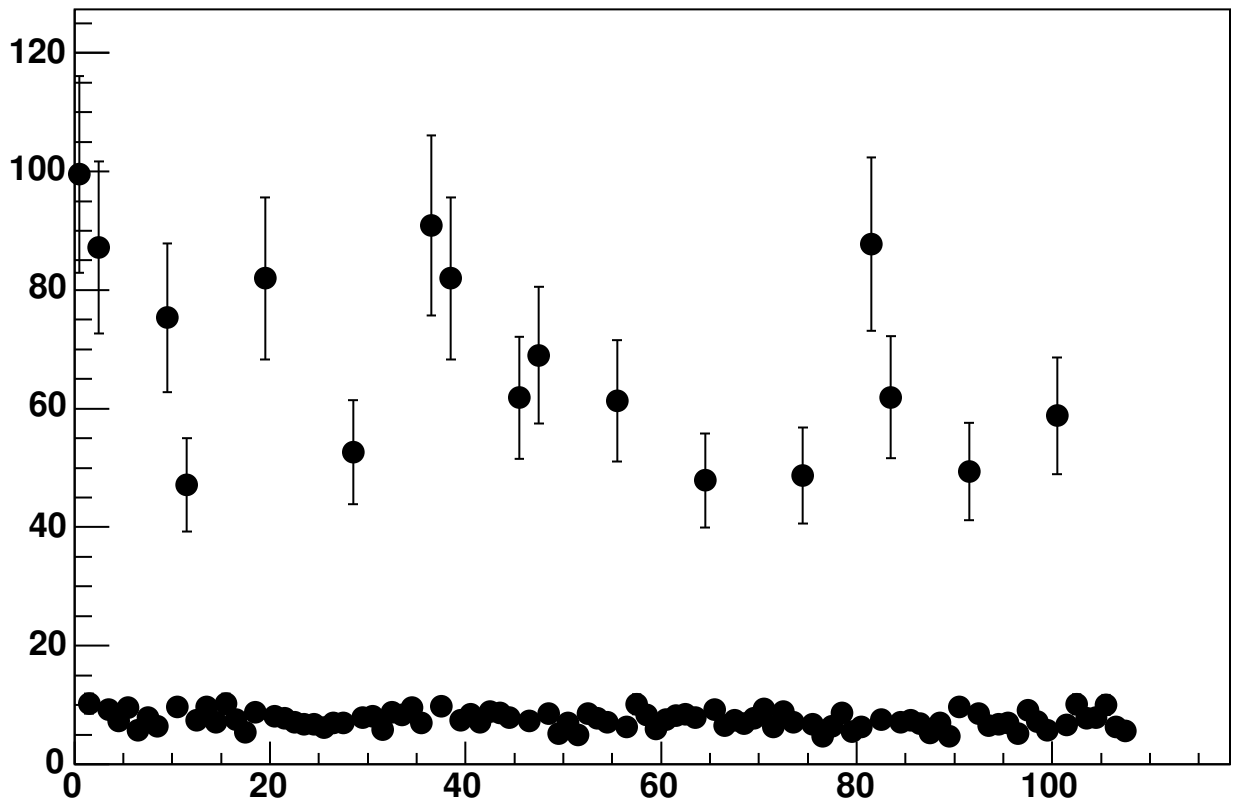
Enable 0, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



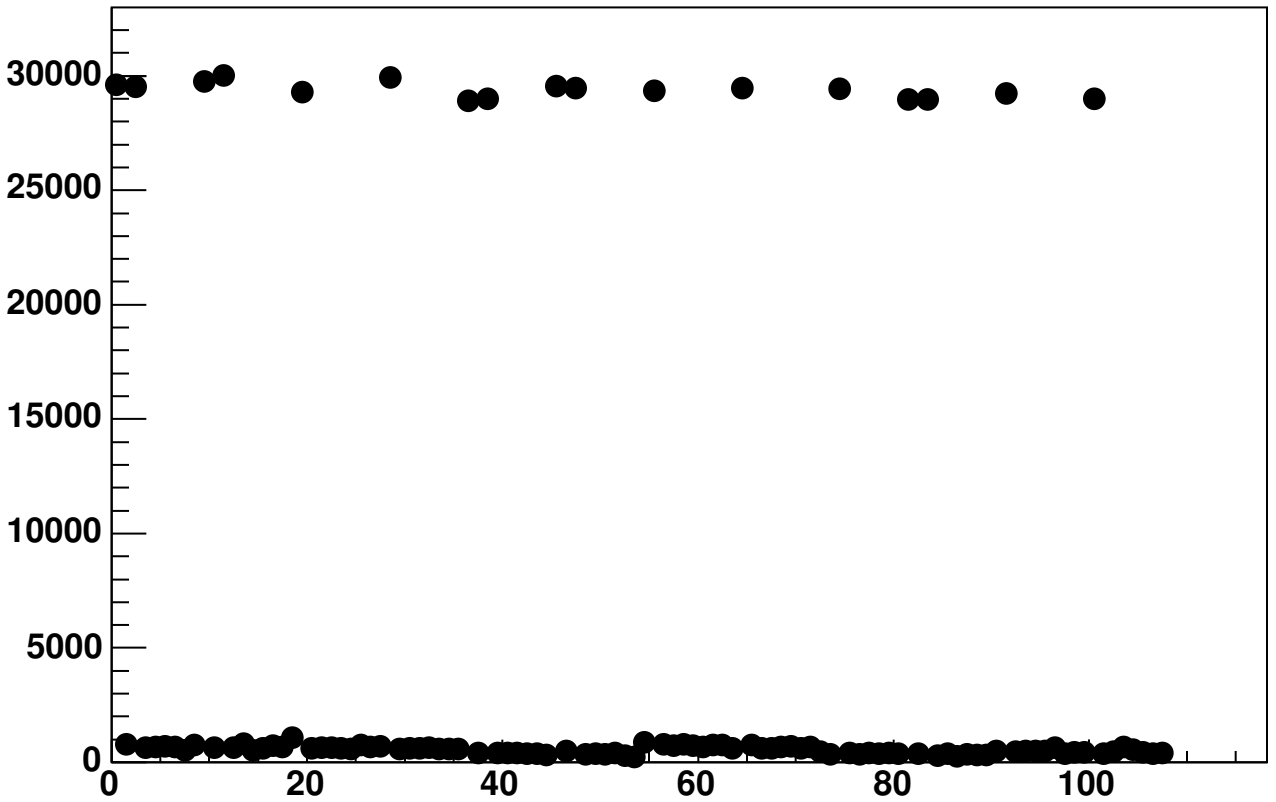
Enable 0, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



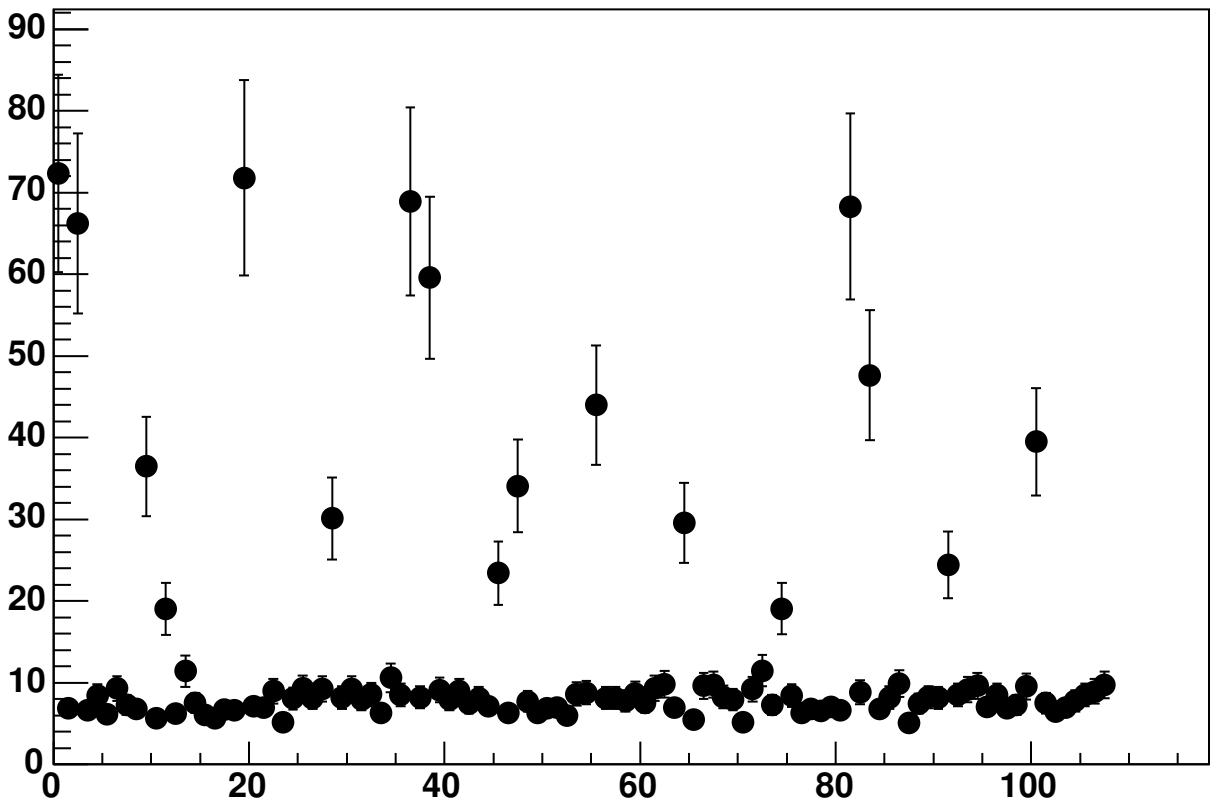
Enable 0, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



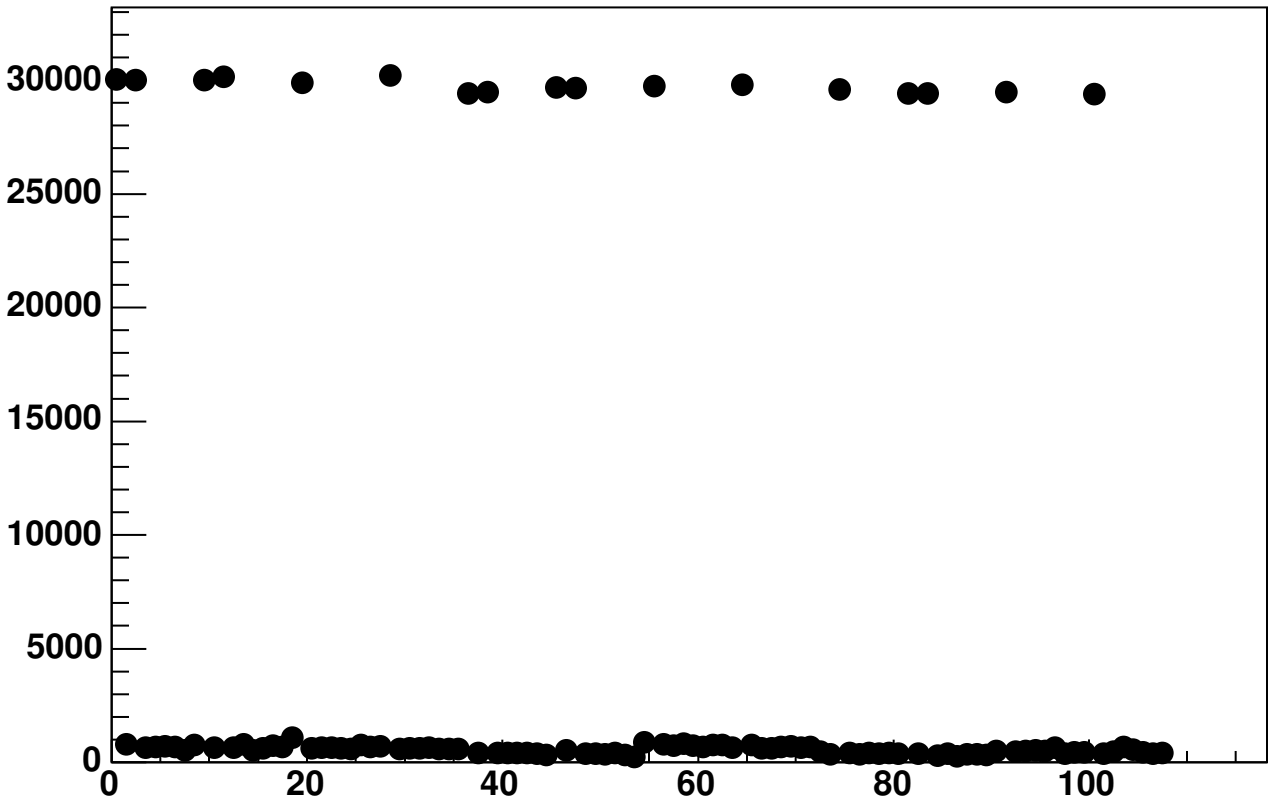
Enable 0, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



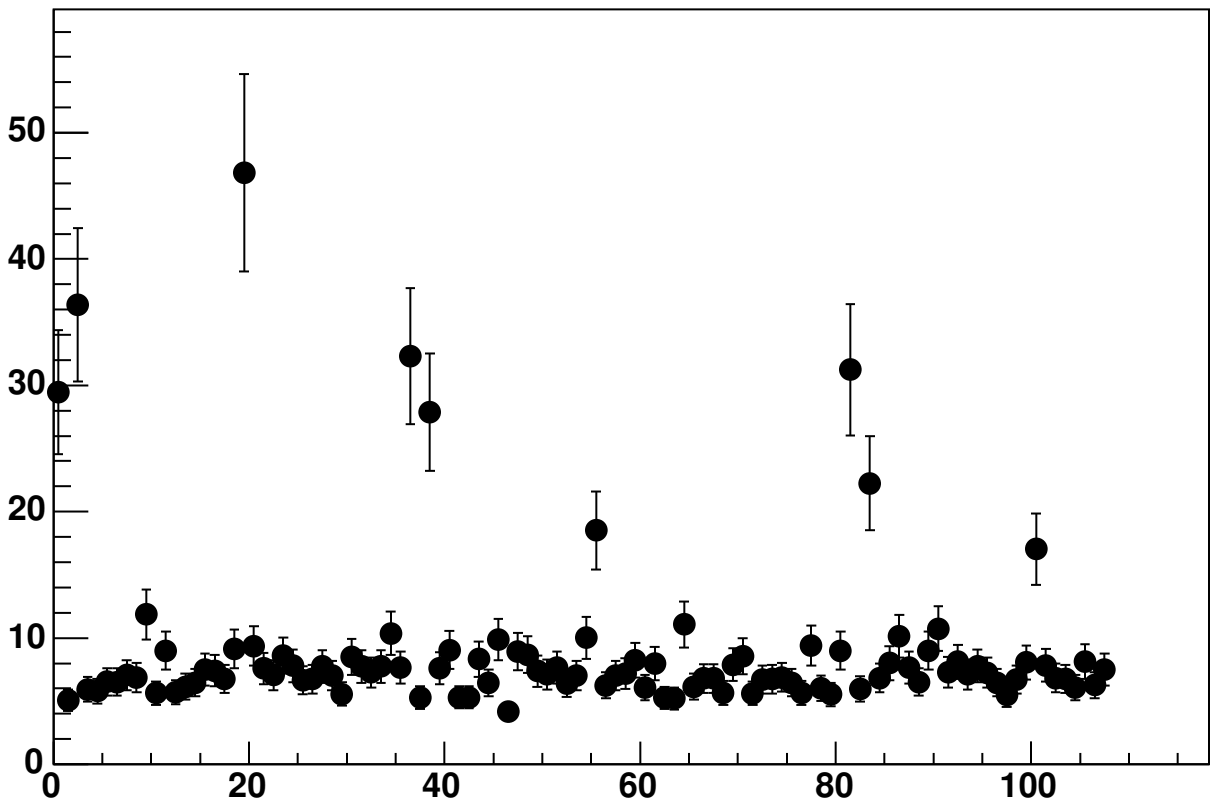
Enable 0, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



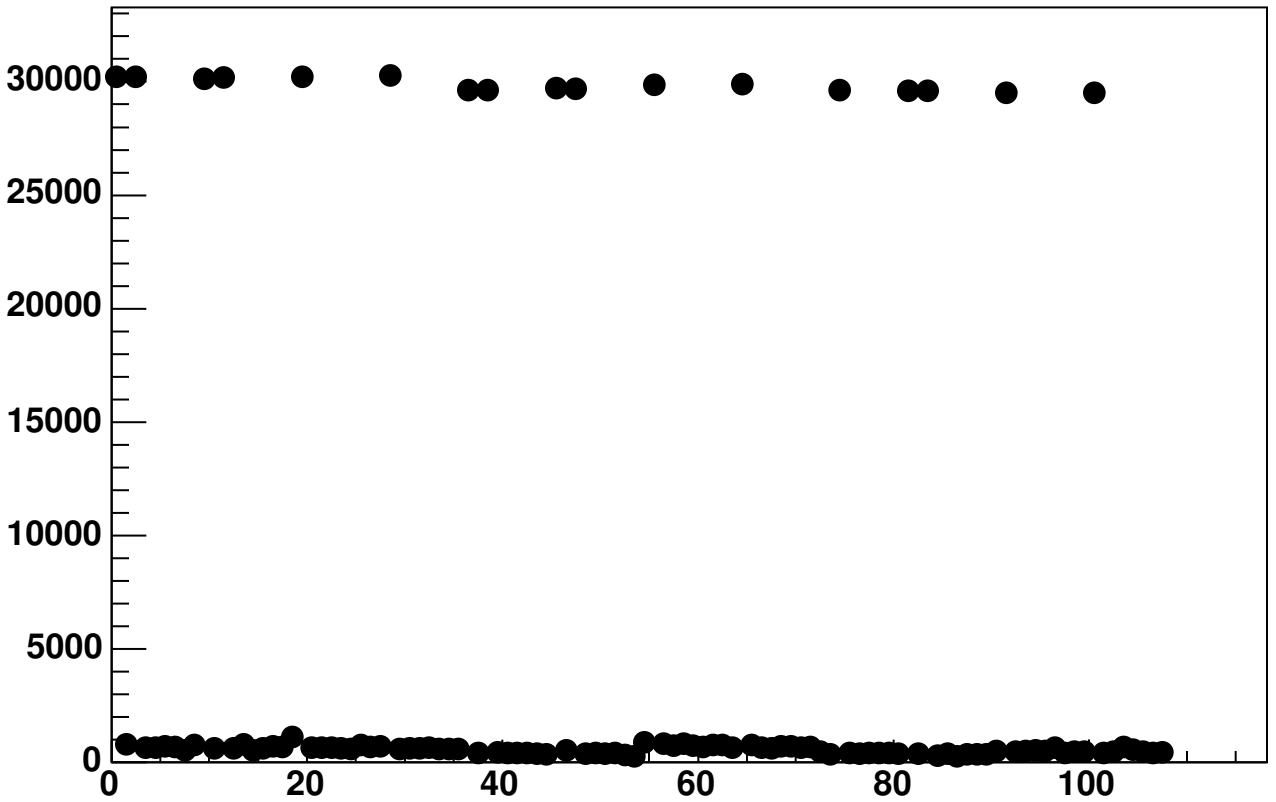
Enable 0, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



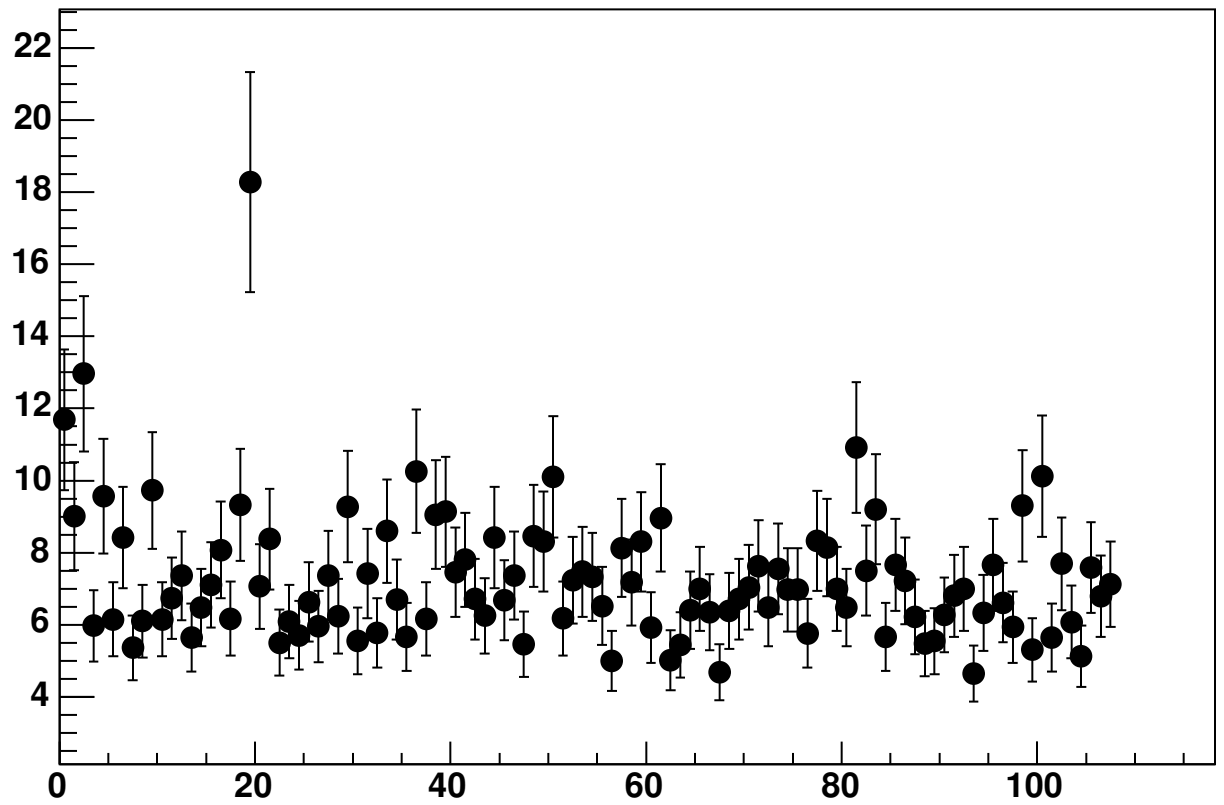
Enable 0, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



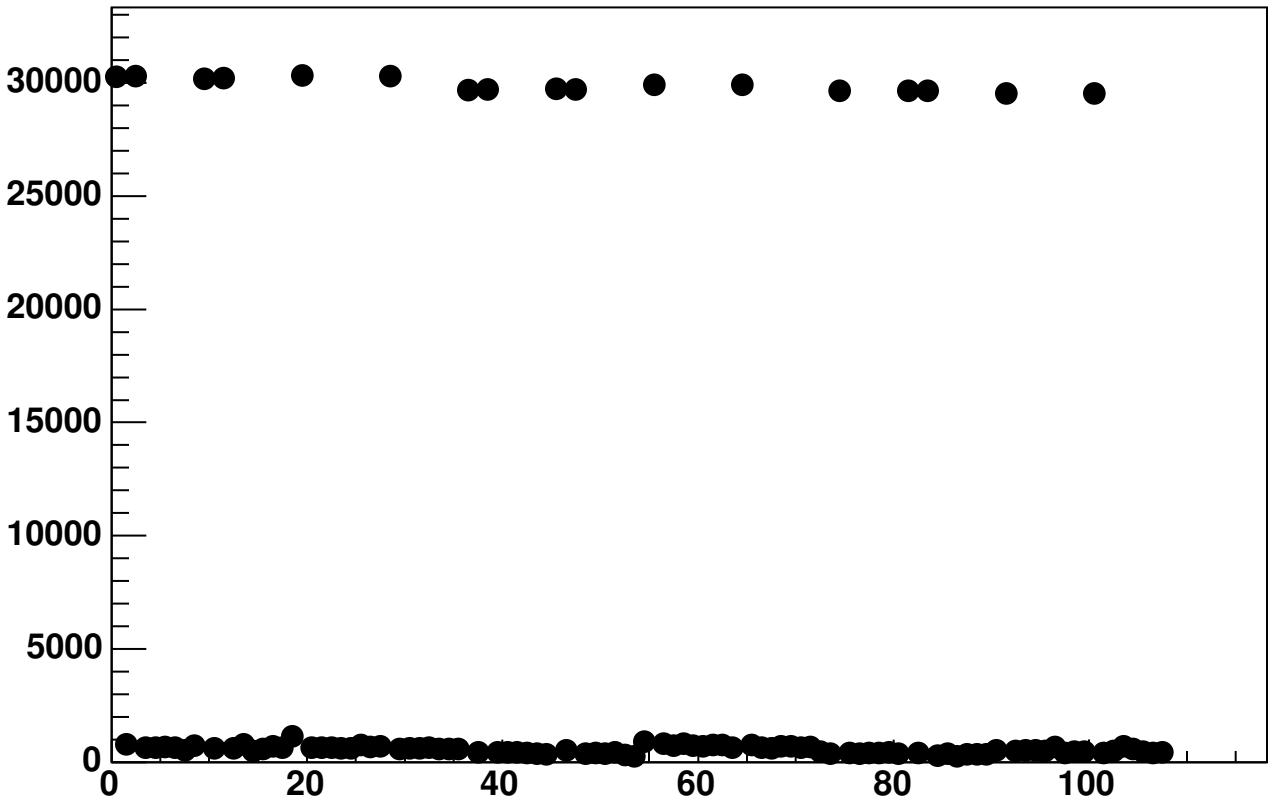
Enable 0, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



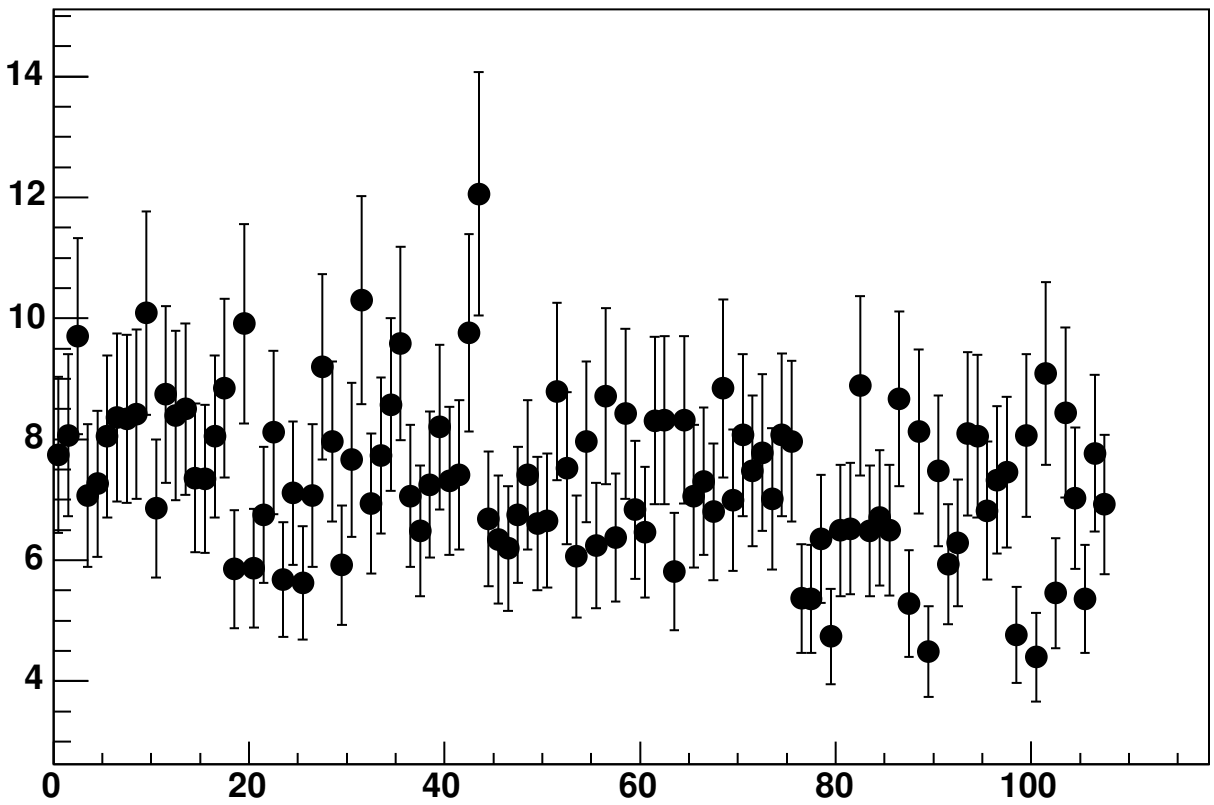
Enable 0, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



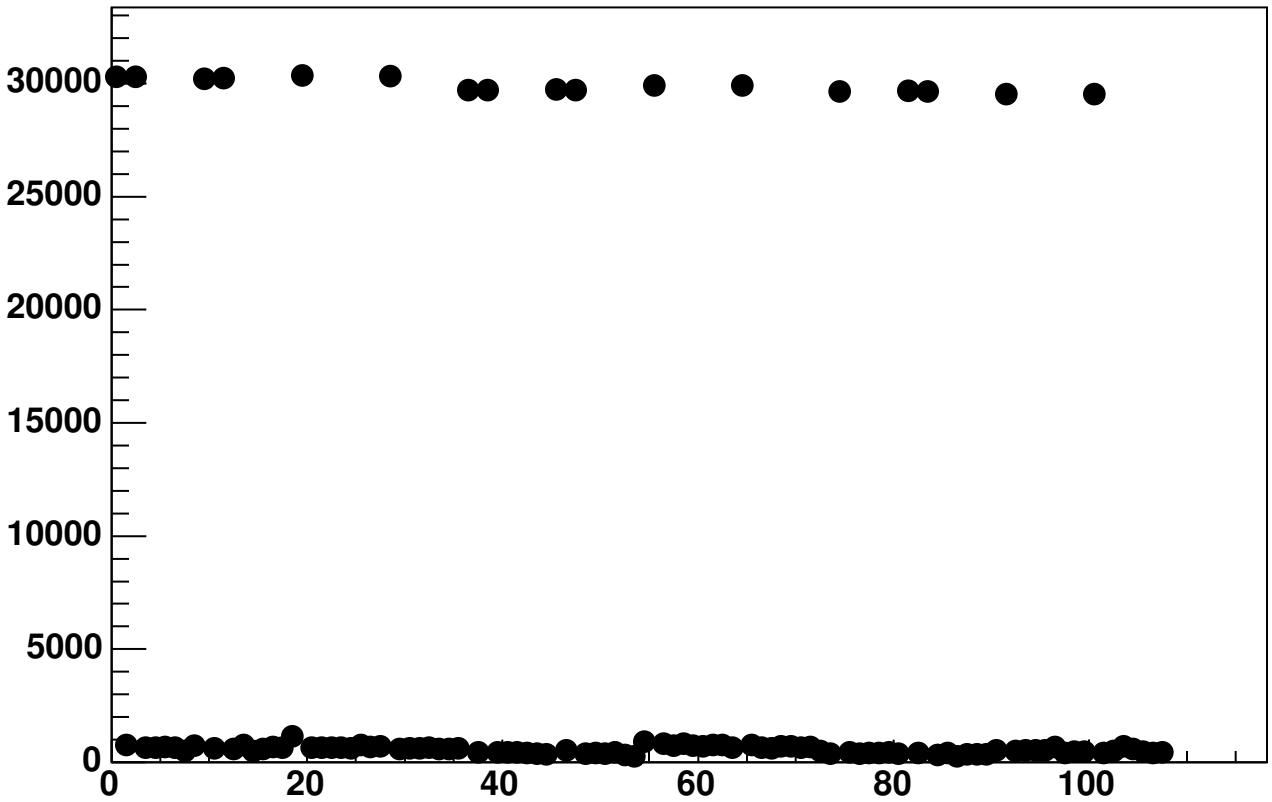
Enable 0, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



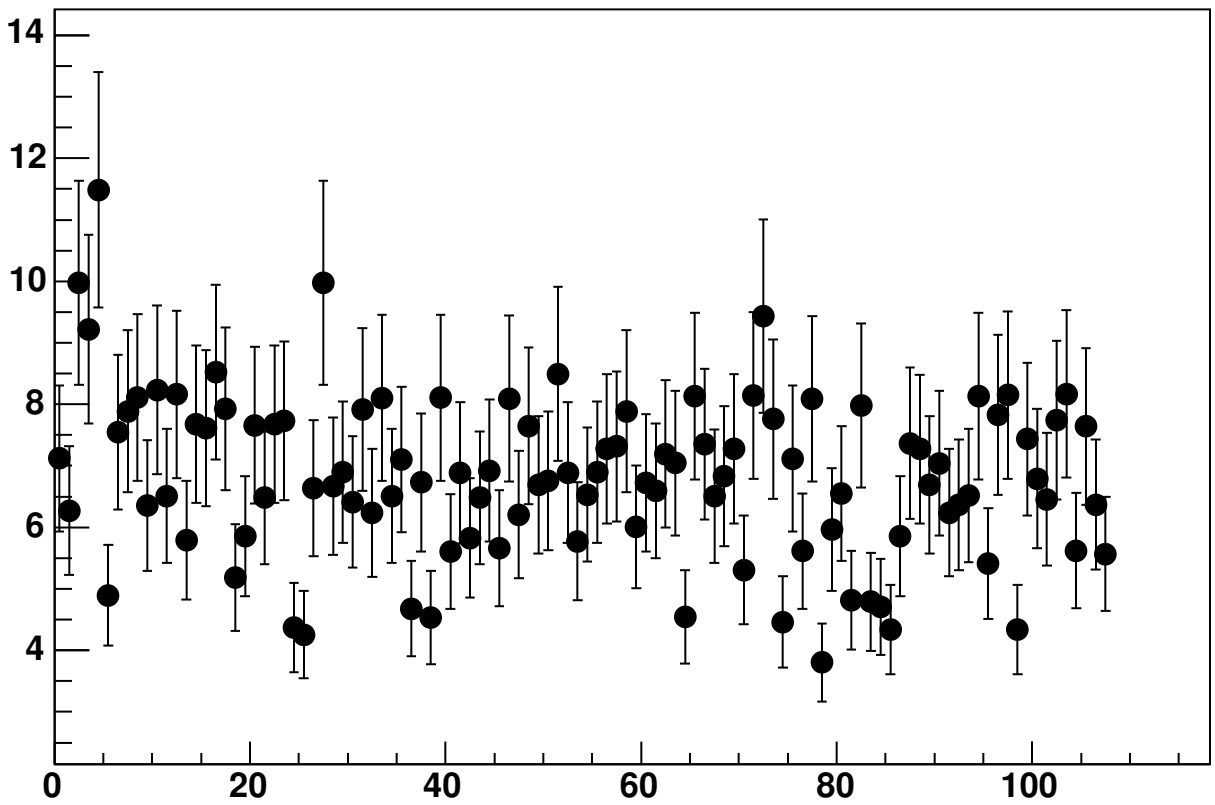
Enable 0, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



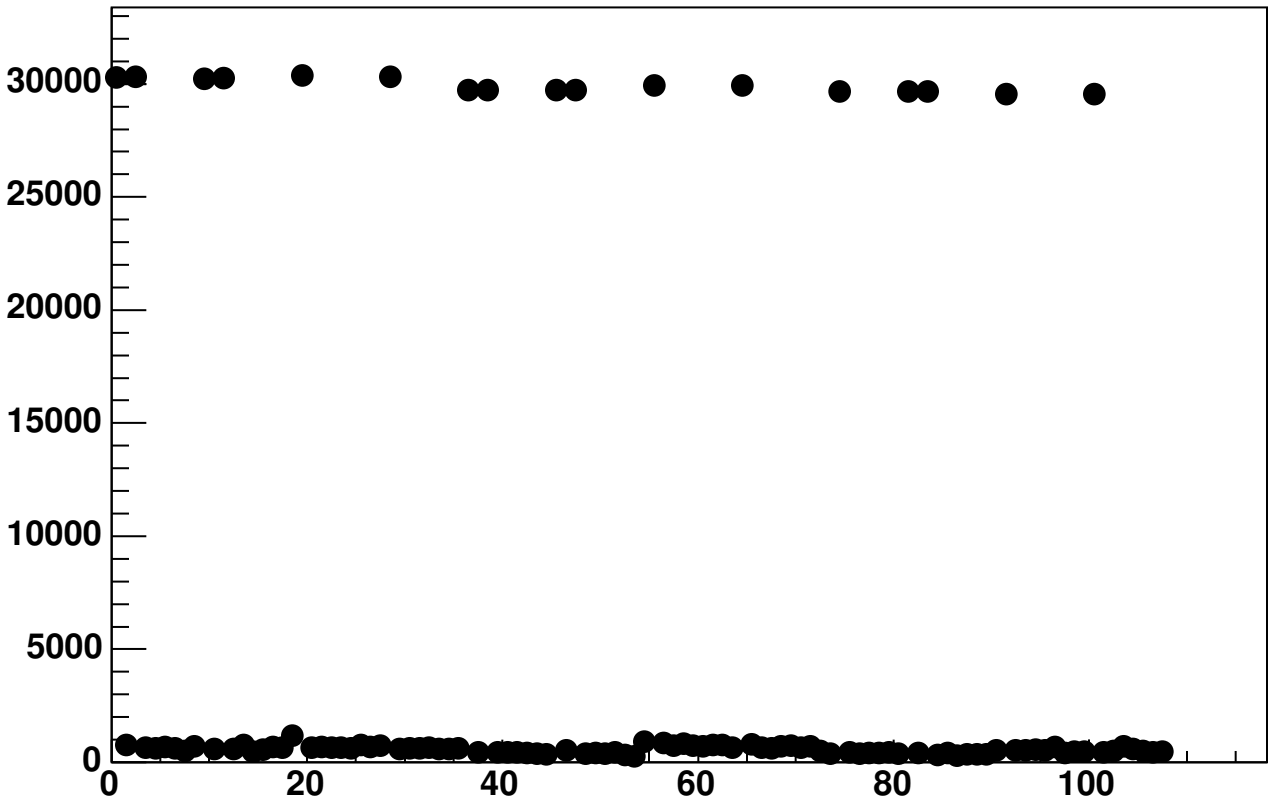
Enable 0, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



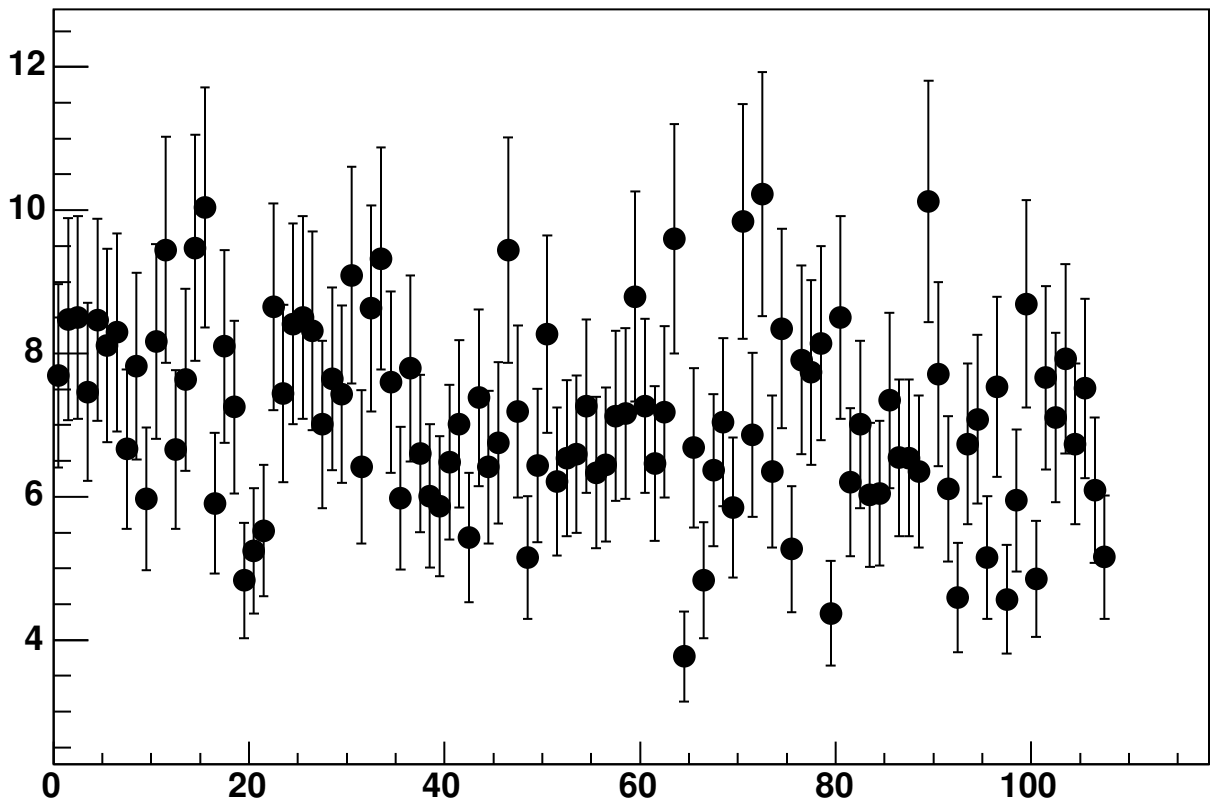
Enable 0, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



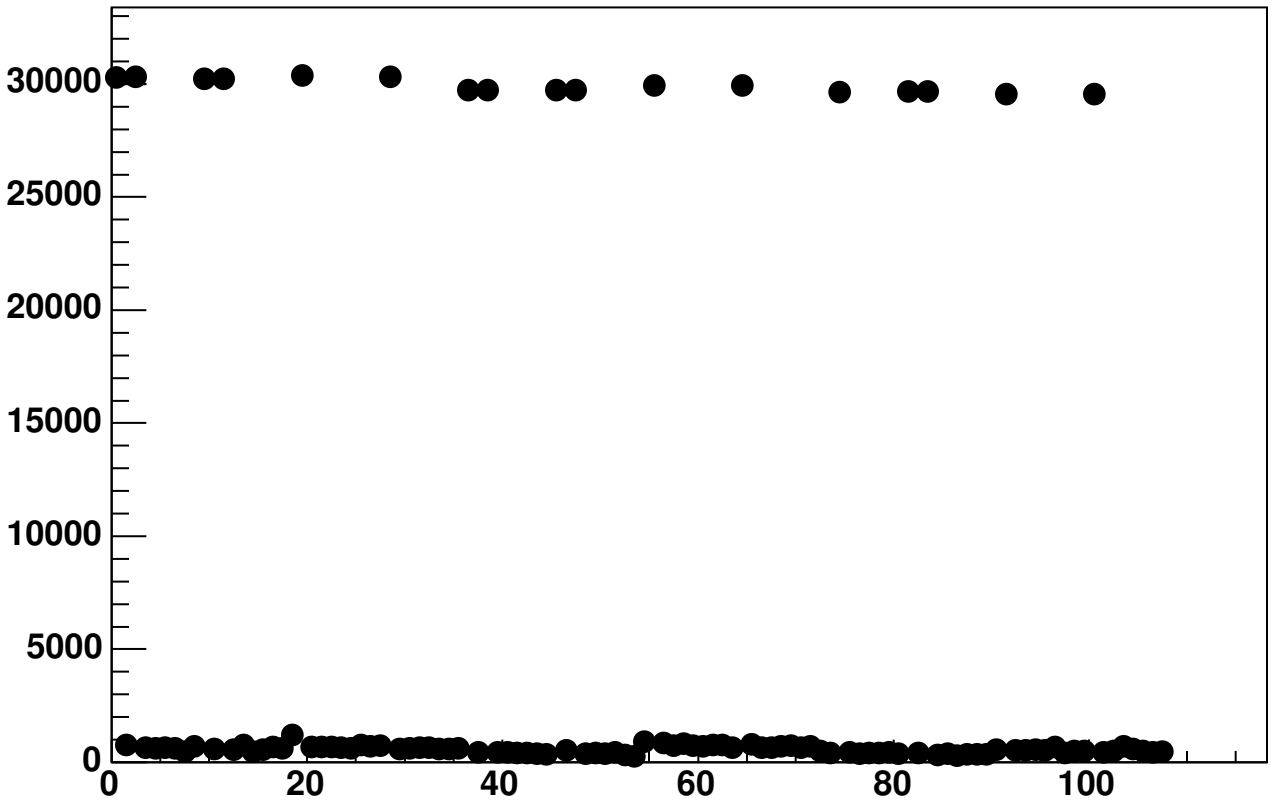
Enable 0, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



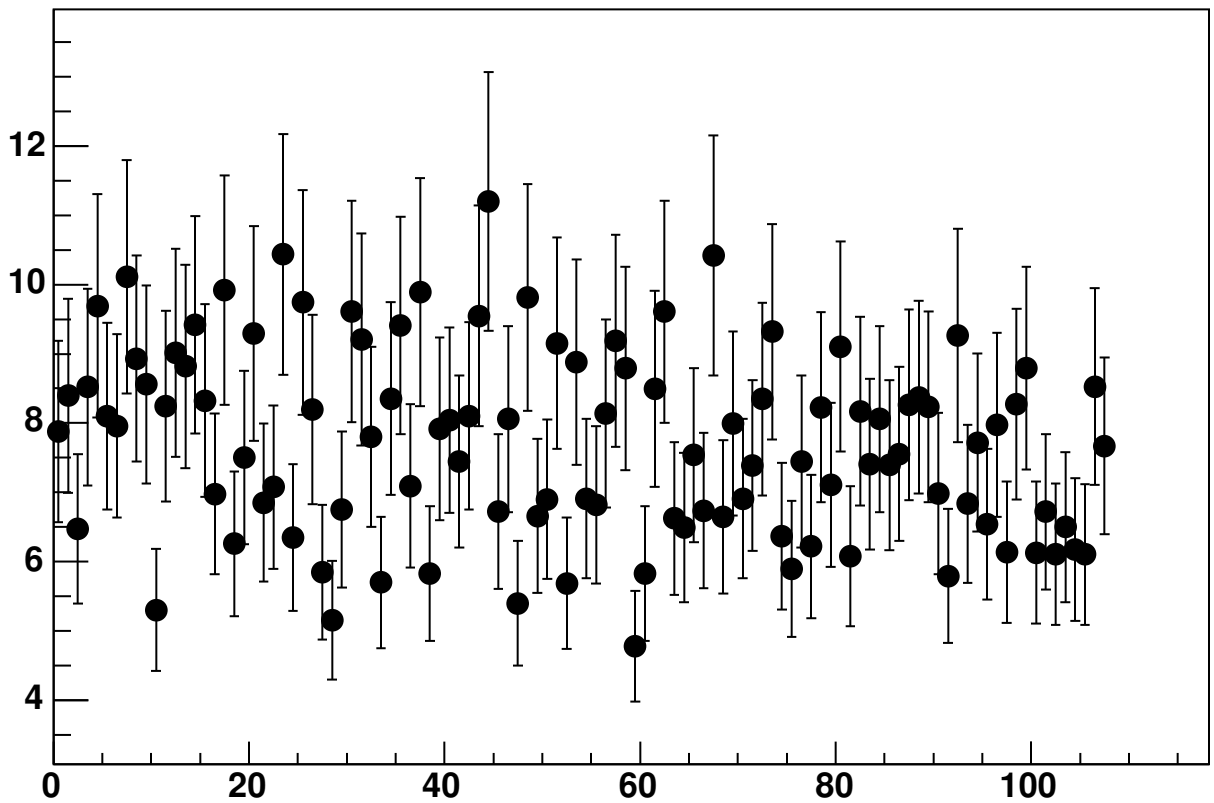
Enable 0, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



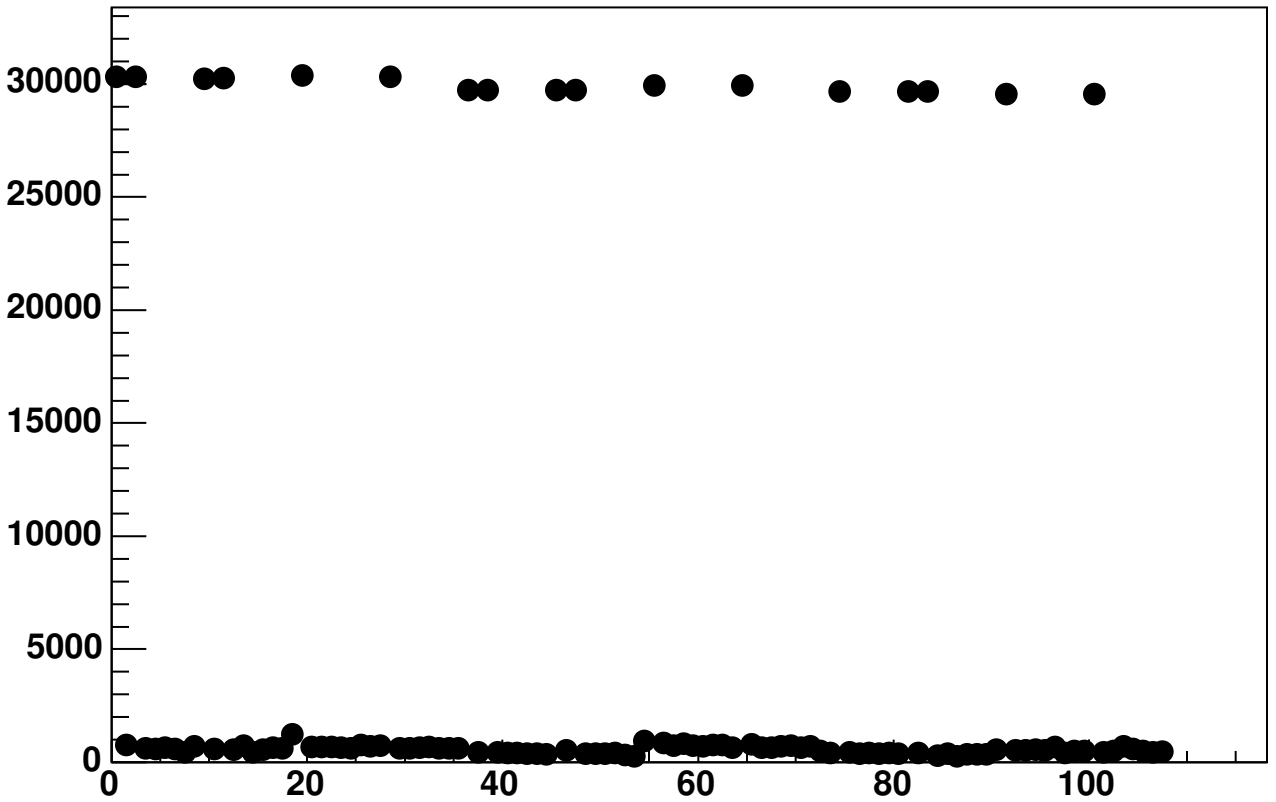
Enable 0, Hold=35, DAC=14400, ADC Mean vs 18*Chip+Chan



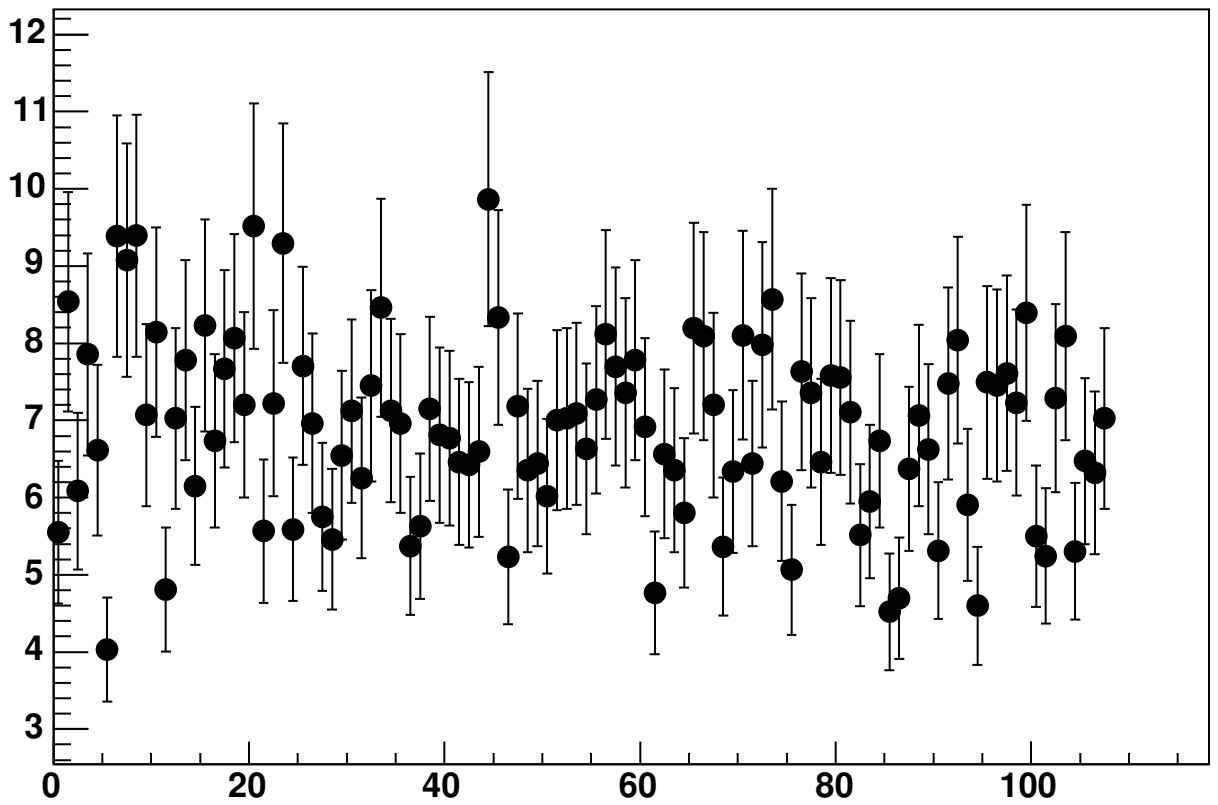
Enable 0, Hold=35, DAC=14400, ADC Noise vs 18*Chip+Chan



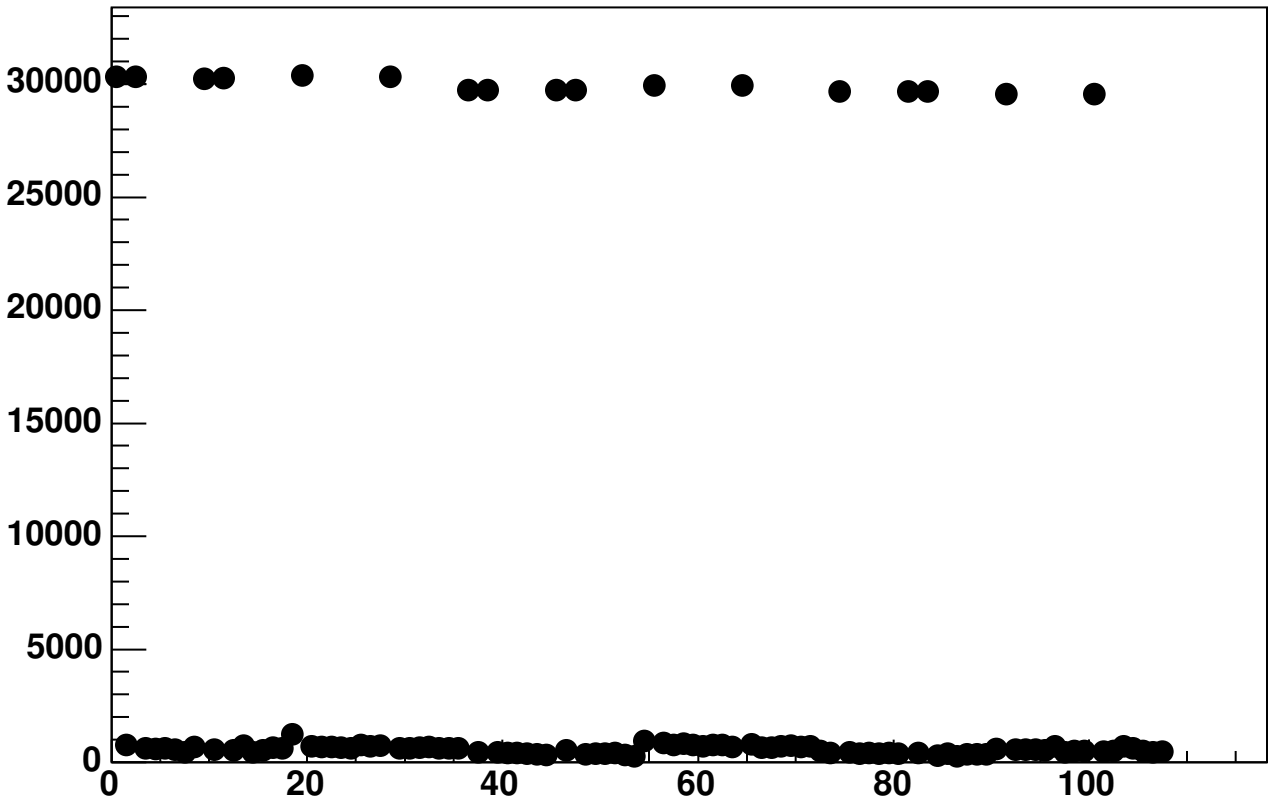
Enable 0, Hold=35, DAC=14800, ADC Mean vs 18*Chip+Chan



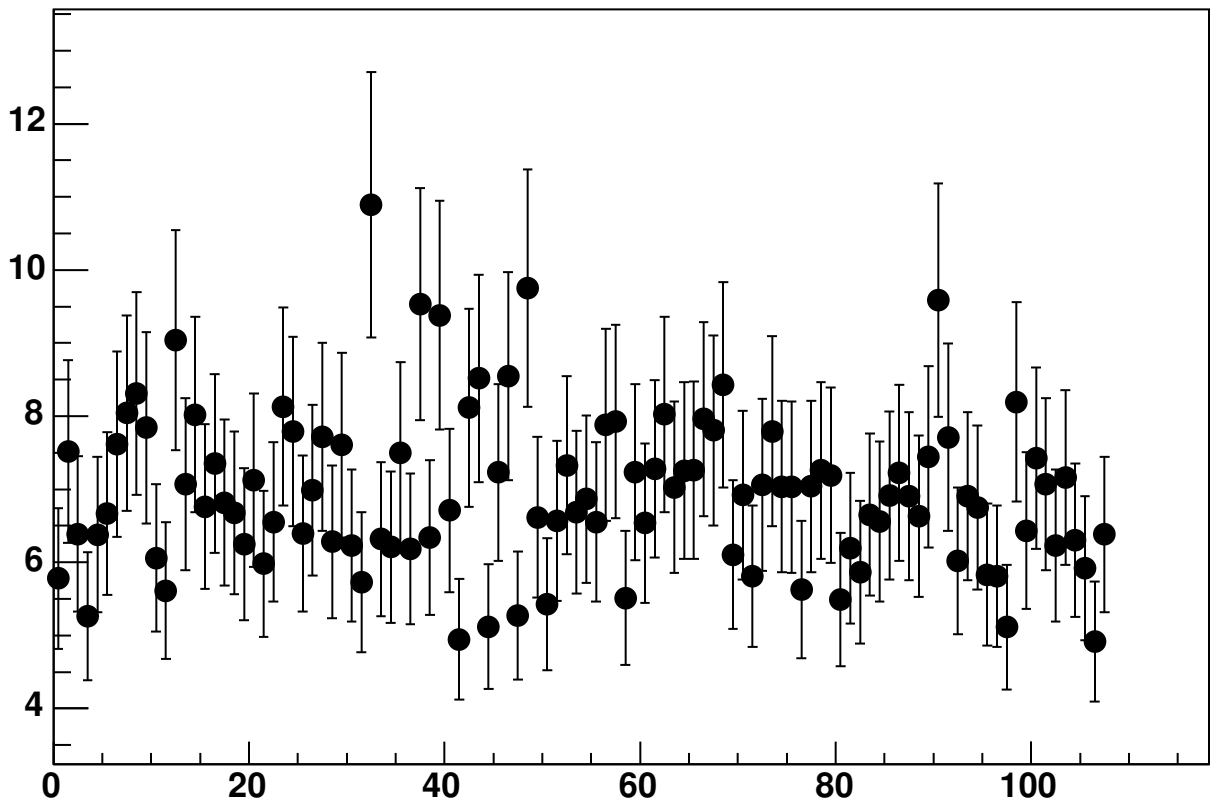
Enable 0, Hold=35, DAC=14800, ADC Noise vs 18*Chip+Chan



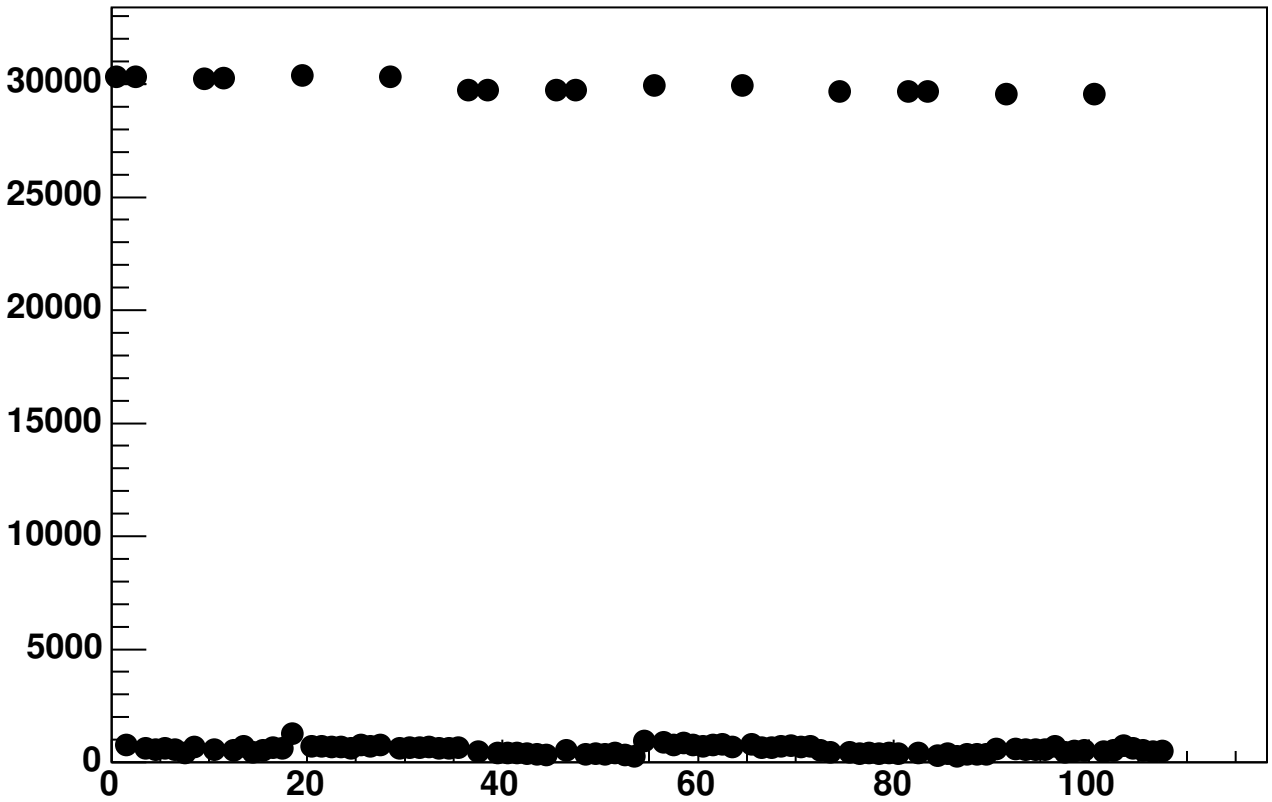
Enable 0, Hold=35, DAC=15200, ADC Mean vs 18*Chip+Chan



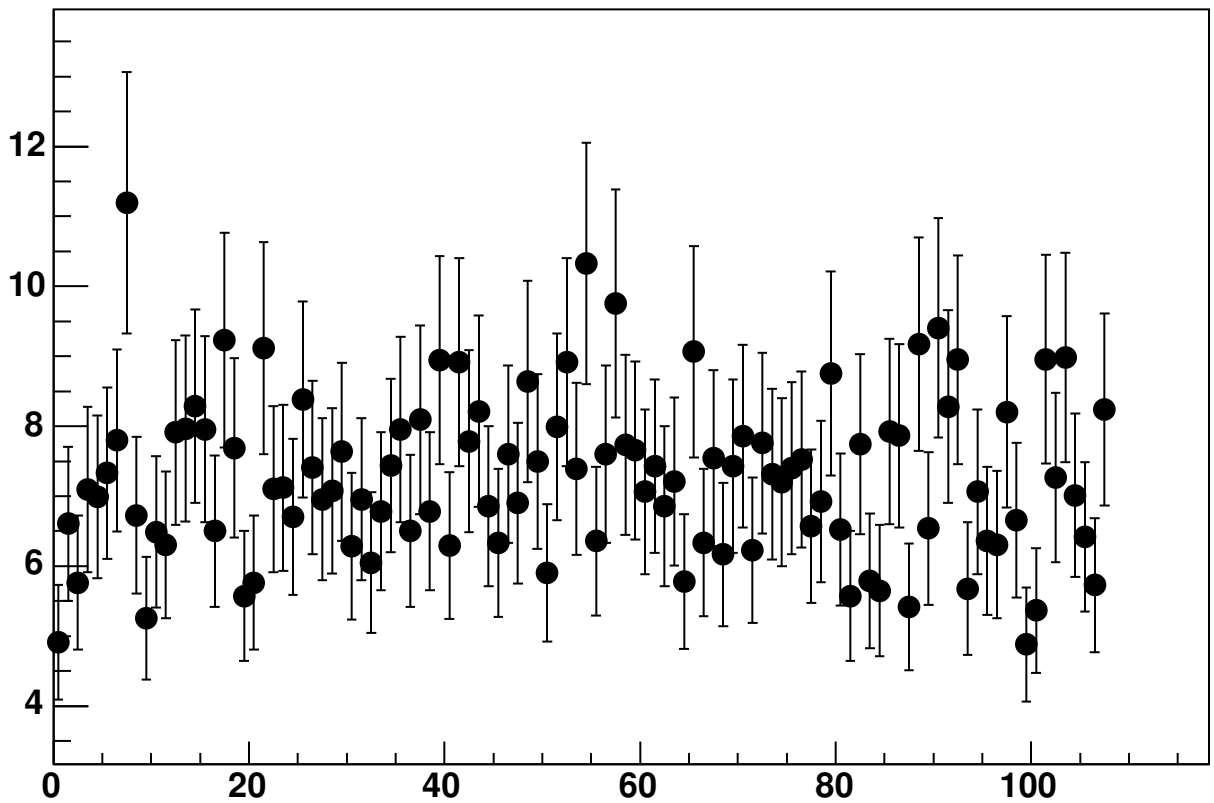
Enable 0, Hold=35, DAC=15200, ADC Noise vs 18*Chip+Chan



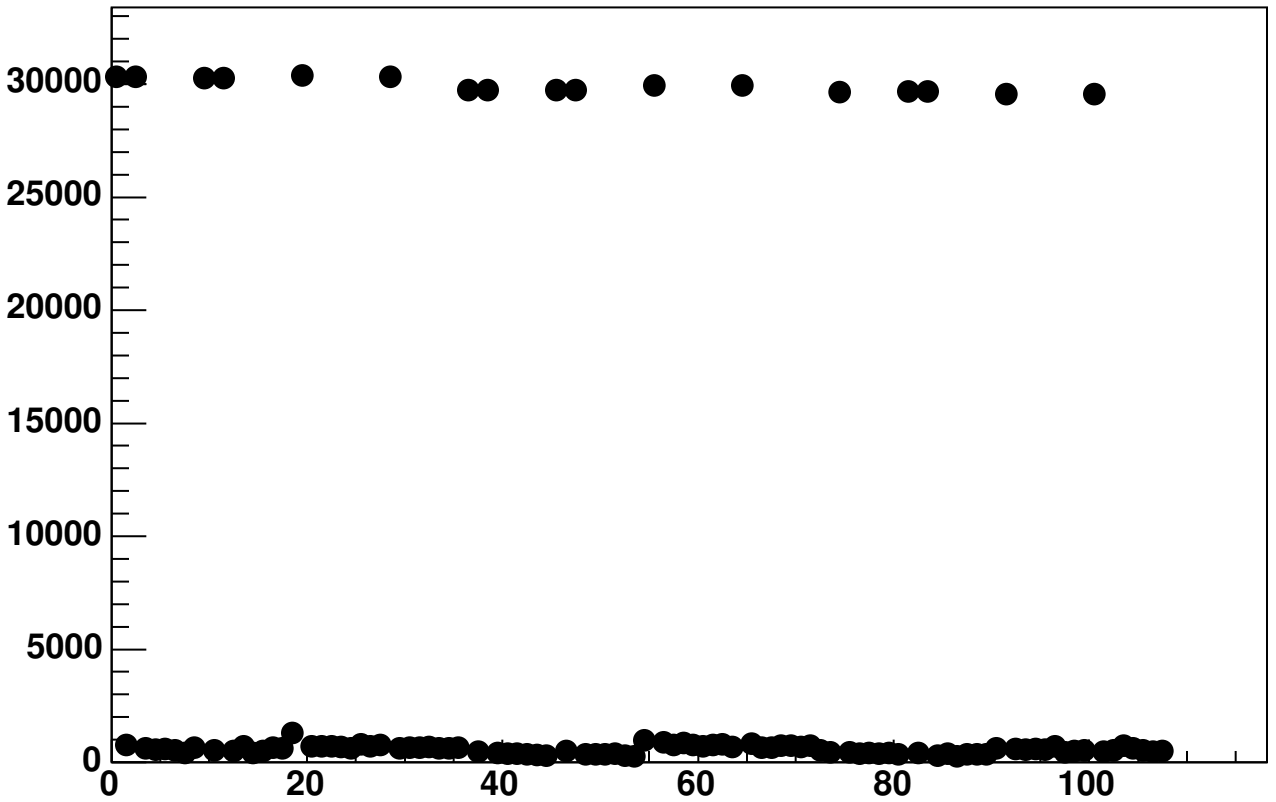
Enable 0, Hold=35, DAC=15600, ADC Mean vs 18*Chip+Chan



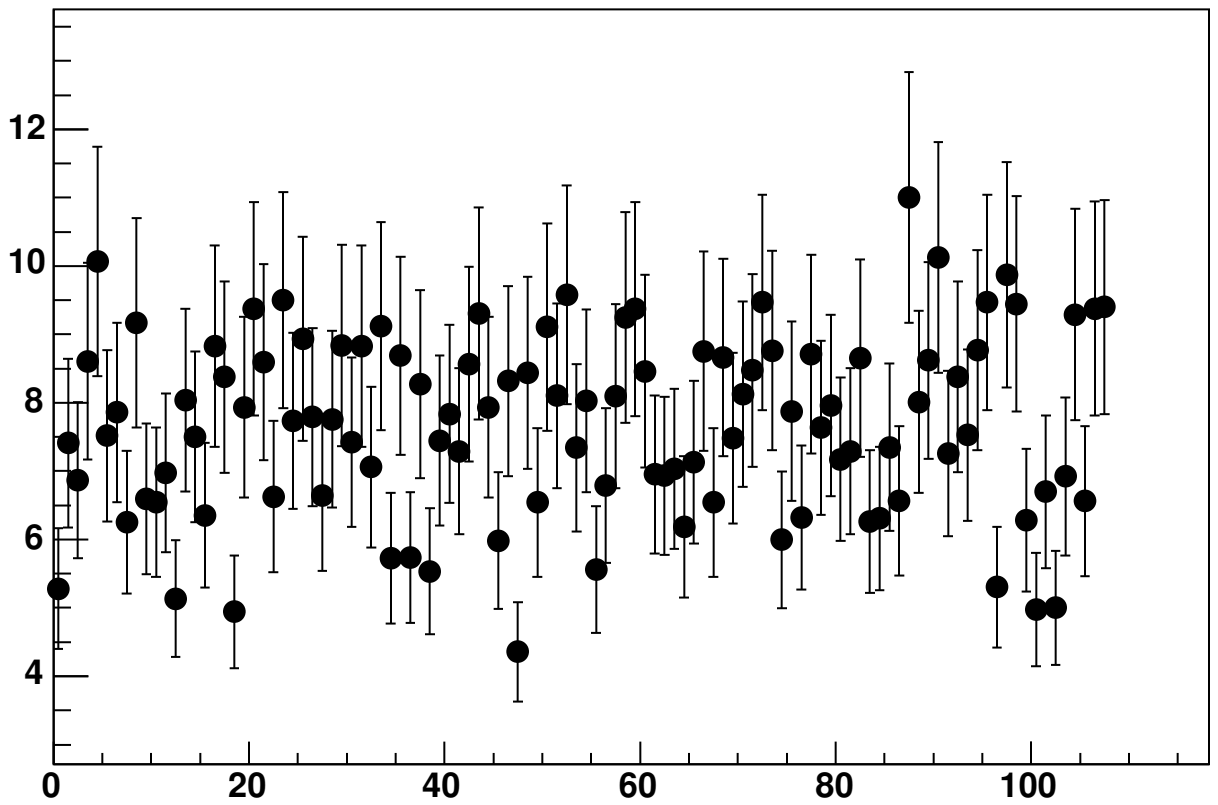
Enable 0, Hold=35, DAC=15600, ADC Noise vs 18*Chip+Chan



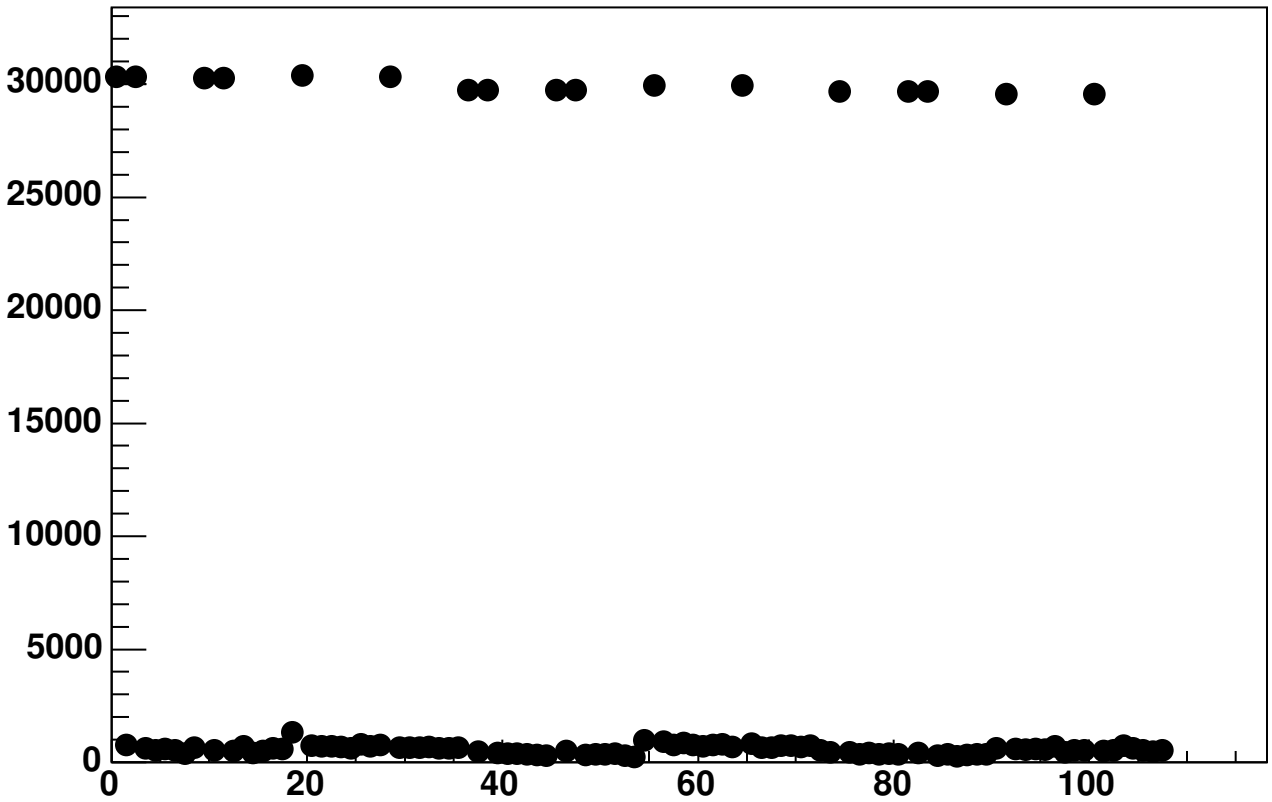
Enable 0, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



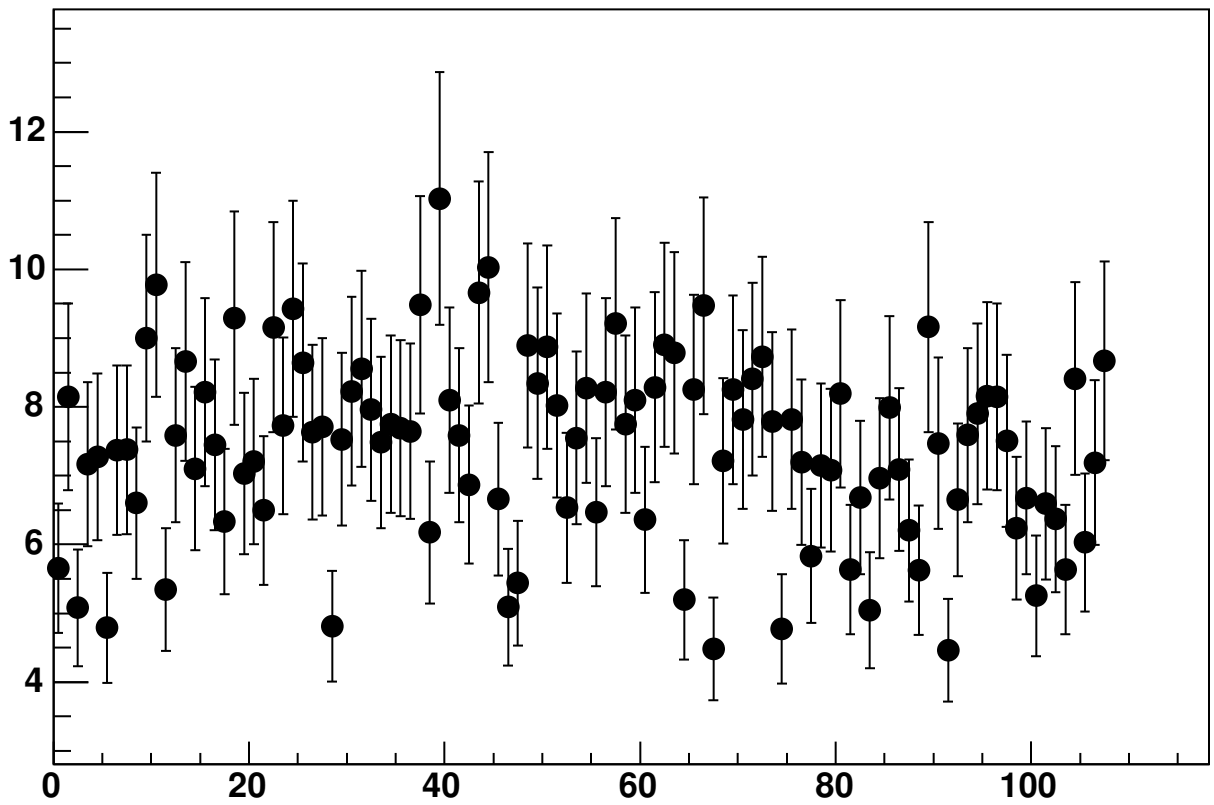
Enable 0, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



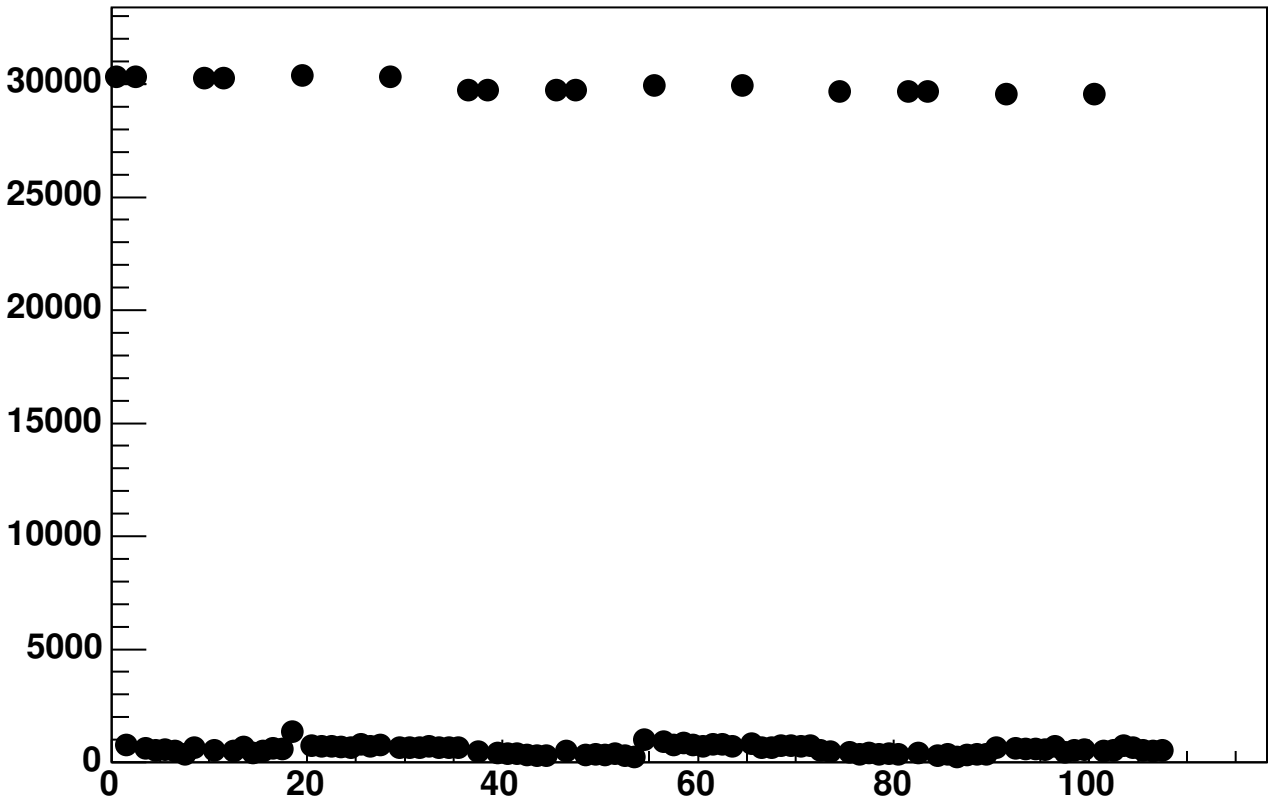
Enable 0, Hold=35, DAC=16400, ADC Mean vs 18*Chip+Chan



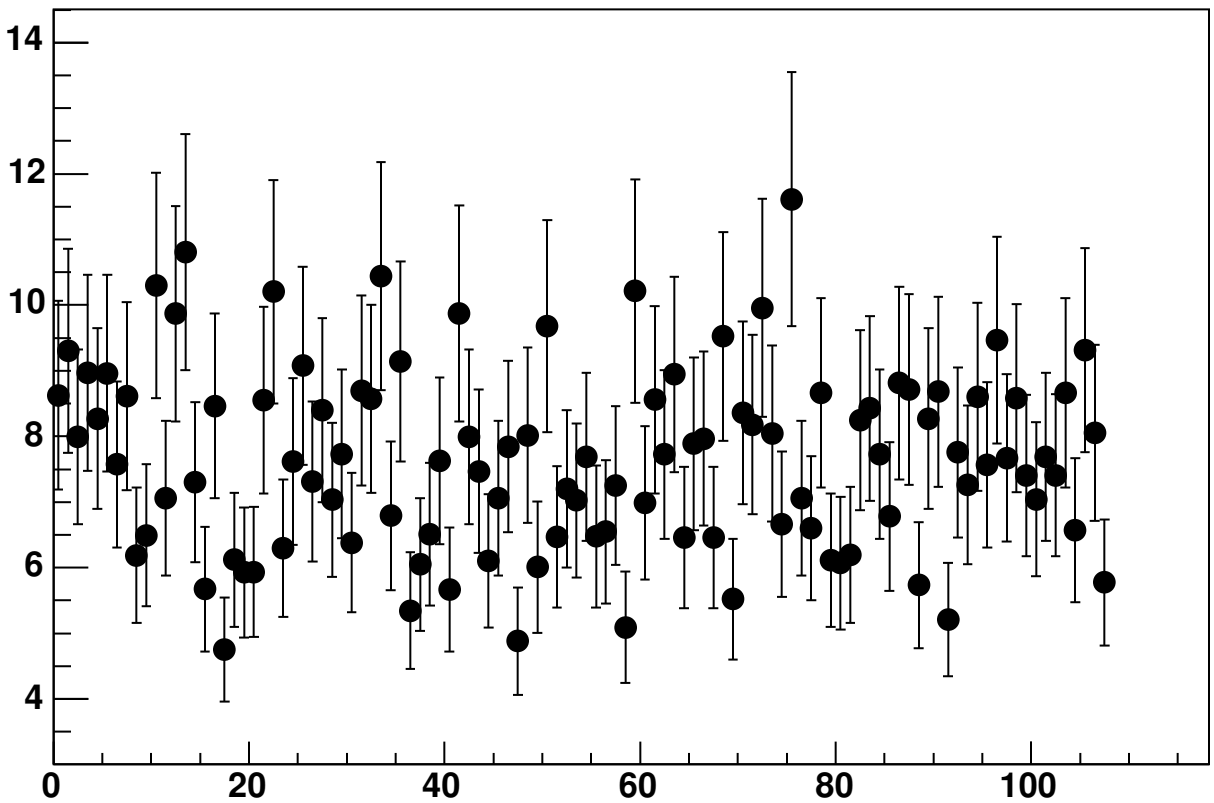
Enable 0, Hold=35, DAC=16400, ADC Noise vs 18*Chip+Chan



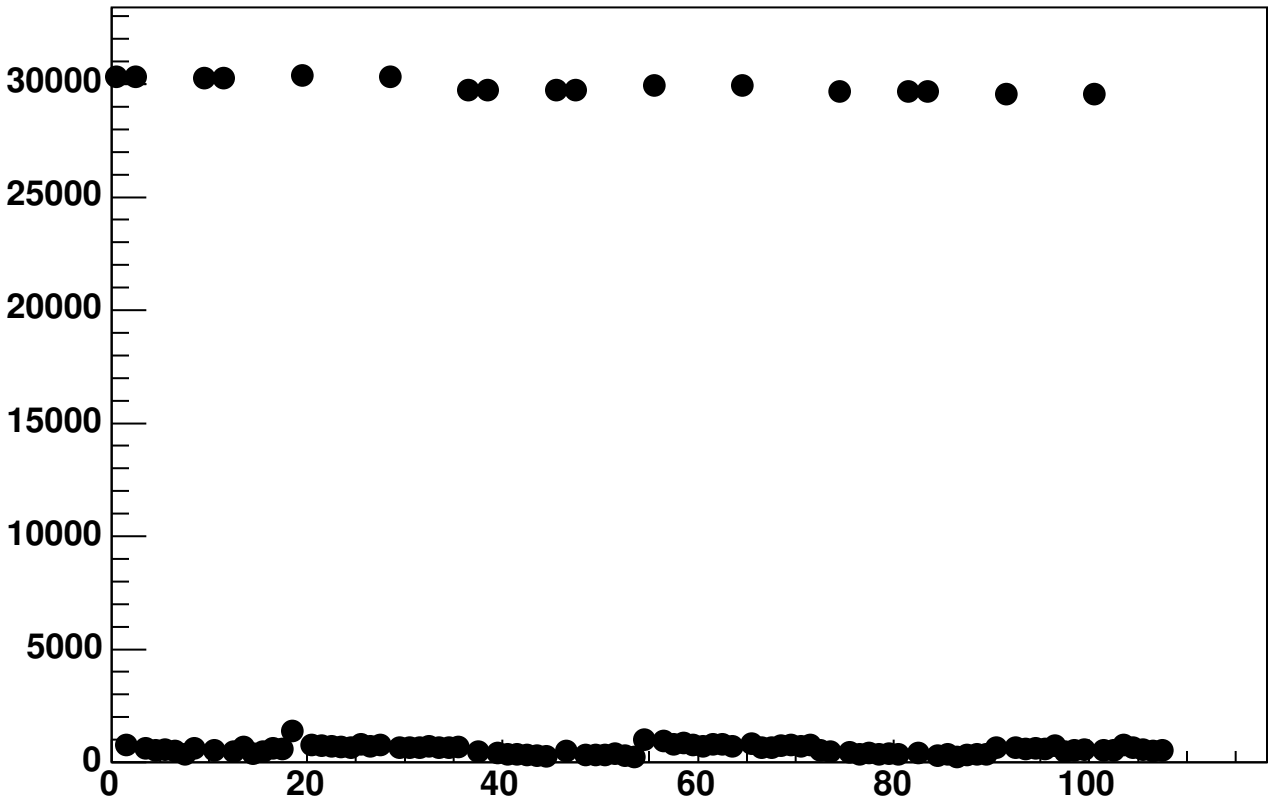
Enable 0, Hold=35, DAC=16800, ADC Mean vs 18*Chip+Chan



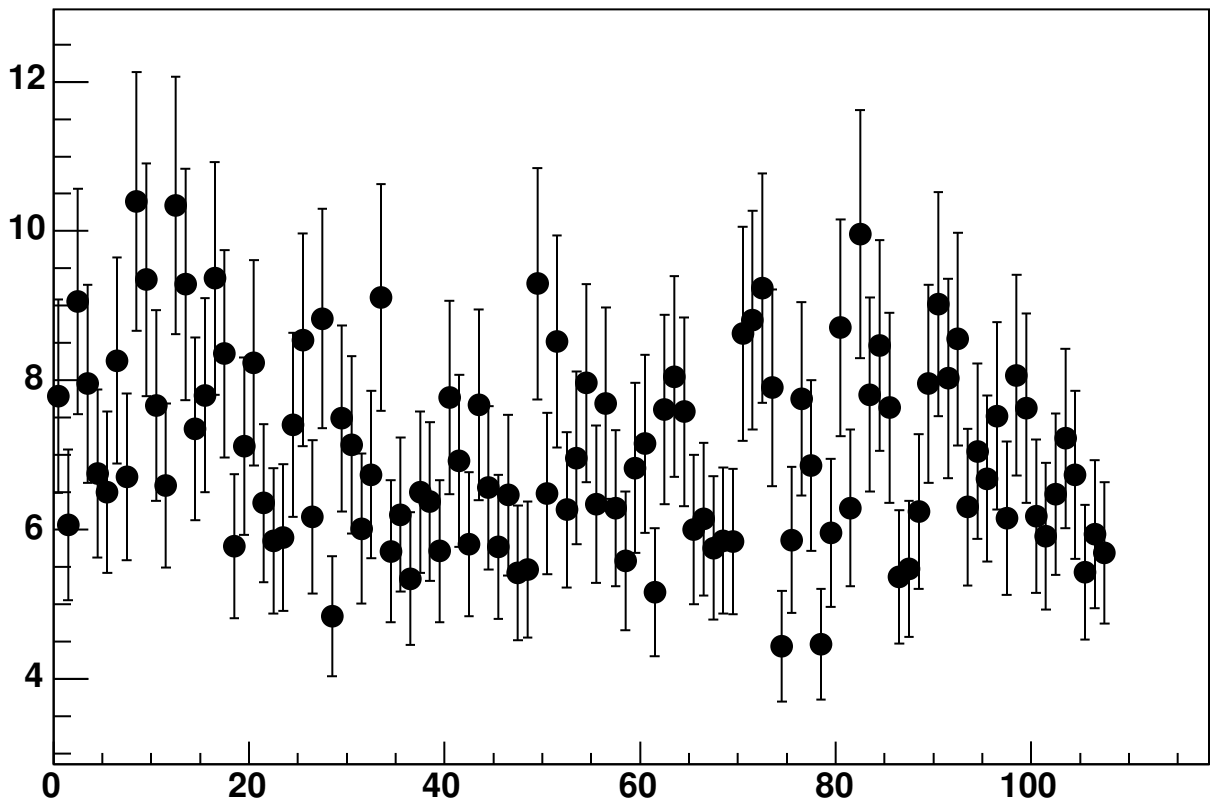
Enable 0, Hold=35, DAC=16800, ADC Noise vs 18*Chip+Chan



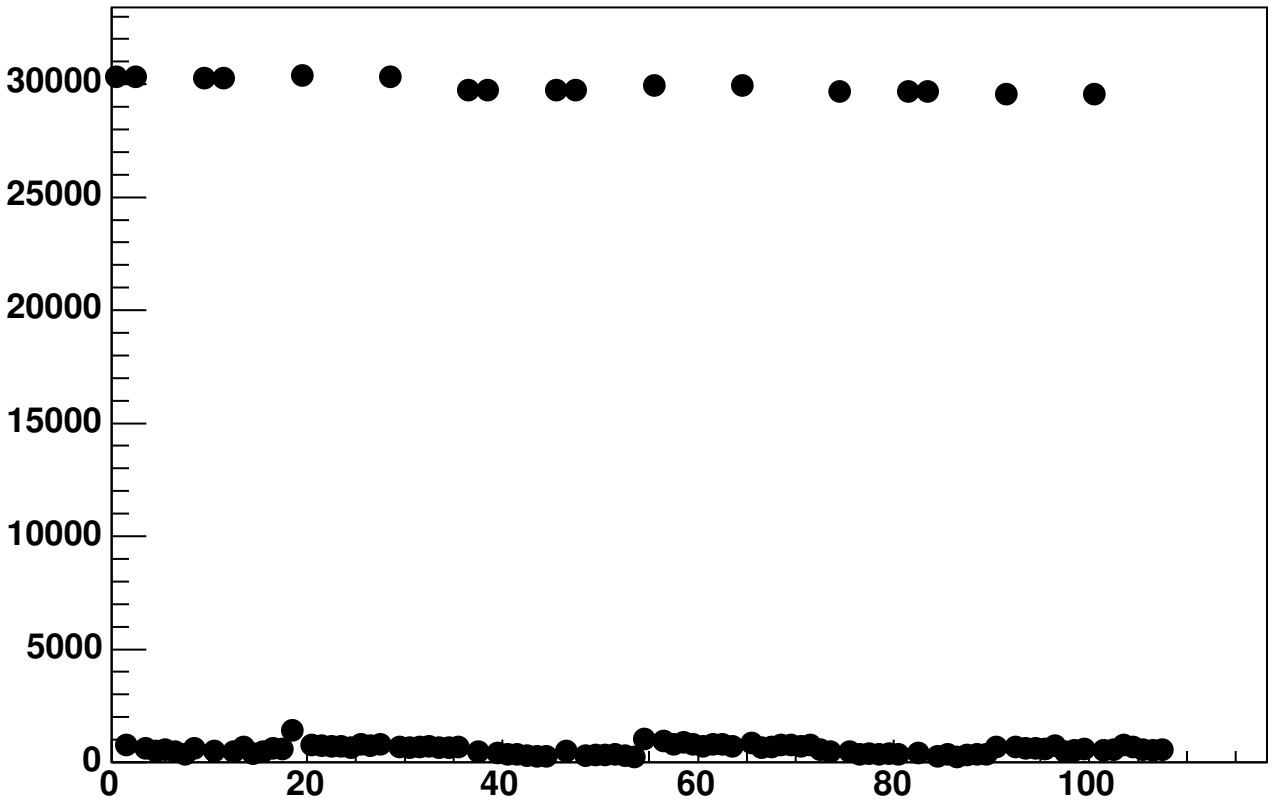
Enable 0, Hold=35, DAC=17200, ADC Mean vs 18*Chip+Chan



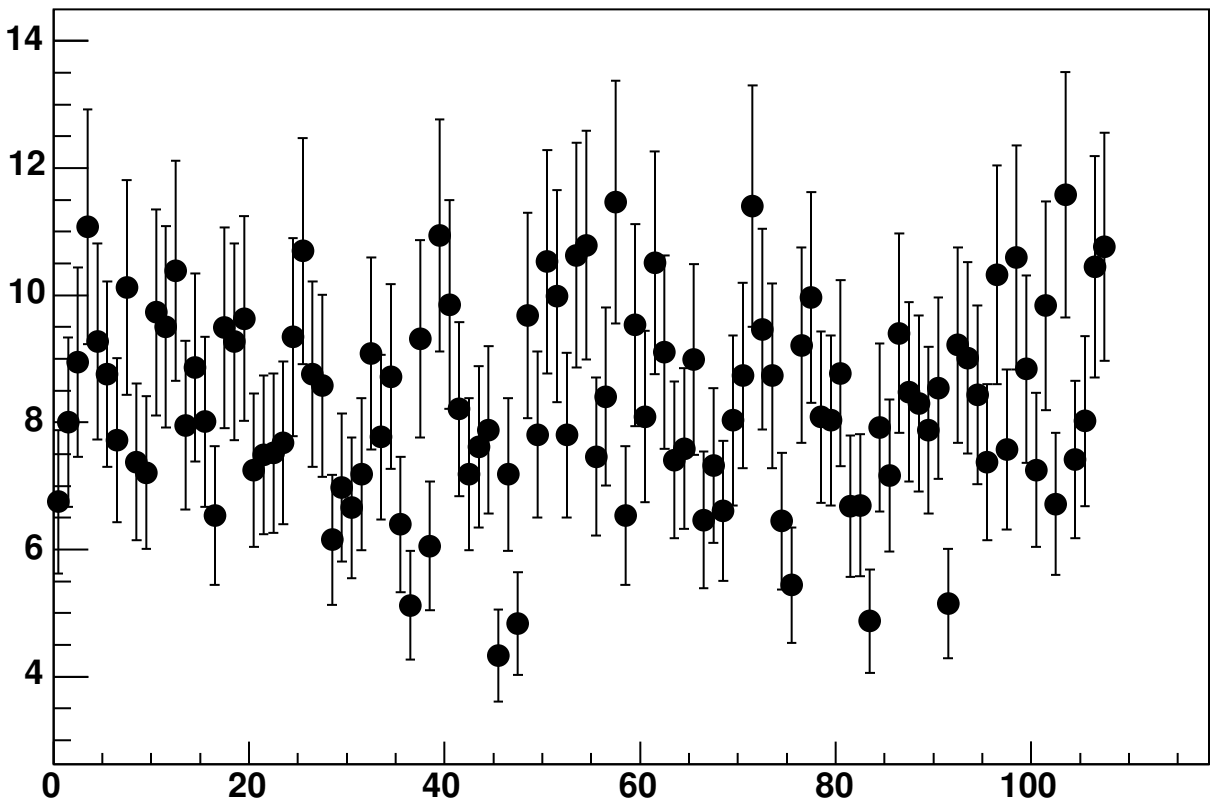
Enable 0, Hold=35, DAC=17200, ADC Noise vs 18*Chip+Chan



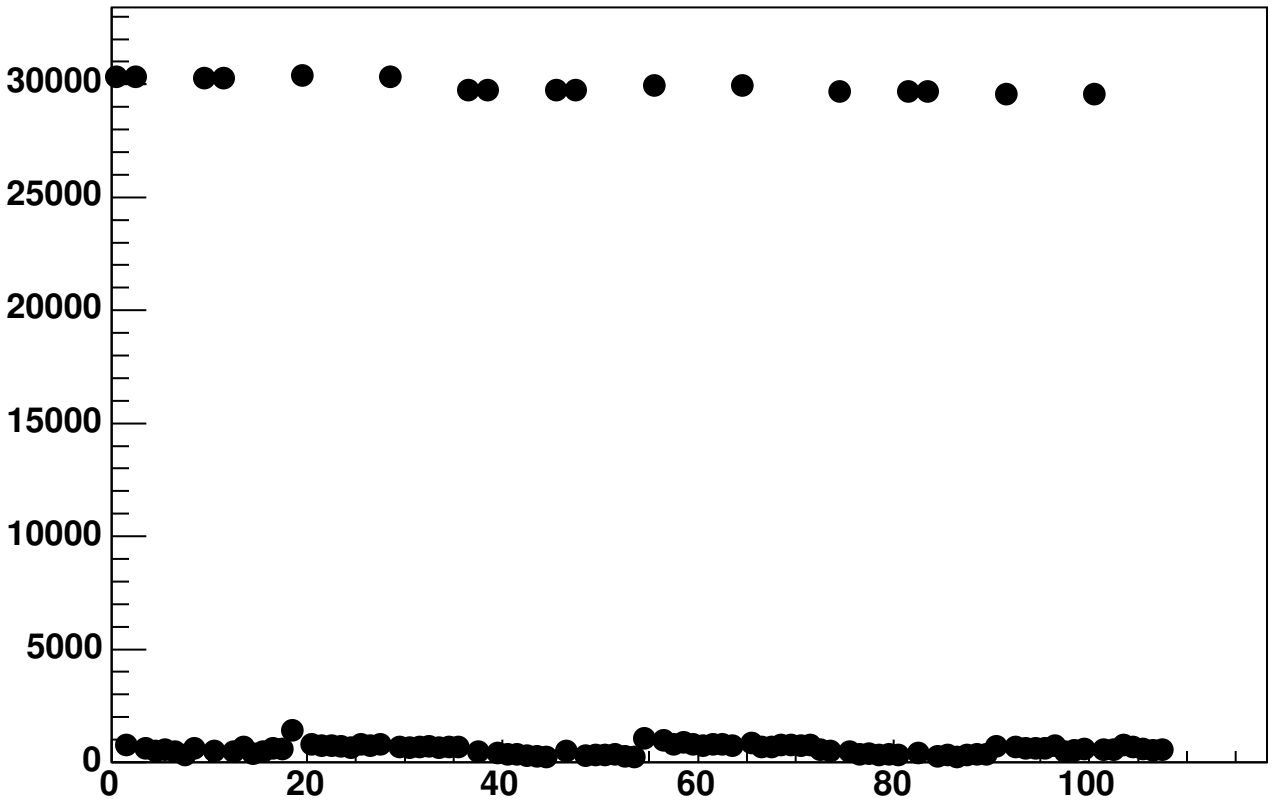
Enable 0, Hold=35, DAC=17600, ADC Mean vs 18*Chip+Chan



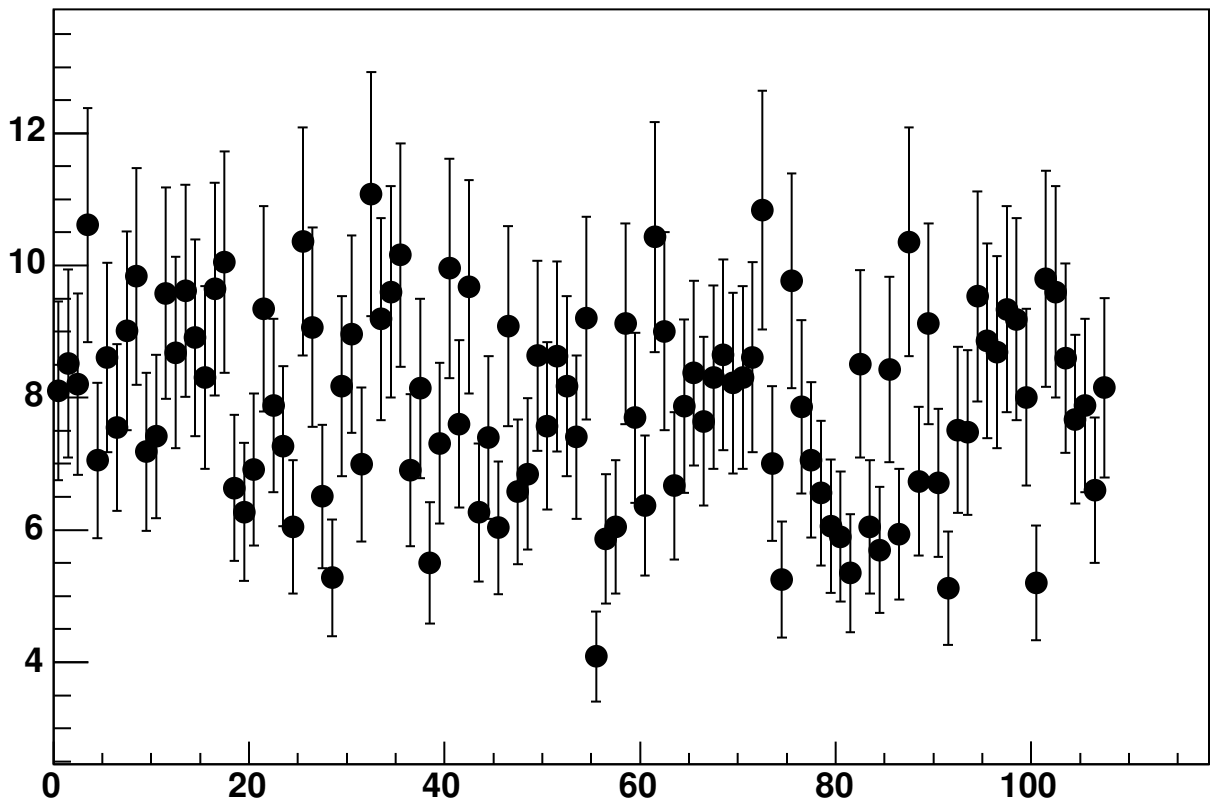
Enable 0, Hold=35, DAC=17600, ADC Noise vs 18*Chip+Chan



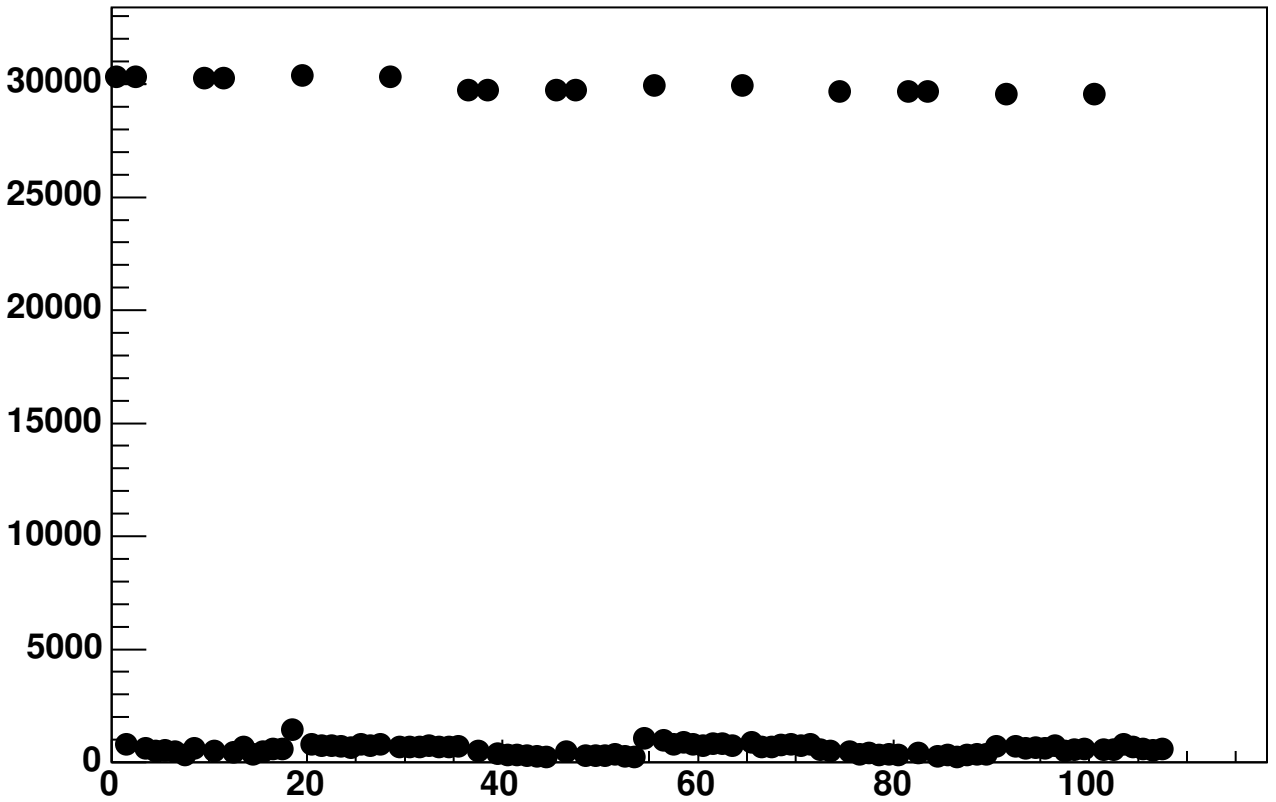
Enable 0, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



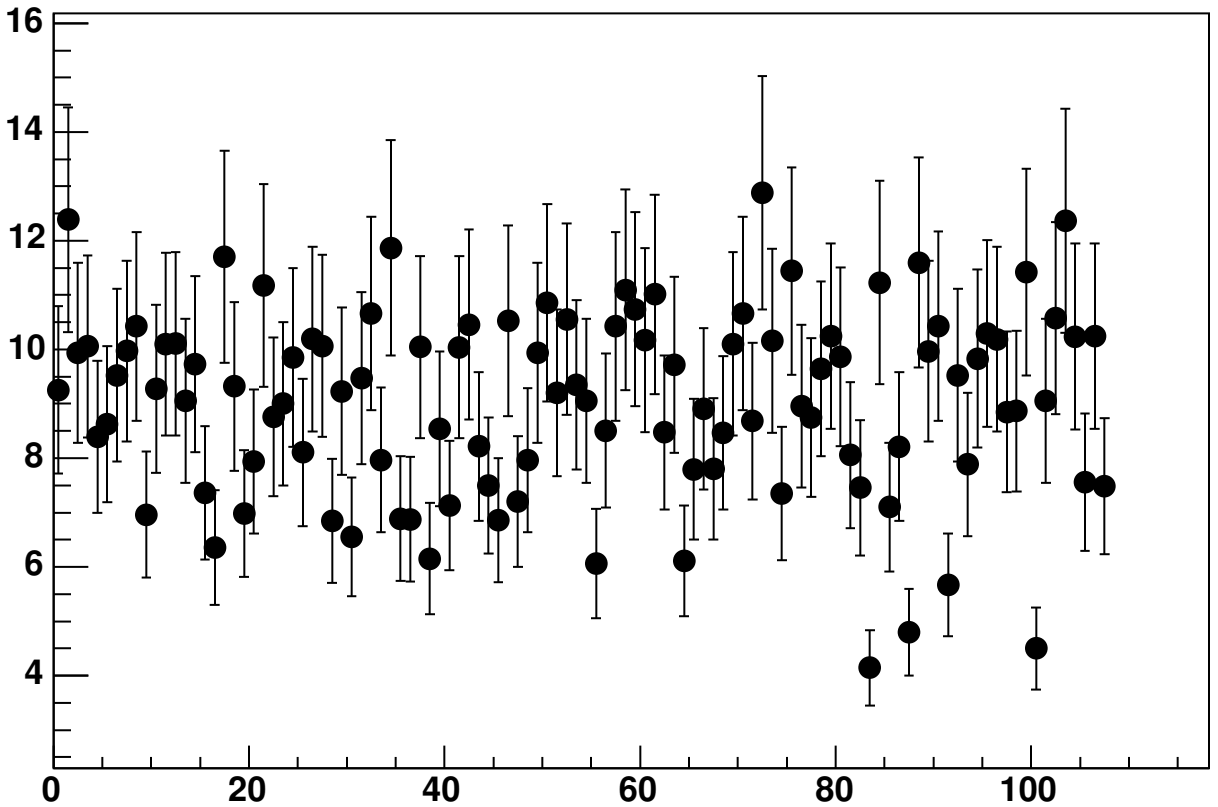
Enable 0, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



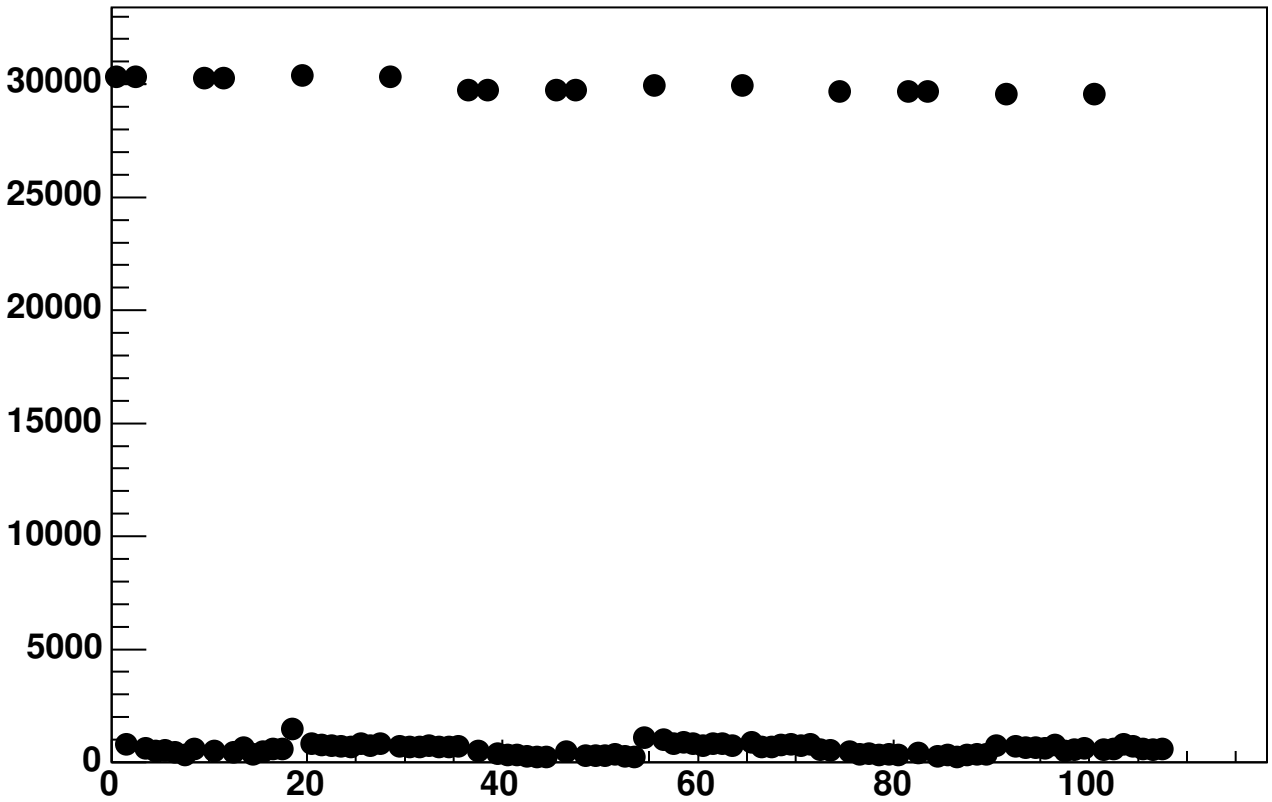
Enable 0, Hold=35, DAC=18400, ADC Mean vs 18*Chip+Chan



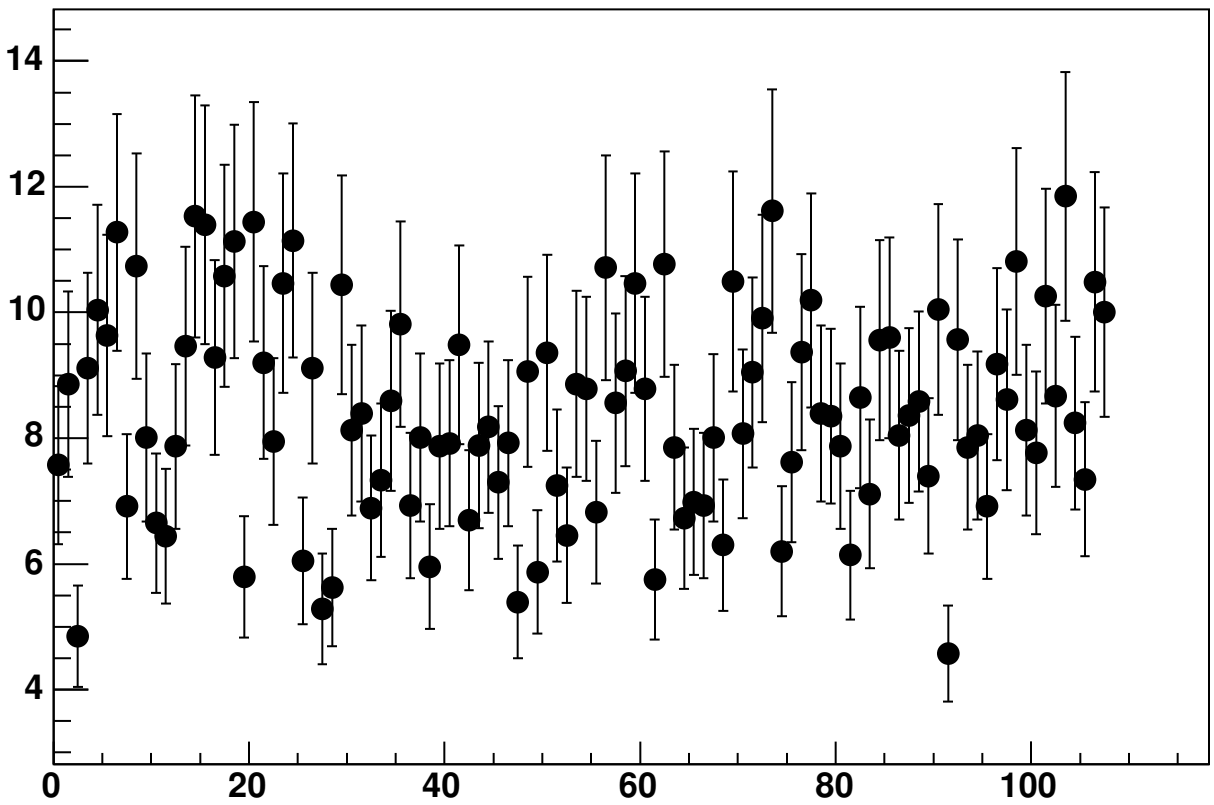
Enable 0, Hold=35, DAC=18400, ADC Noise vs 18*Chip+Chan



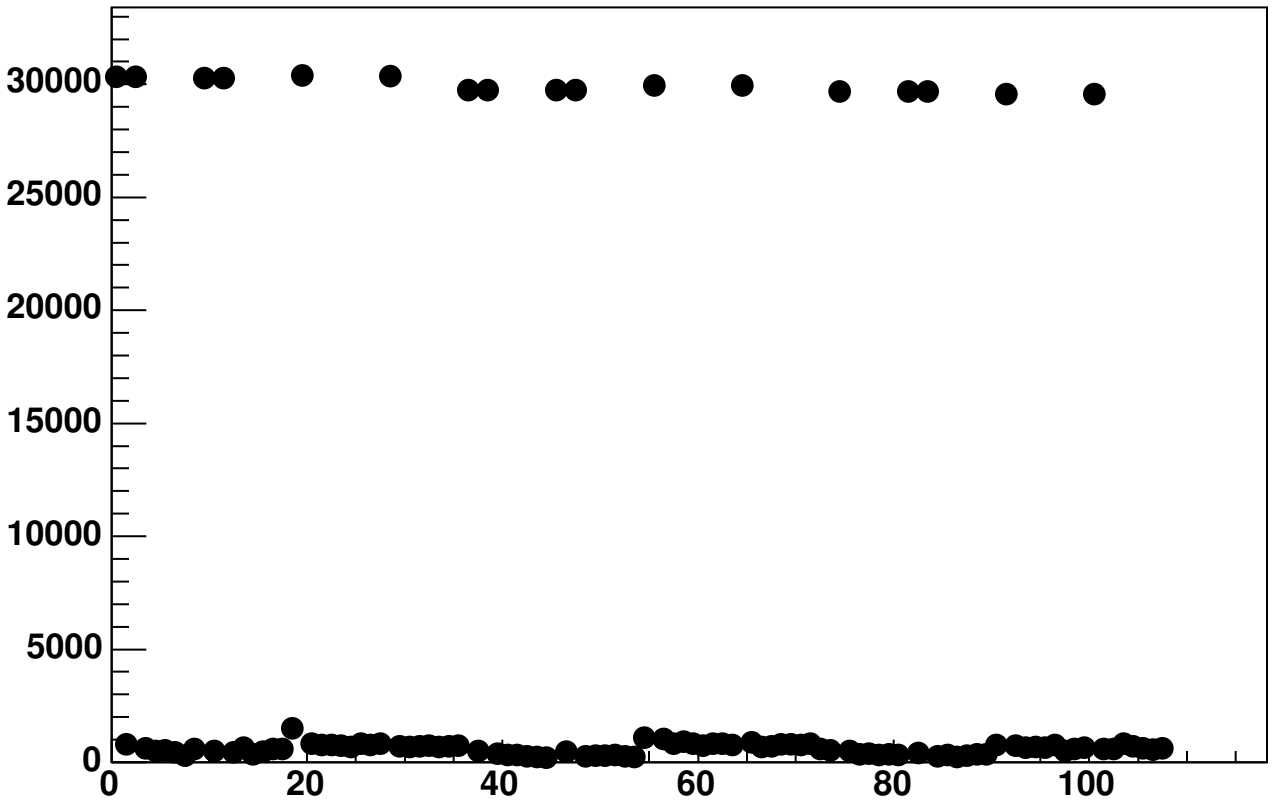
Enable 0, Hold=35, DAC=18800, ADC Mean vs 18*Chip+Chan



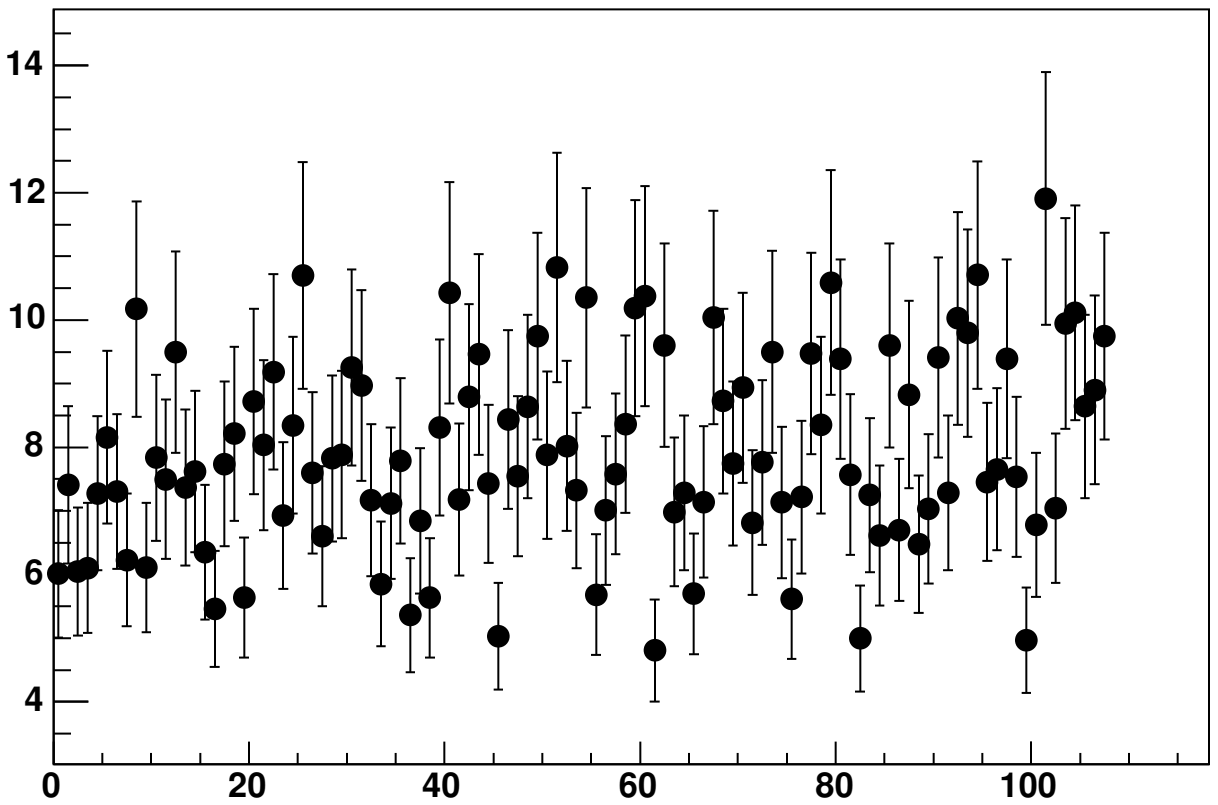
Enable 0, Hold=35, DAC=18800, ADC Noise vs 18*Chip+Chan



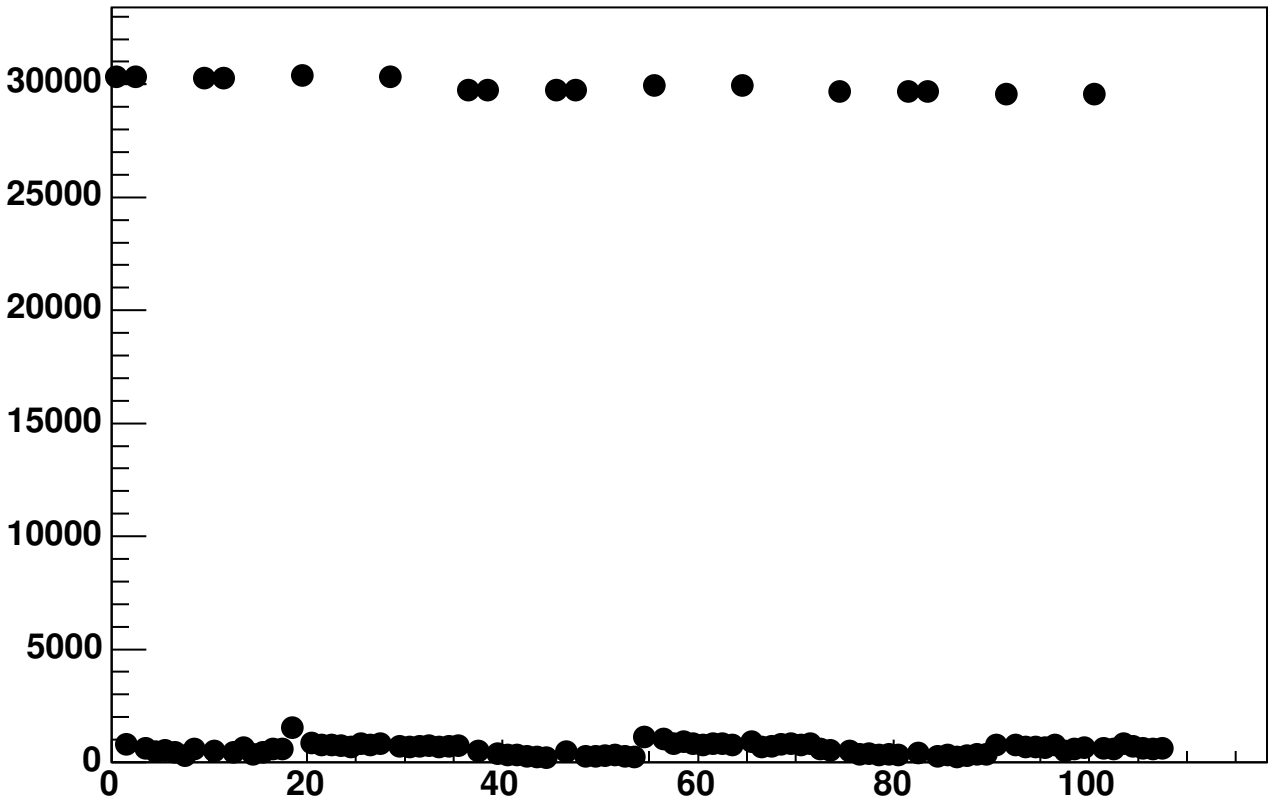
Enable 0, Hold=35, DAC=19200, ADC Mean vs 18*Chip+Chan



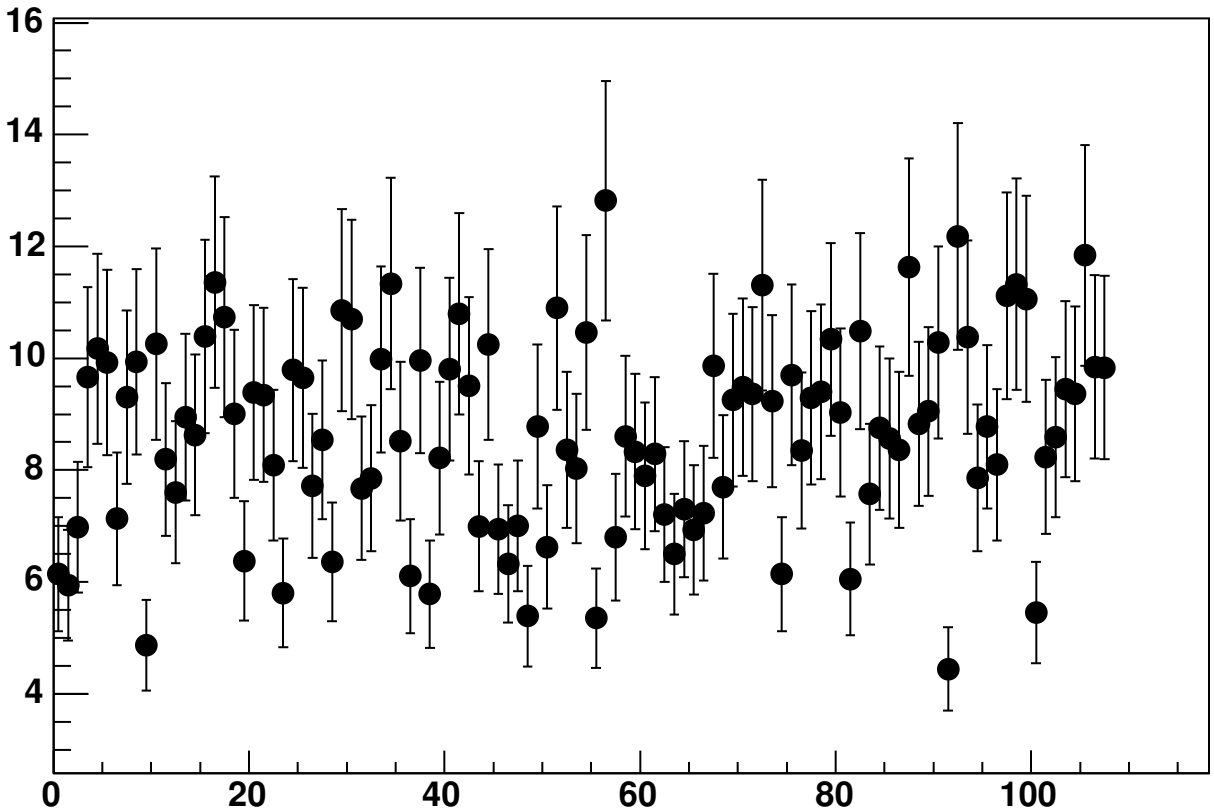
Enable 0, Hold=35, DAC=19200, ADC Noise vs 18*Chip+Chan



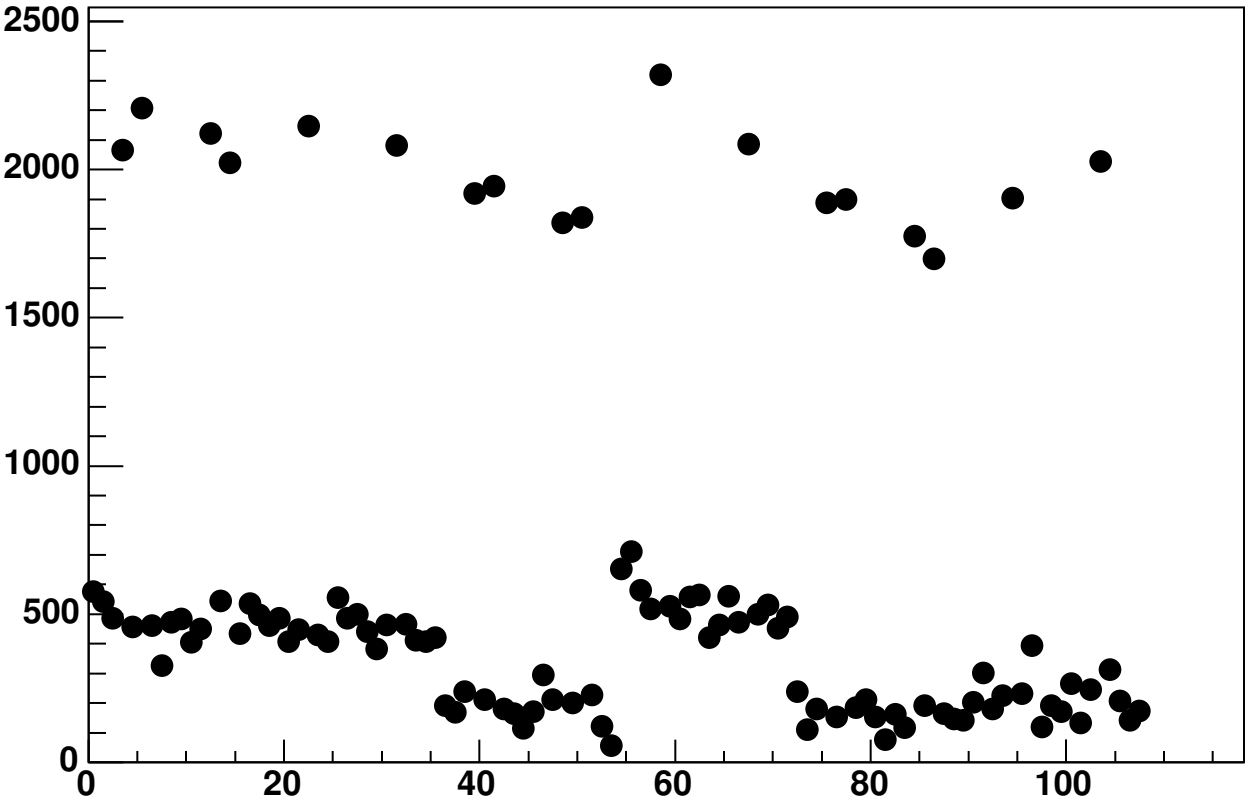
Enable 0, Hold=35, DAC=19600, ADC Mean vs 18*Chip+Chan



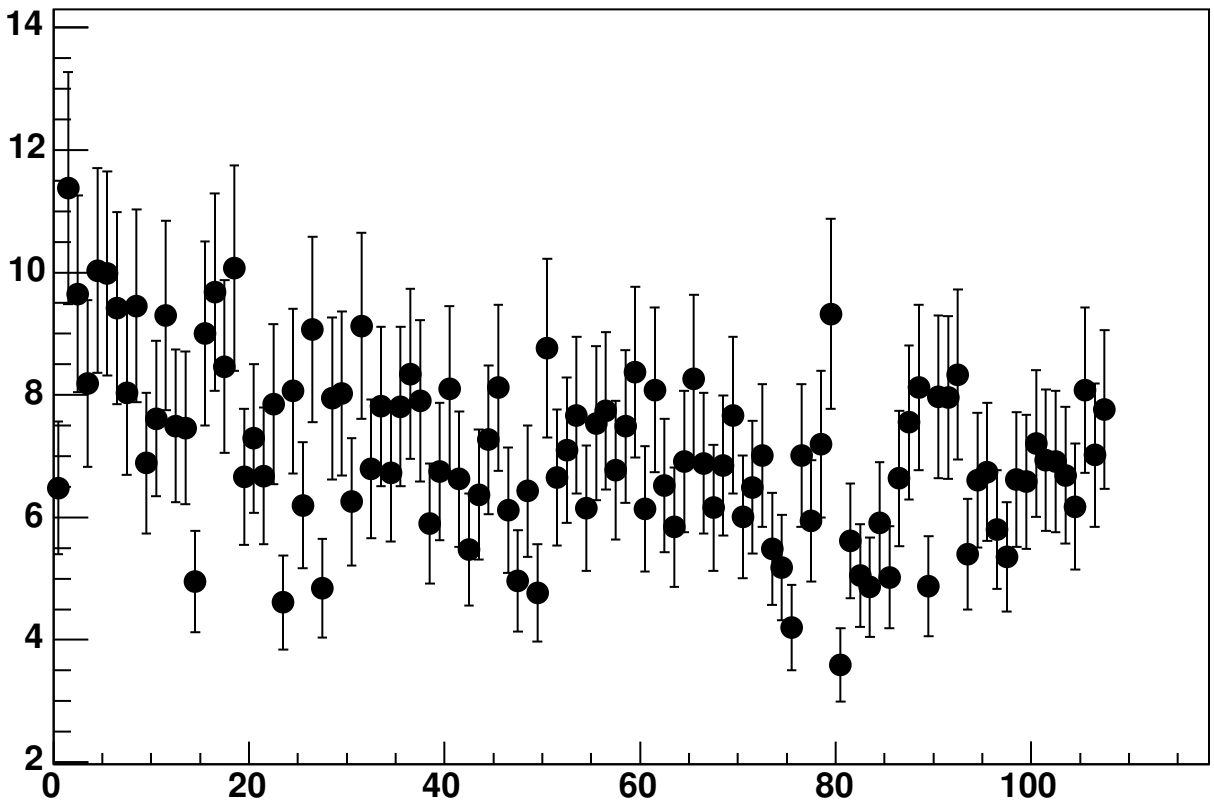
Enable 0, Hold=35, DAC=19600, ADC Noise vs 18*Chip+Chan



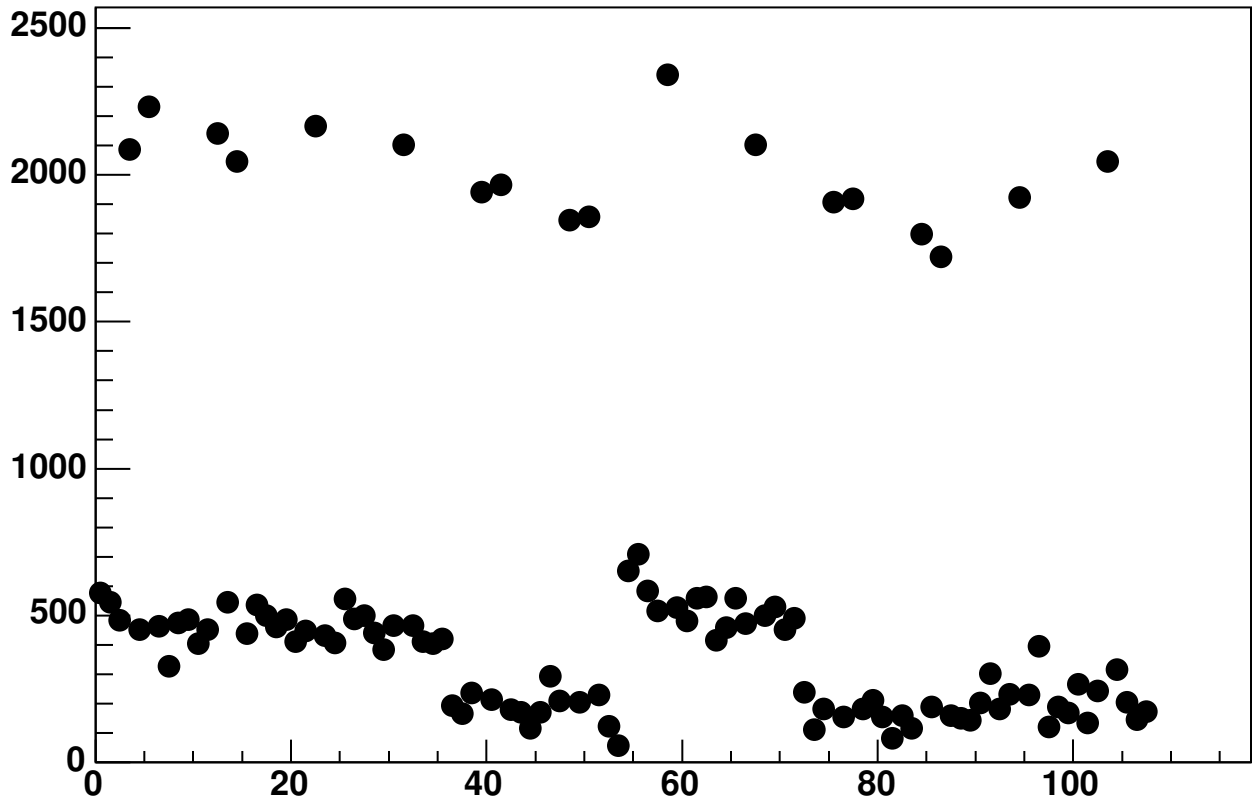
Enable 1, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



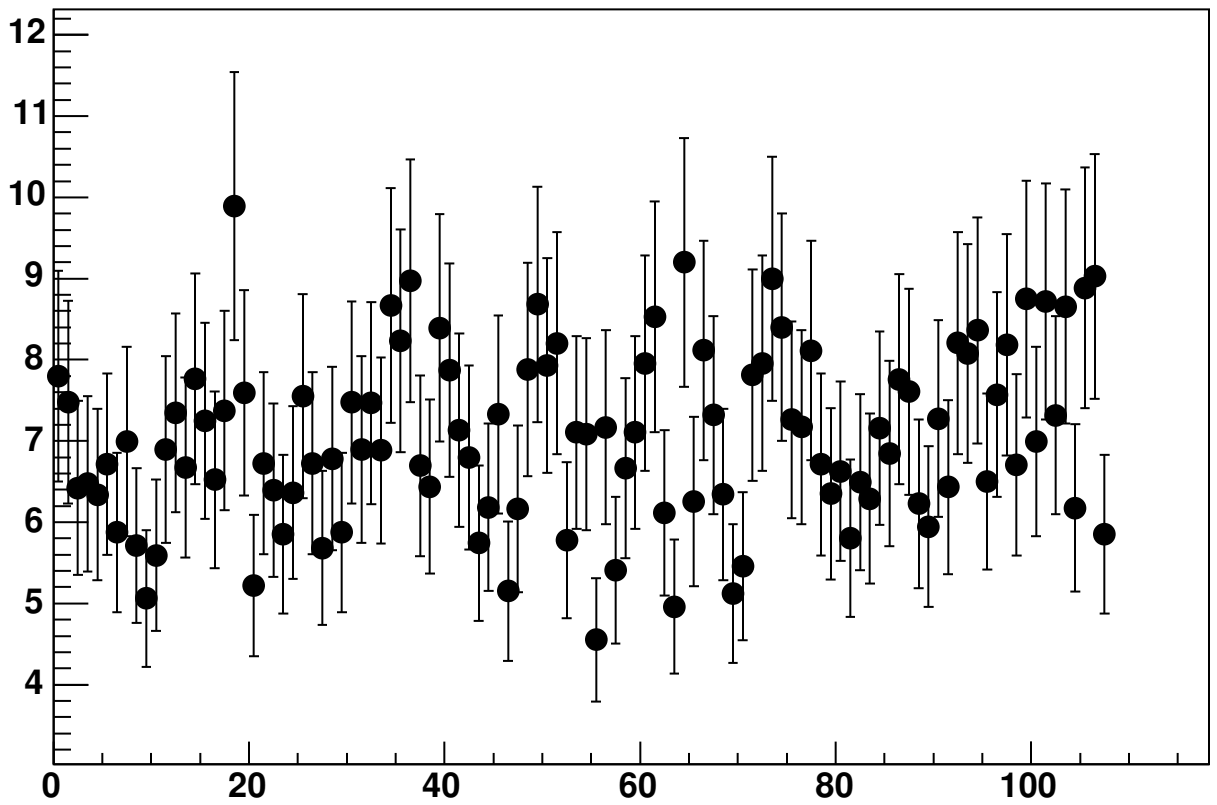
Enable 1, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



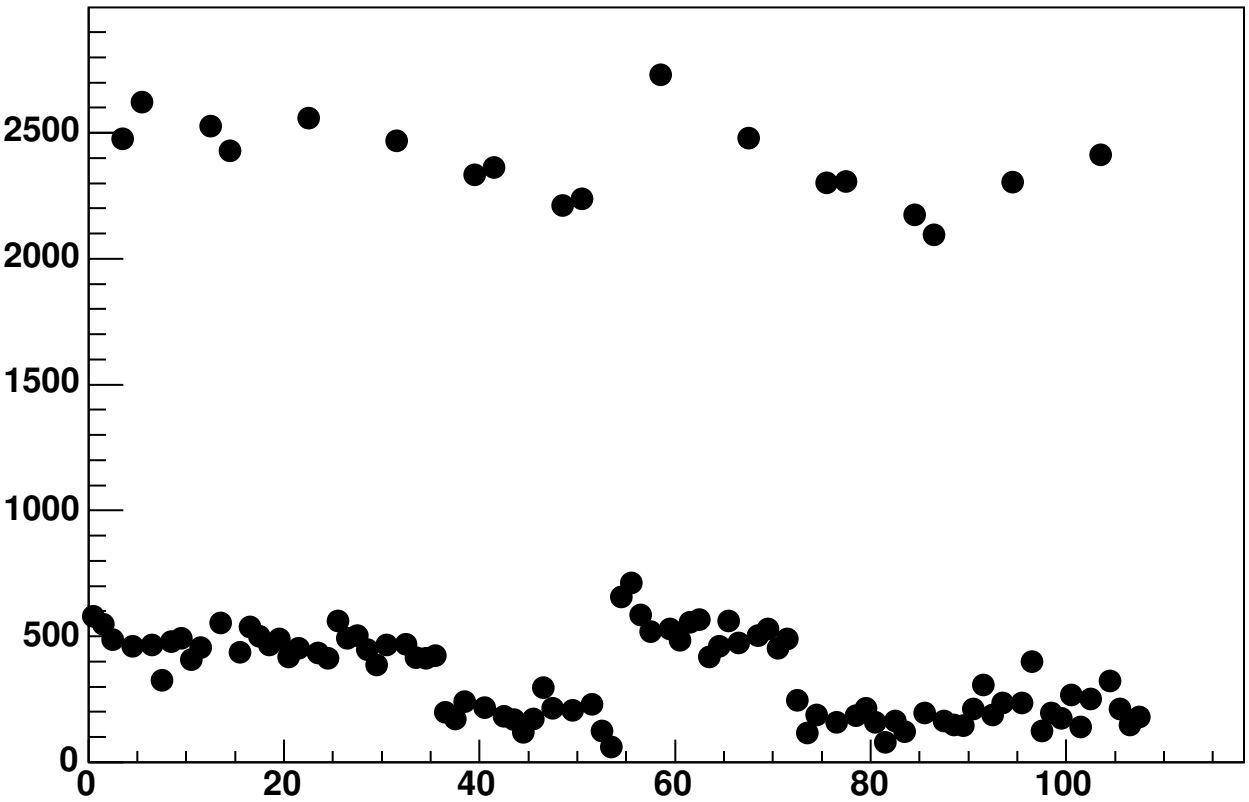
Enable 1, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



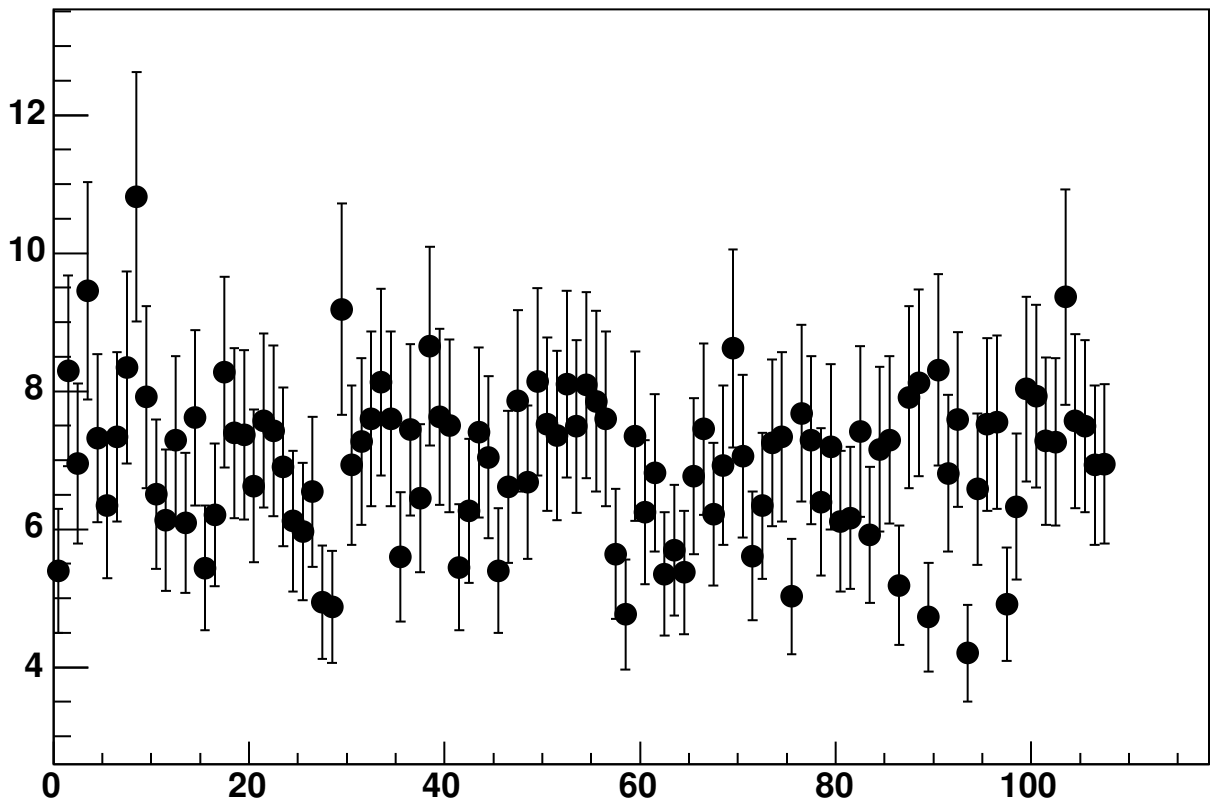
Enable 1, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



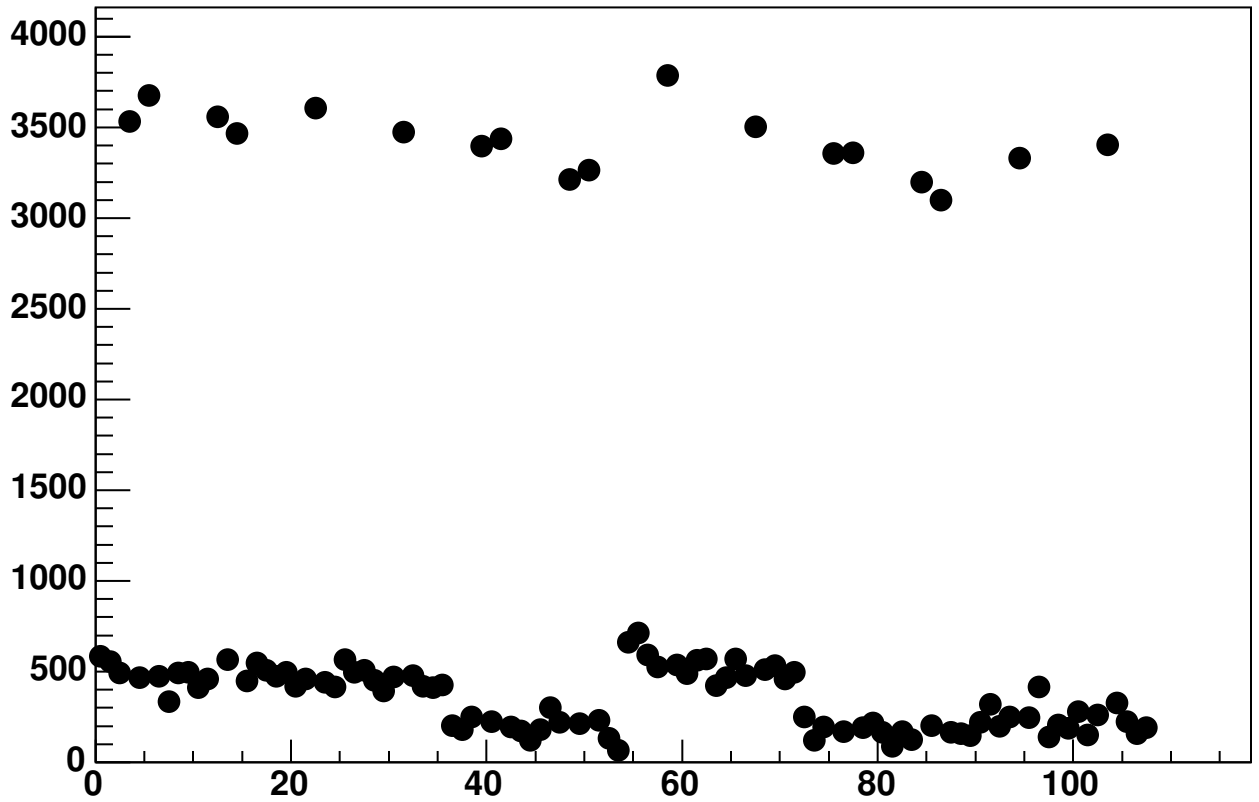
Enable 1, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



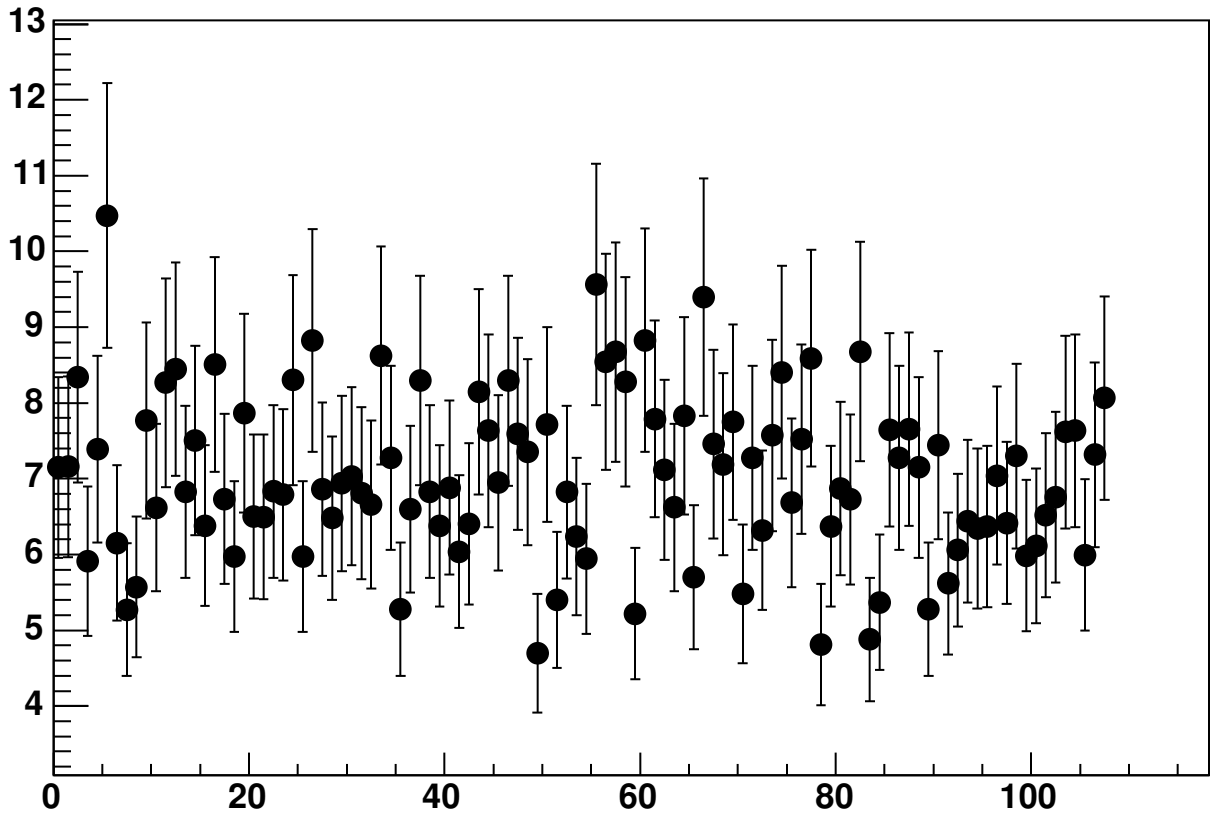
Enable 1, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



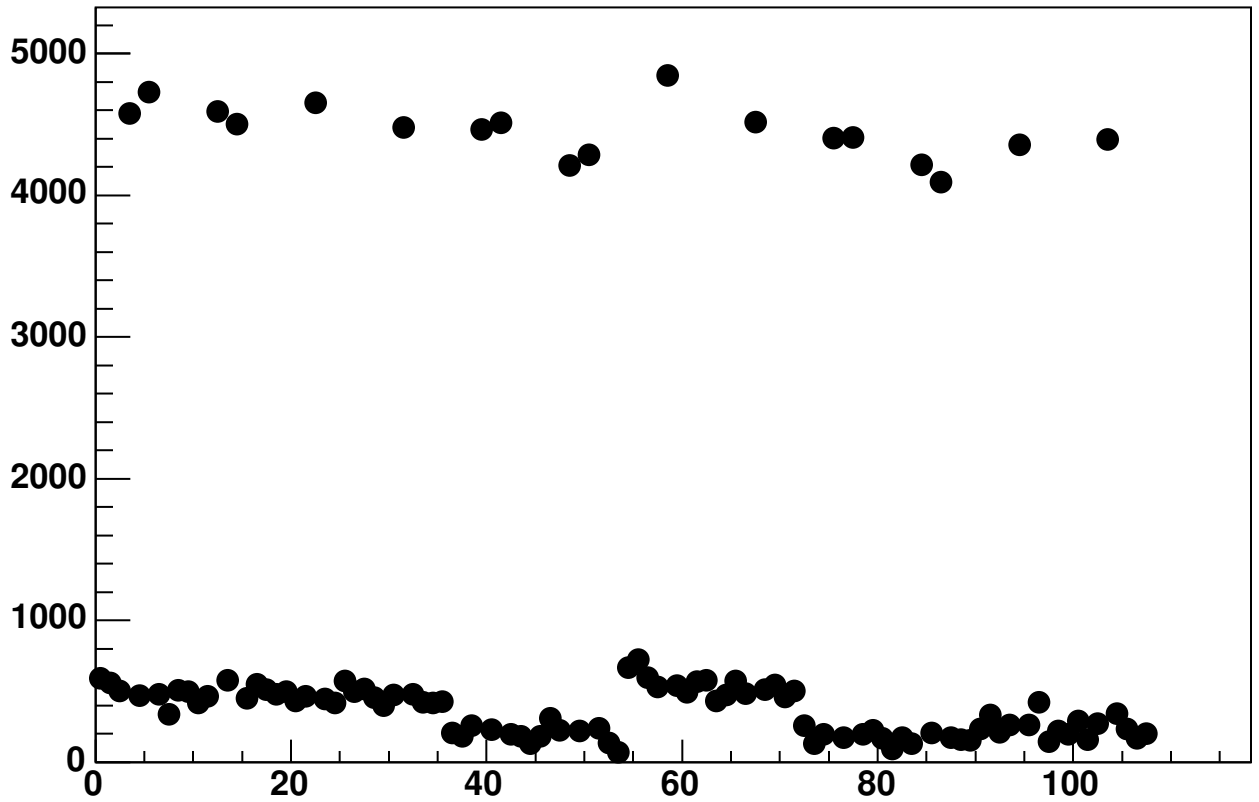
Enable 1, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



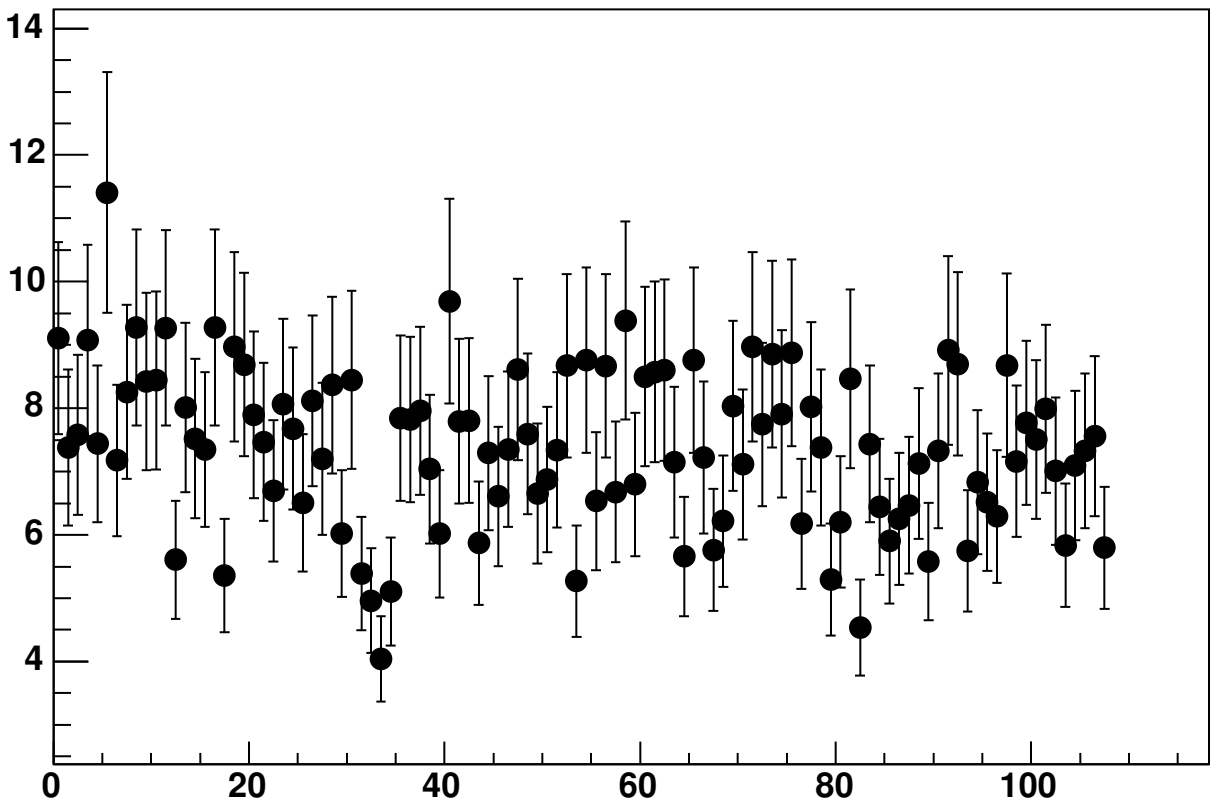
Enable 1, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



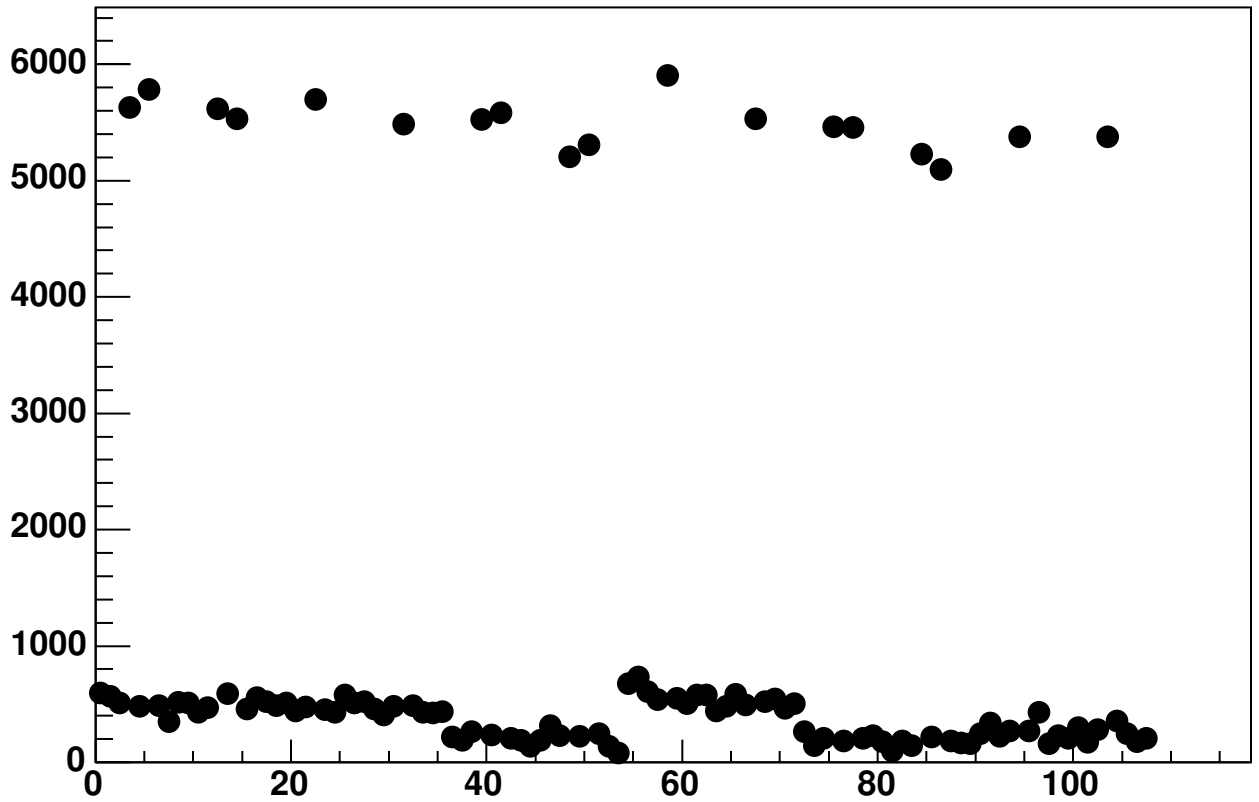
Enable 1, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



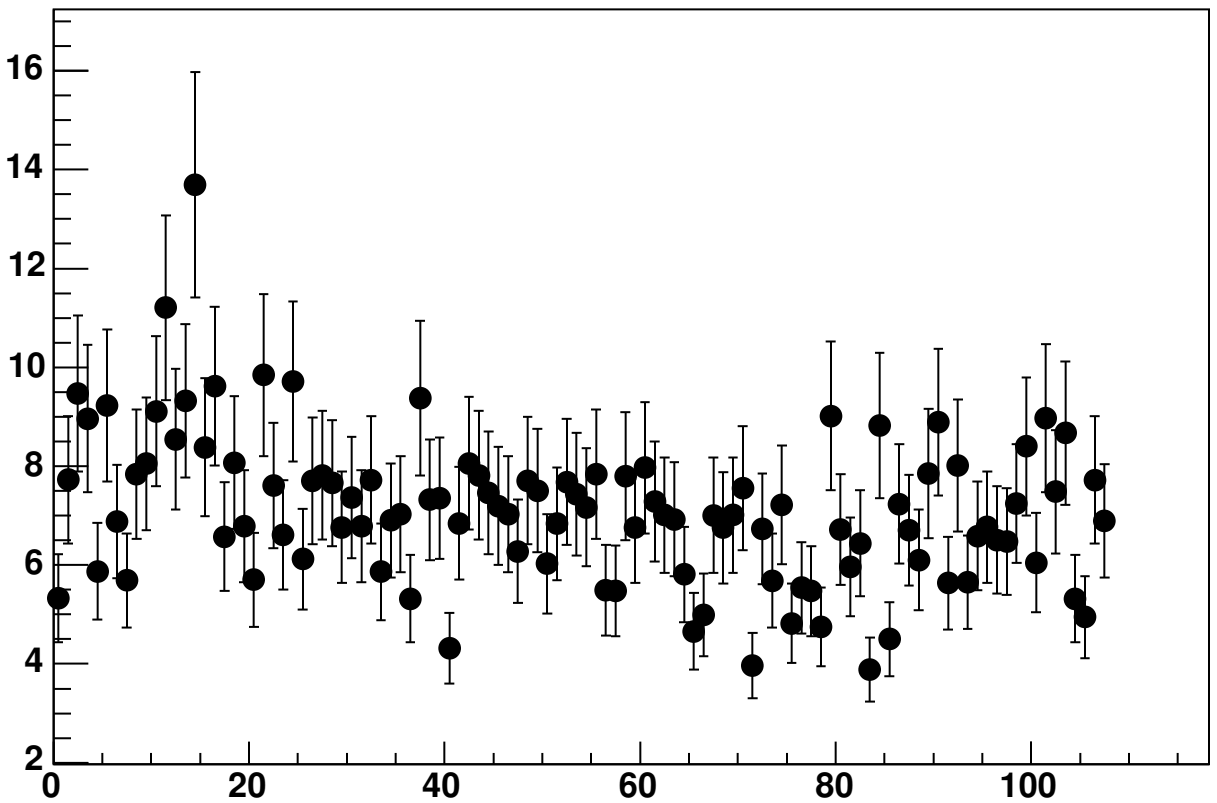
Enable 1, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



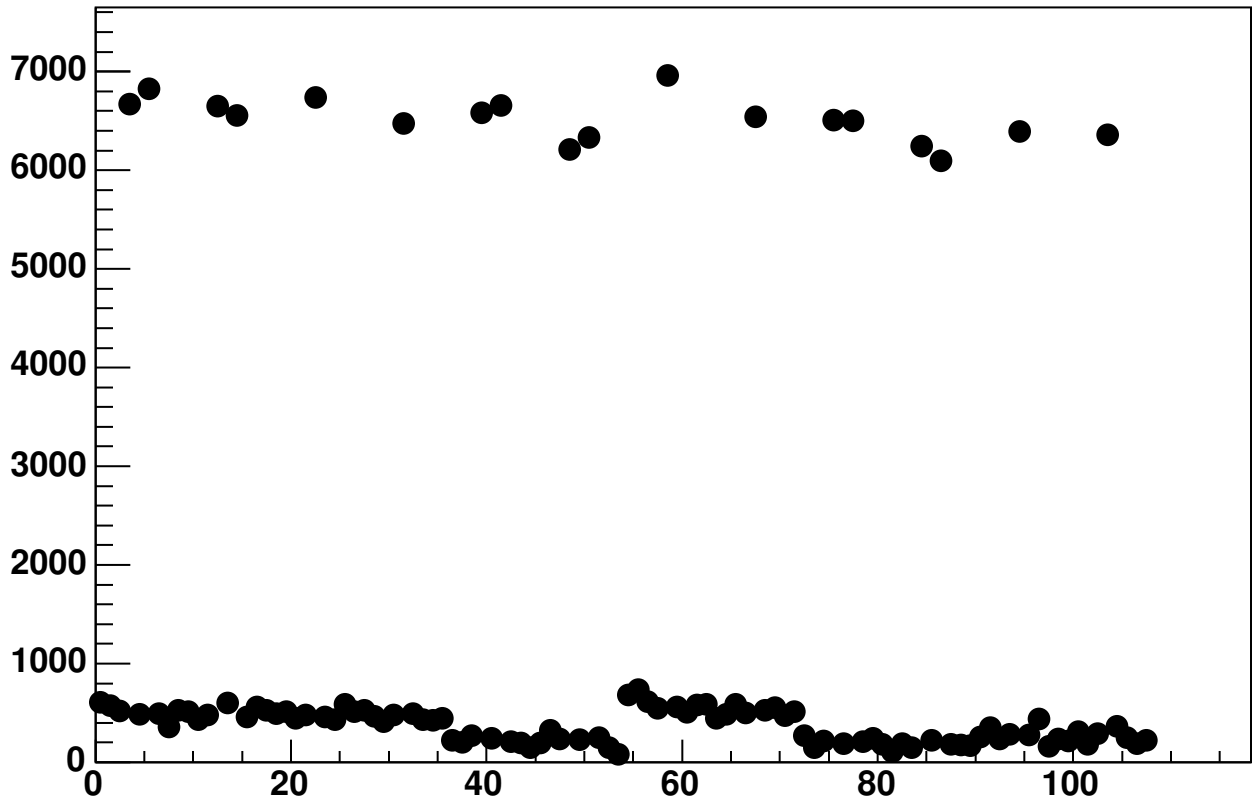
Enable 1, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



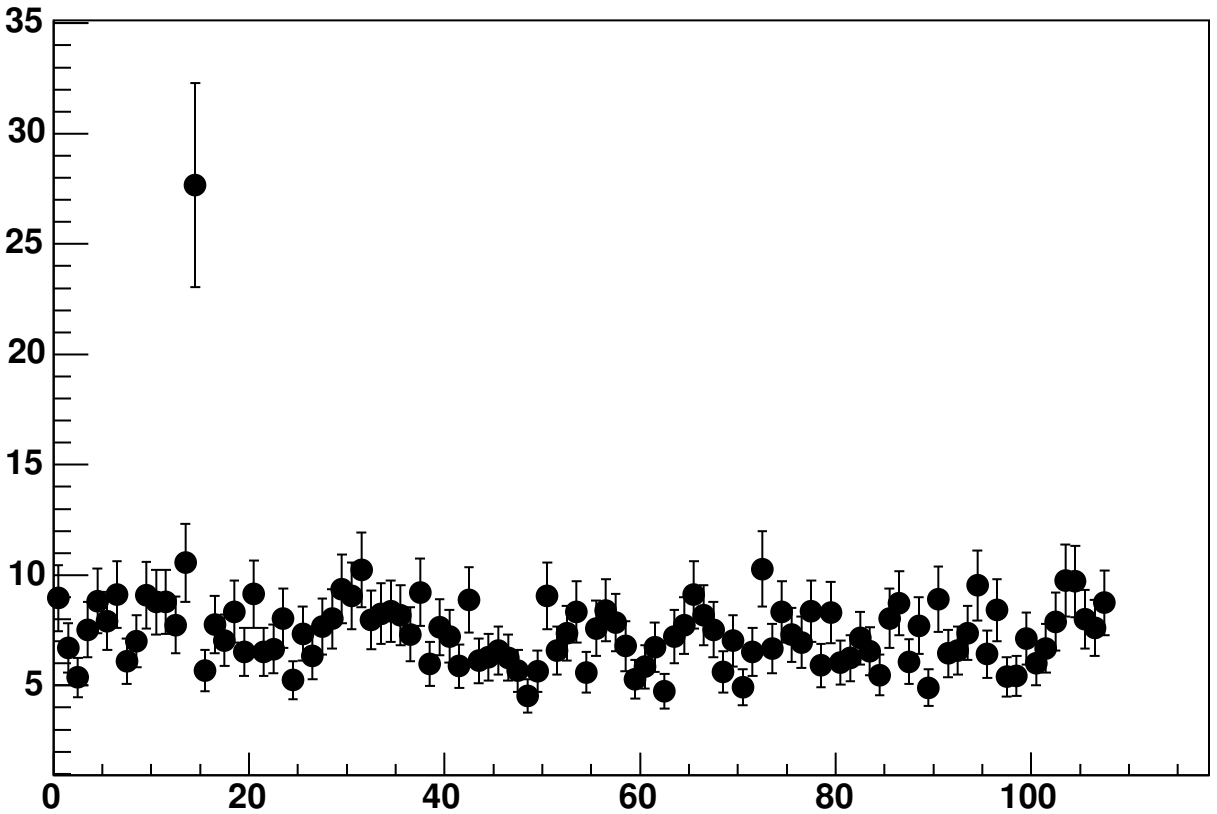
Enable 1, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



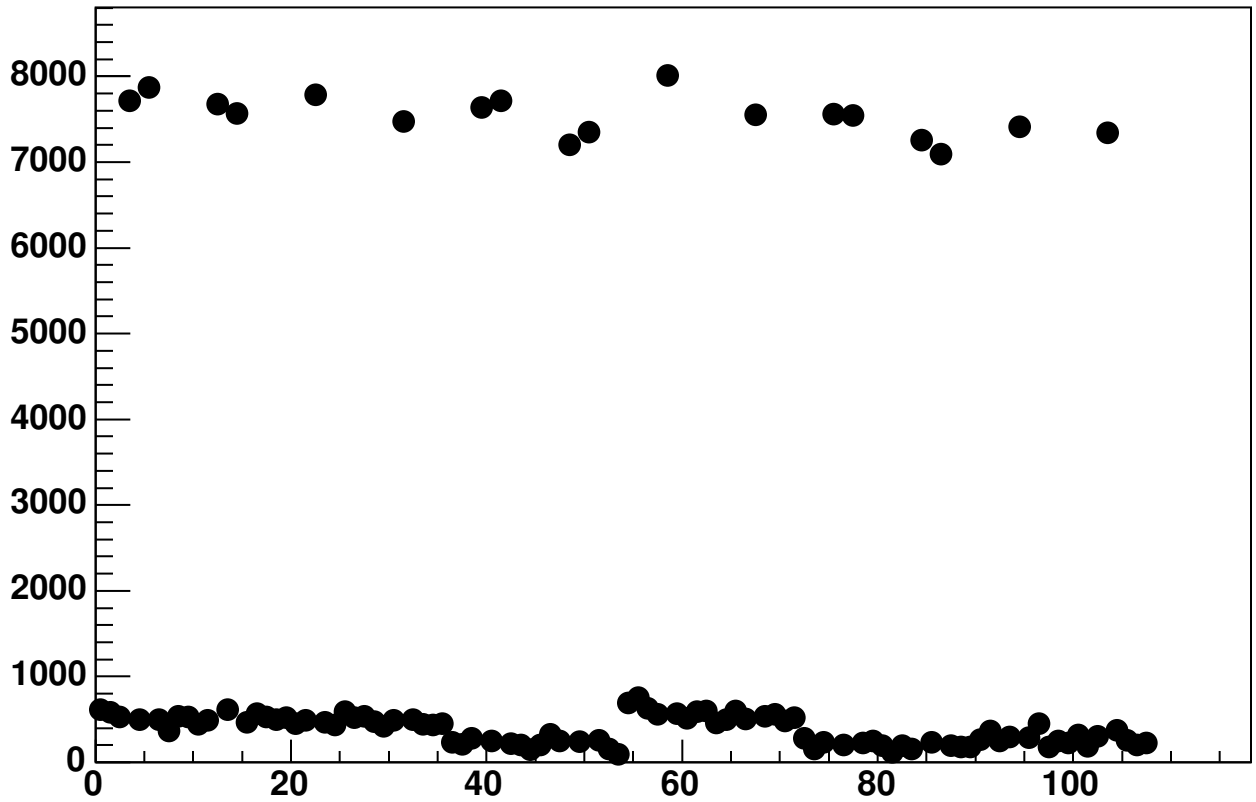
Enable 1, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



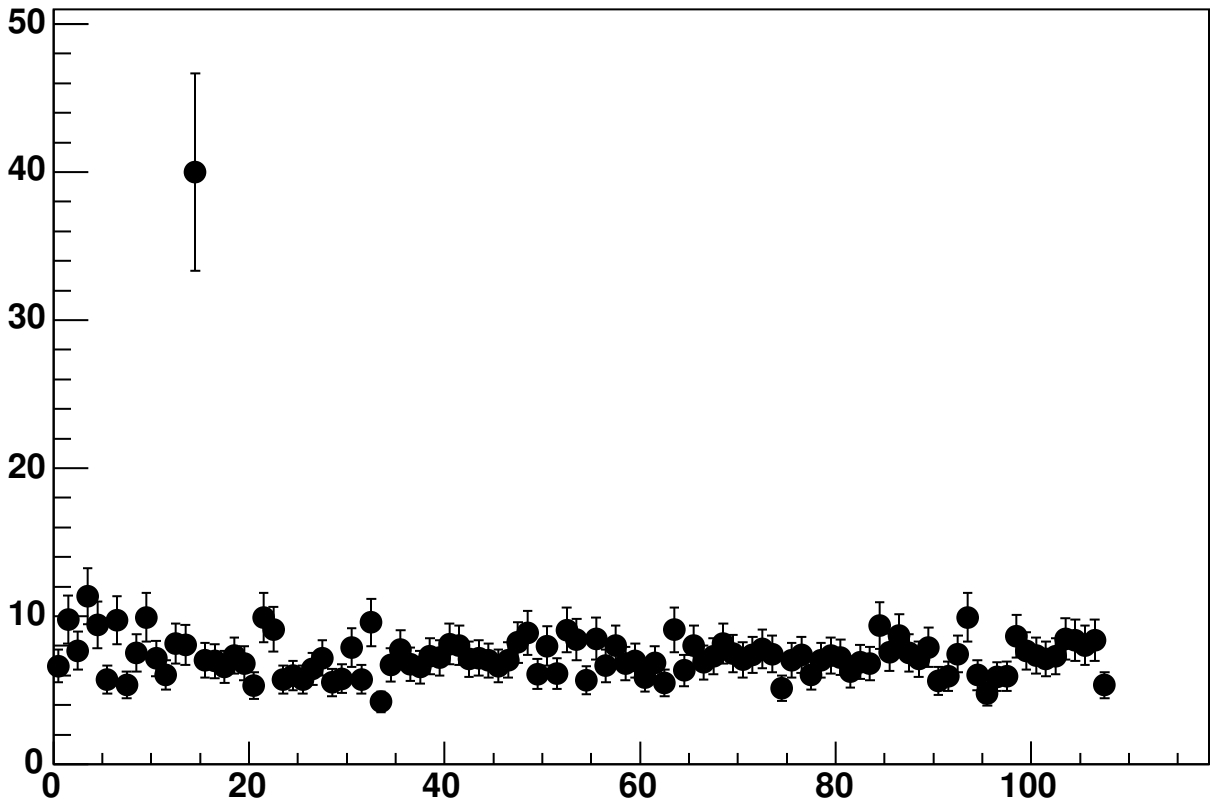
Enable 1, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



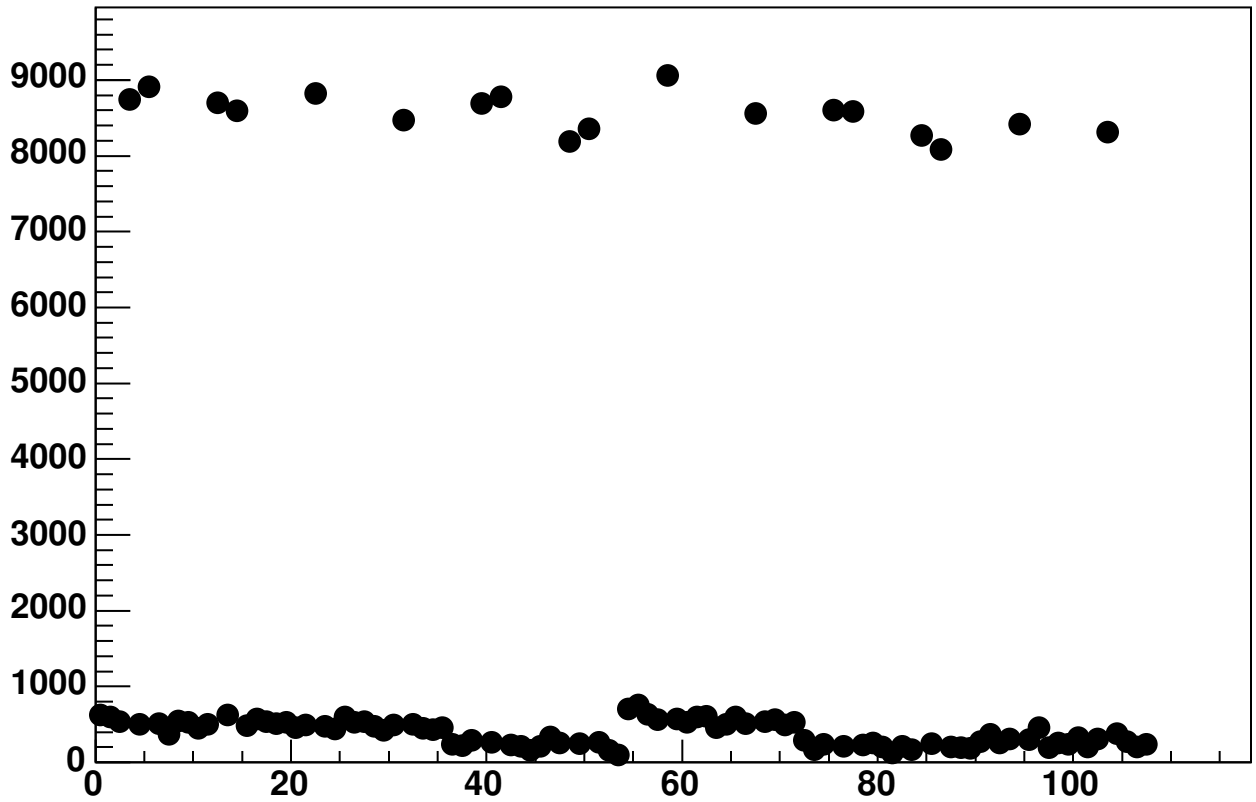
Enable 1, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



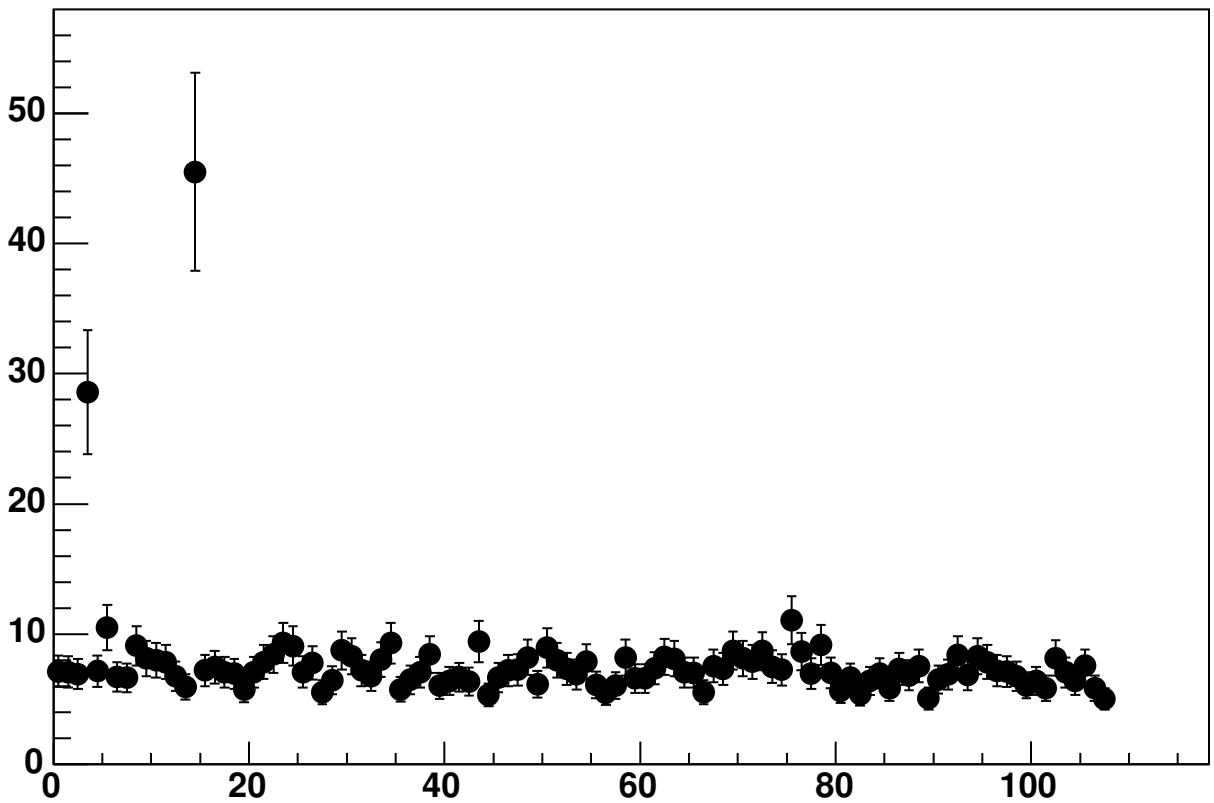
Enable 1, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



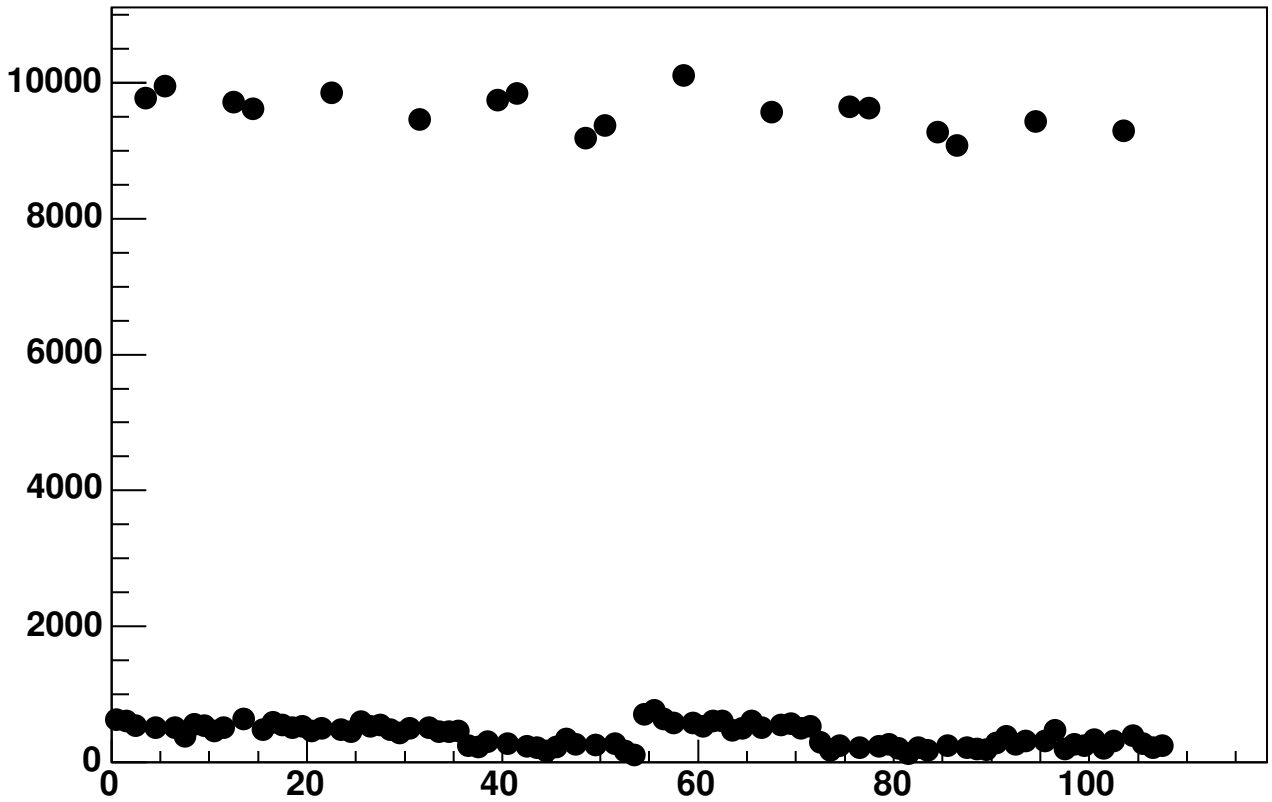
Enable 1, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



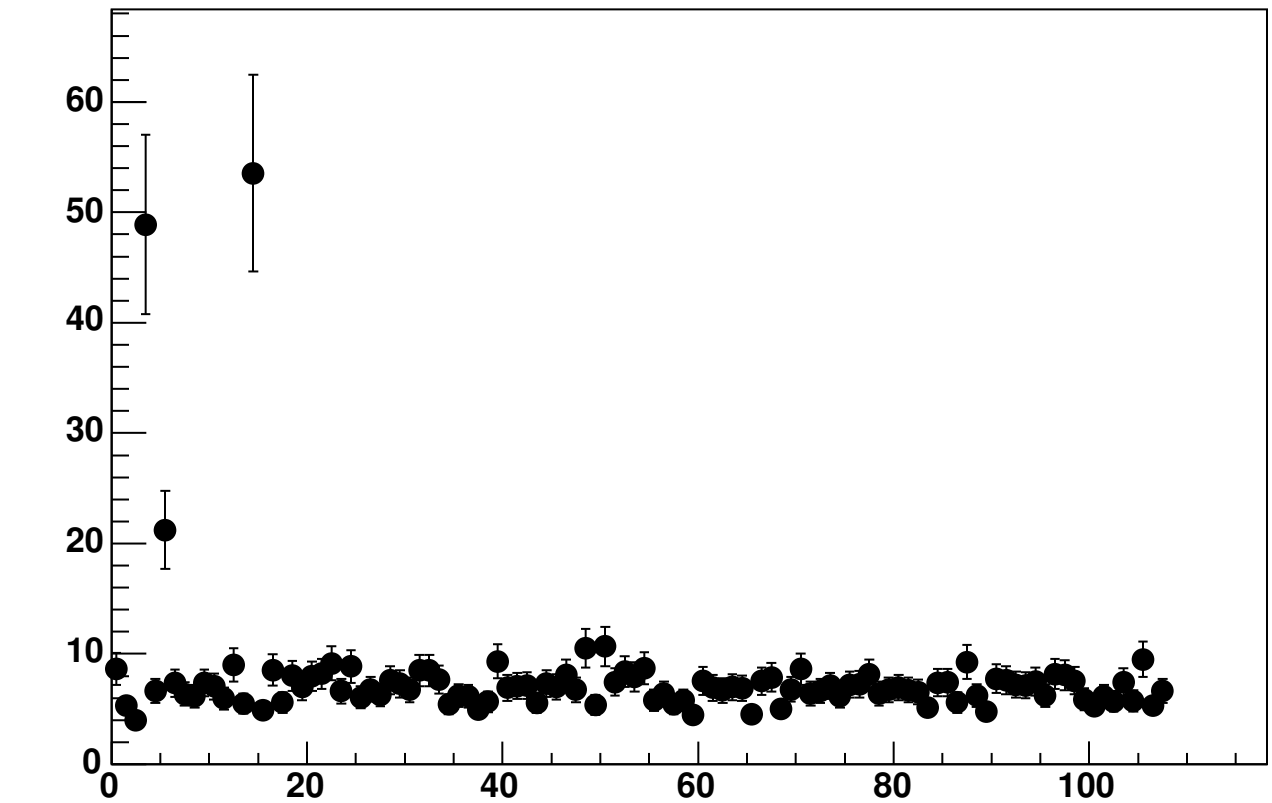
Enable 1, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



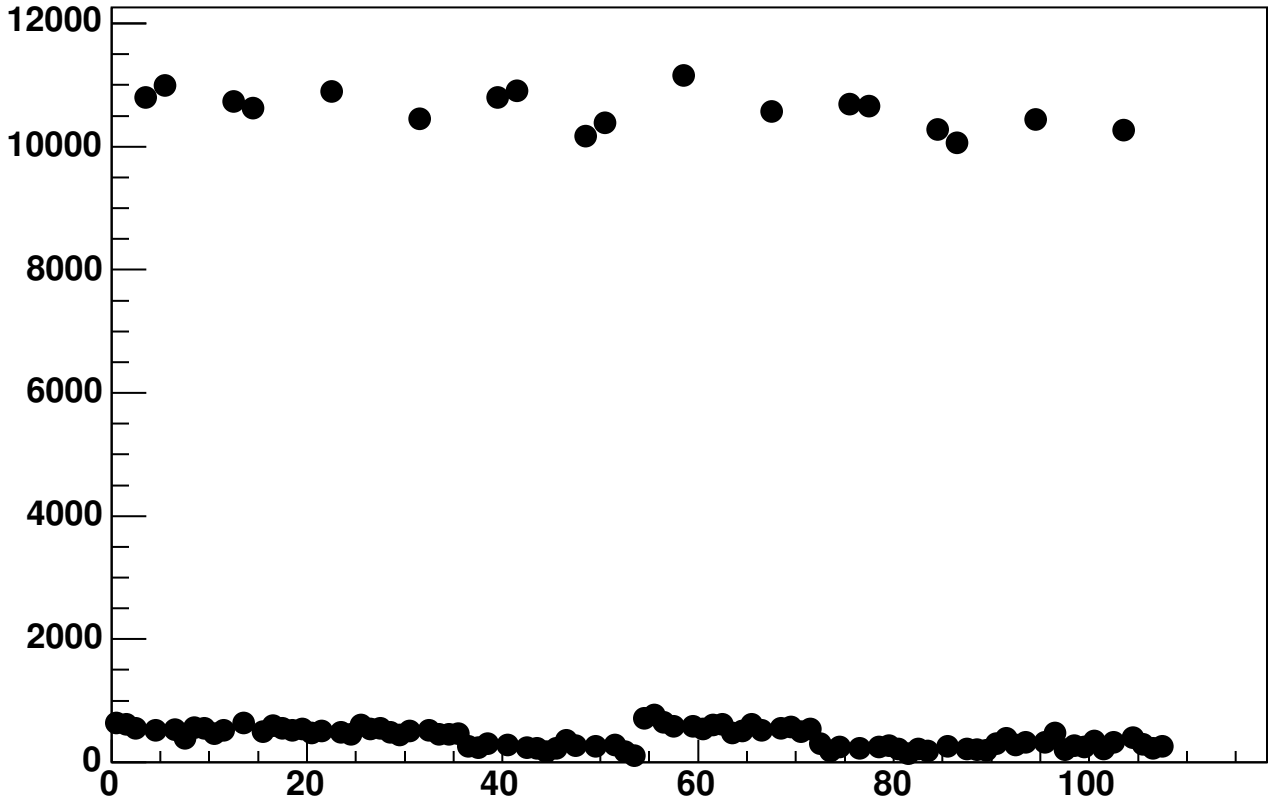
Enable 1, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



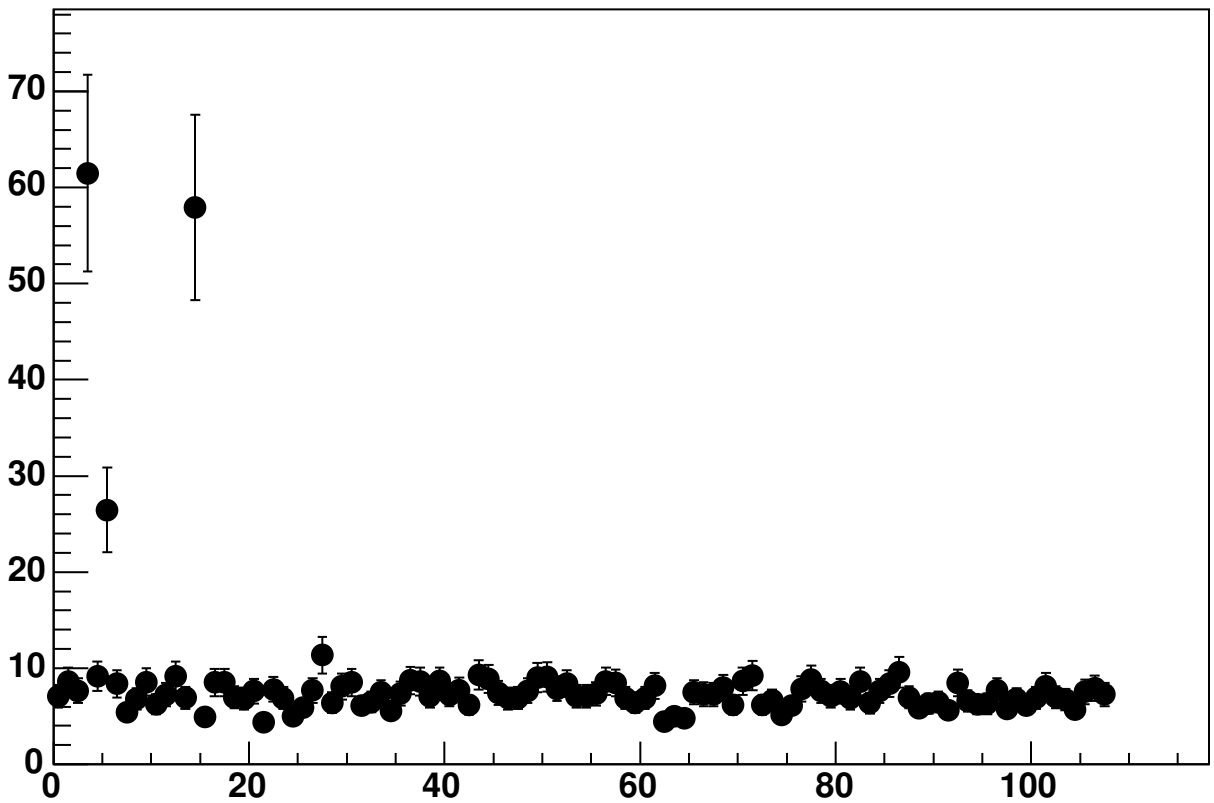
Enable 1, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



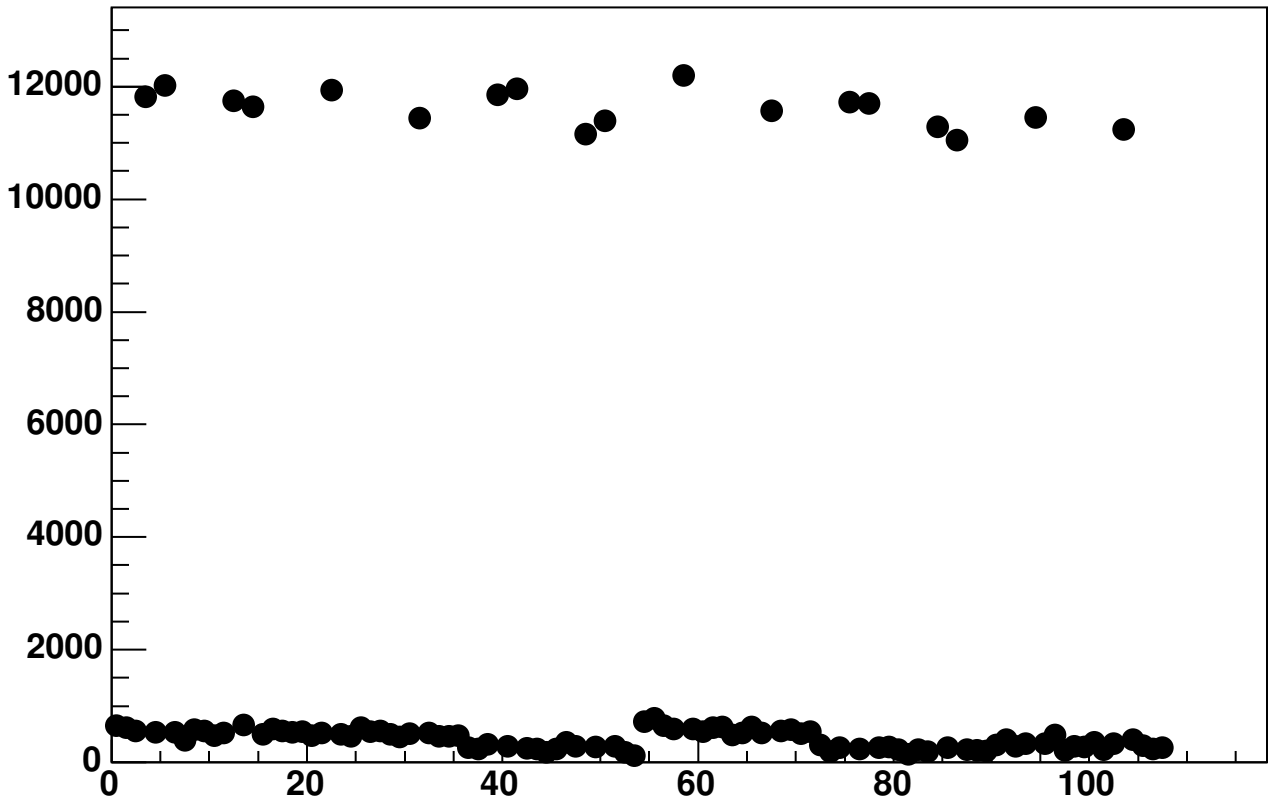
Enable 1, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



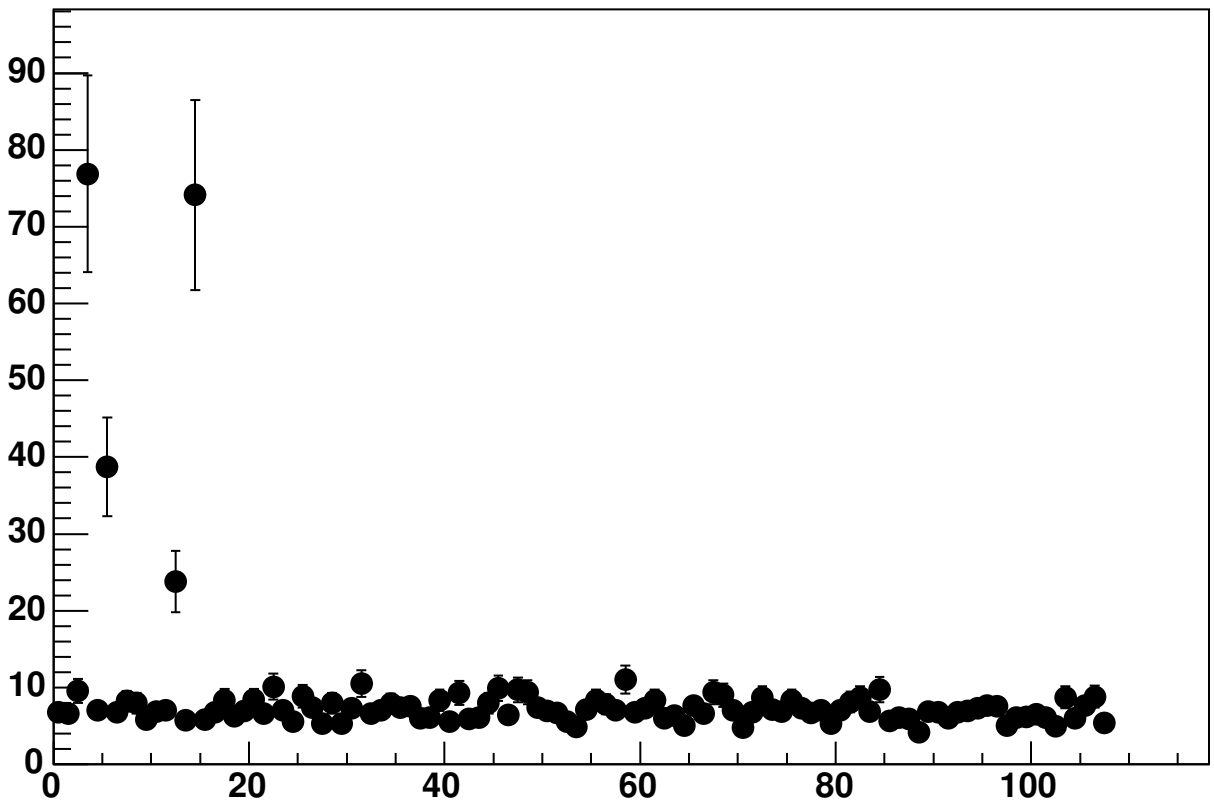
Enable 1, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



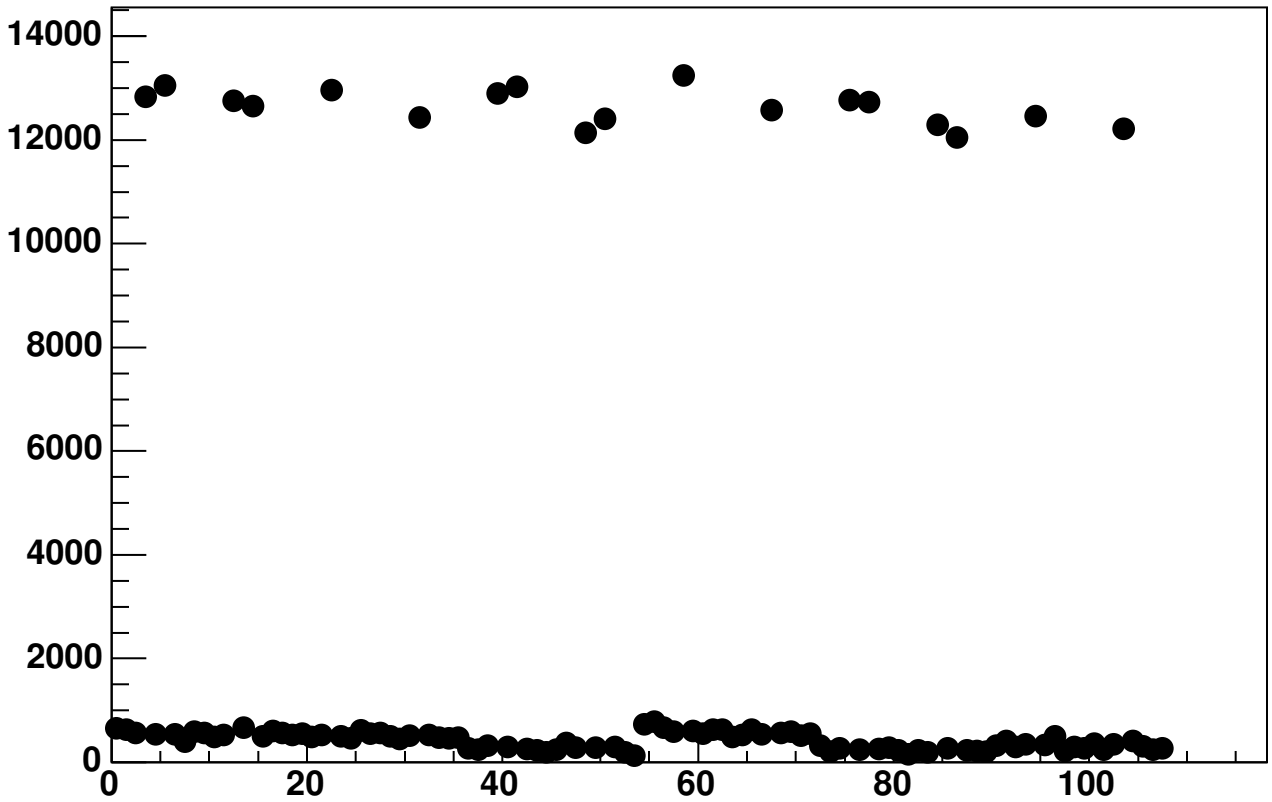
Enable 1, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



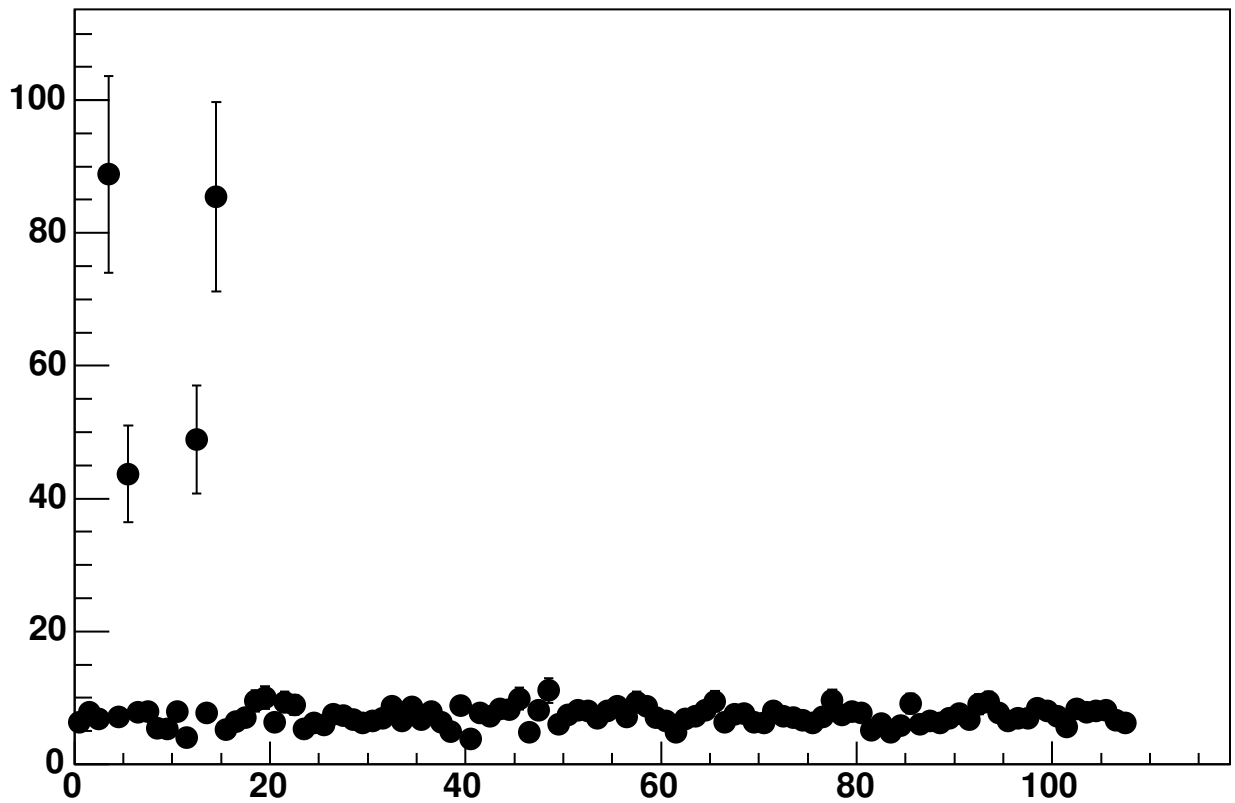
Enable 1, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



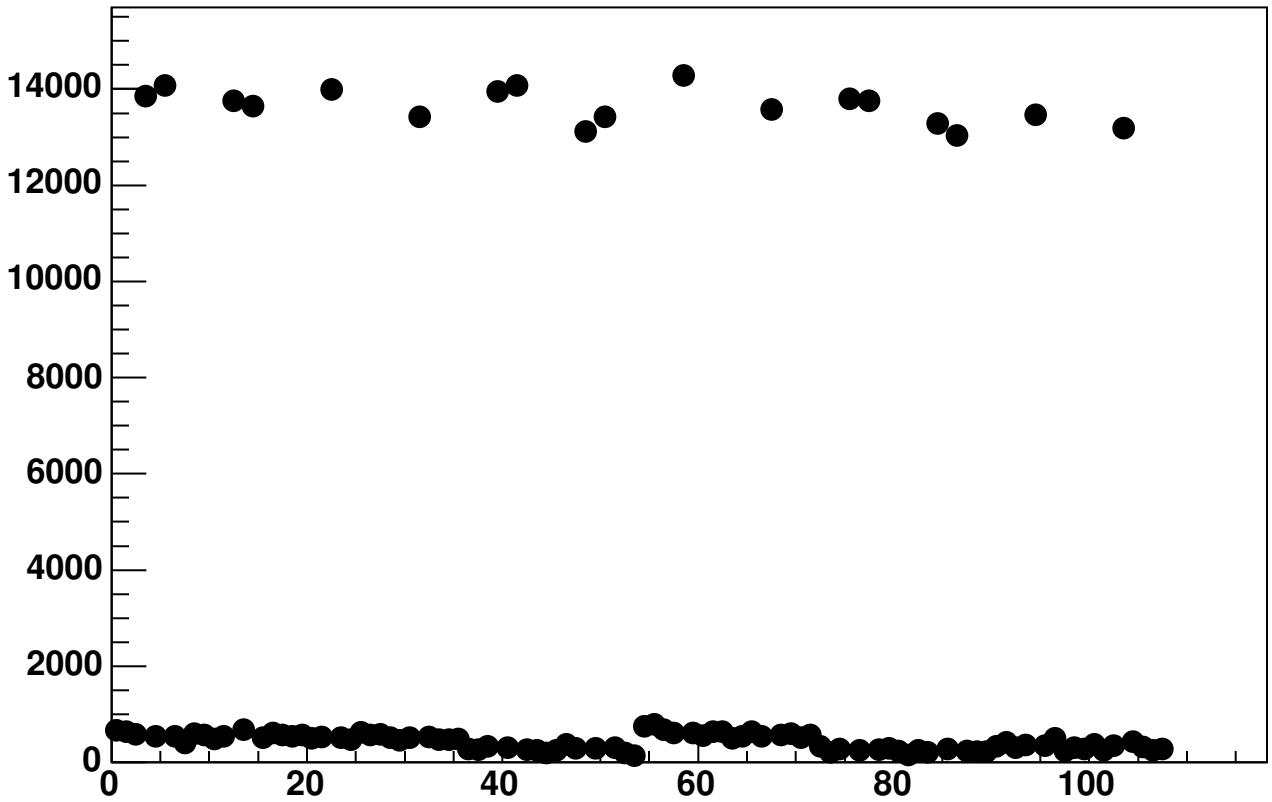
Enable 1, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



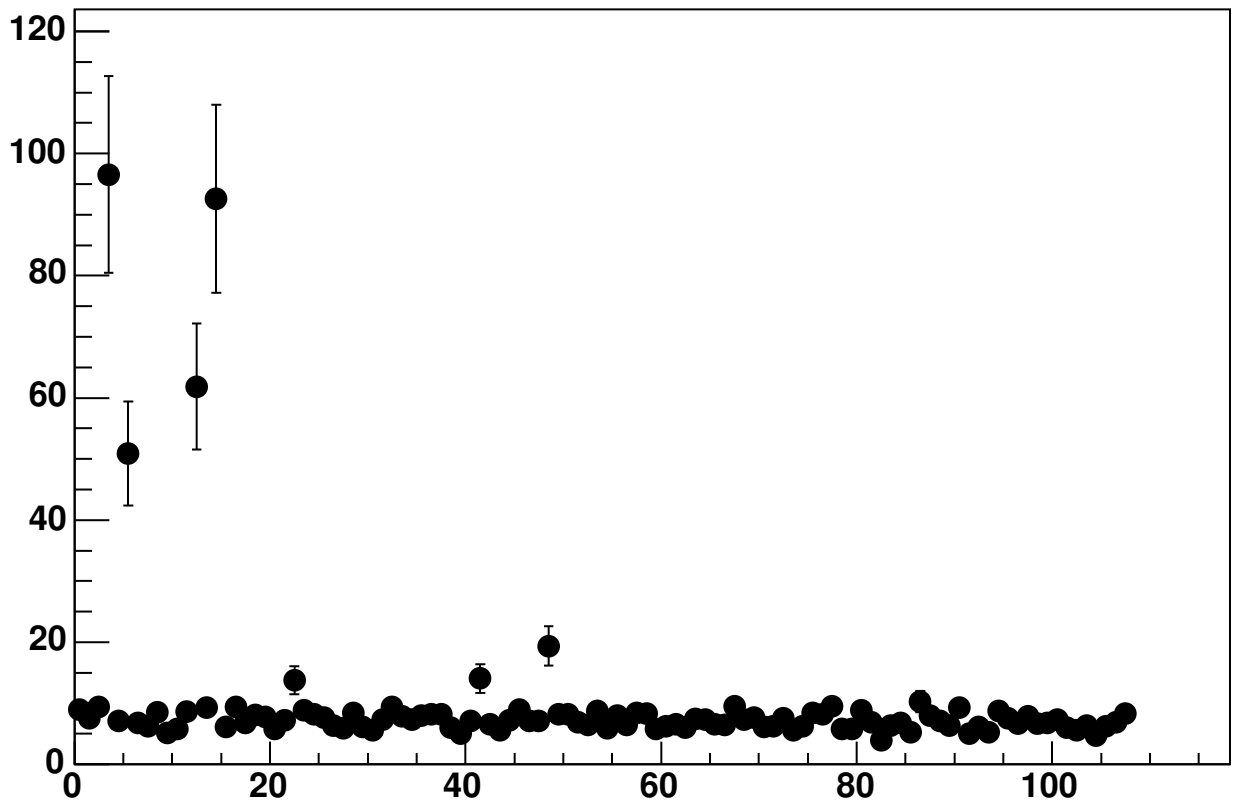
Enable 1, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



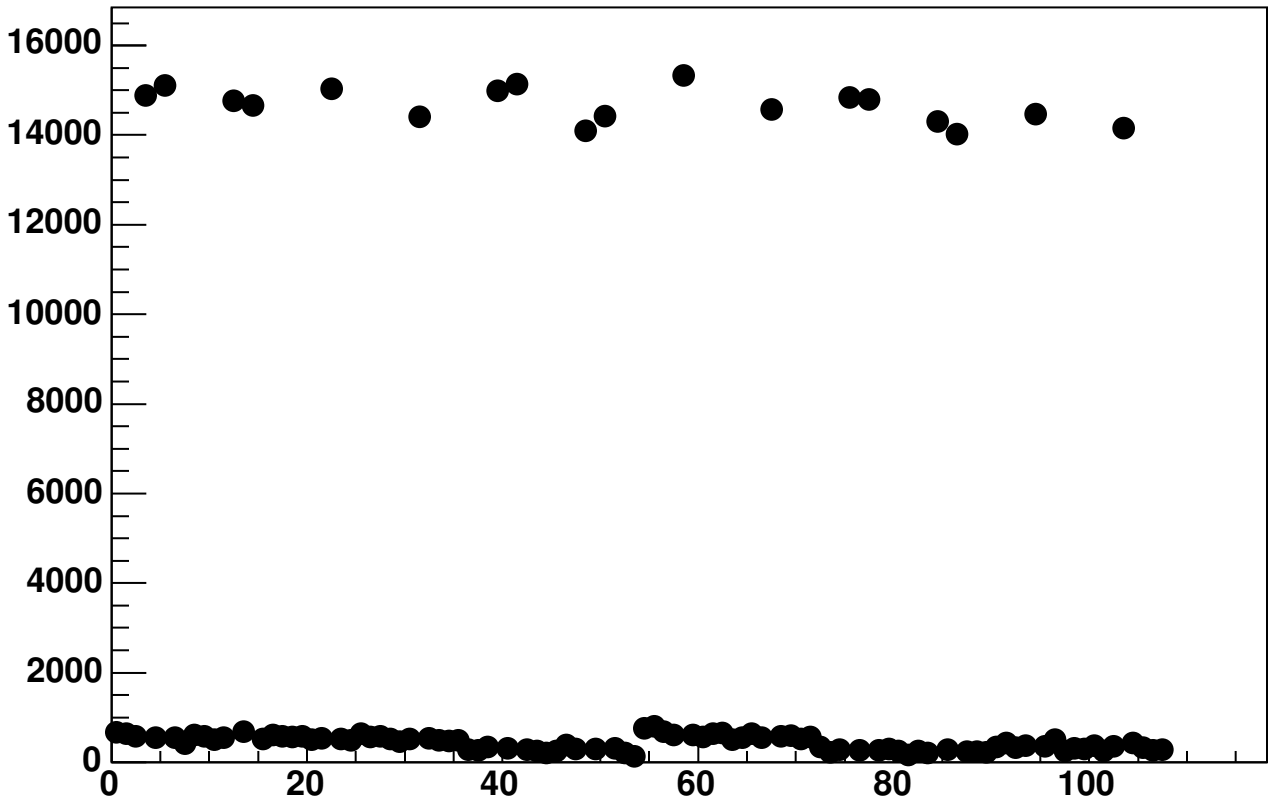
Enable 1, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



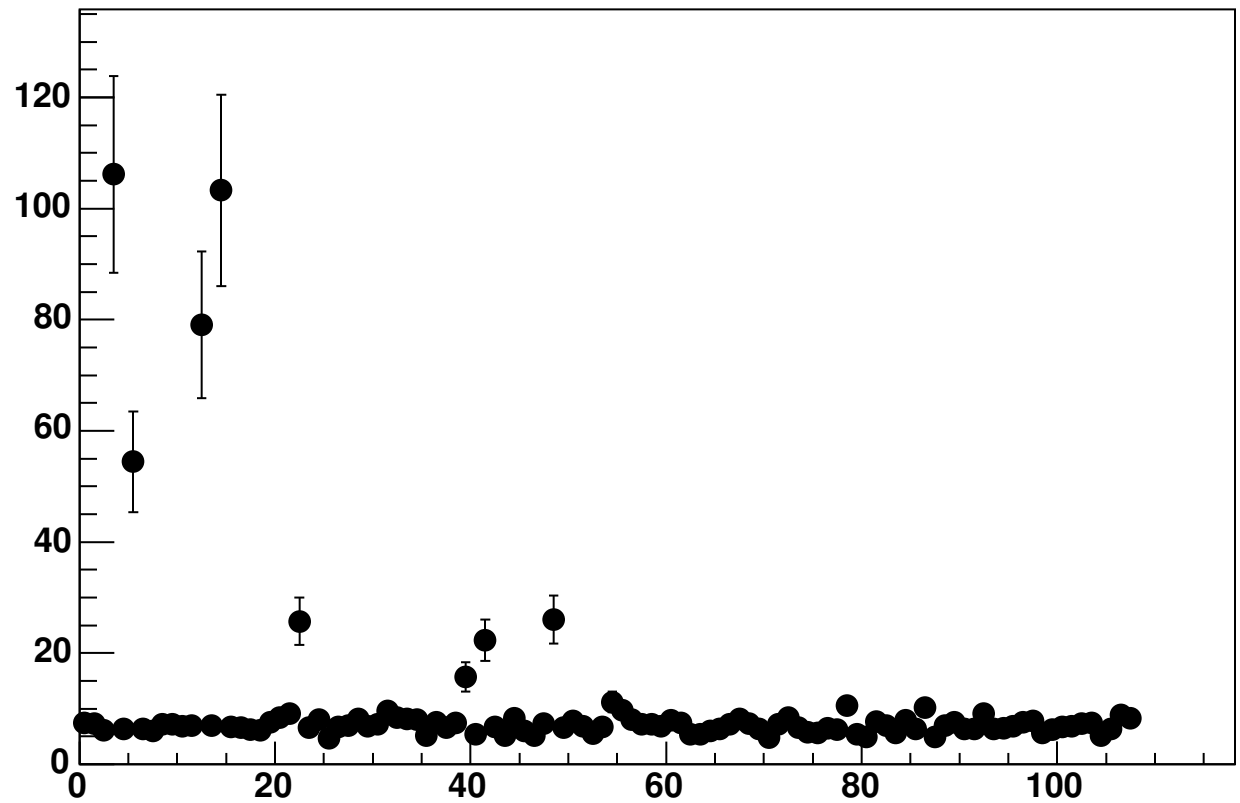
Enable 1, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



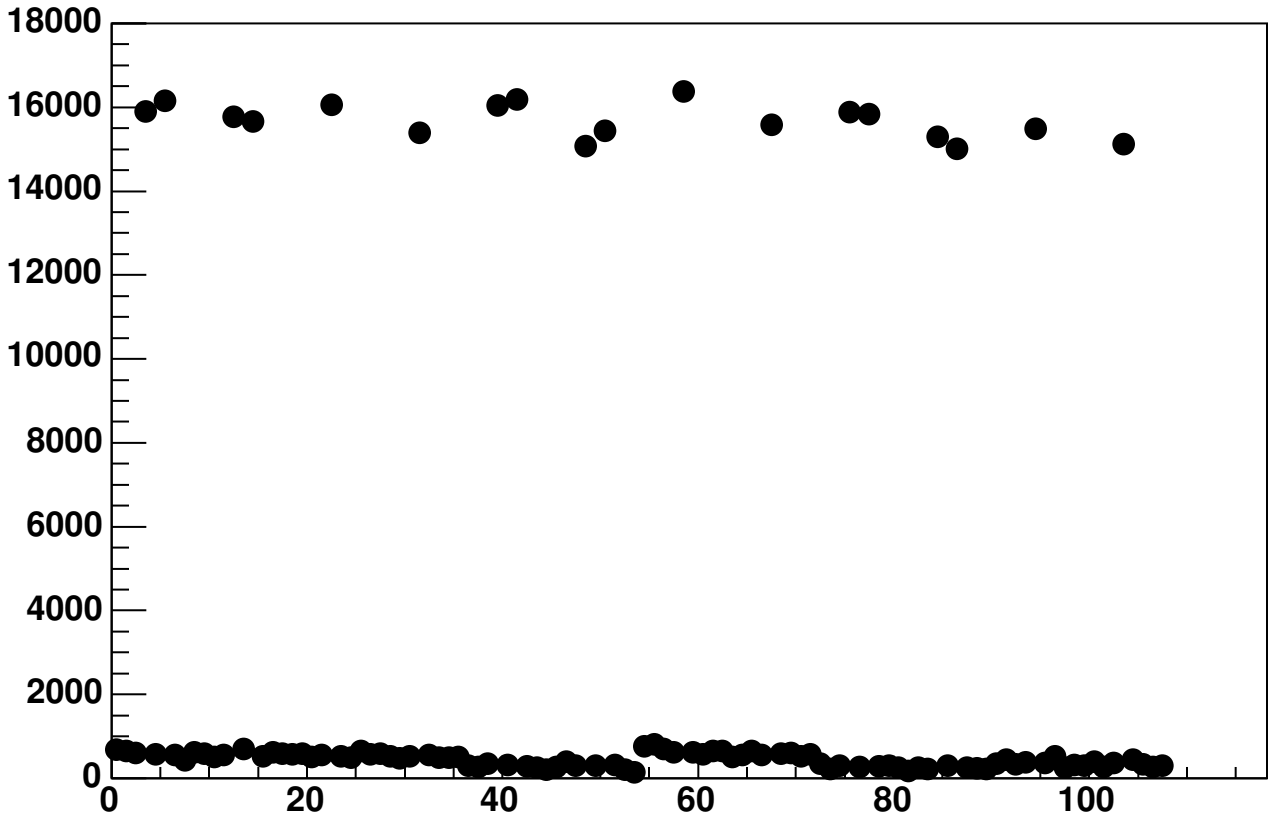
Enable 1, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



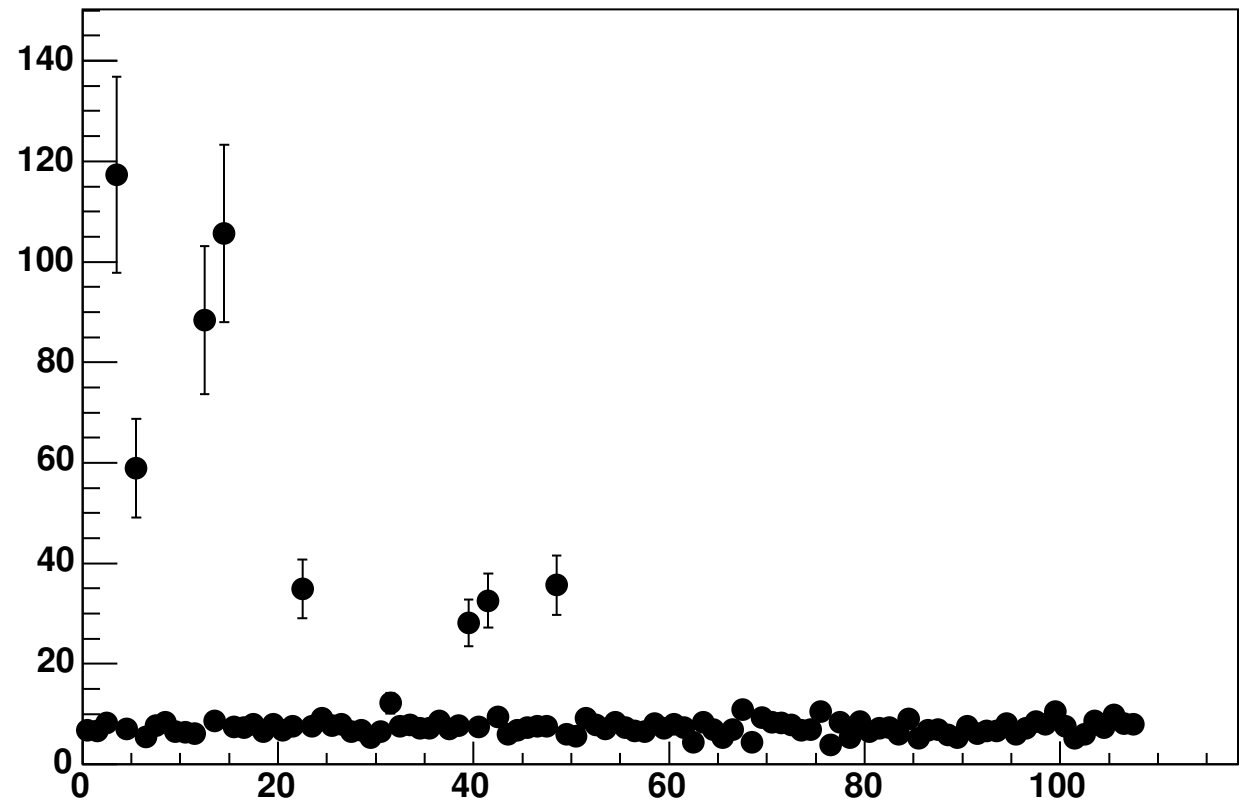
Enable 1, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



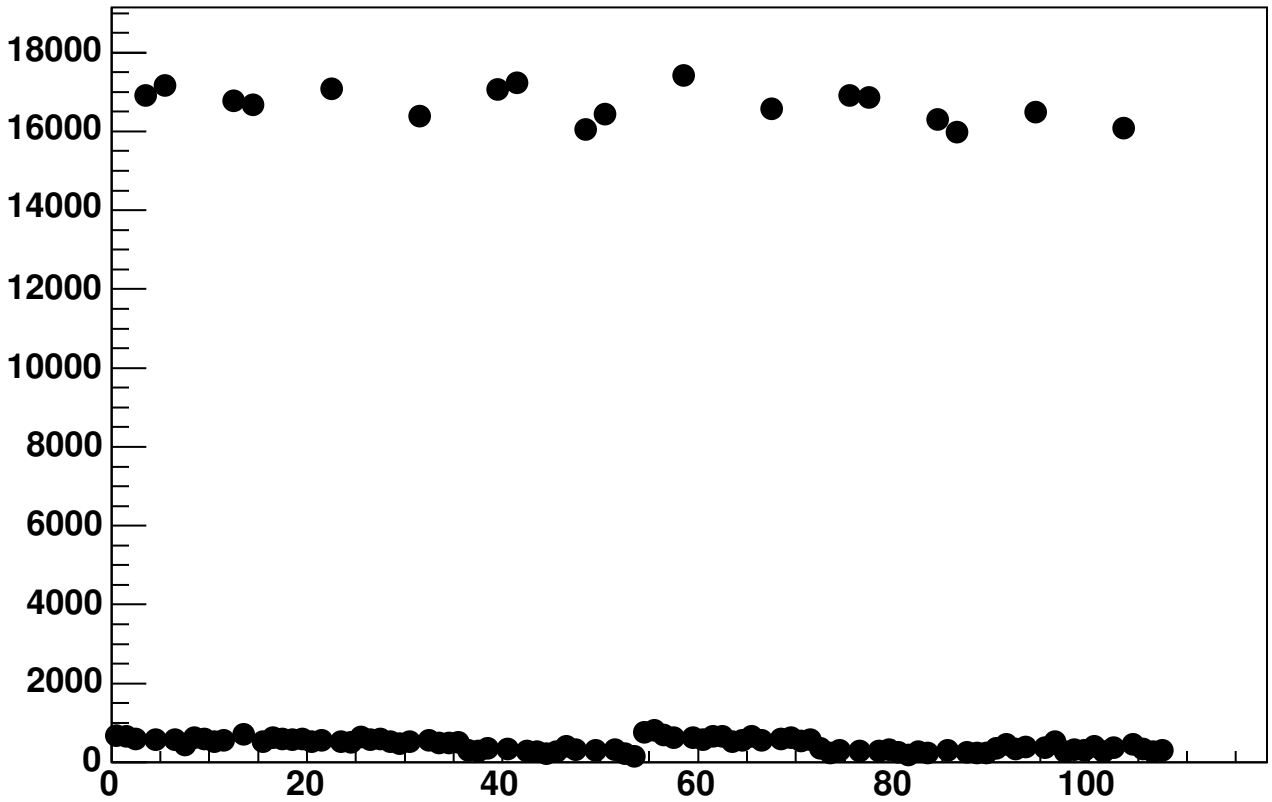
Enable 1, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



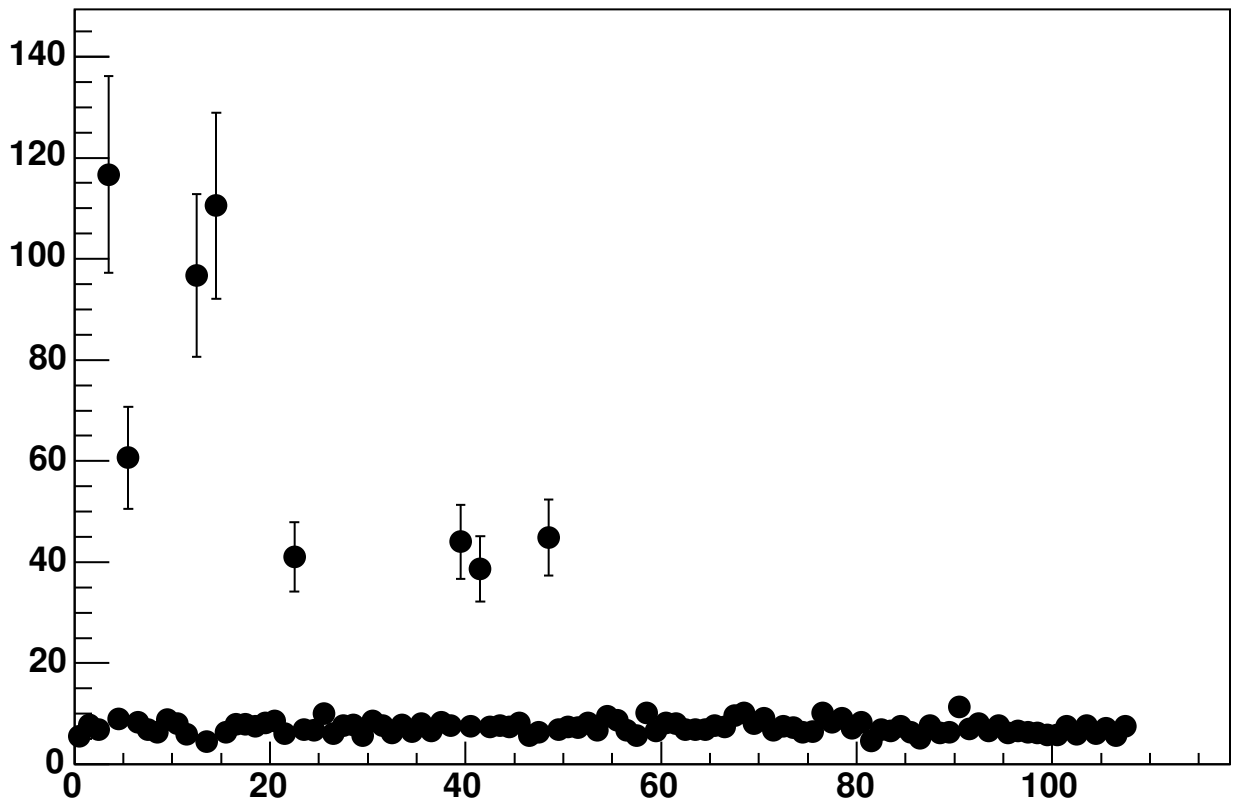
Enable 1, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



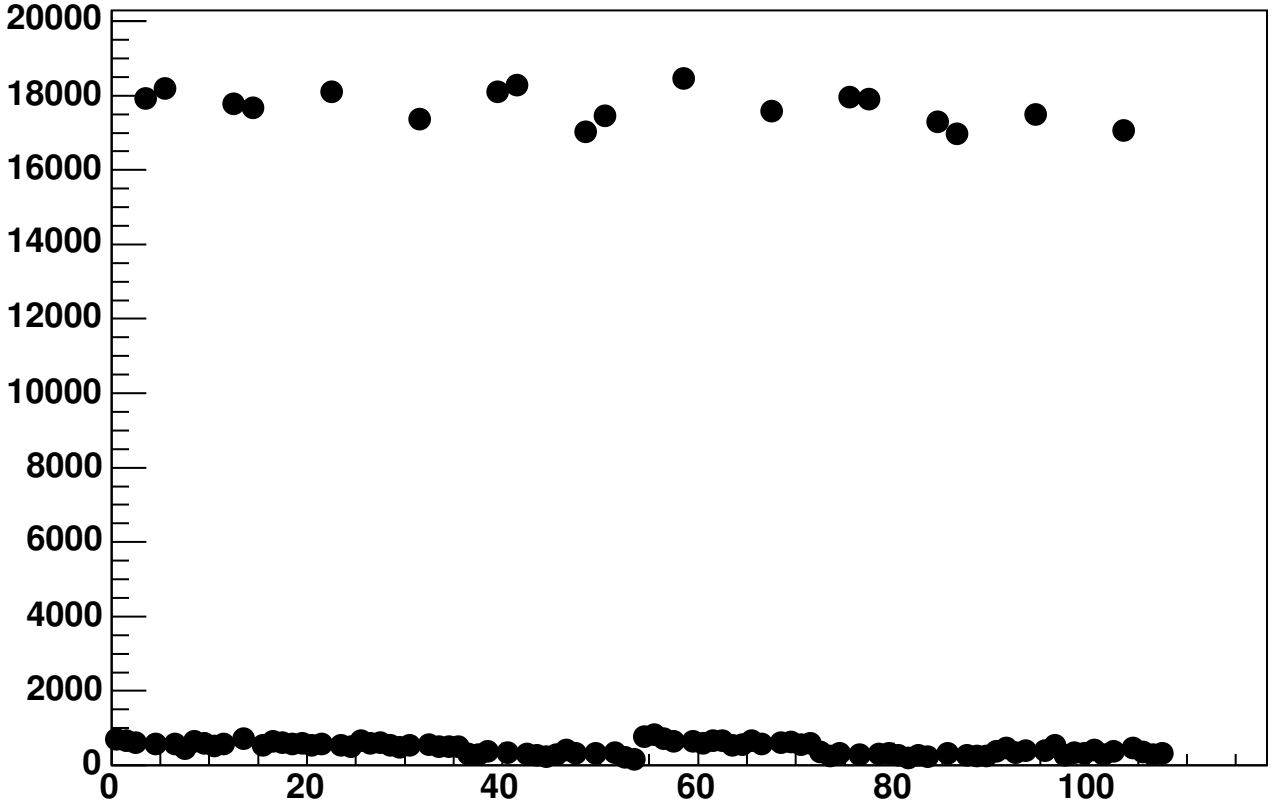
Enable 1, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



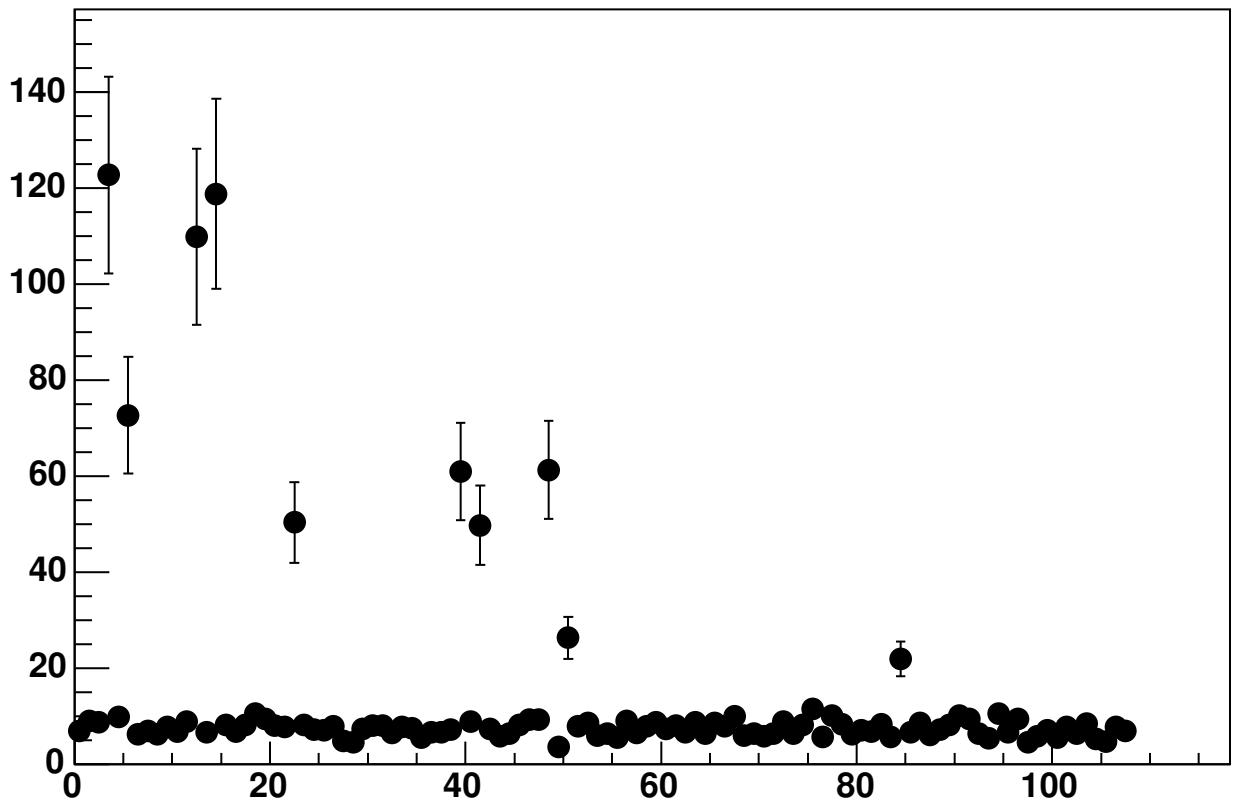
Enable 1, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



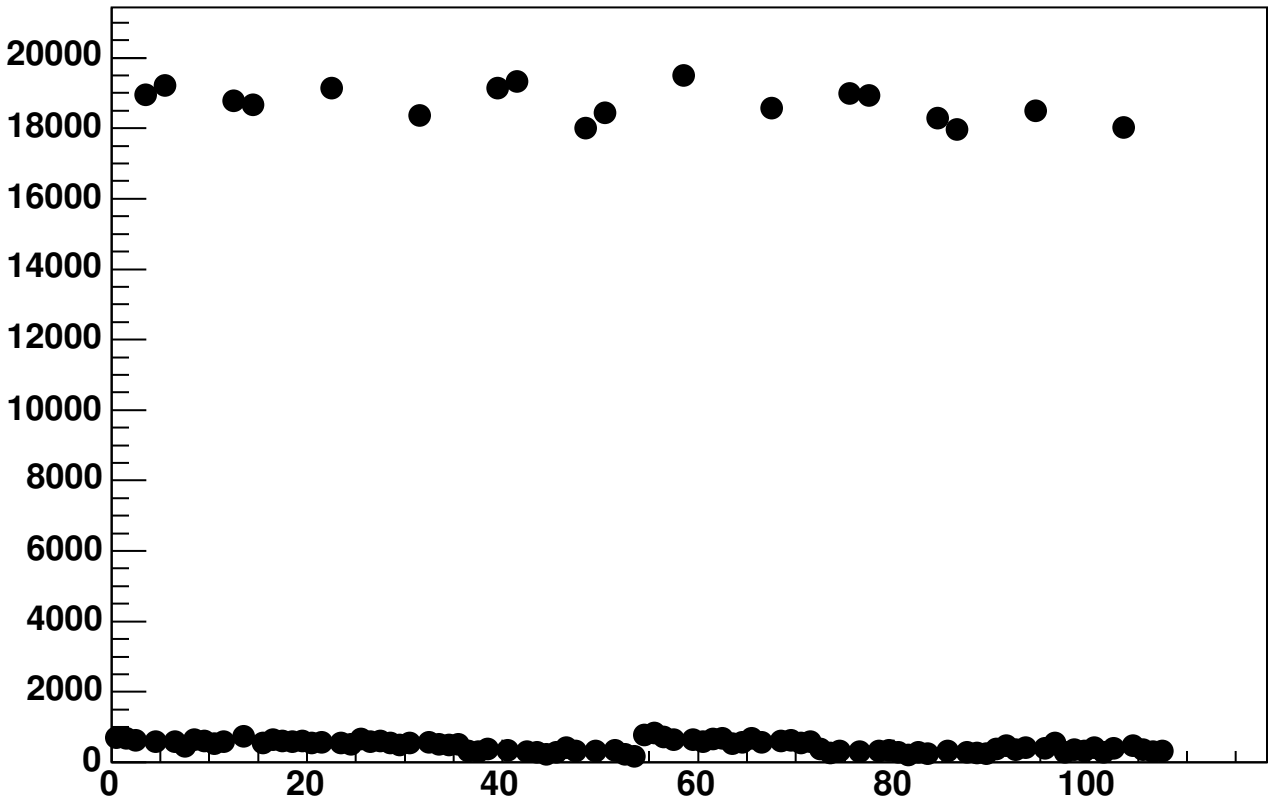
Enable 1, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



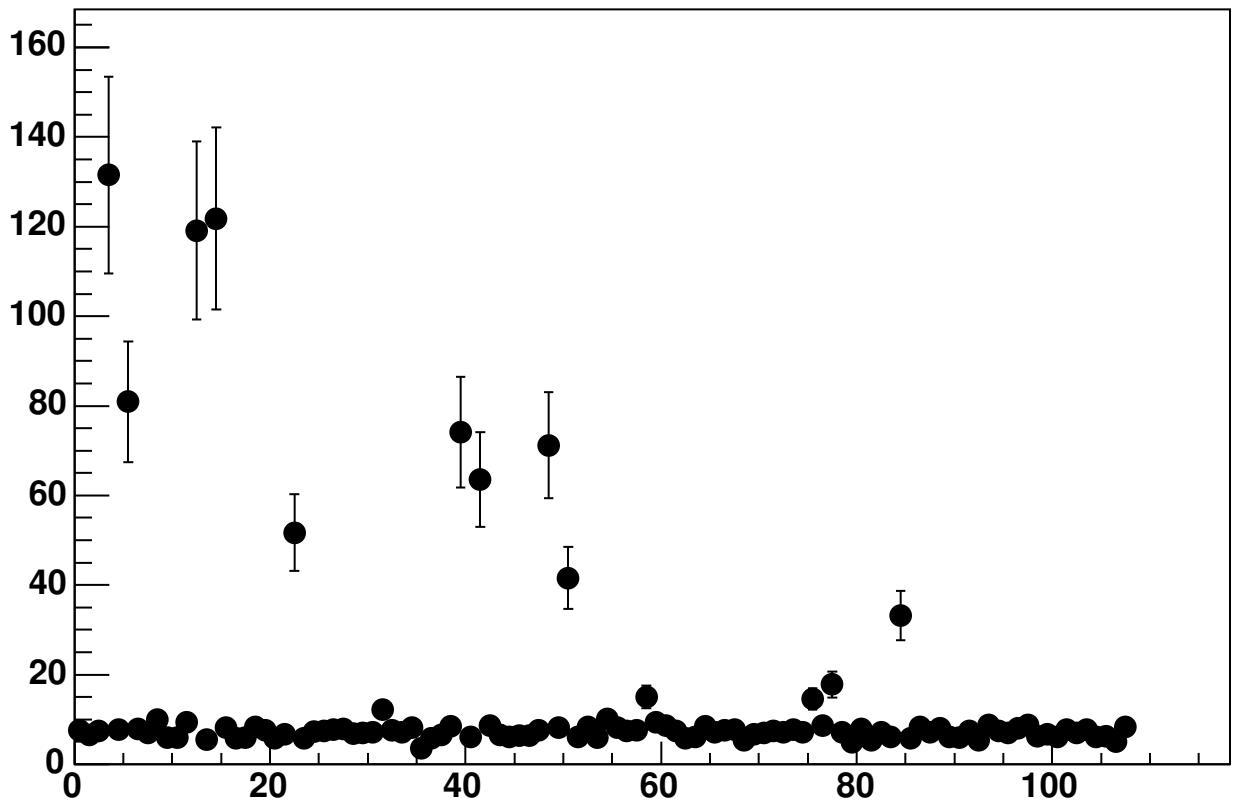
Enable 1, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



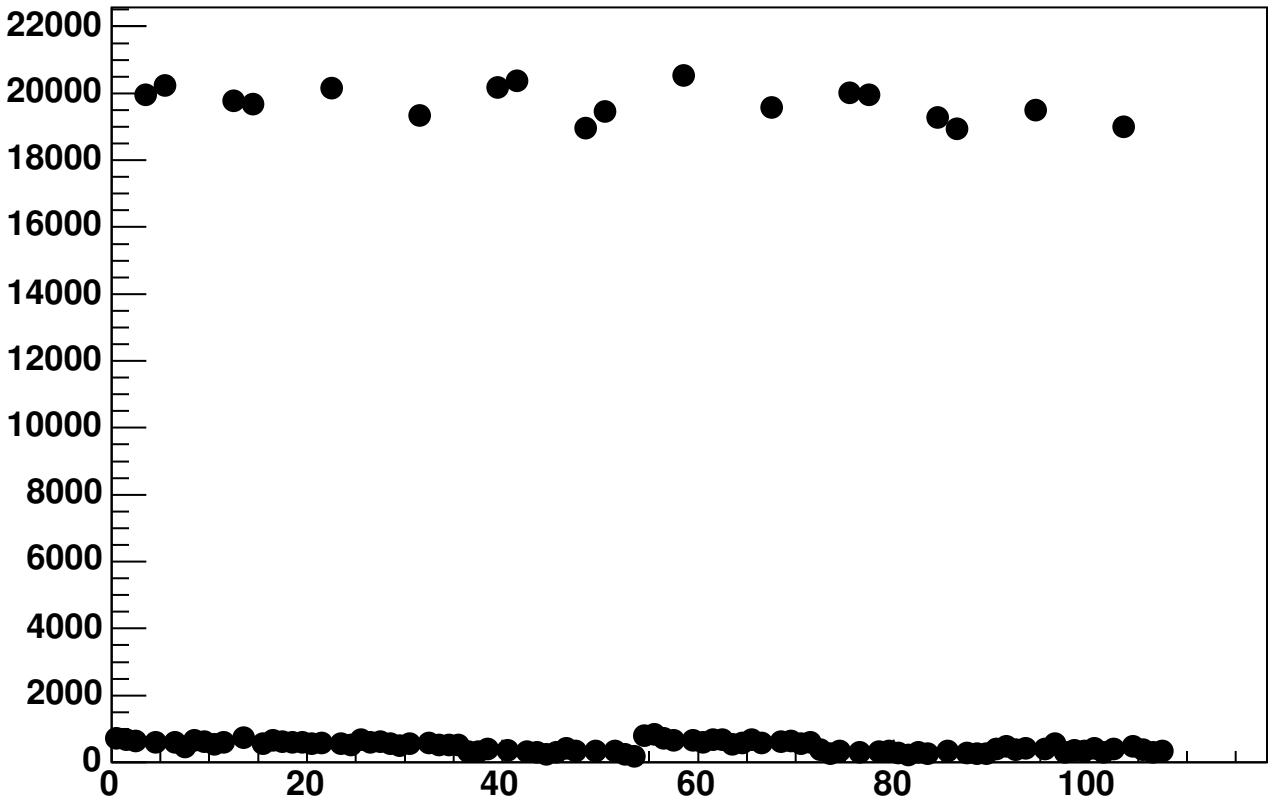
Enable 1, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



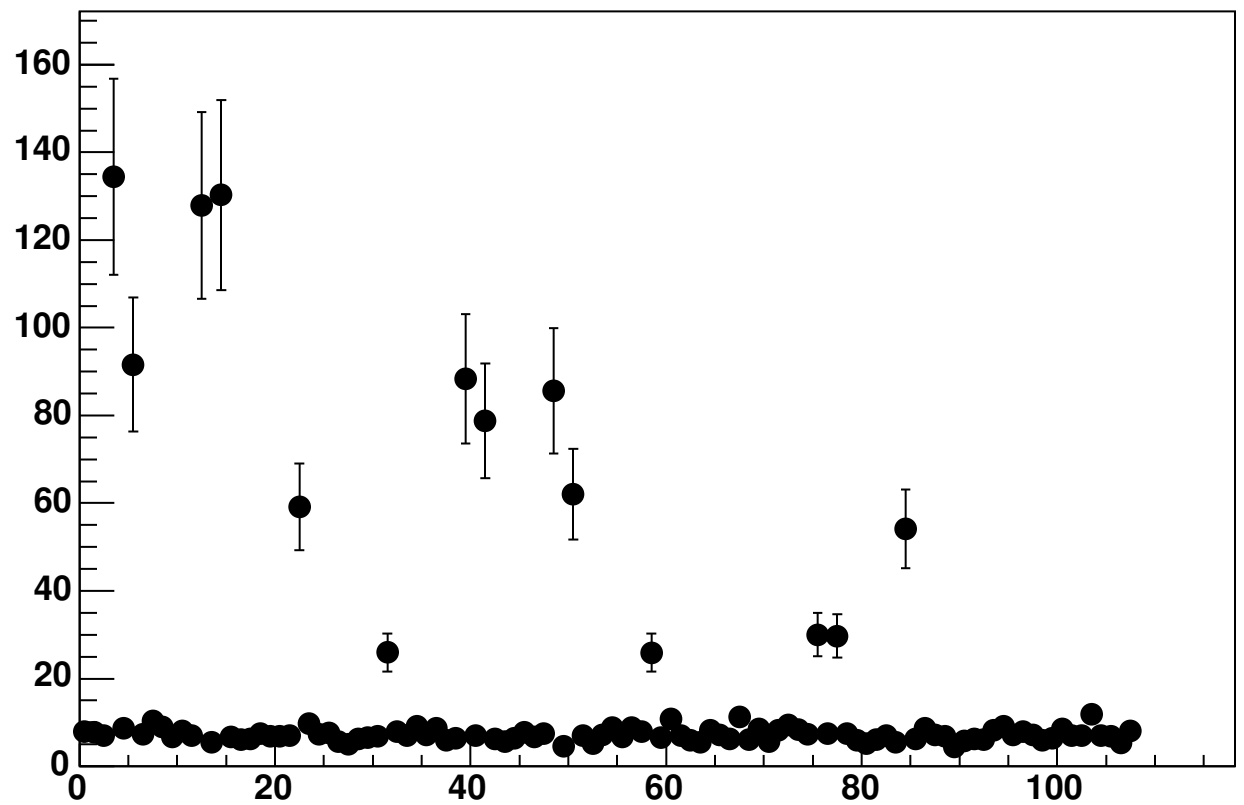
Enable 1, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



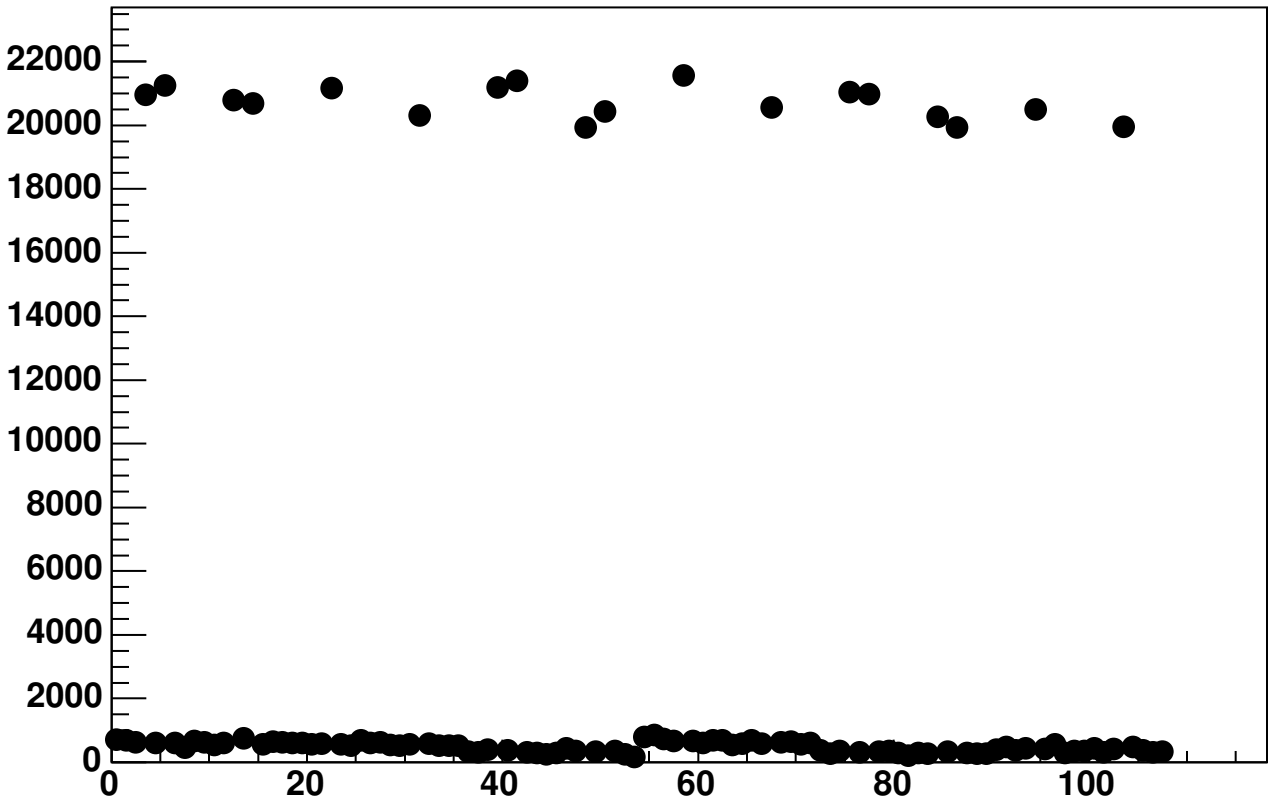
Enable 1, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



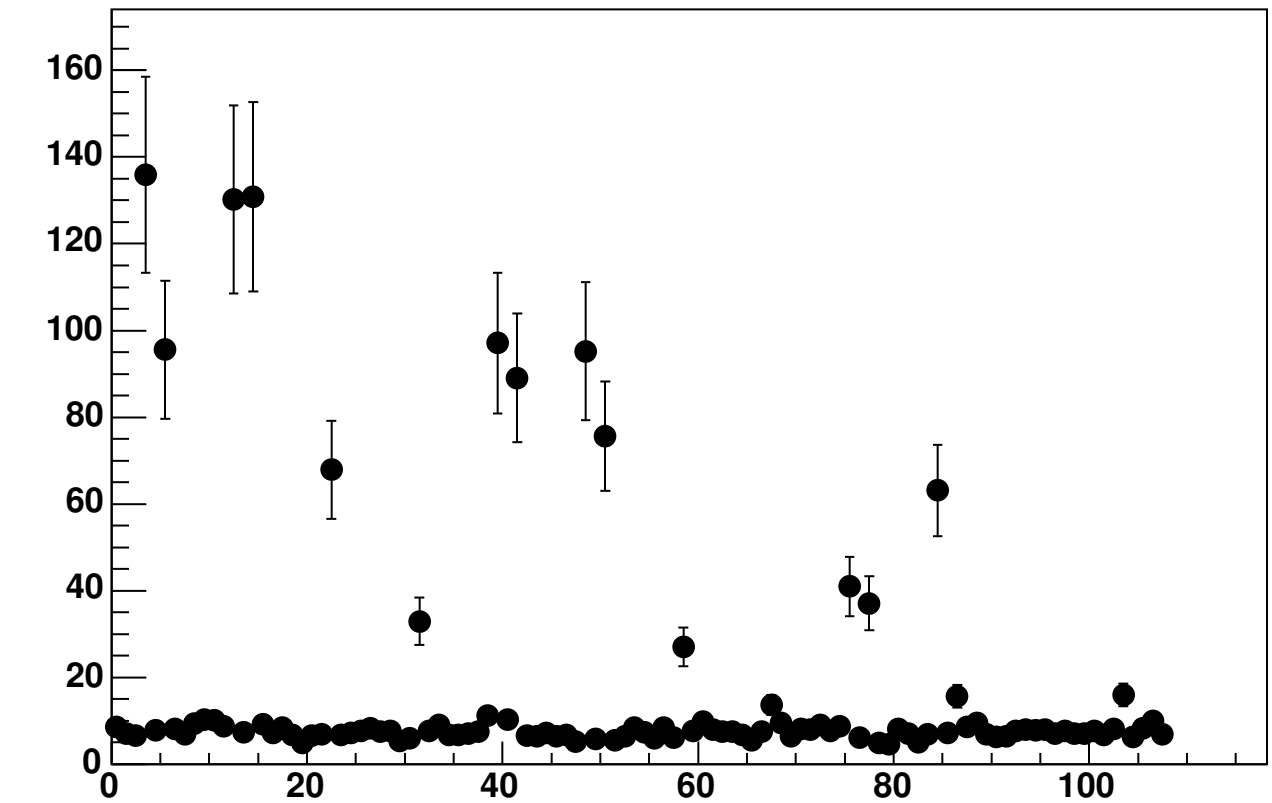
Enable 1, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



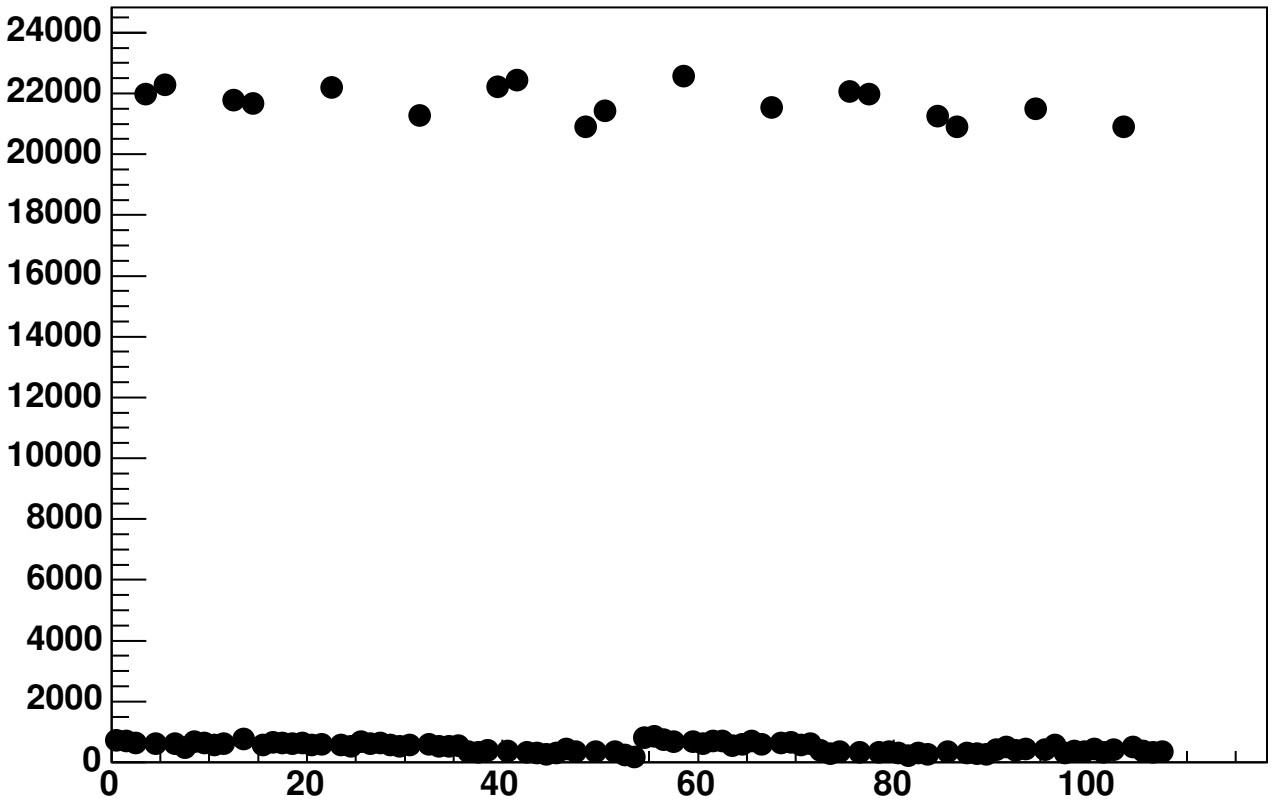
Enable 1, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



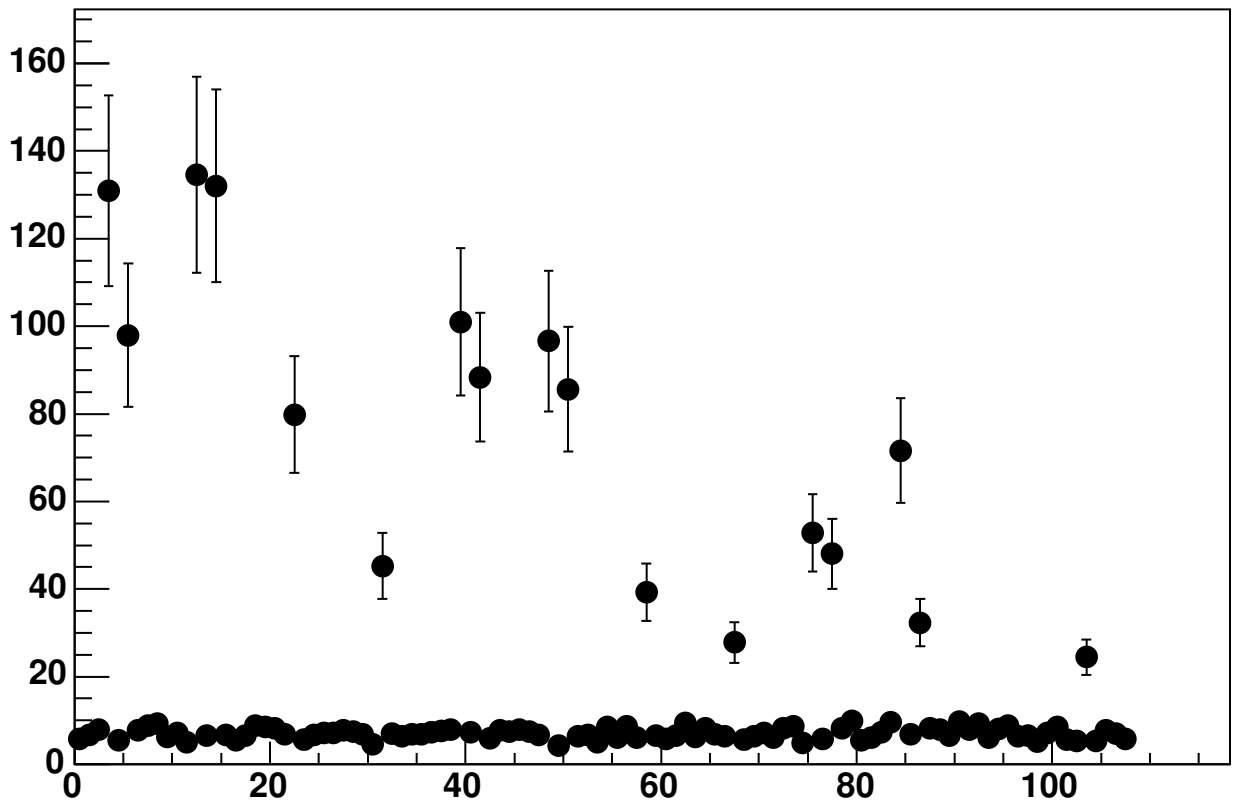
Enable 1, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



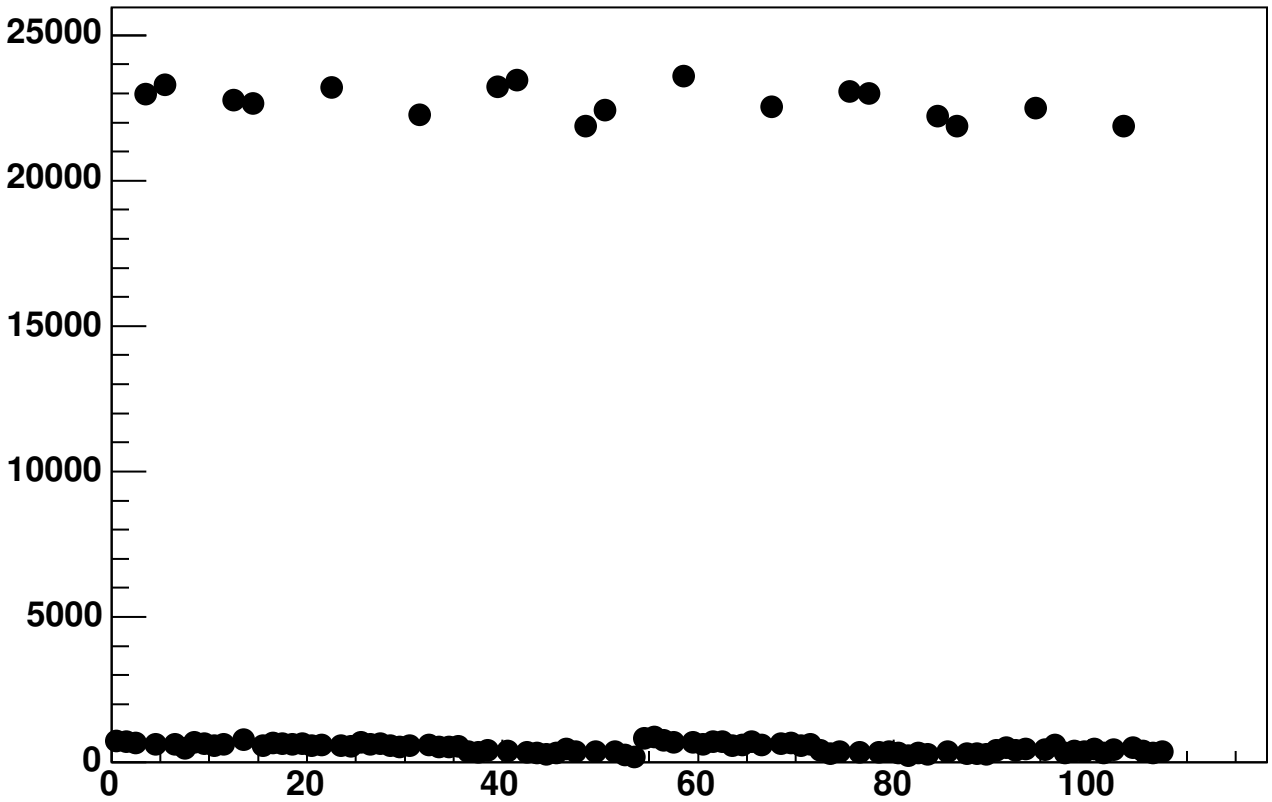
Enable 1, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



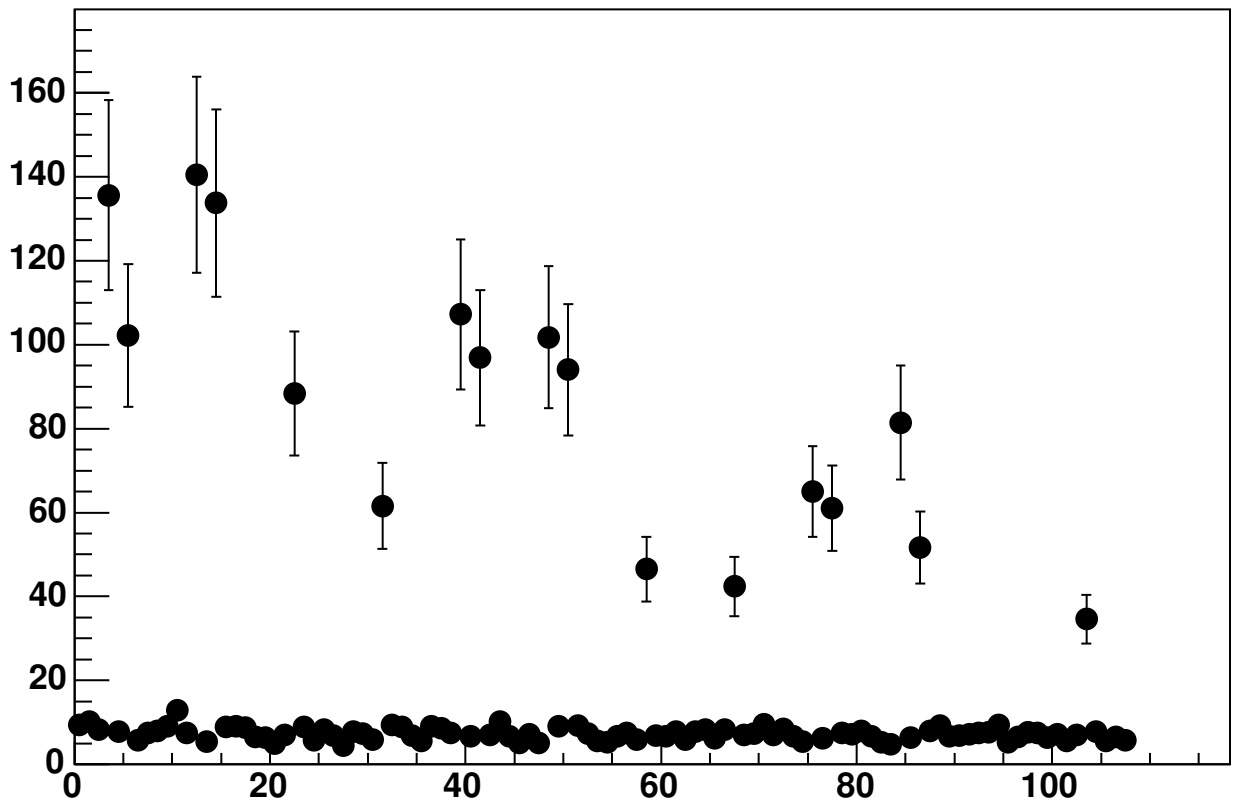
Enable 1, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



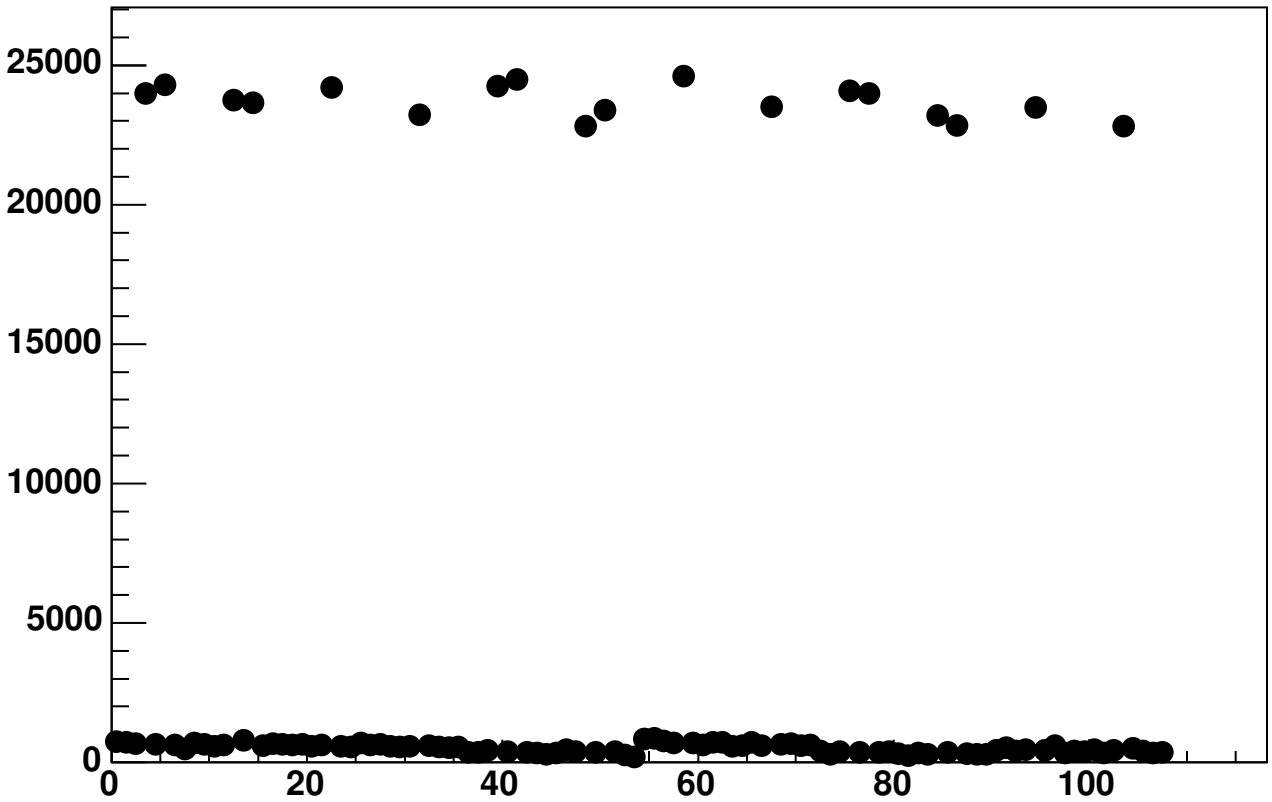
Enable 1, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



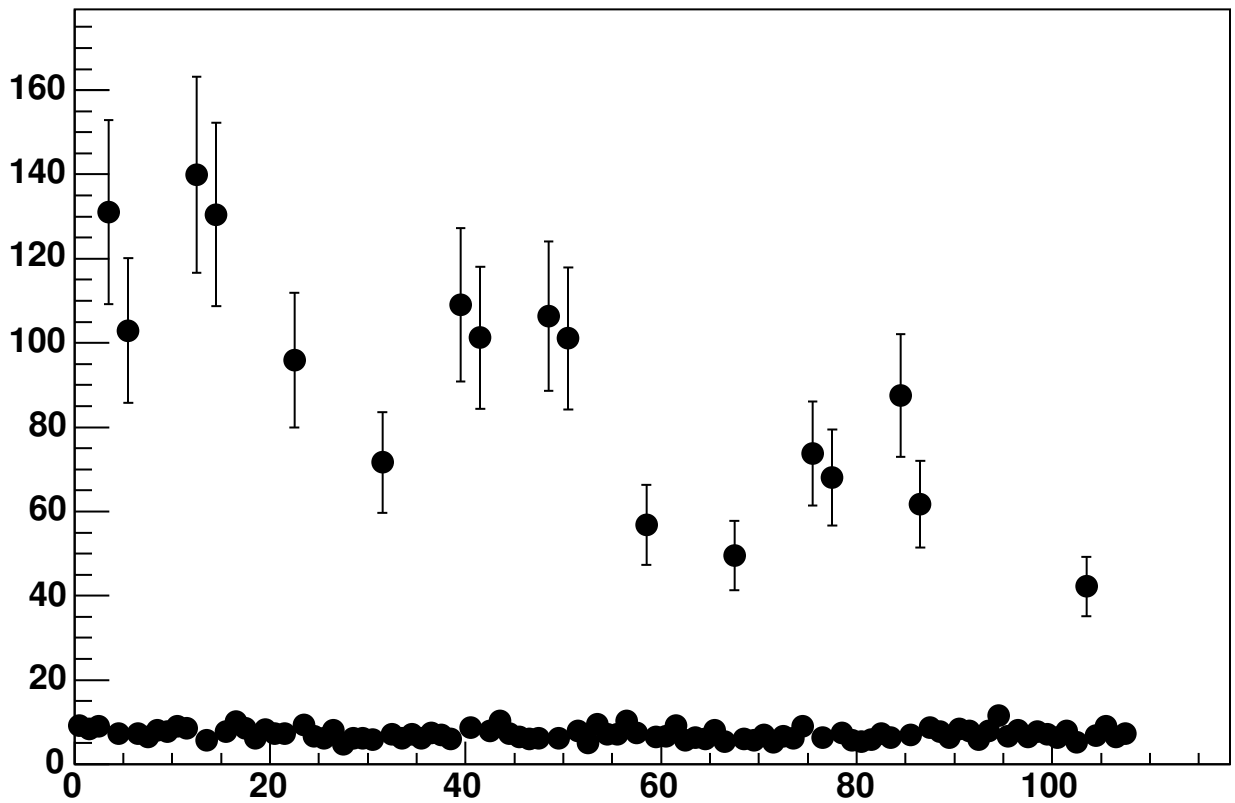
Enable 1, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



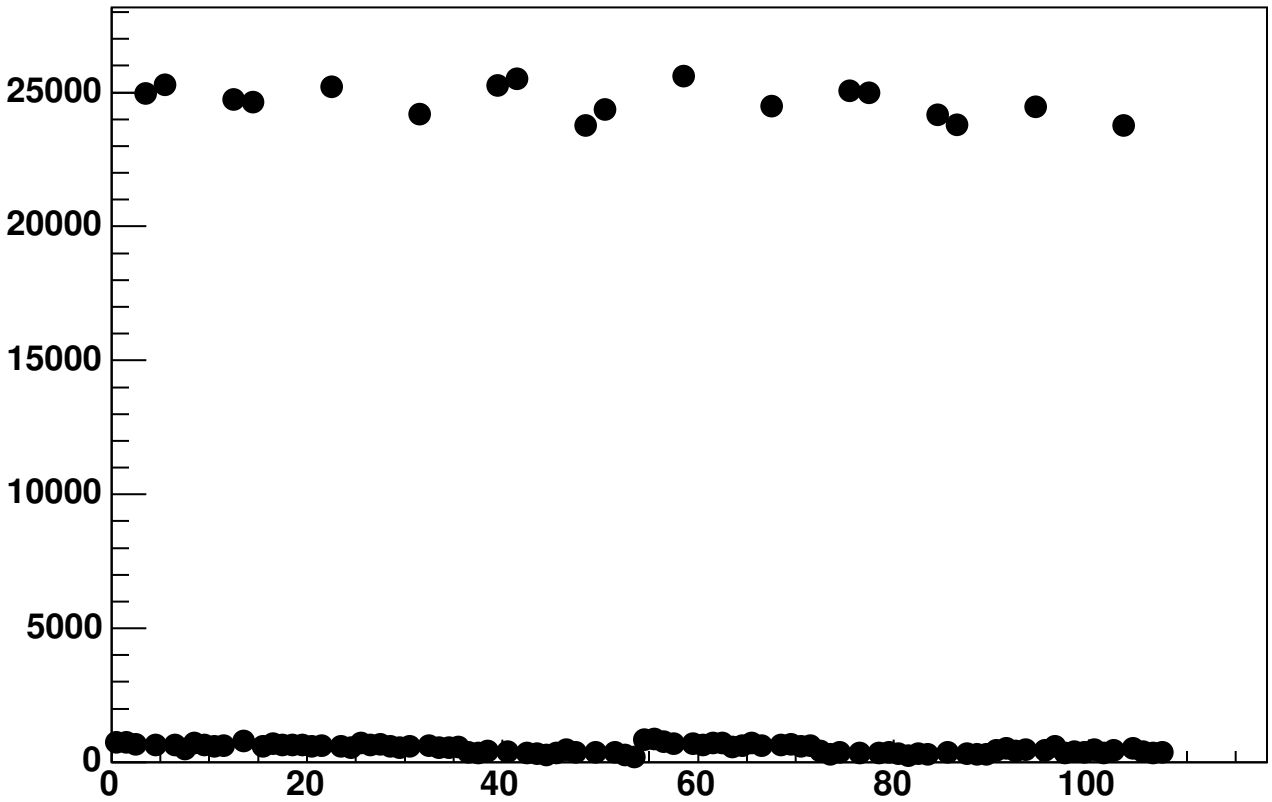
Enable 1, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



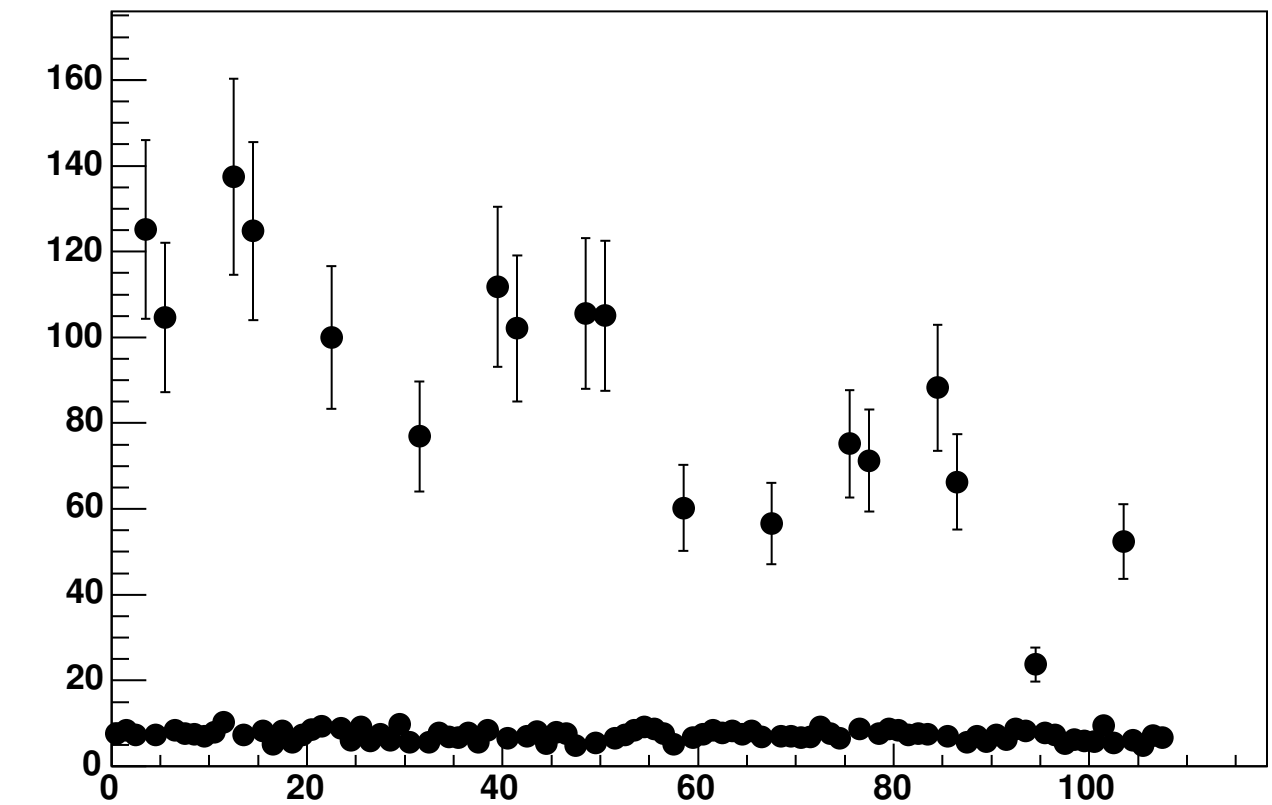
Enable 1, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



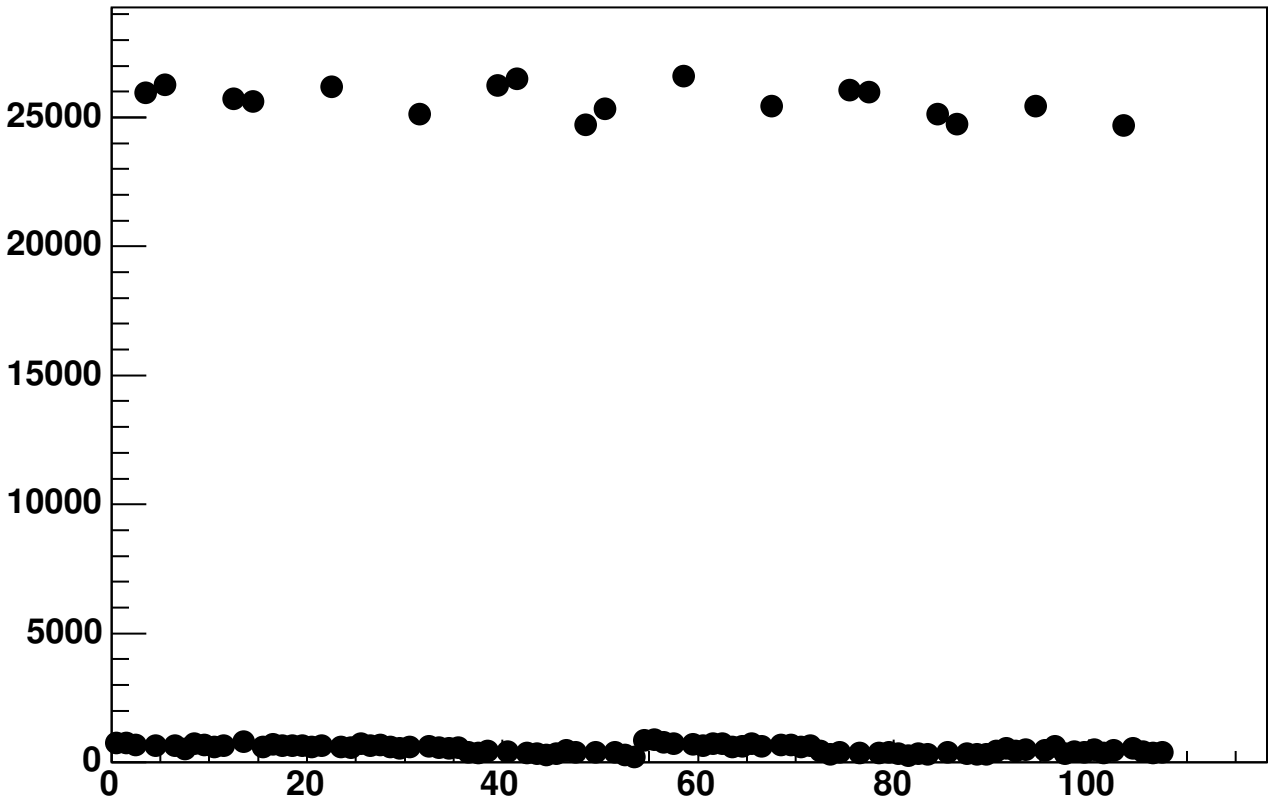
Enable 1, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



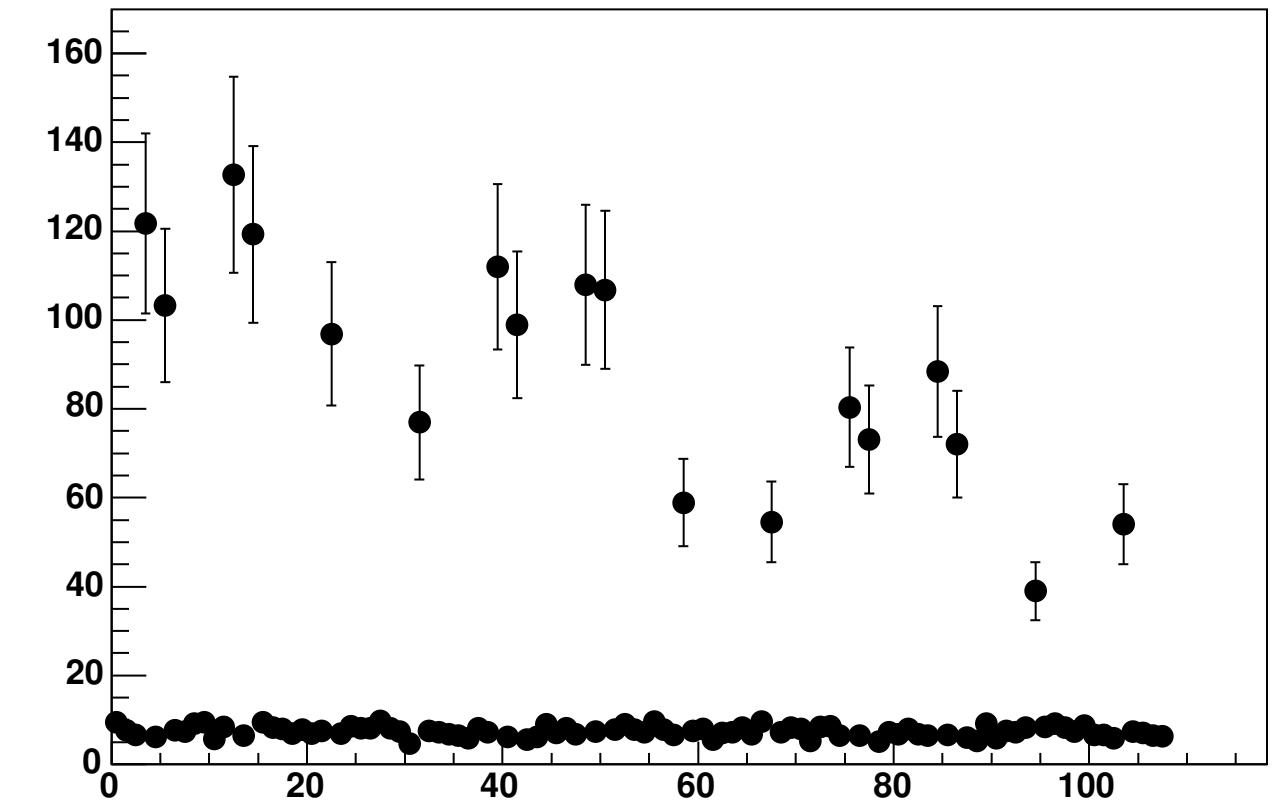
Enable 1, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



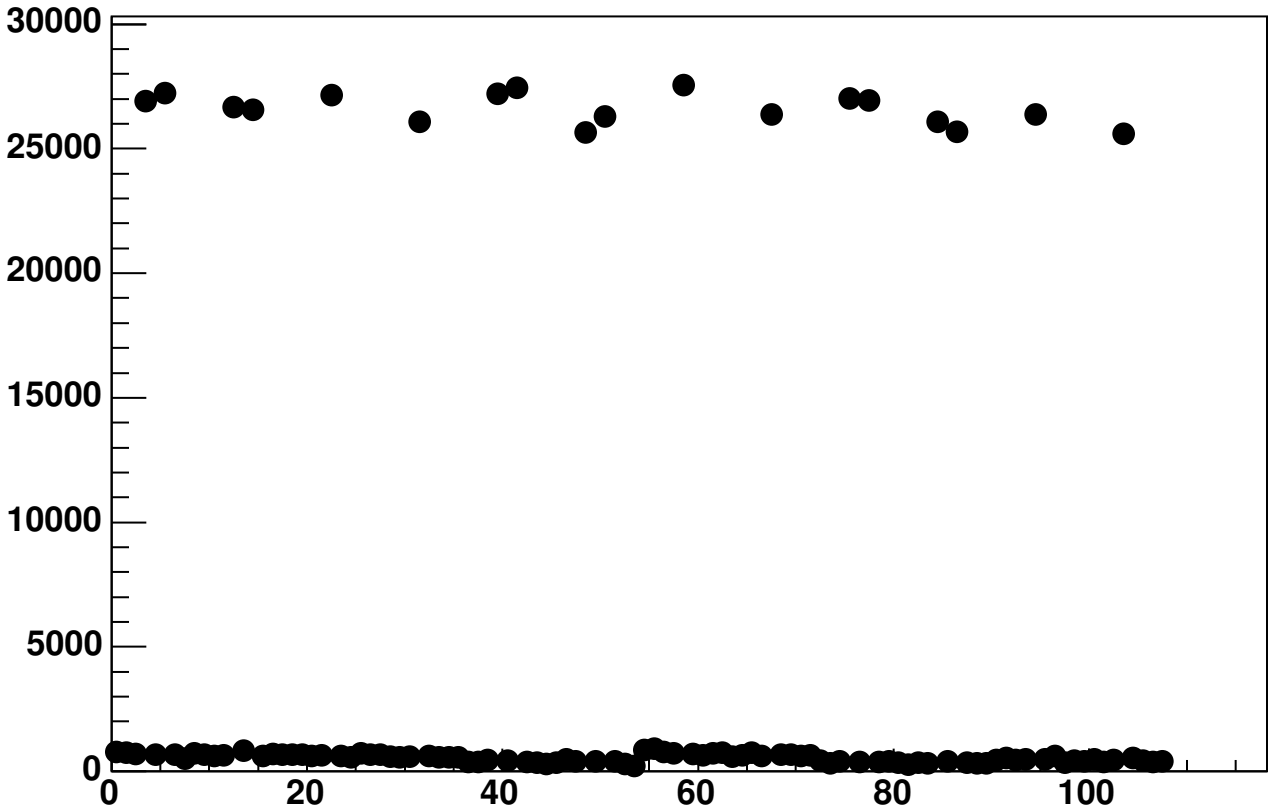
Enable 1, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



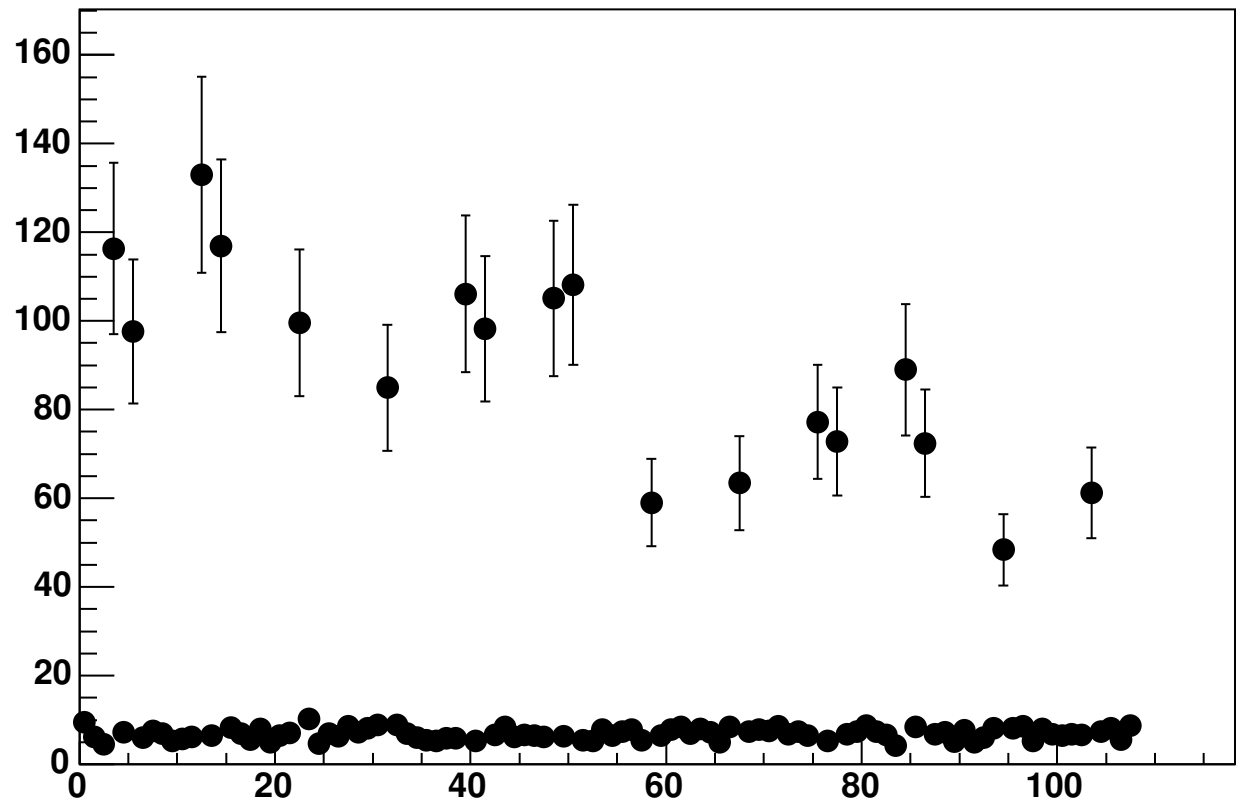
Enable 1, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



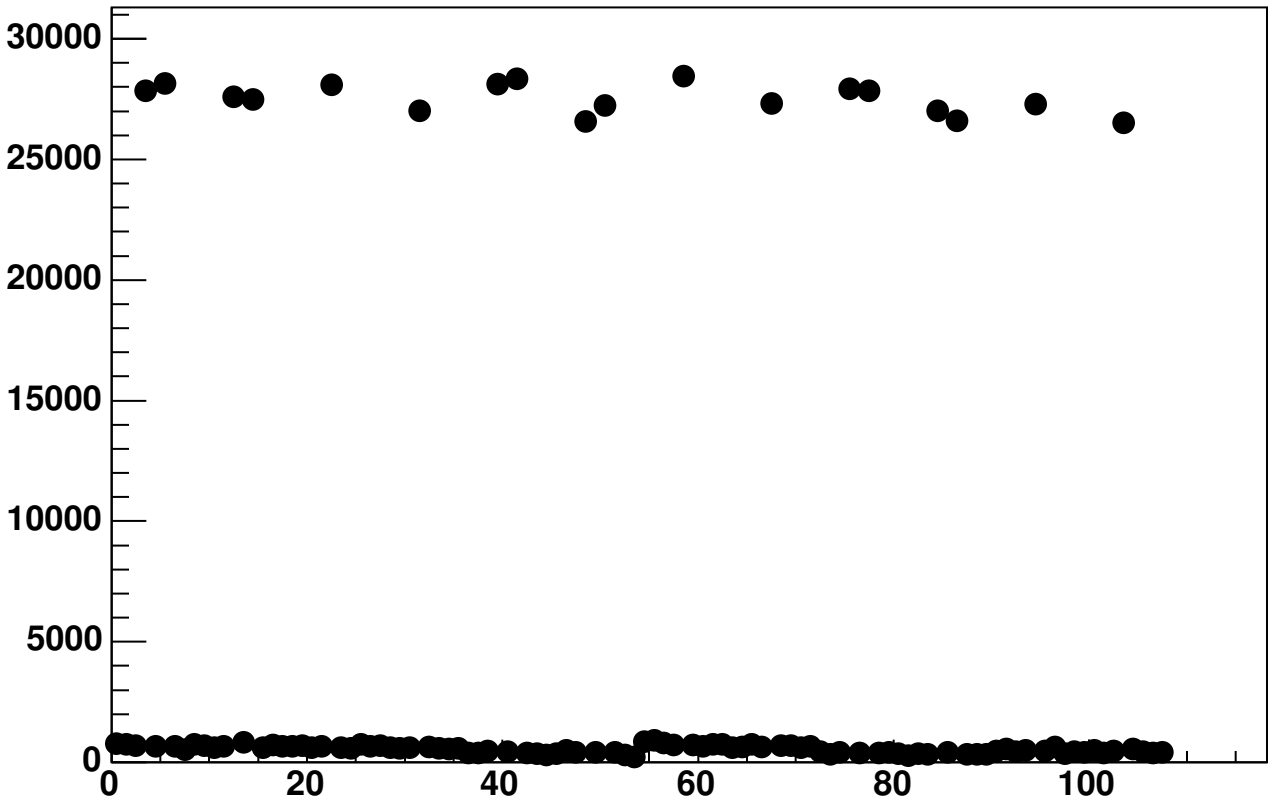
Enable 1, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



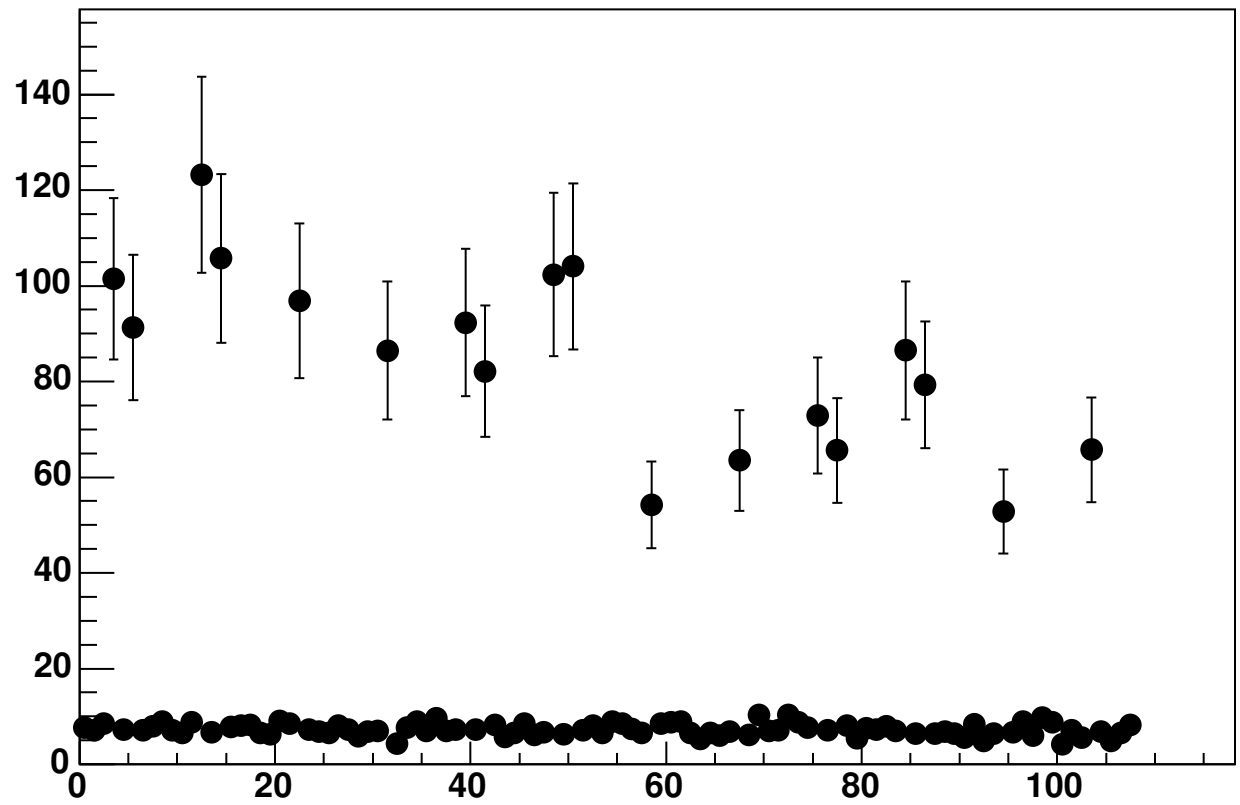
Enable 1, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



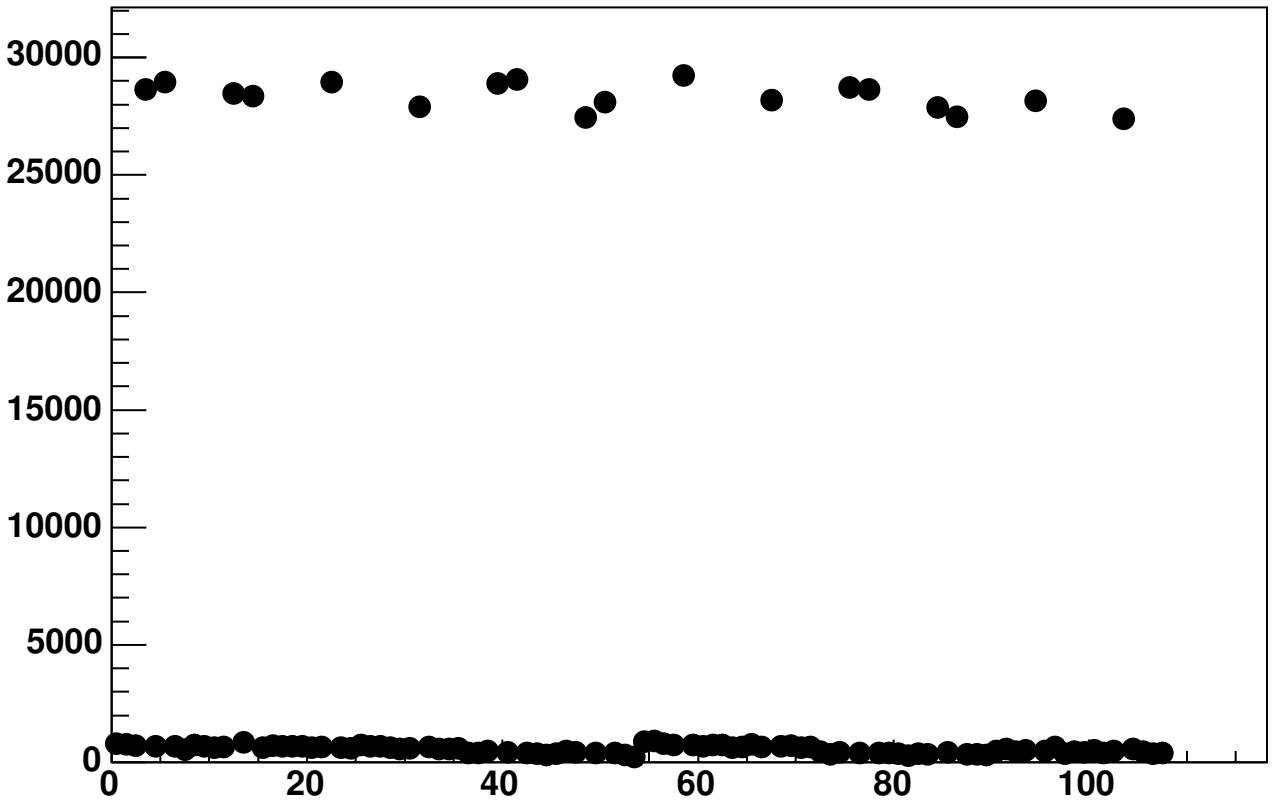
Enable 1, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



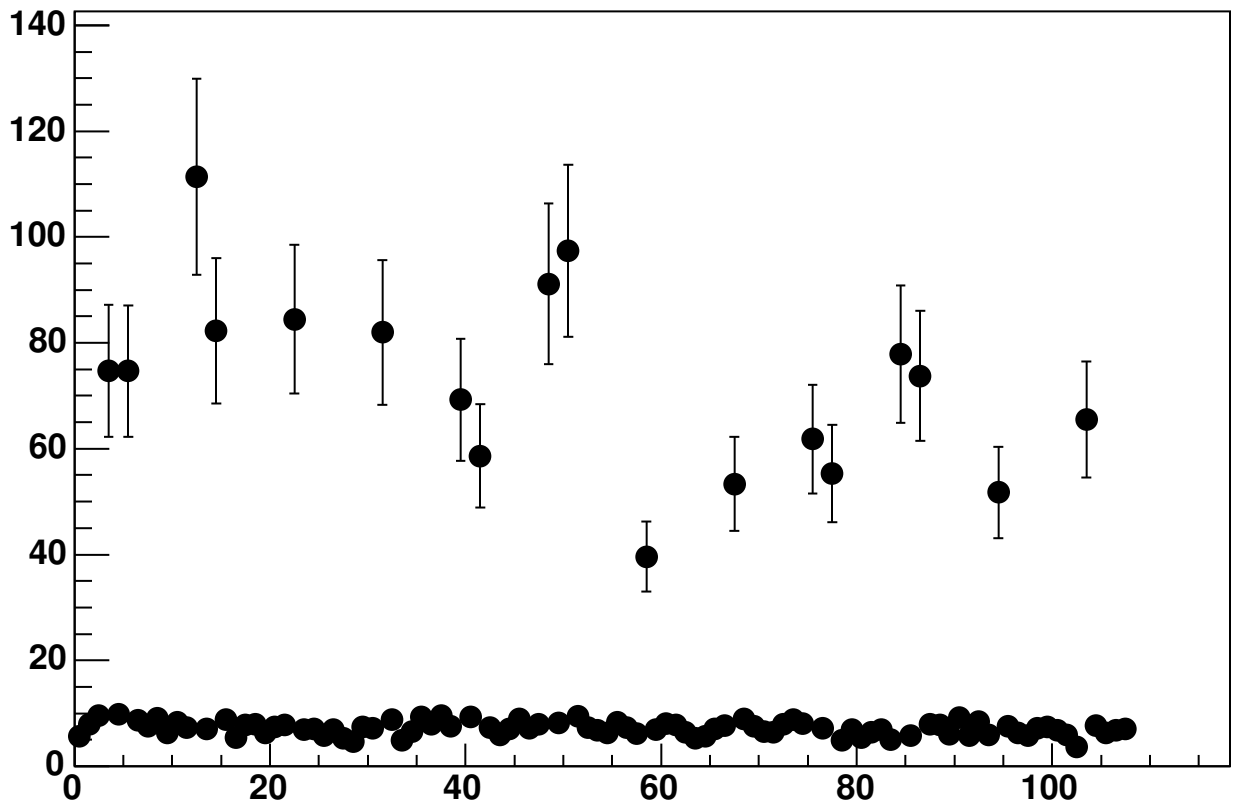
Enable 1, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



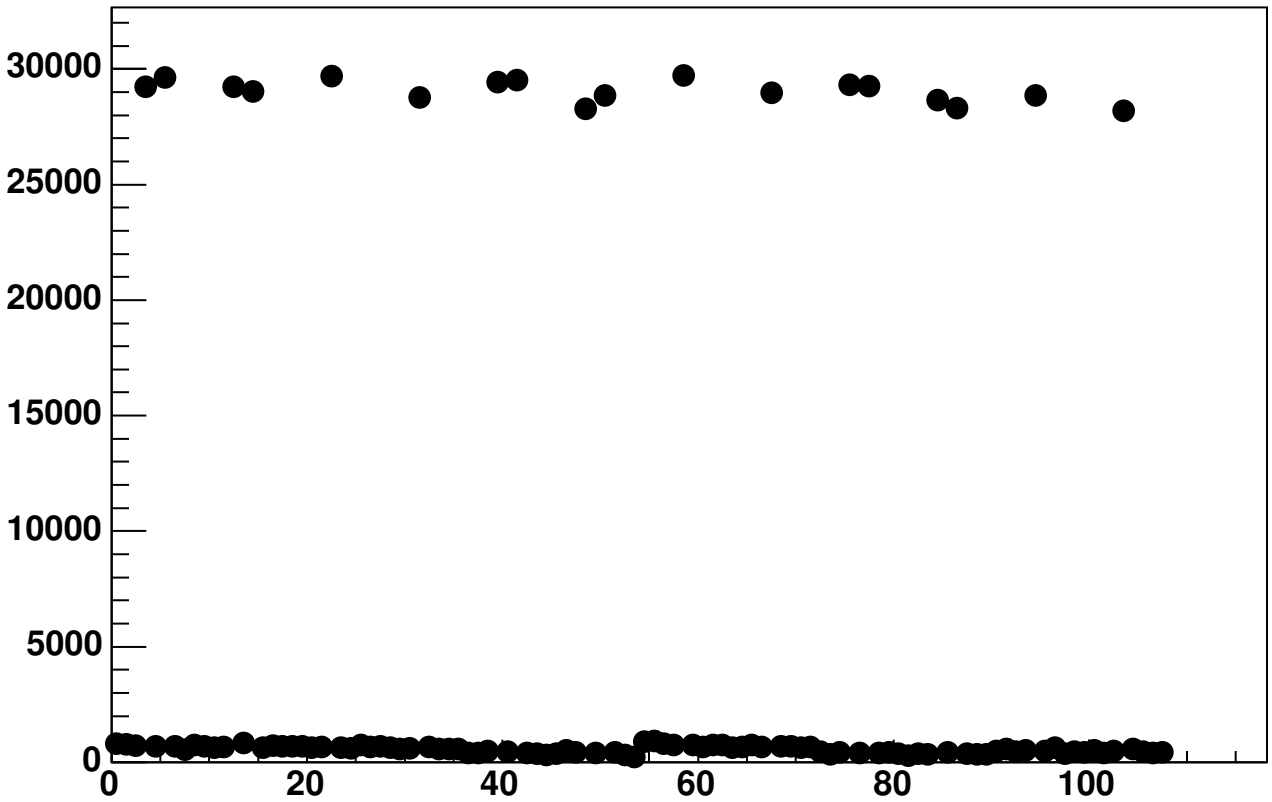
Enable 1, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



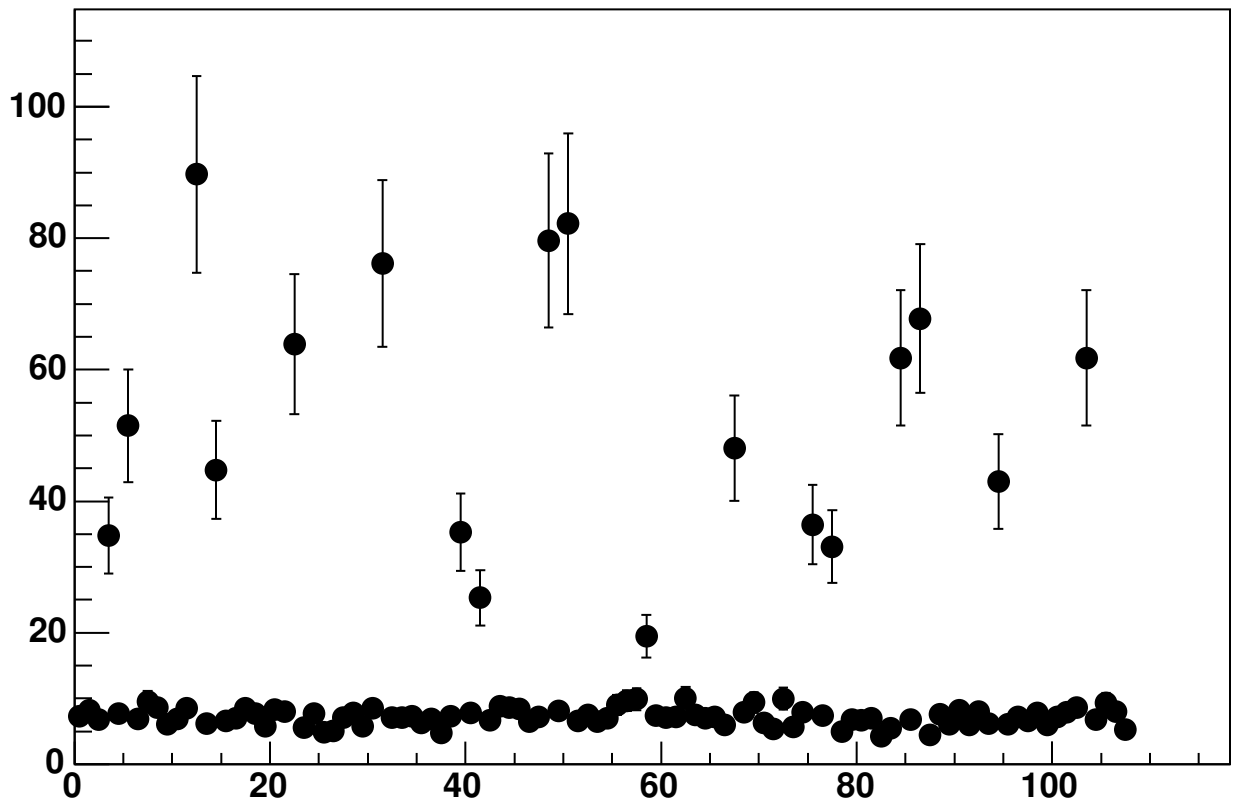
Enable 1, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



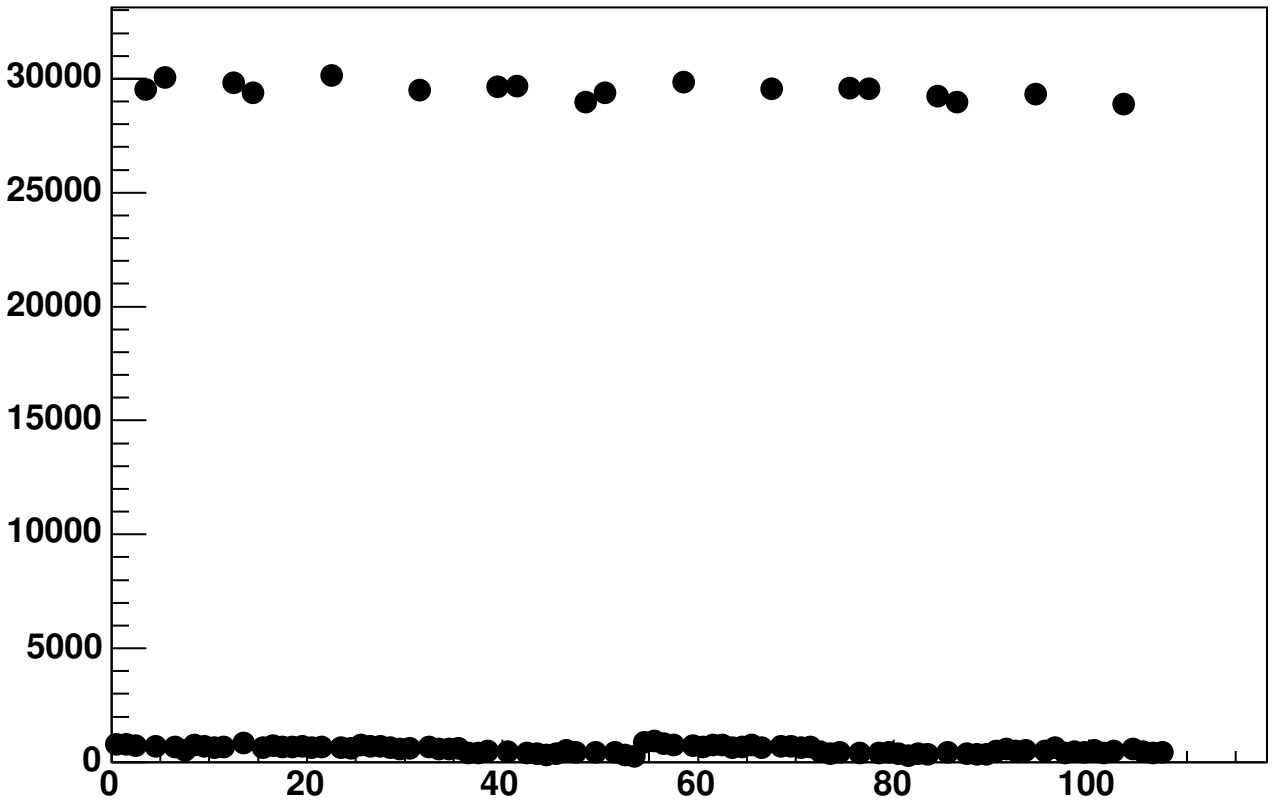
Enable 1, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



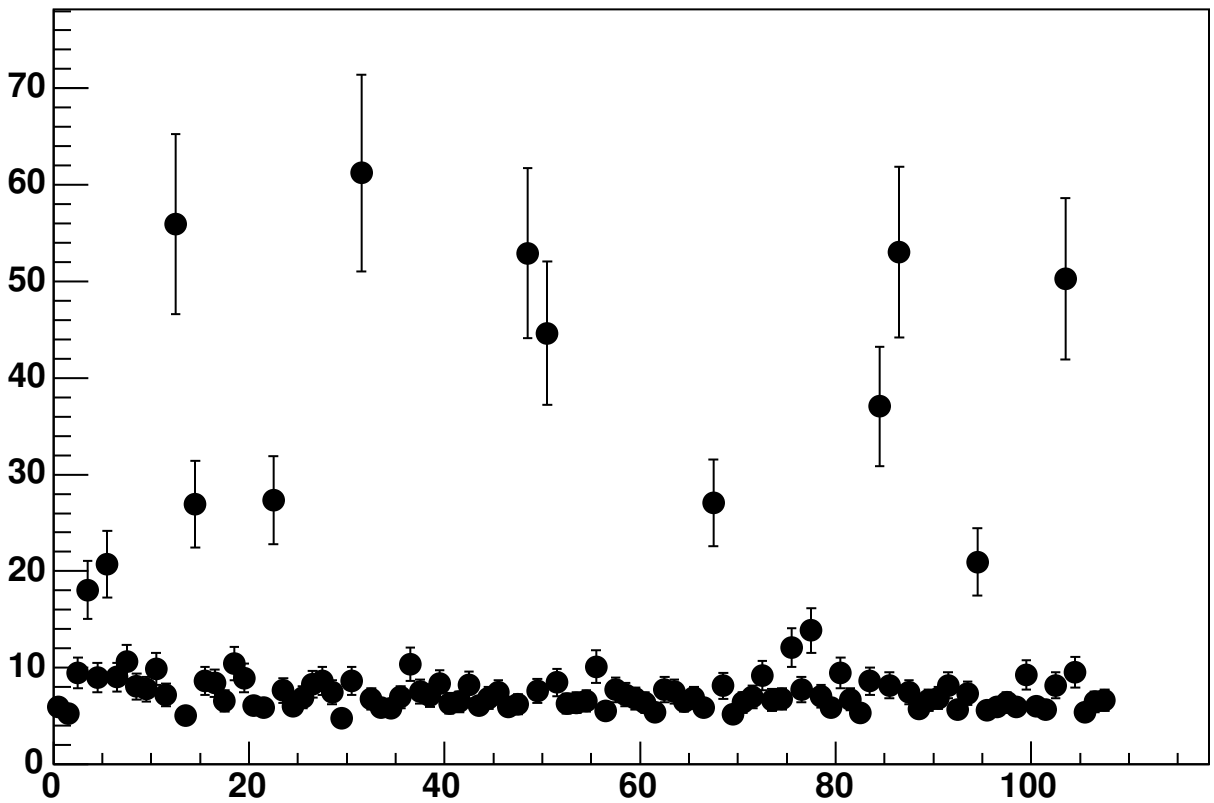
Enable 1, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



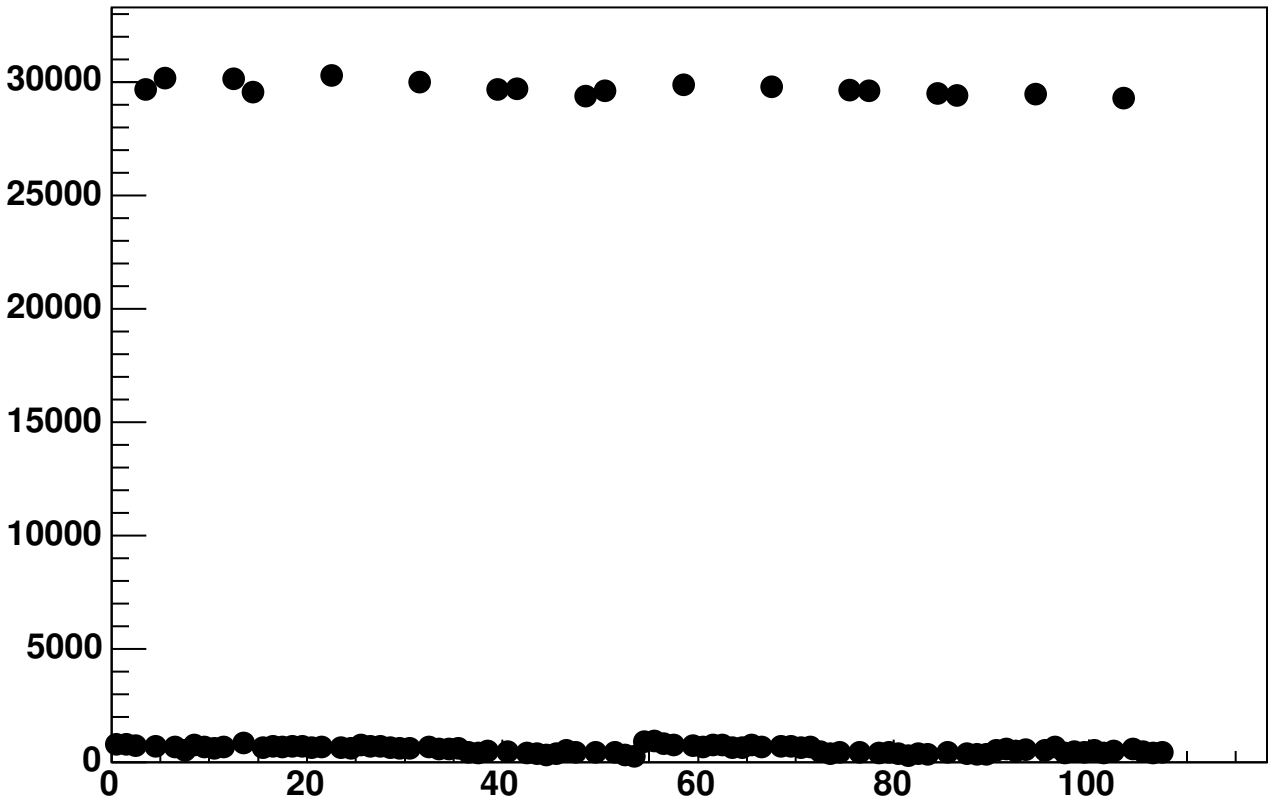
Enable 1, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



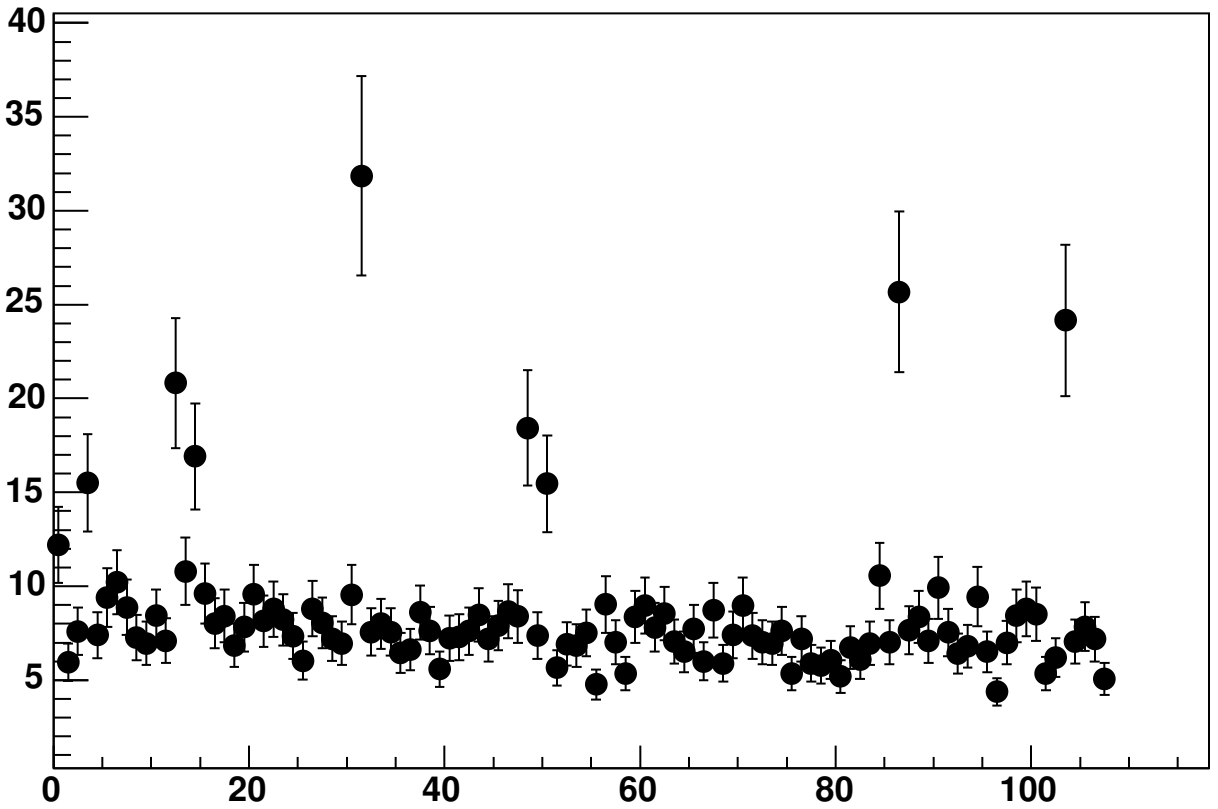
Enable 1, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



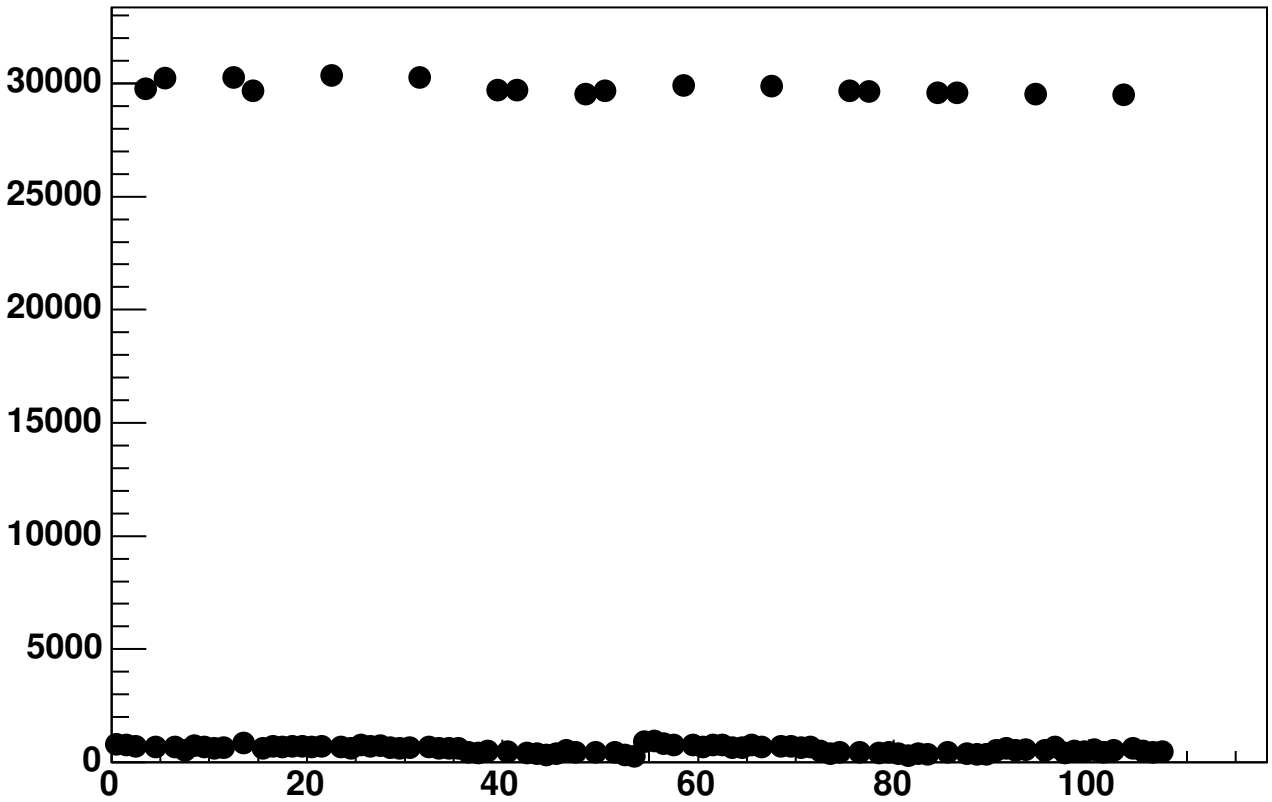
Enable 1, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



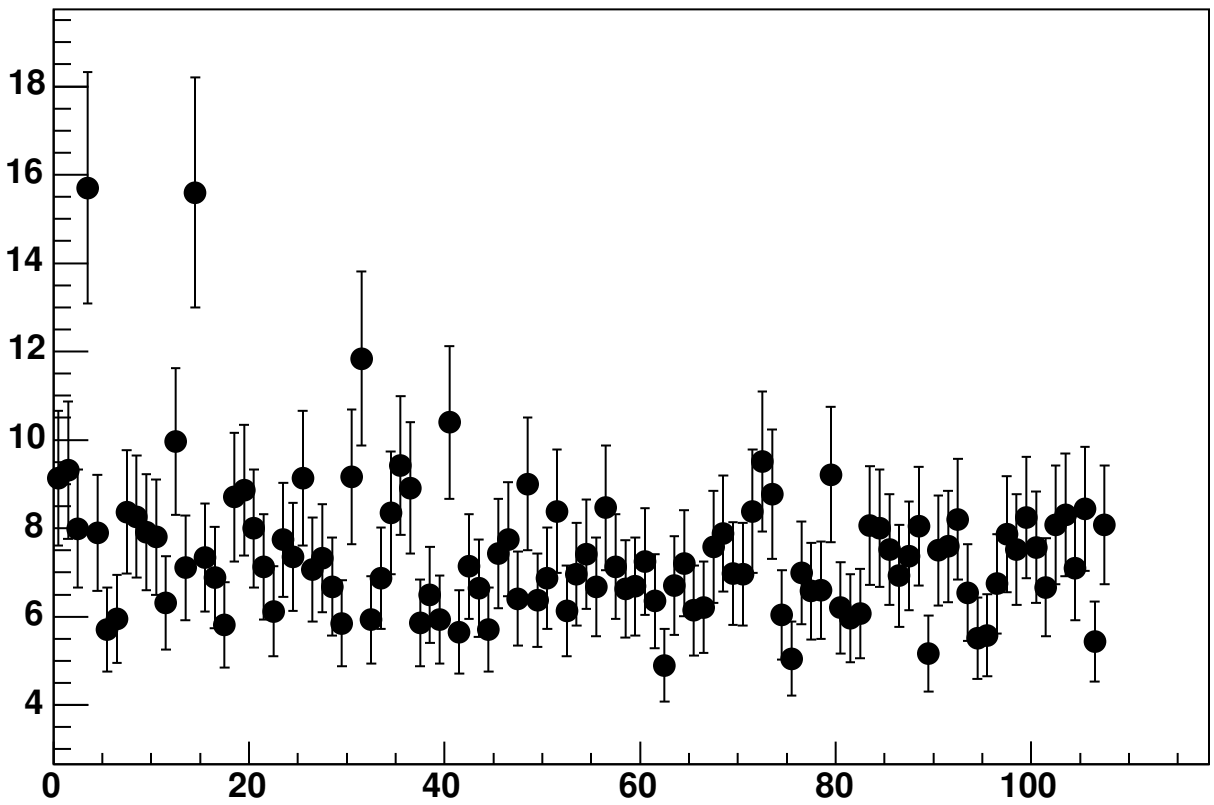
Enable 1, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



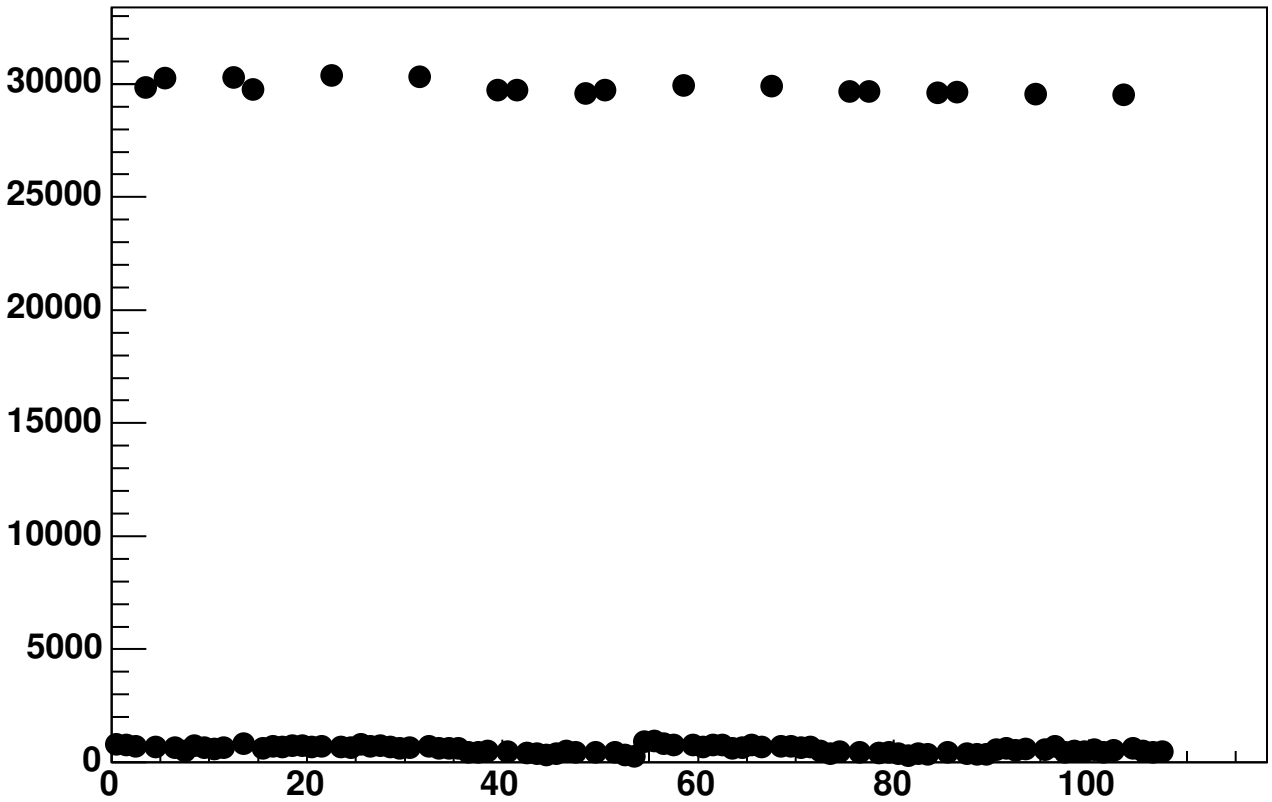
Enable 1, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



Enable 1, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



Enable 1, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



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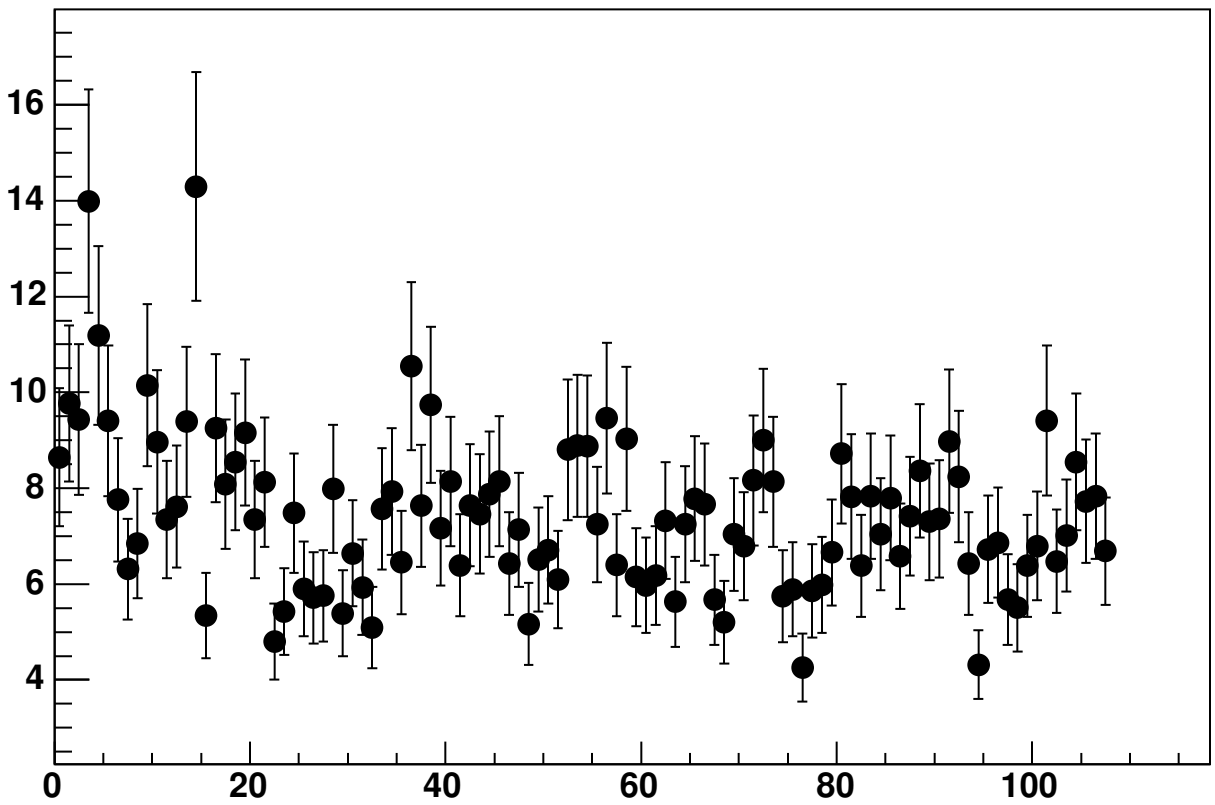
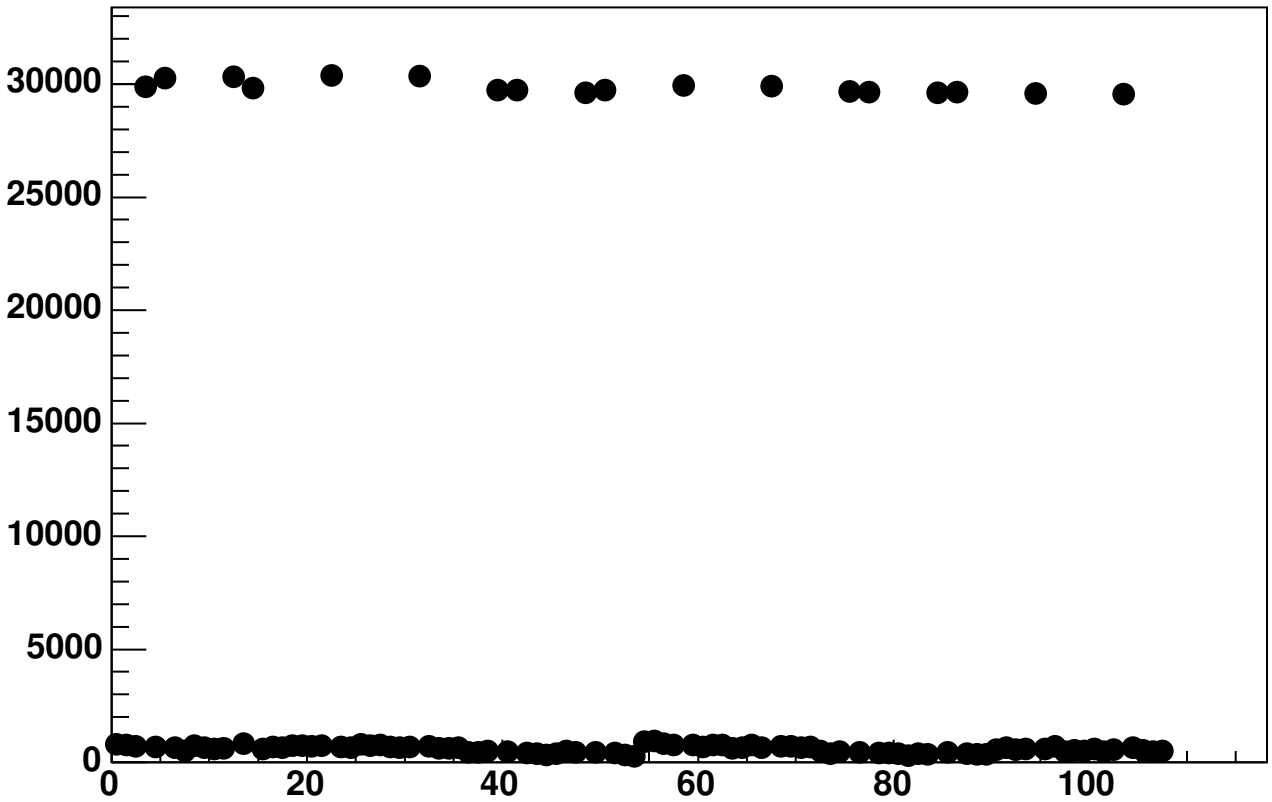
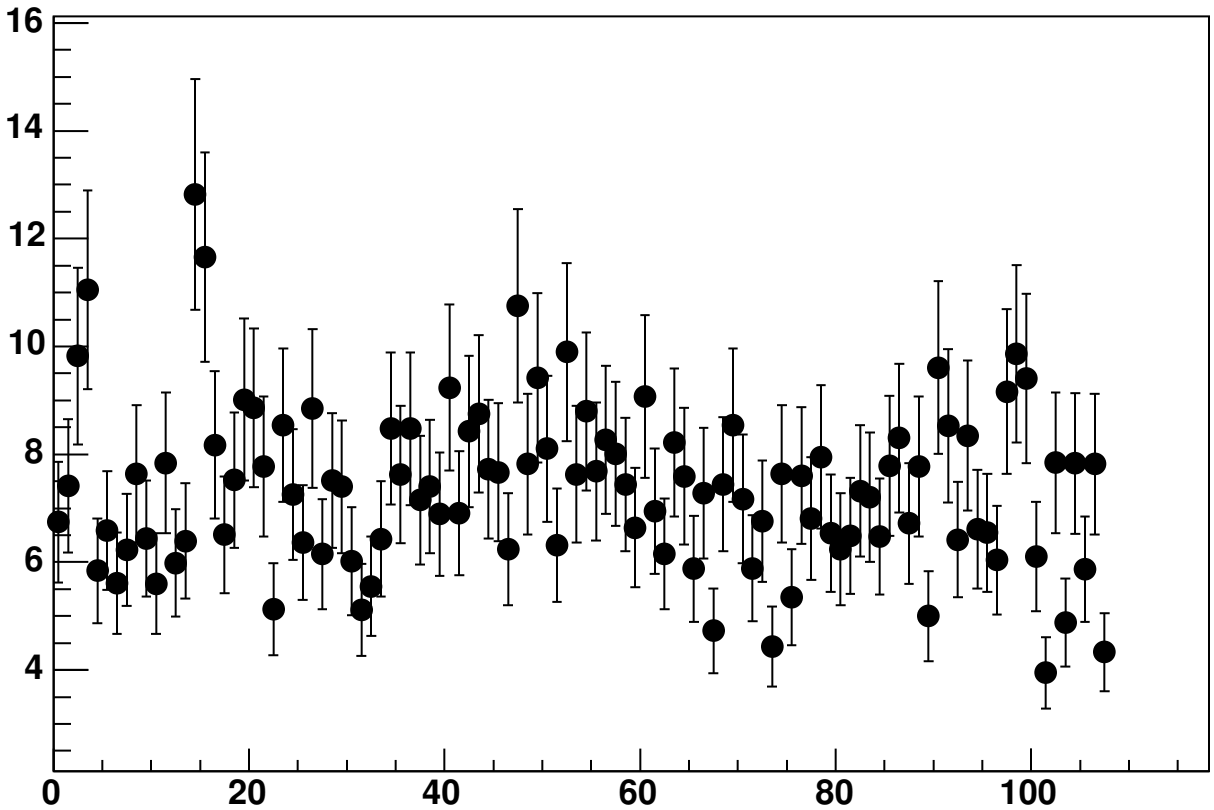


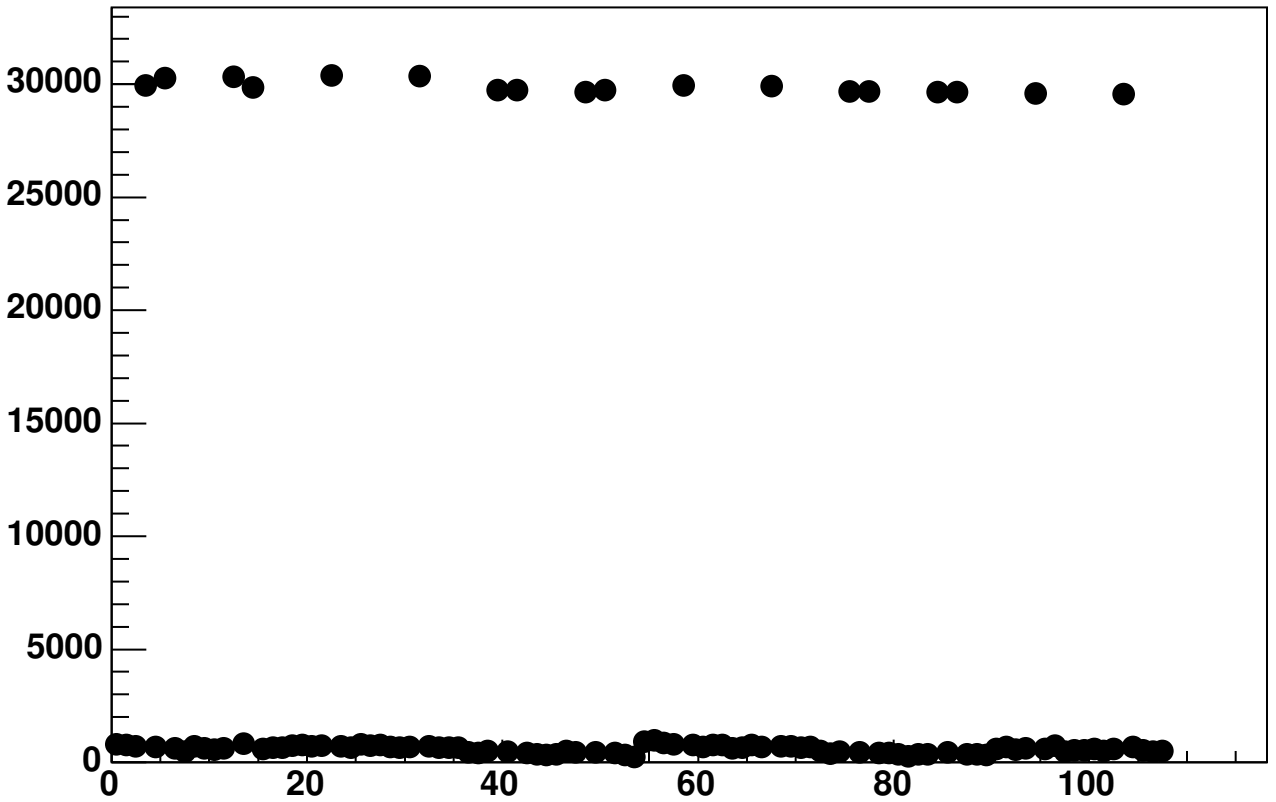
Enable 1, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



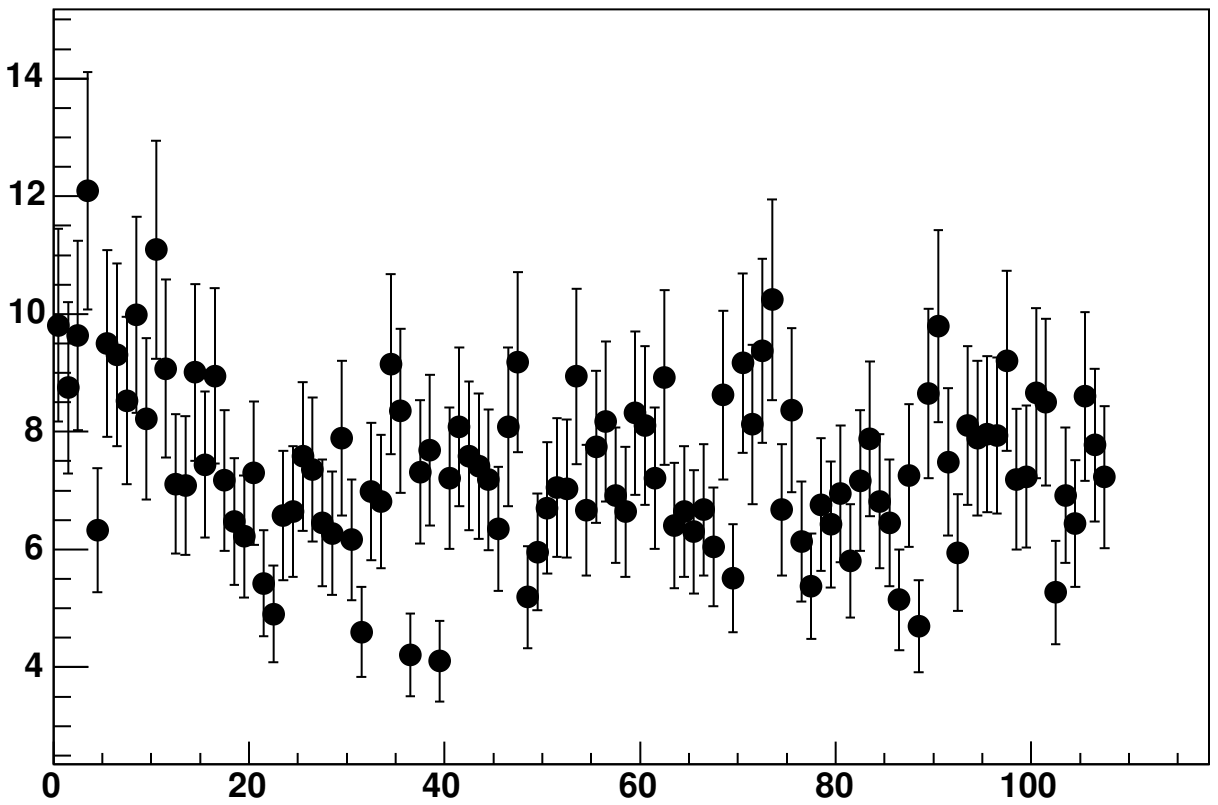
Enable 1, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



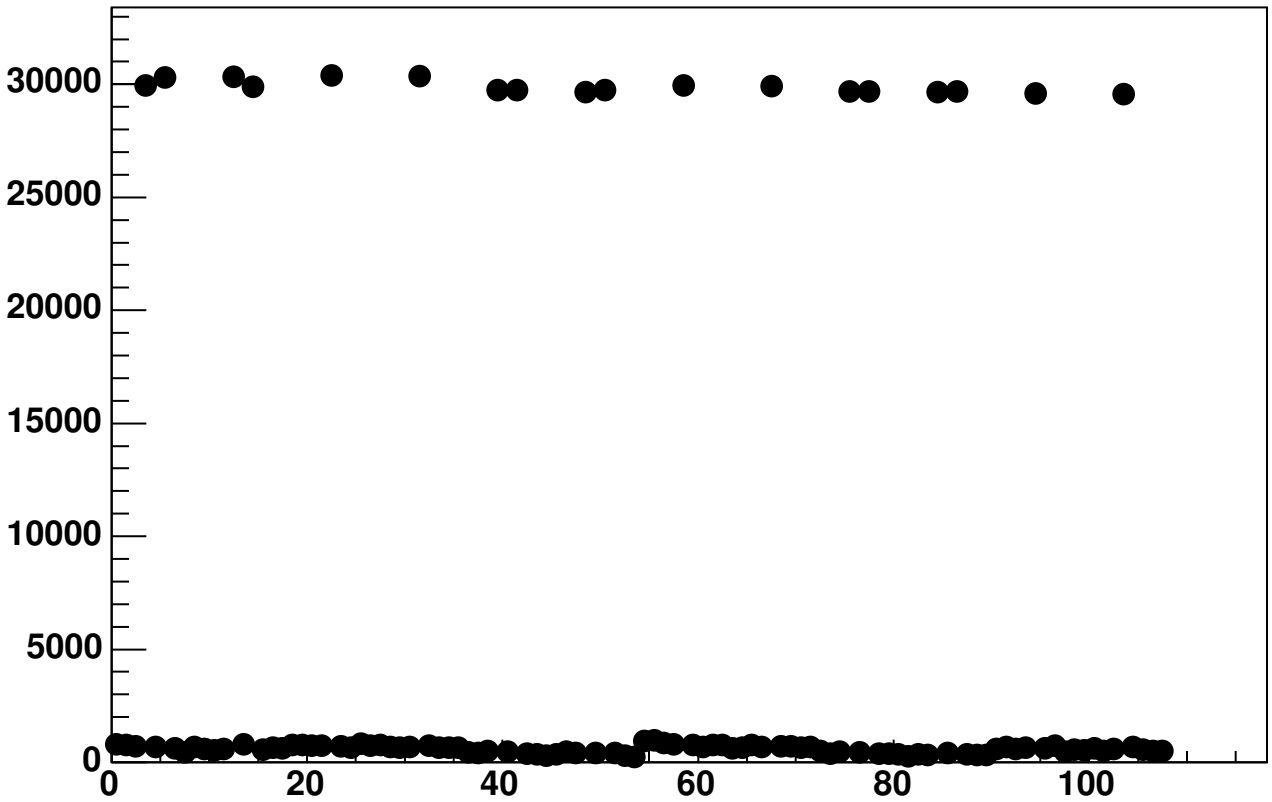
Enable 1, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



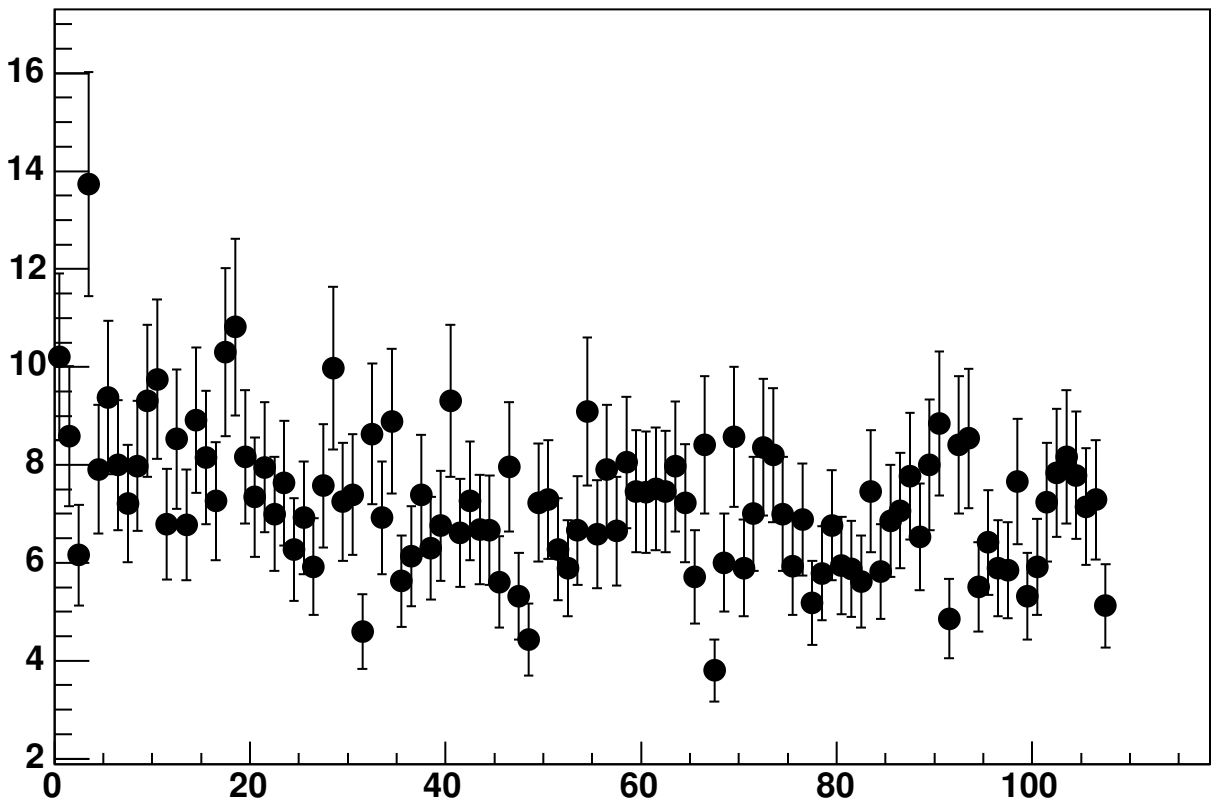
Enable 1, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



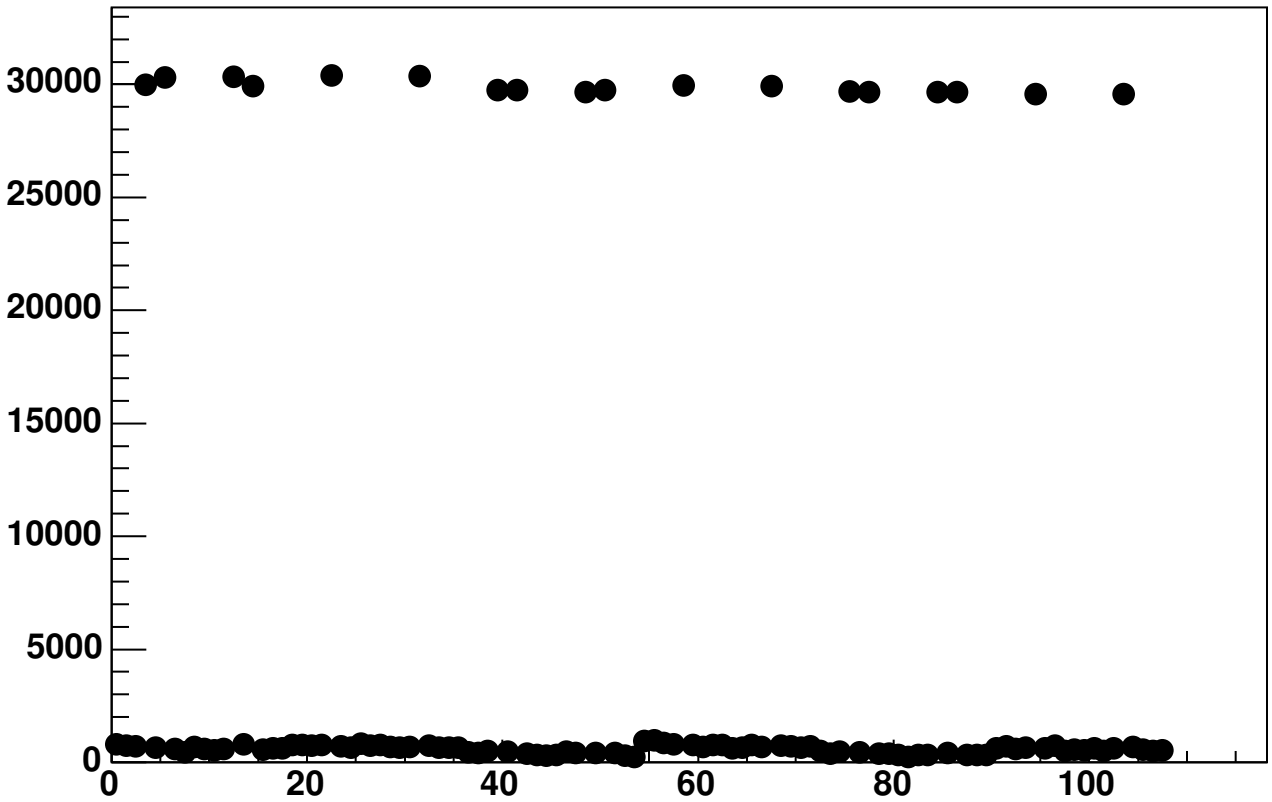
Enable 1, Hold=35, DAC=14400, ADC Mean vs 18*Chip+Chan



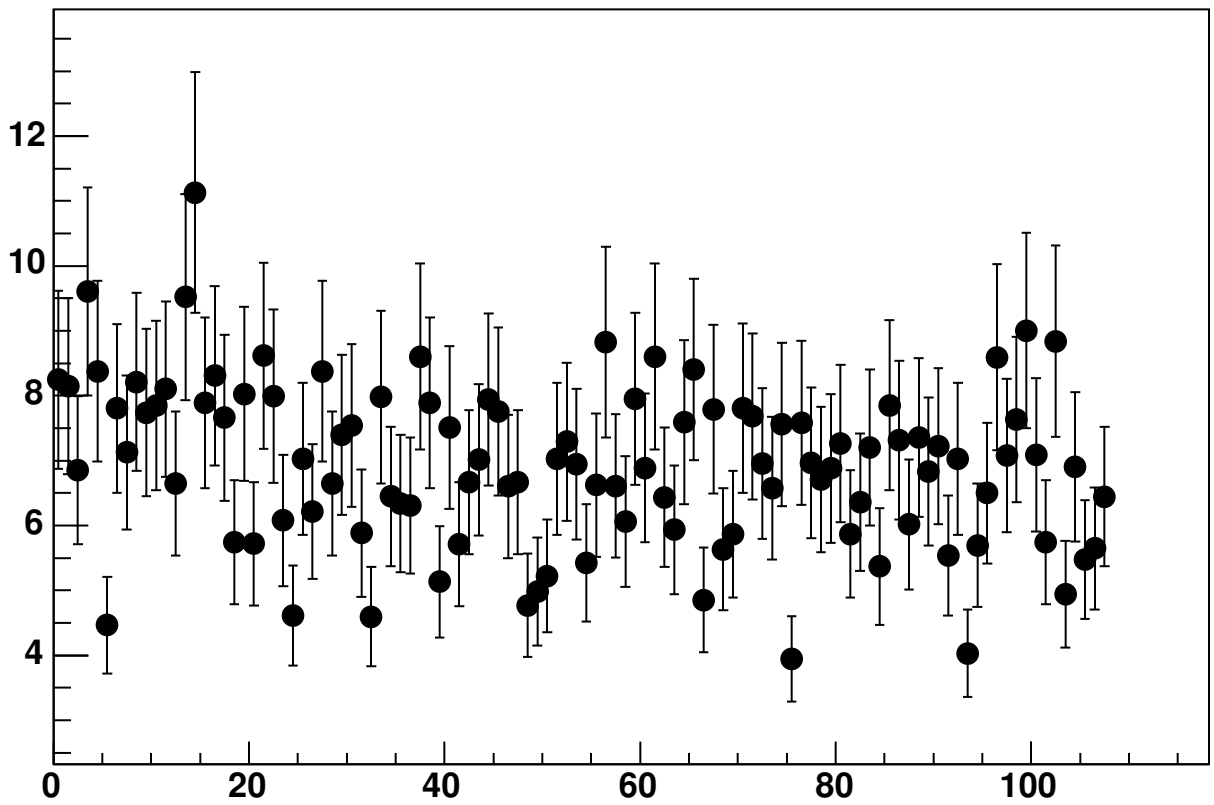
Enable 1, Hold=35, DAC=14400, ADC Noise vs 18*Chip+Chan



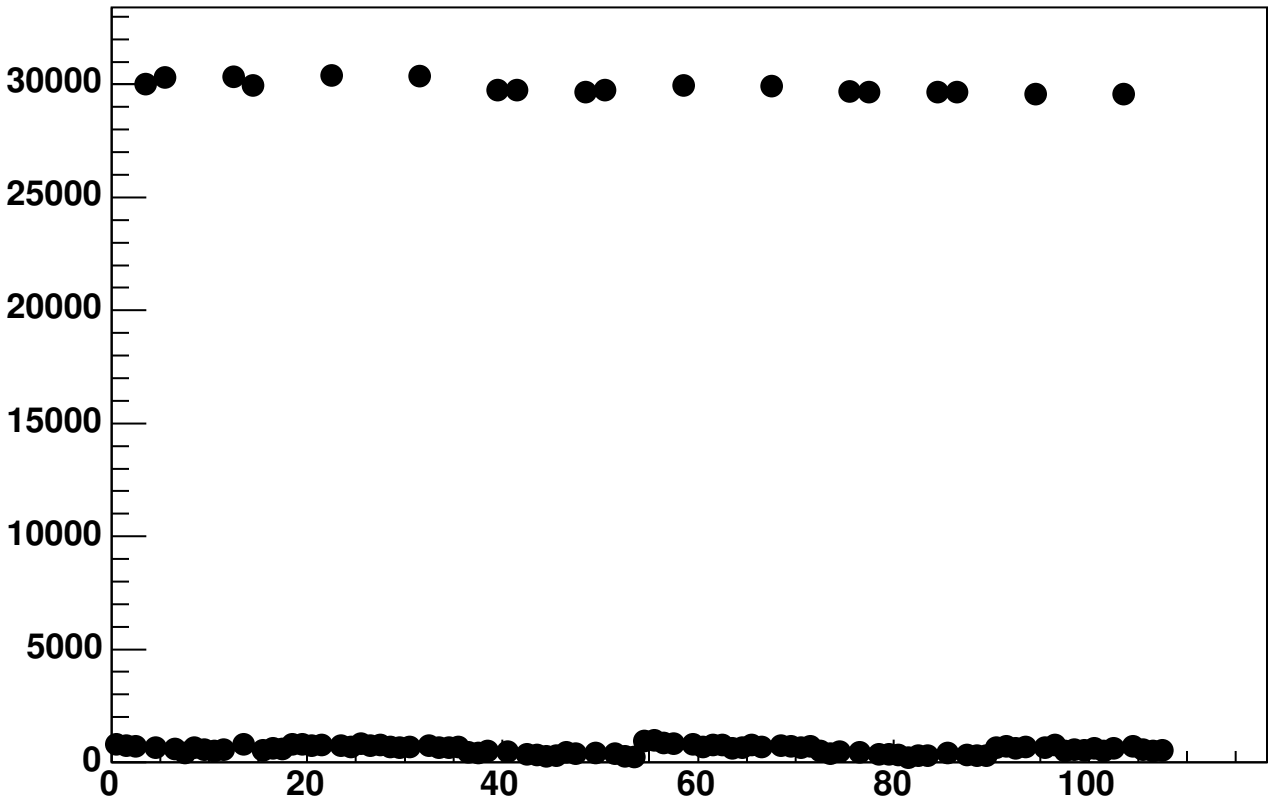
Enable 1, Hold=35, DAC=14800, ADC Mean vs 18*Chip+Chan



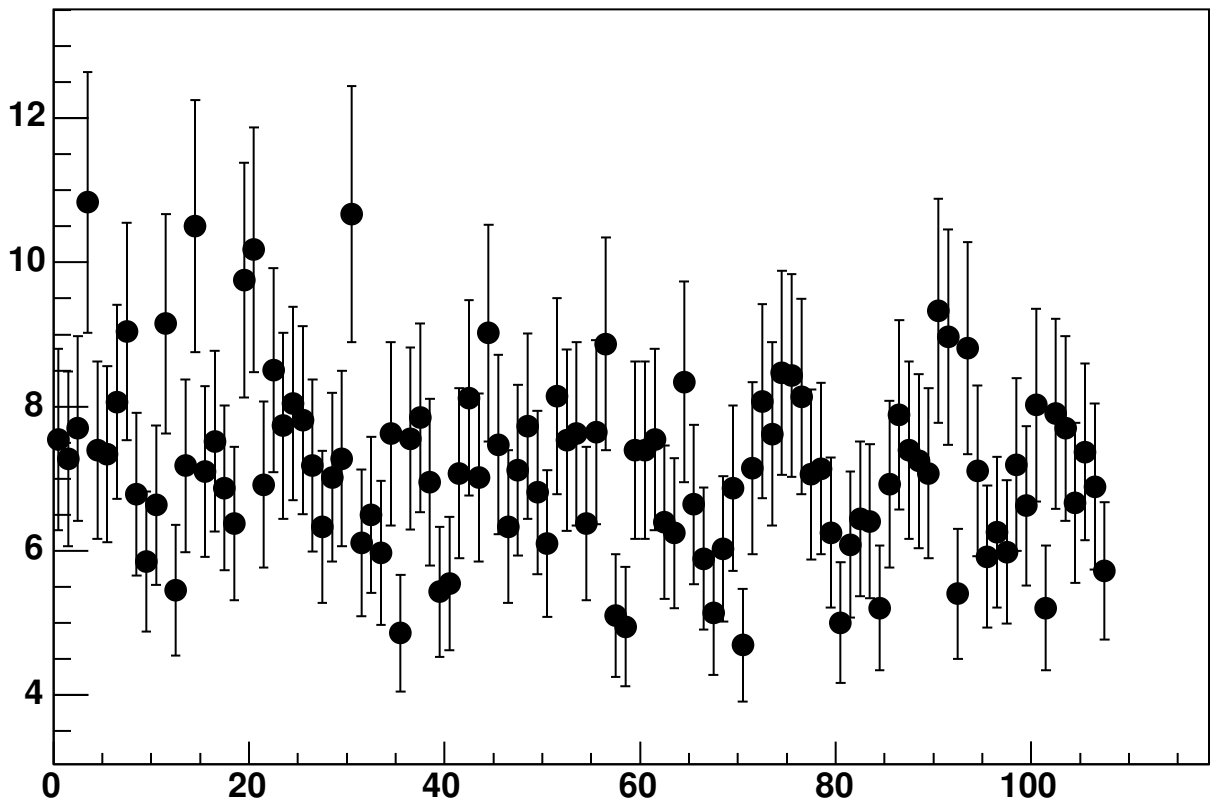
Enable 1, Hold=35, DAC=14800, ADC Noise vs 18*Chip+Chan



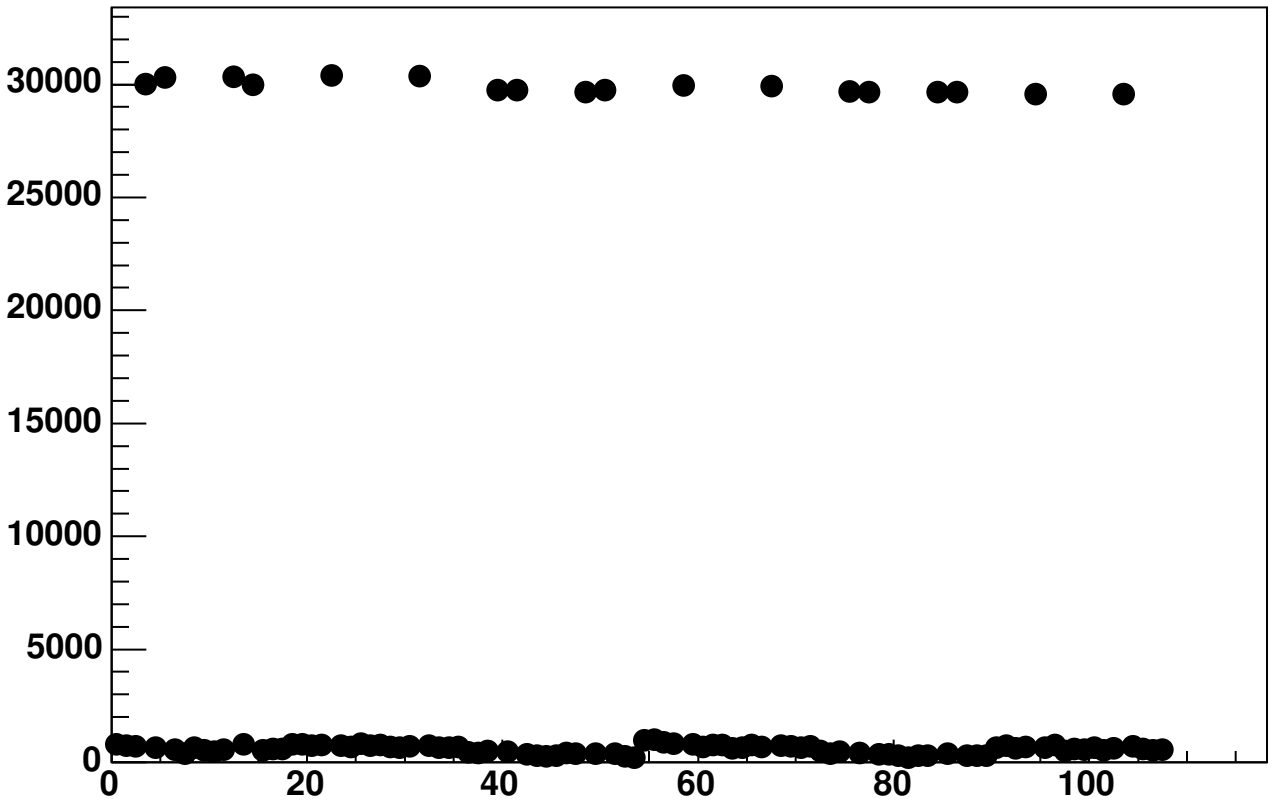
Enable 1, Hold=35, DAC=15200, ADC Mean vs 18*Chip+Chan



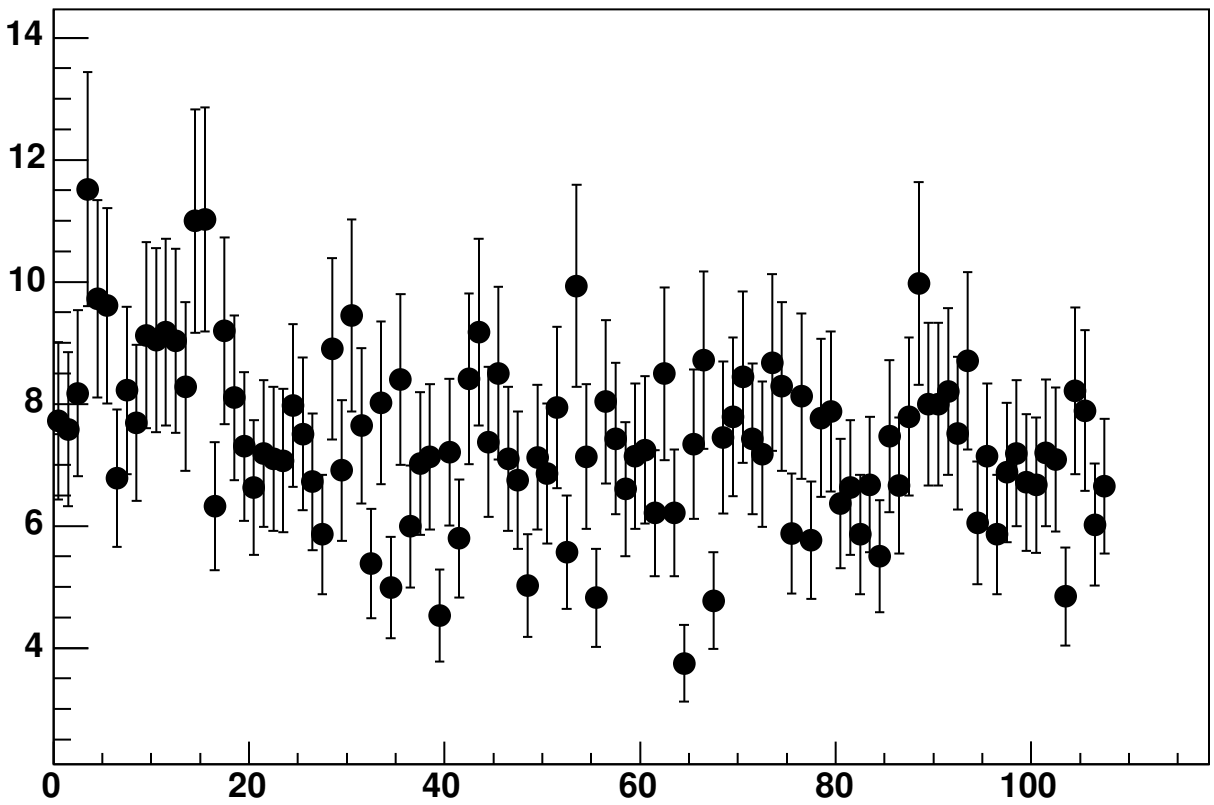
Enable 1, Hold=35, DAC=15200, ADC Noise vs 18*Chip+Chan



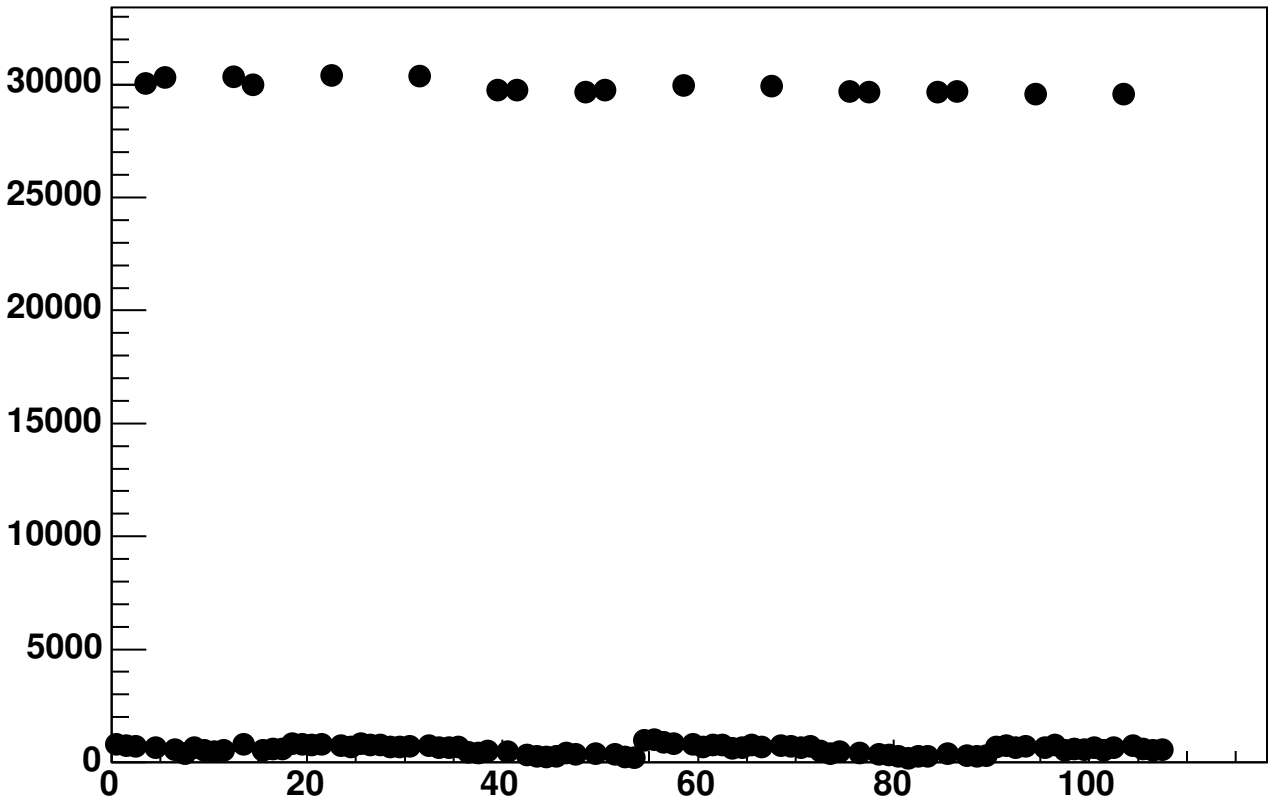
Enable 1, Hold=35, DAC=15600, ADC Mean vs 18*Chip+Chan



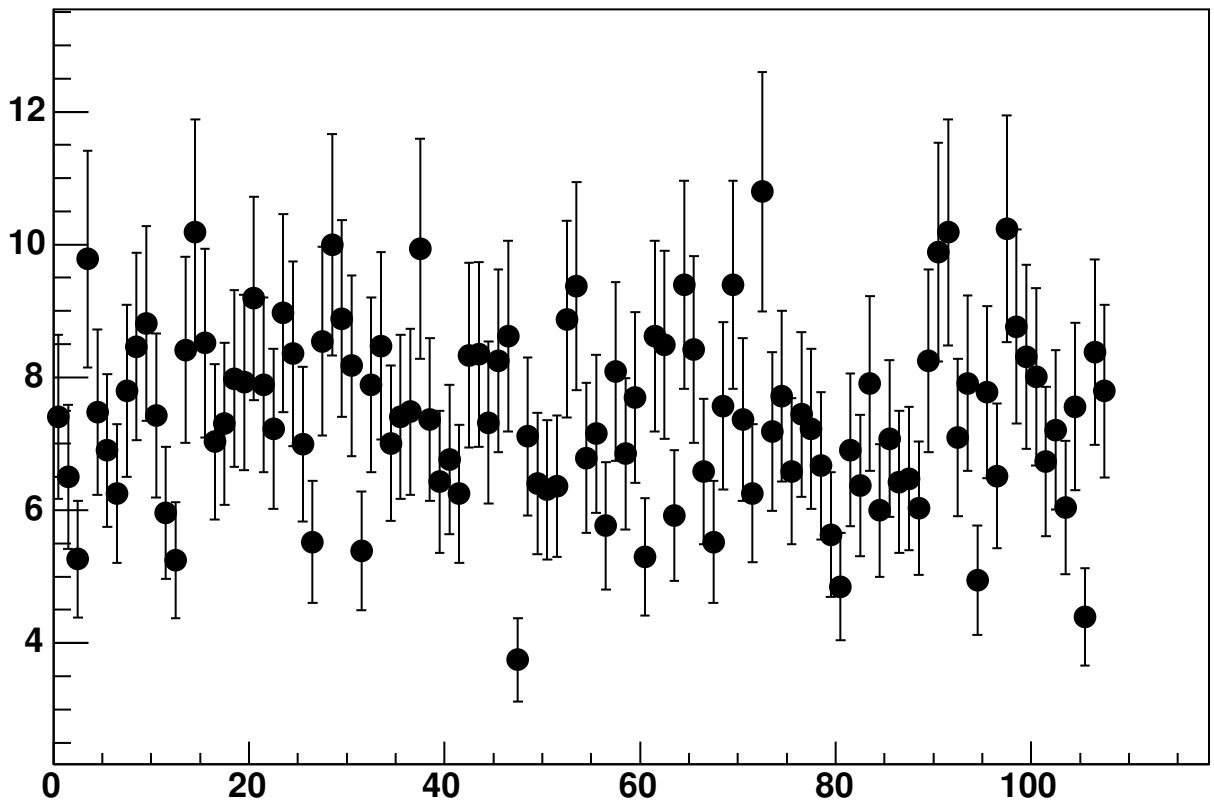
Enable 1, Hold=35, DAC=15600, ADC Noise vs 18*Chip+Chan



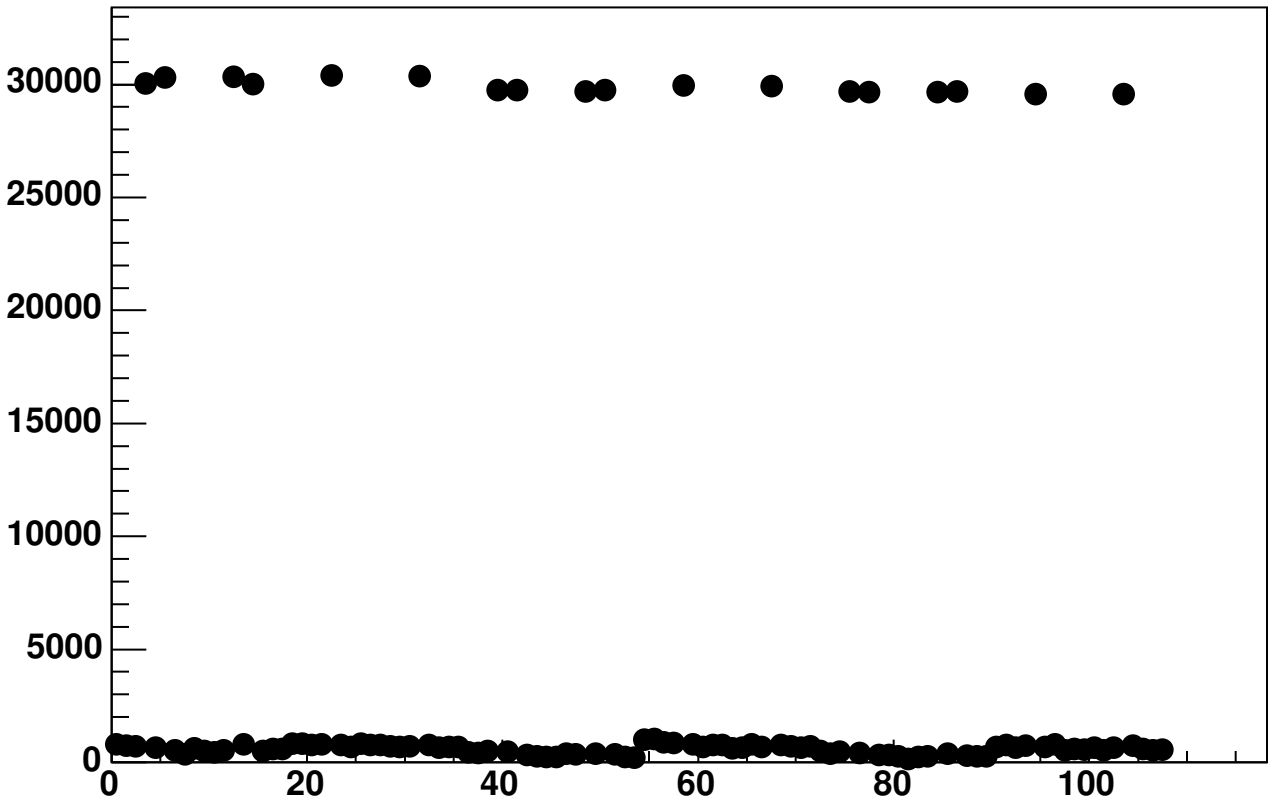
Enable 1, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



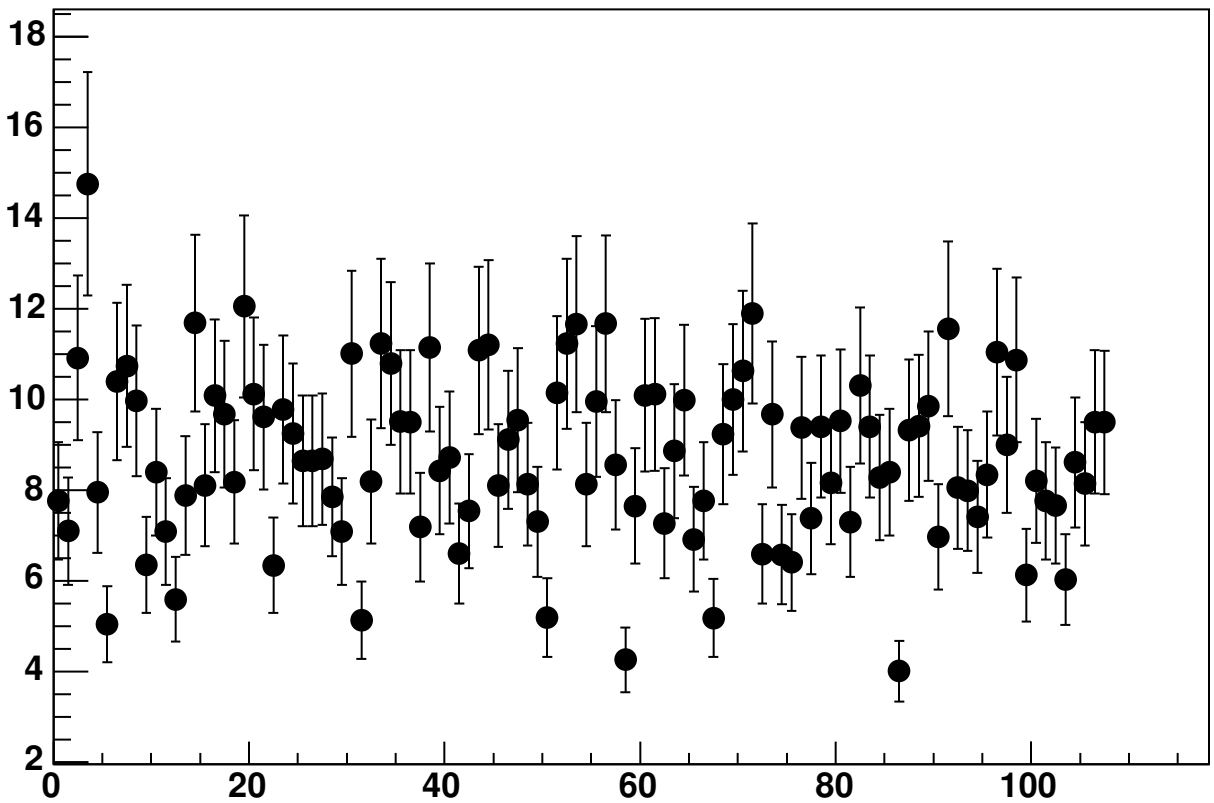
Enable 1, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



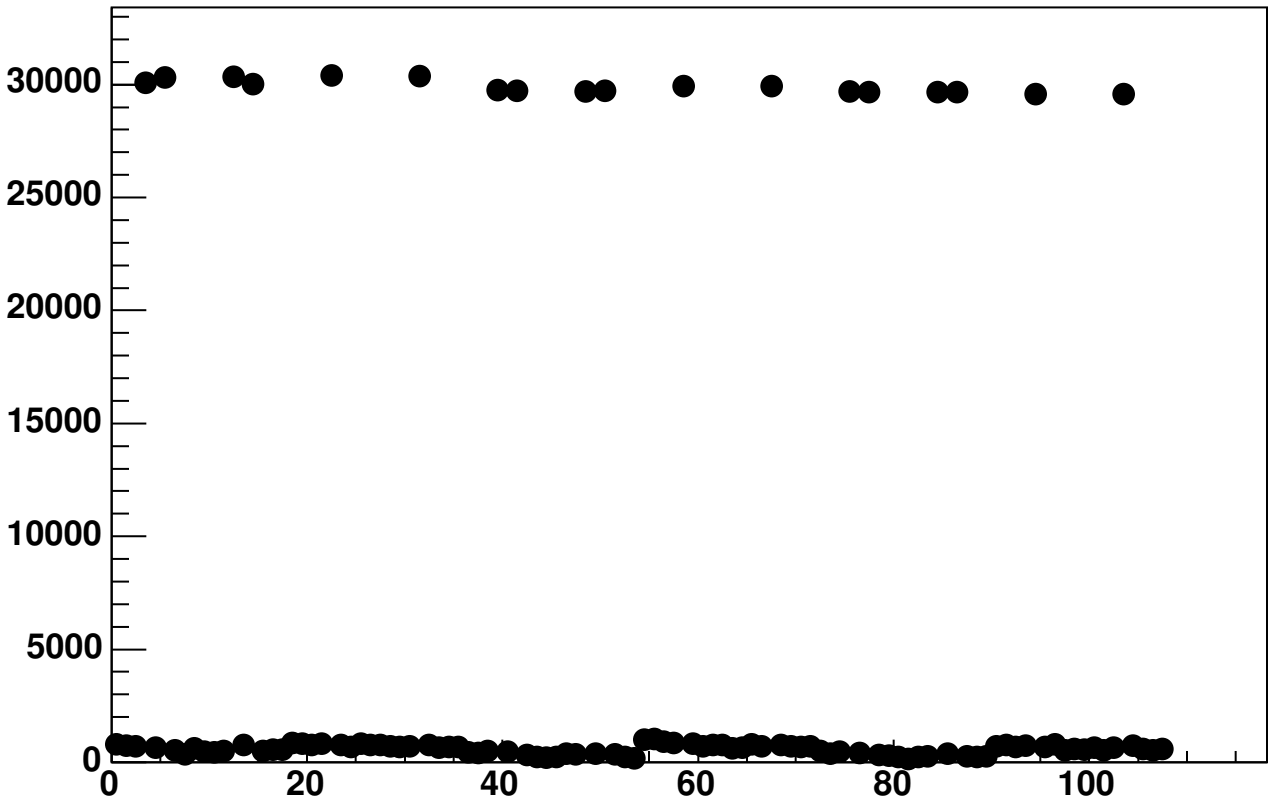
Enable 1, Hold=35, DAC=16400, ADC Mean vs 18*Chip+Chan



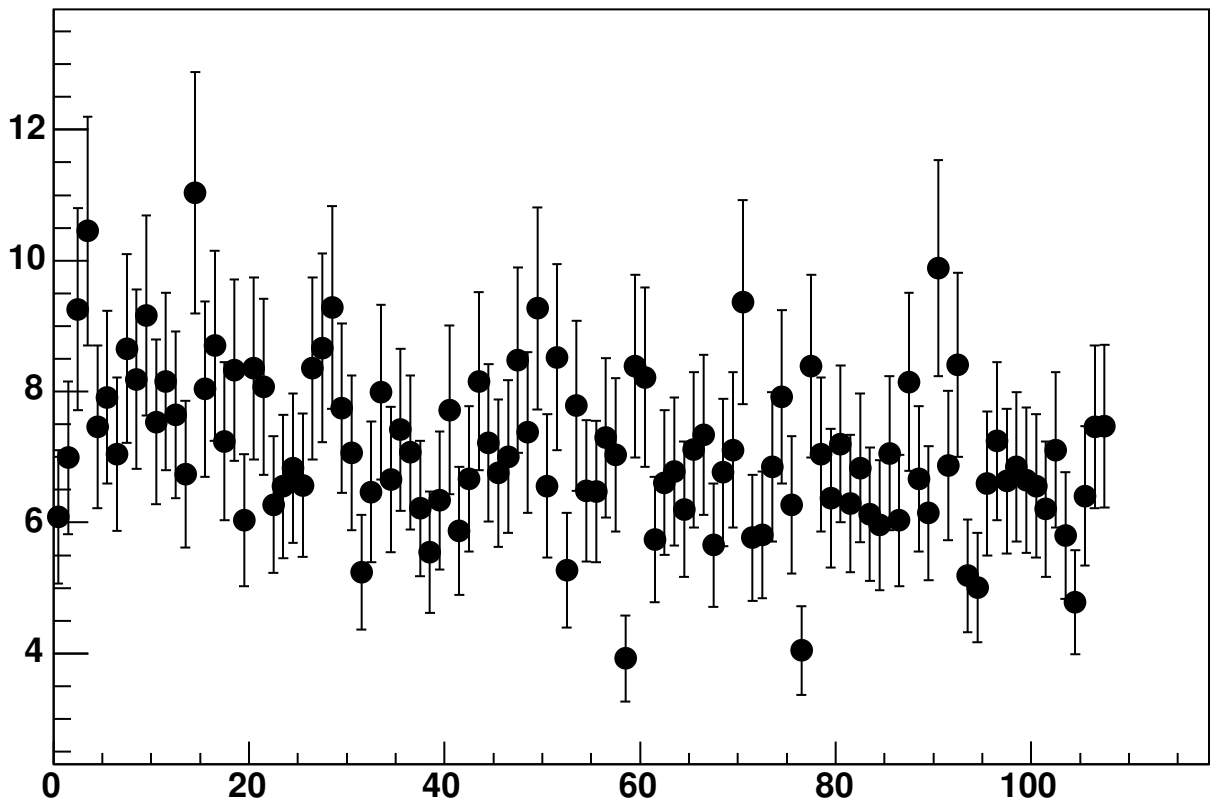
Enable 1, Hold=35, DAC=16400, ADC Noise vs 18*Chip+Chan



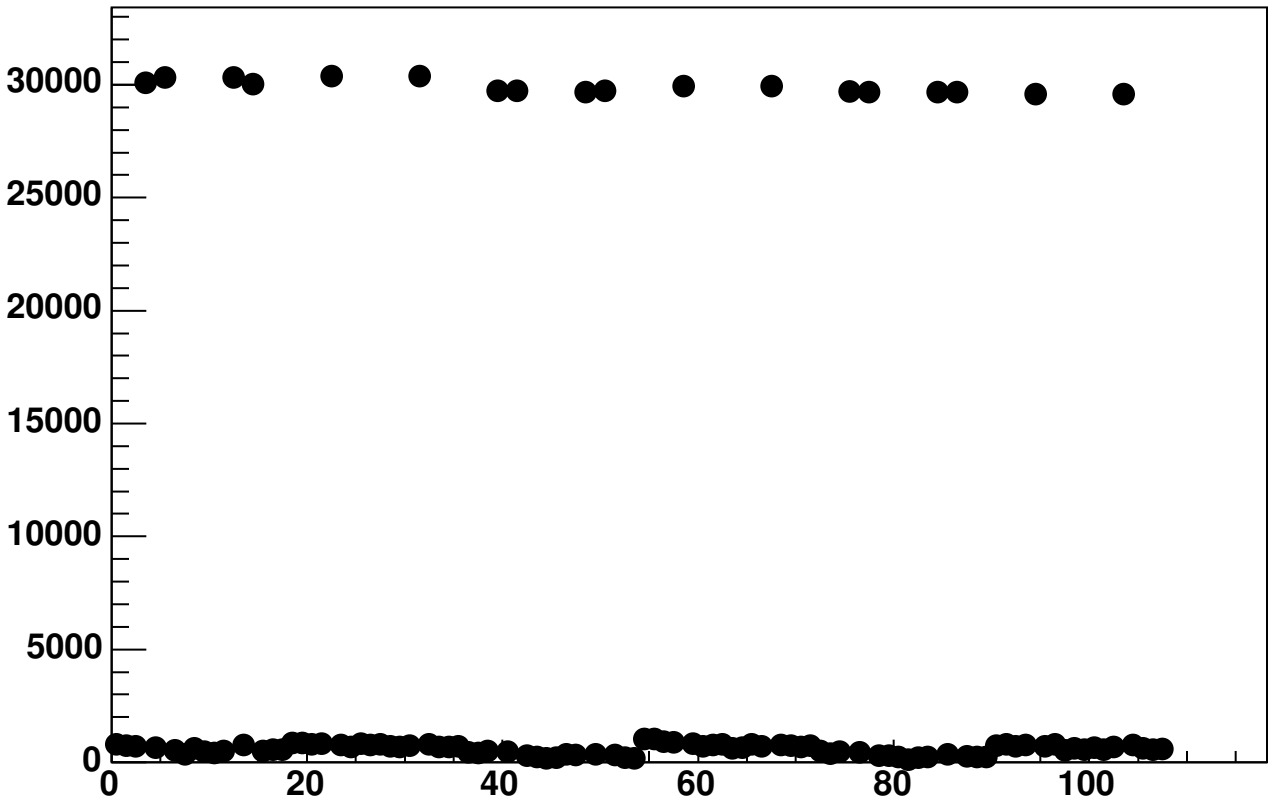
Enable 1, Hold=35, DAC=16800, ADC Mean vs 18*Chip+Chan



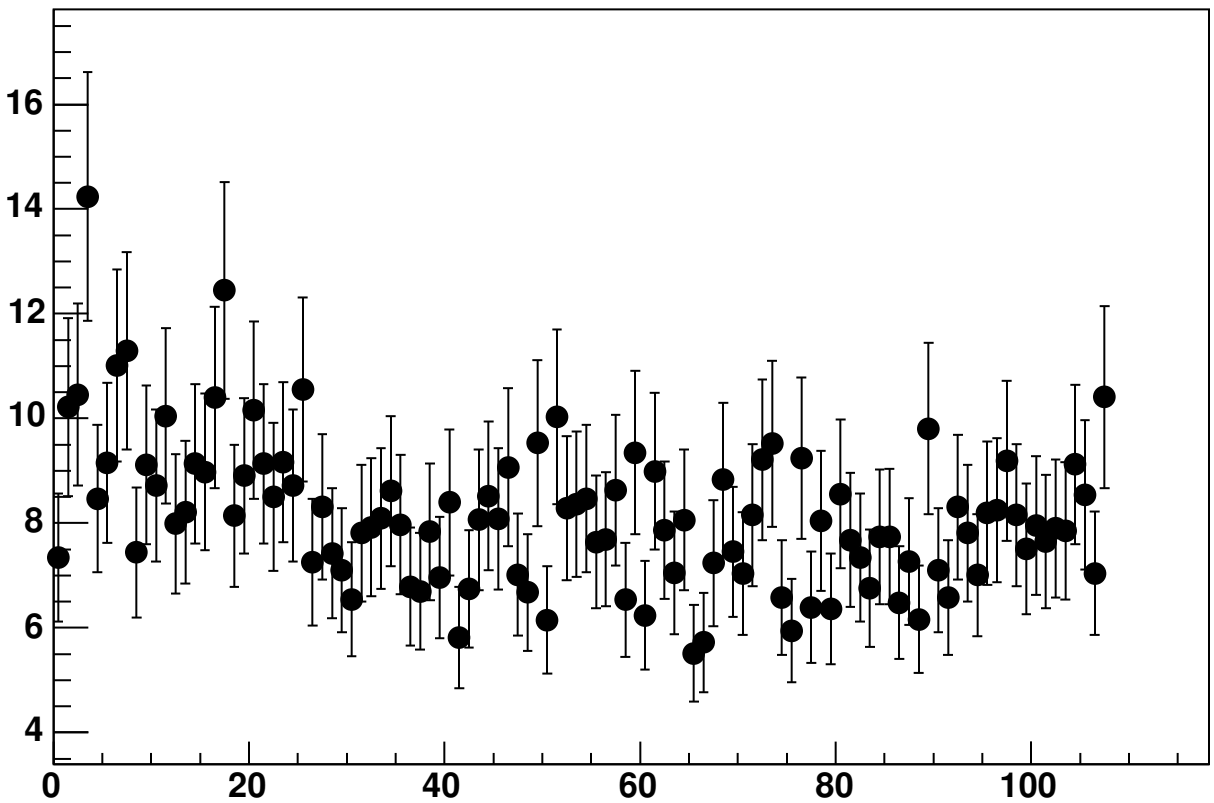
Enable 1, Hold=35, DAC=16800, ADC Noise vs 18*Chip+Chan



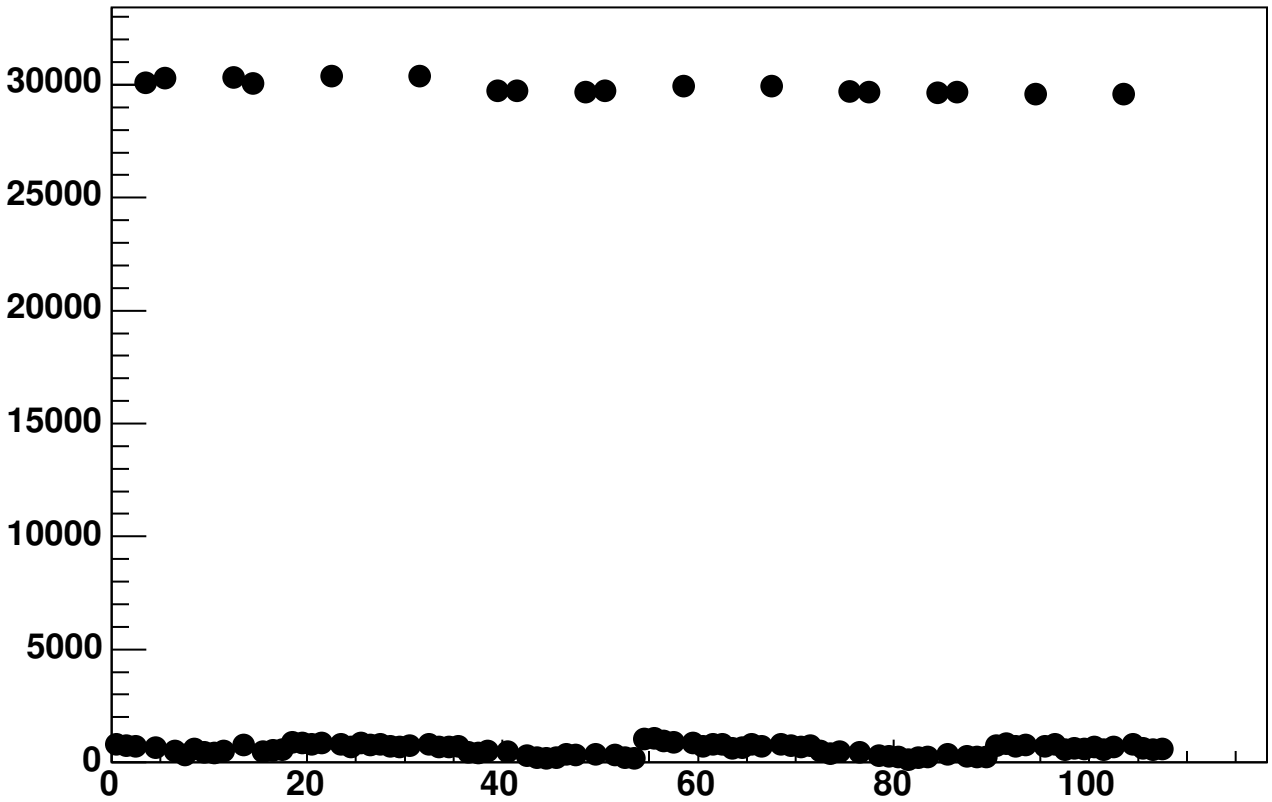
Enable 1, Hold=35, DAC=17200, ADC Mean vs 18*Chip+Chan



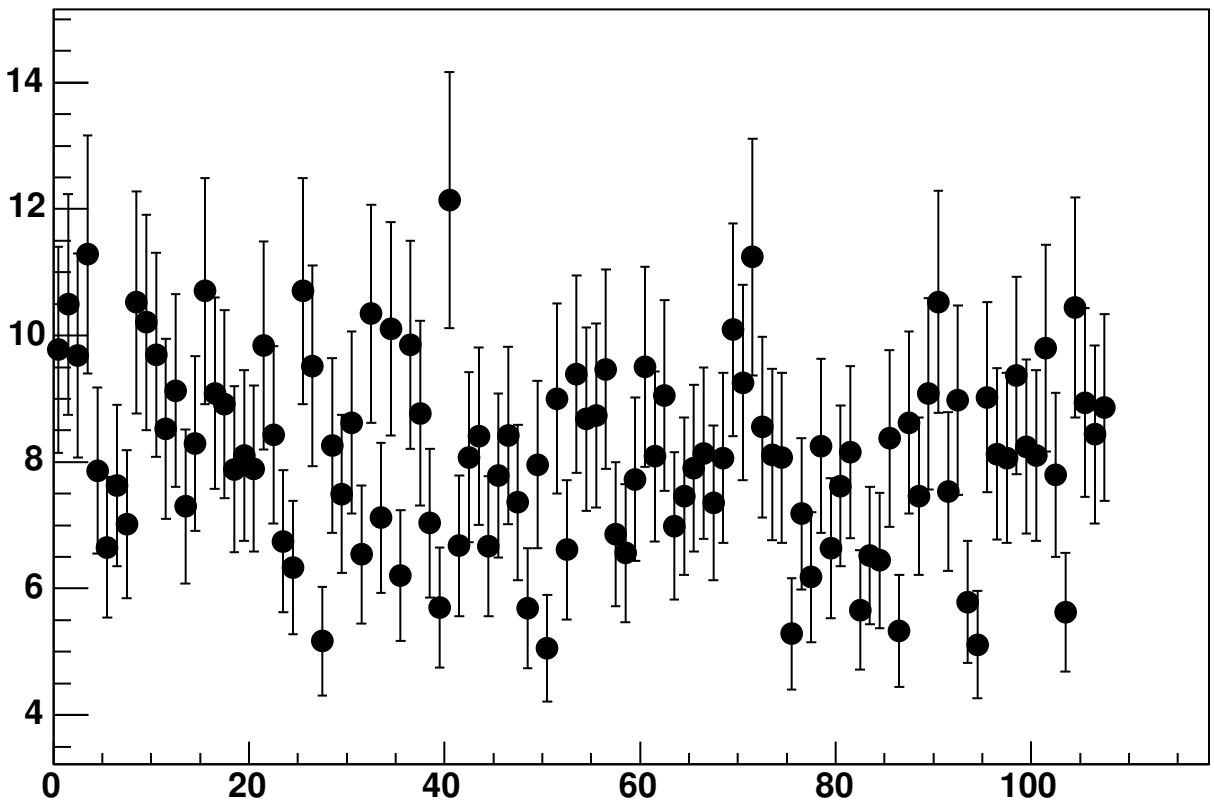
Enable 1, Hold=35, DAC=17200, ADC Noise vs 18*Chip+Chan



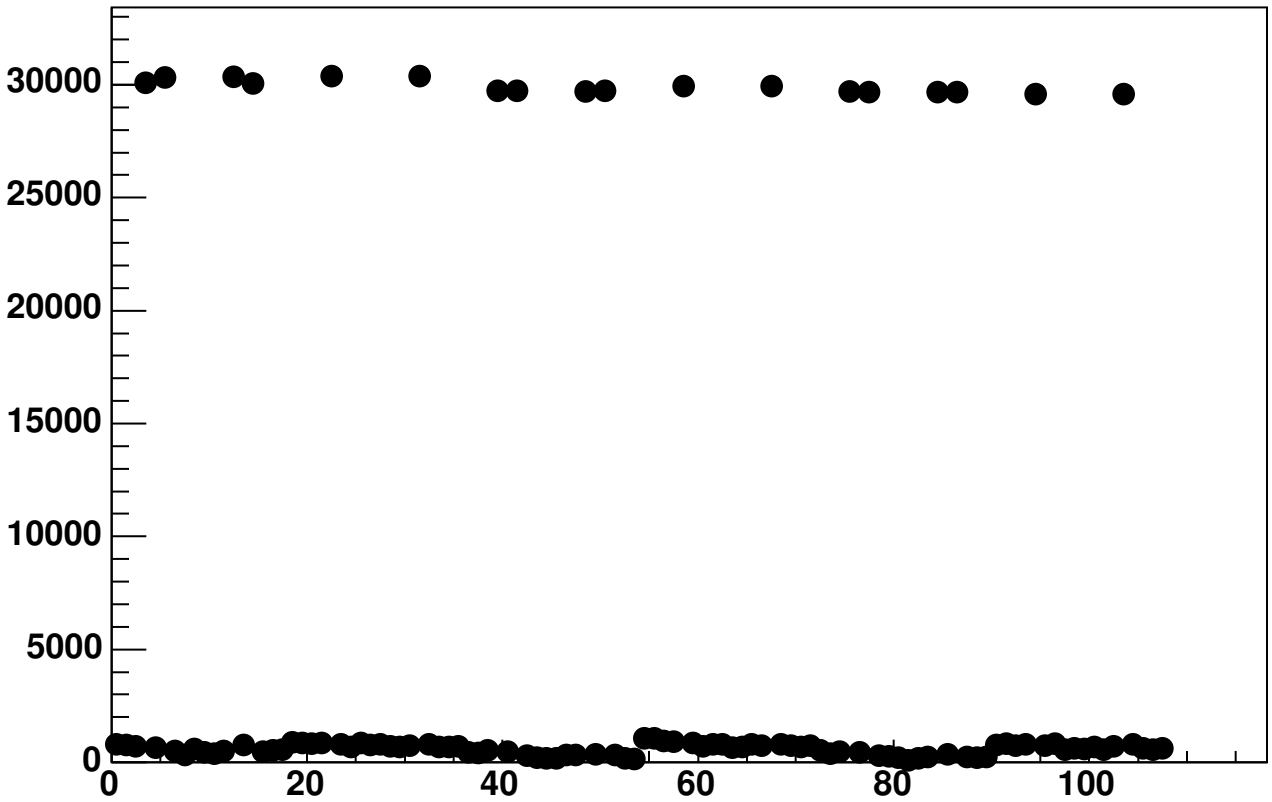
Enable 1, Hold=35, DAC=17600, ADC Mean vs 18*Chip+Chan



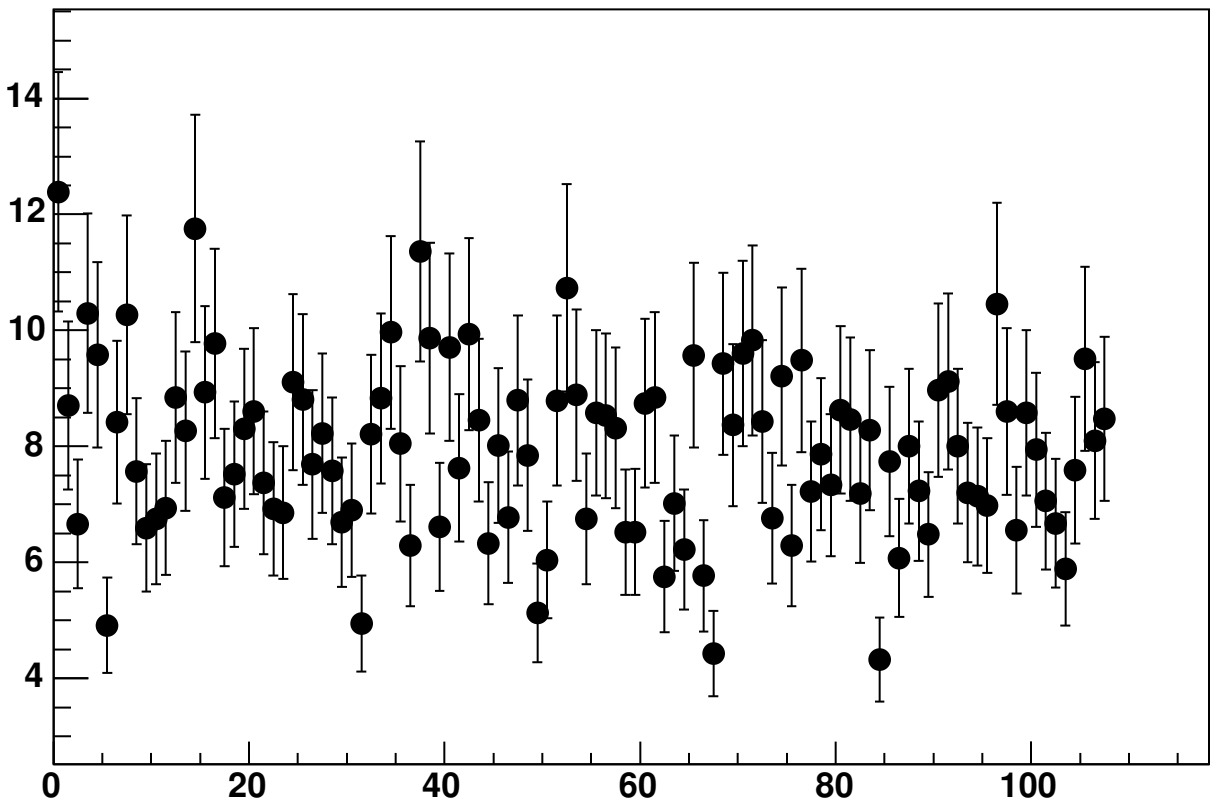
Enable 1, Hold=35, DAC=17600, ADC Noise vs 18*Chip+Chan



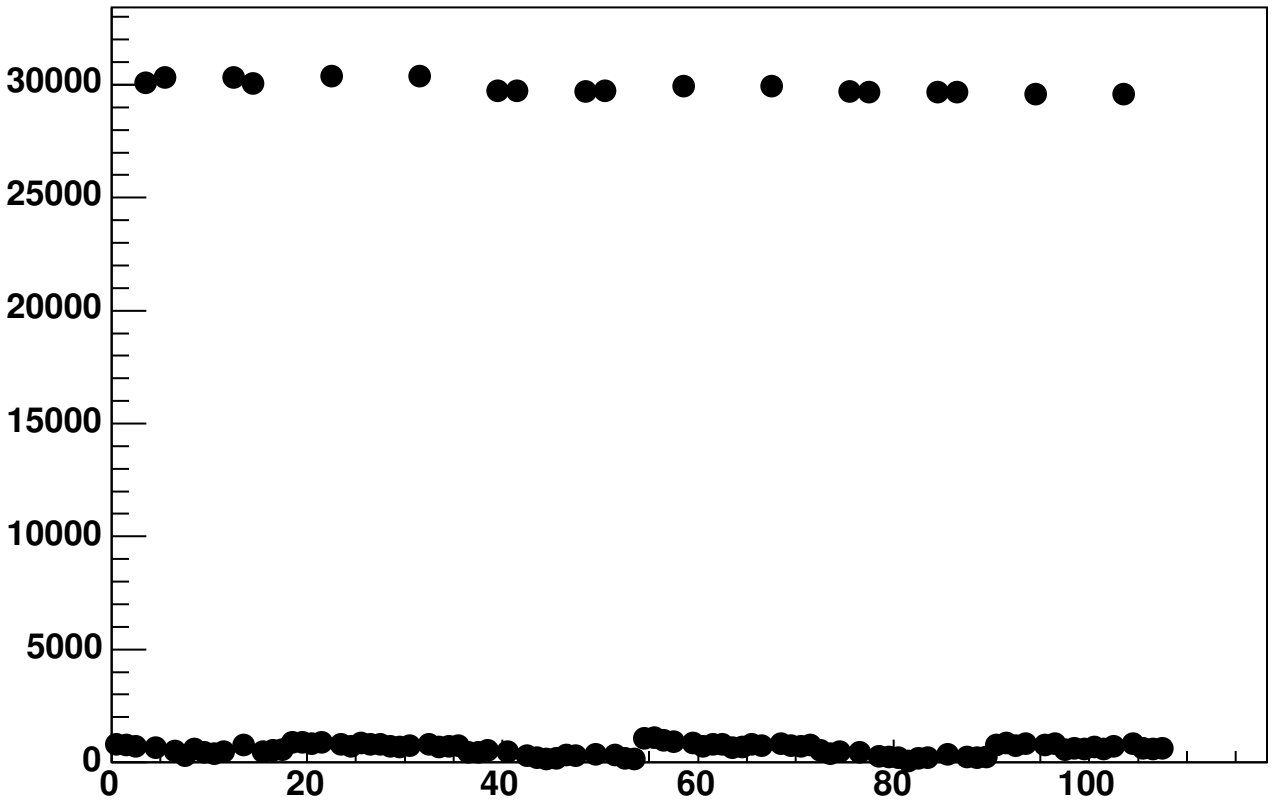
Enable 1, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



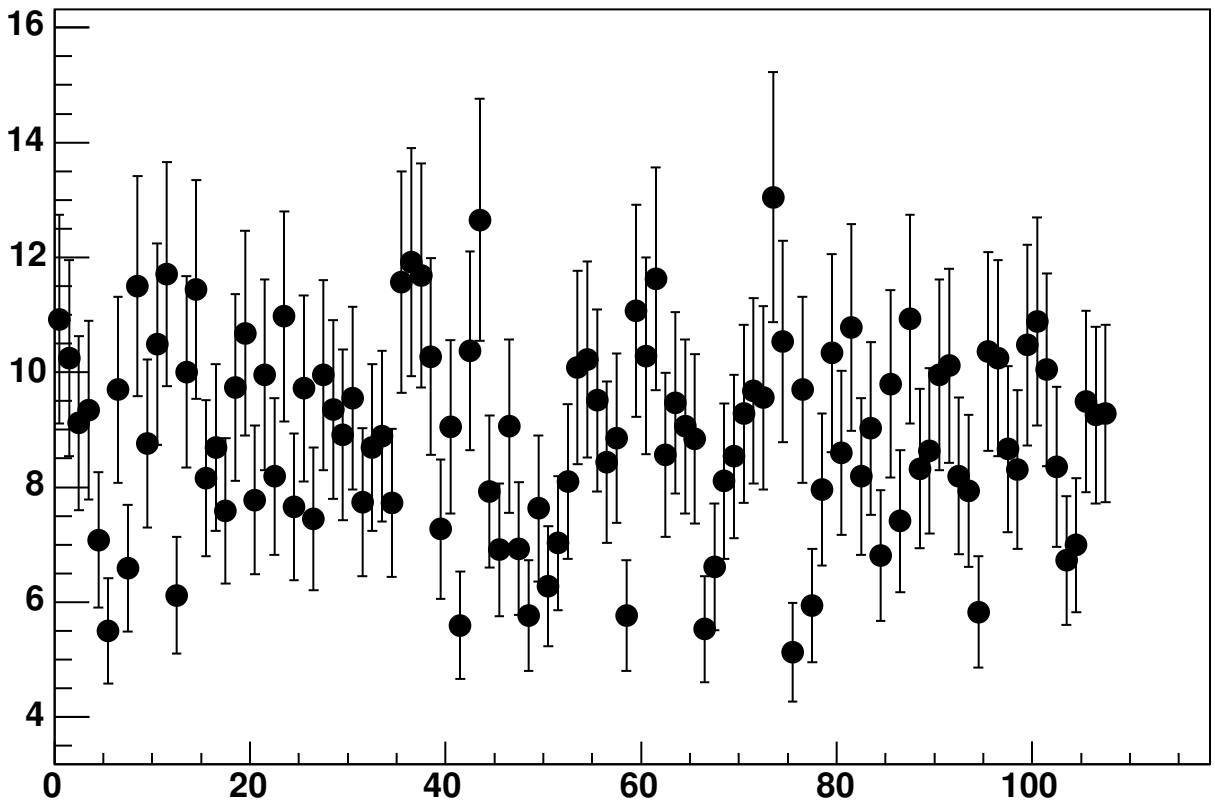
Enable 1, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



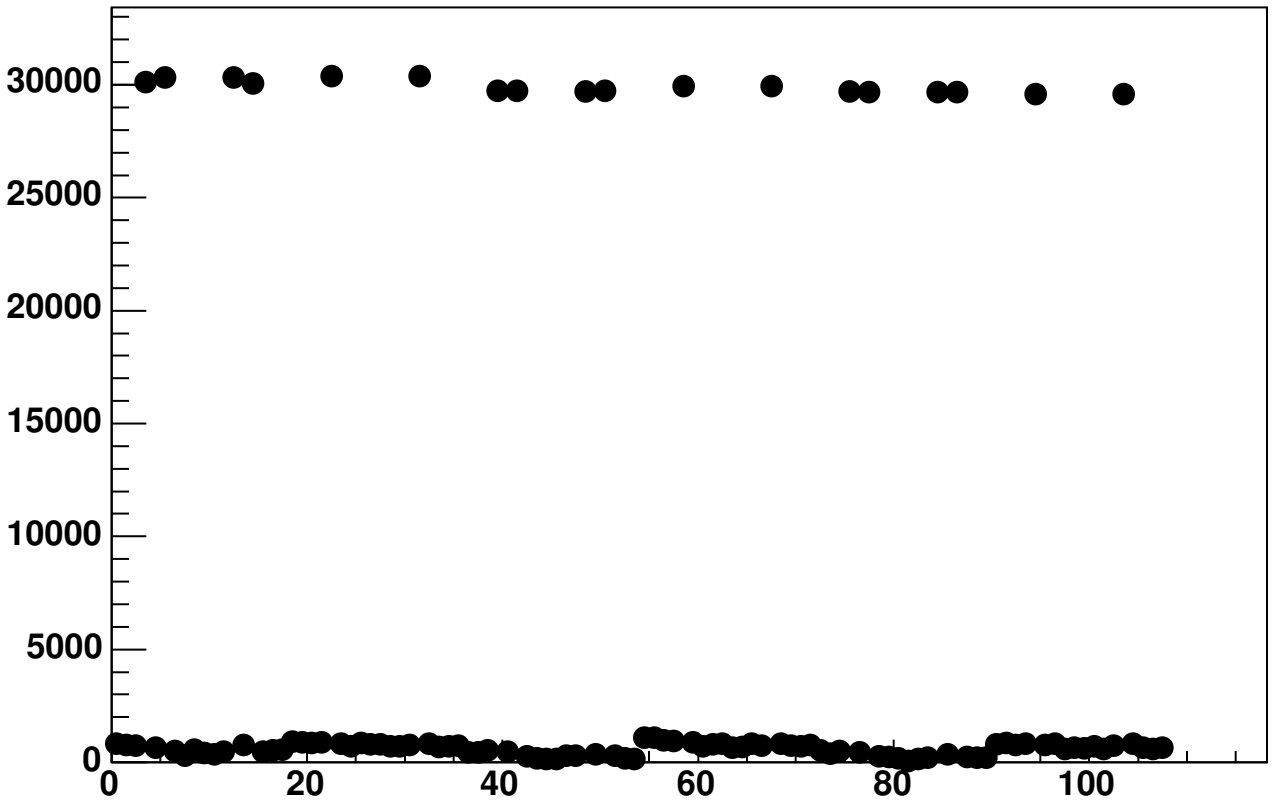
Enable 1, Hold=35, DAC=18400, ADC Mean vs 18*Chip+Chan



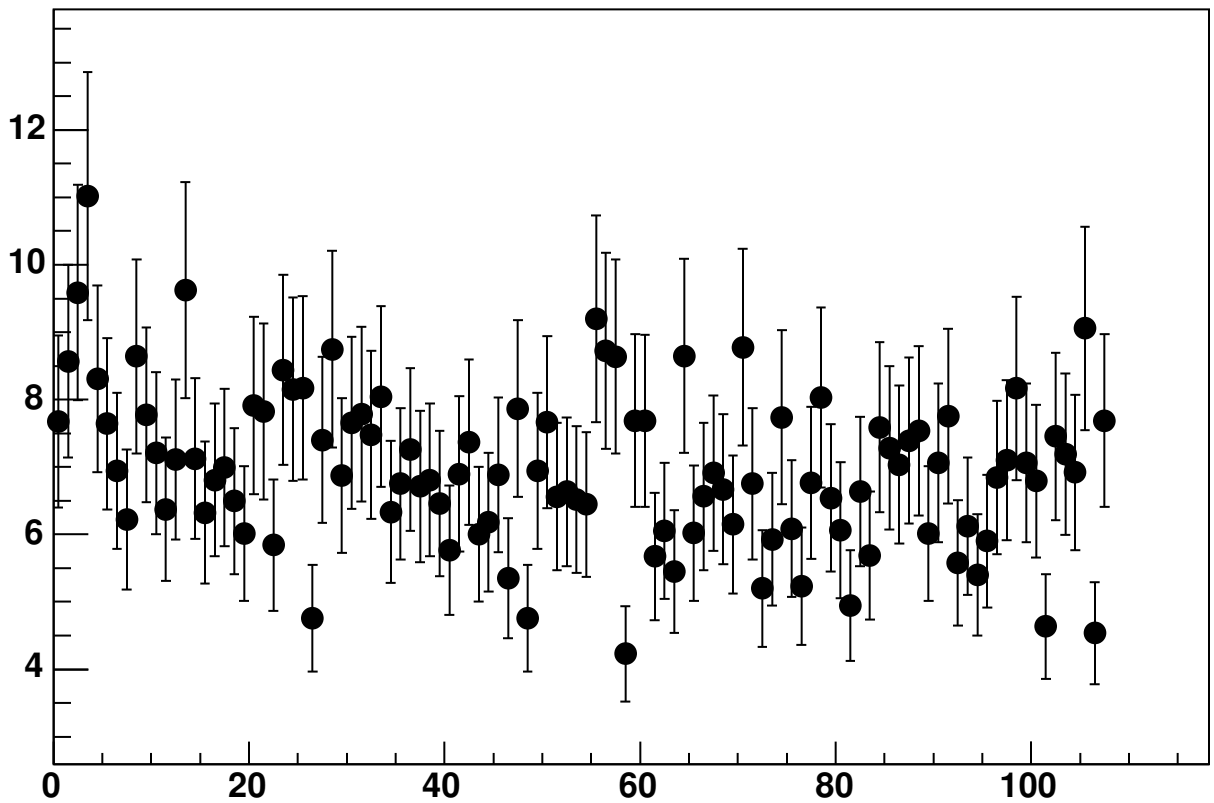
Enable 1, Hold=35, DAC=18400, ADC Noise vs 18*Chip+Chan



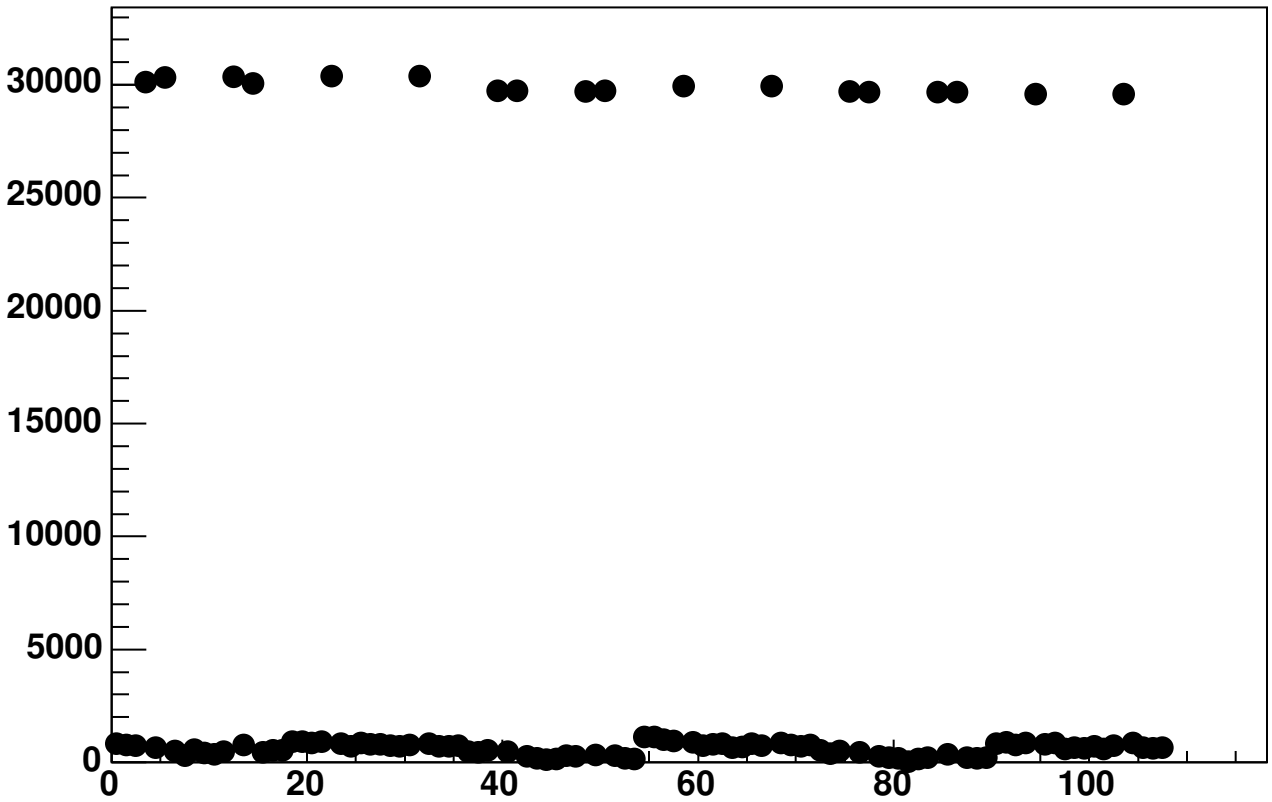
Enable 1, Hold=35, DAC=18800, ADC Mean vs 18*Chip+Chan



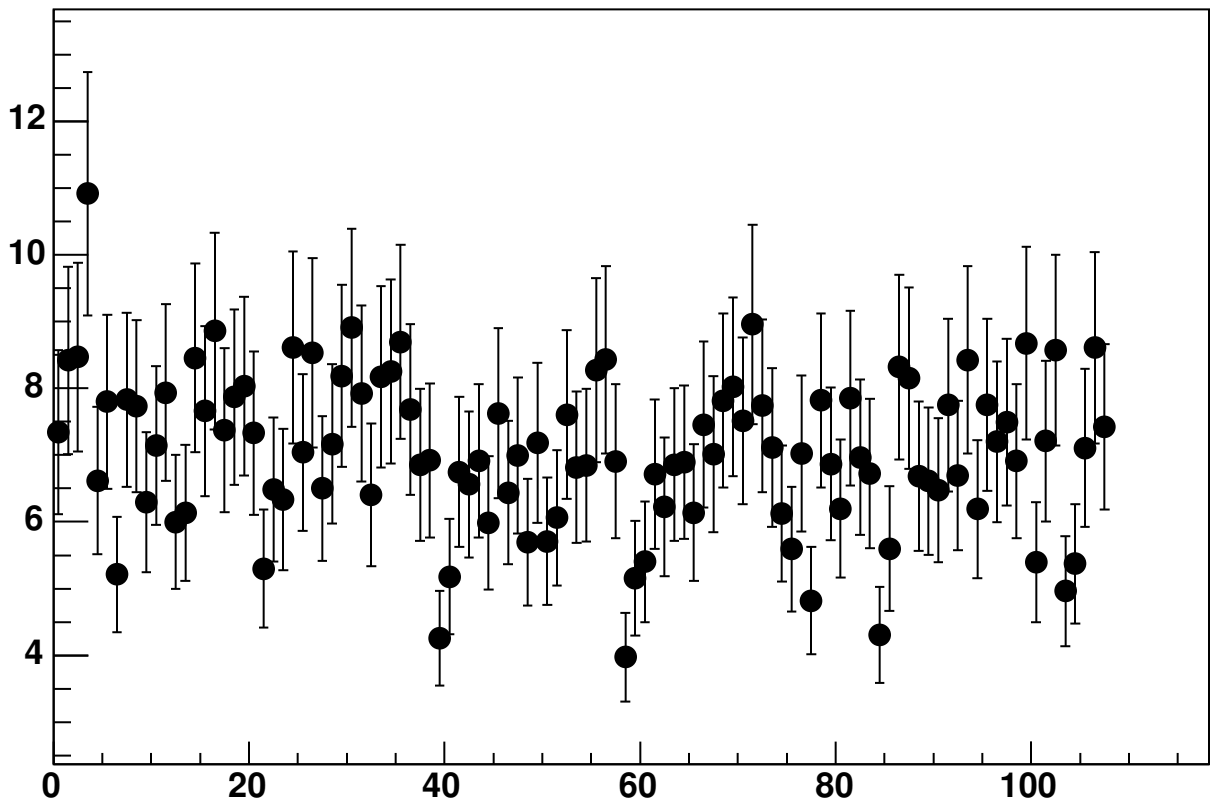
Enable 1, Hold=35, DAC=18800, ADC Noise vs 18*Chip+Chan



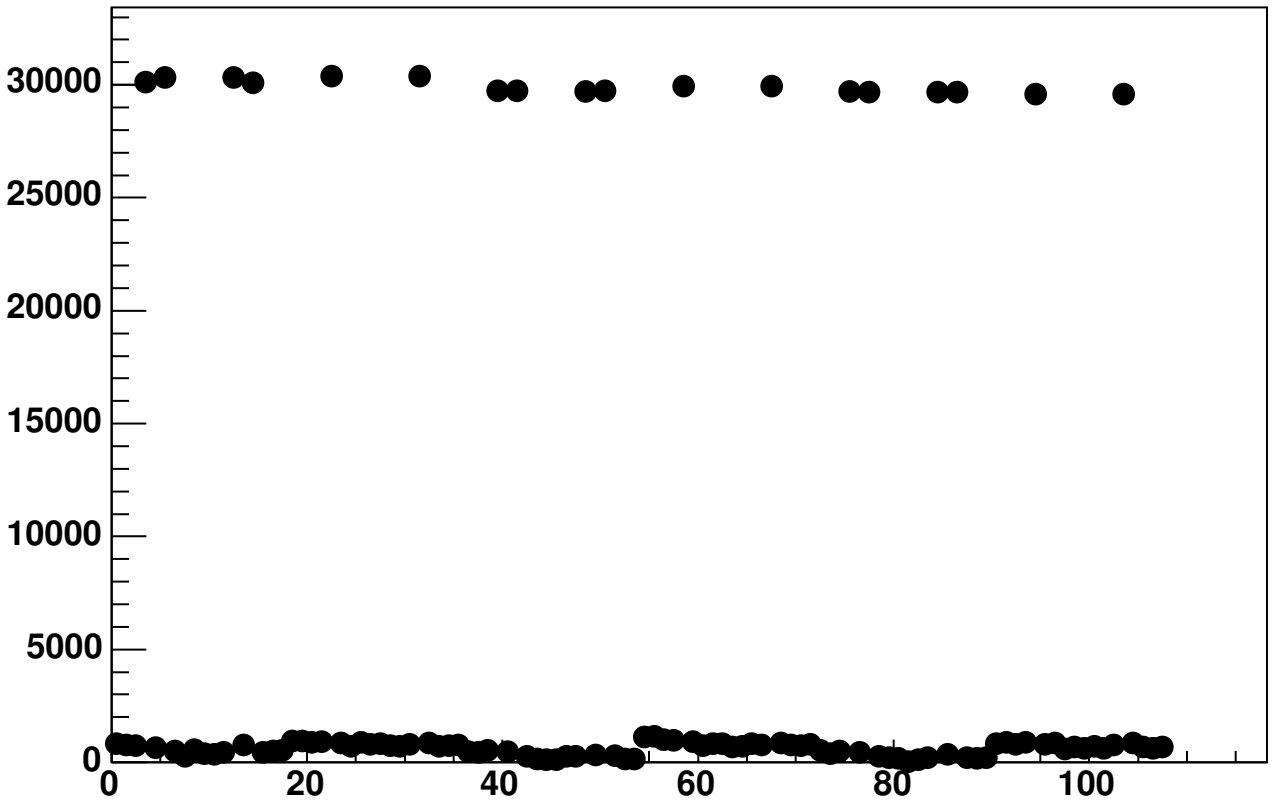
Enable 1, Hold=35, DAC=19200, ADC Mean vs 18*Chip+Chan



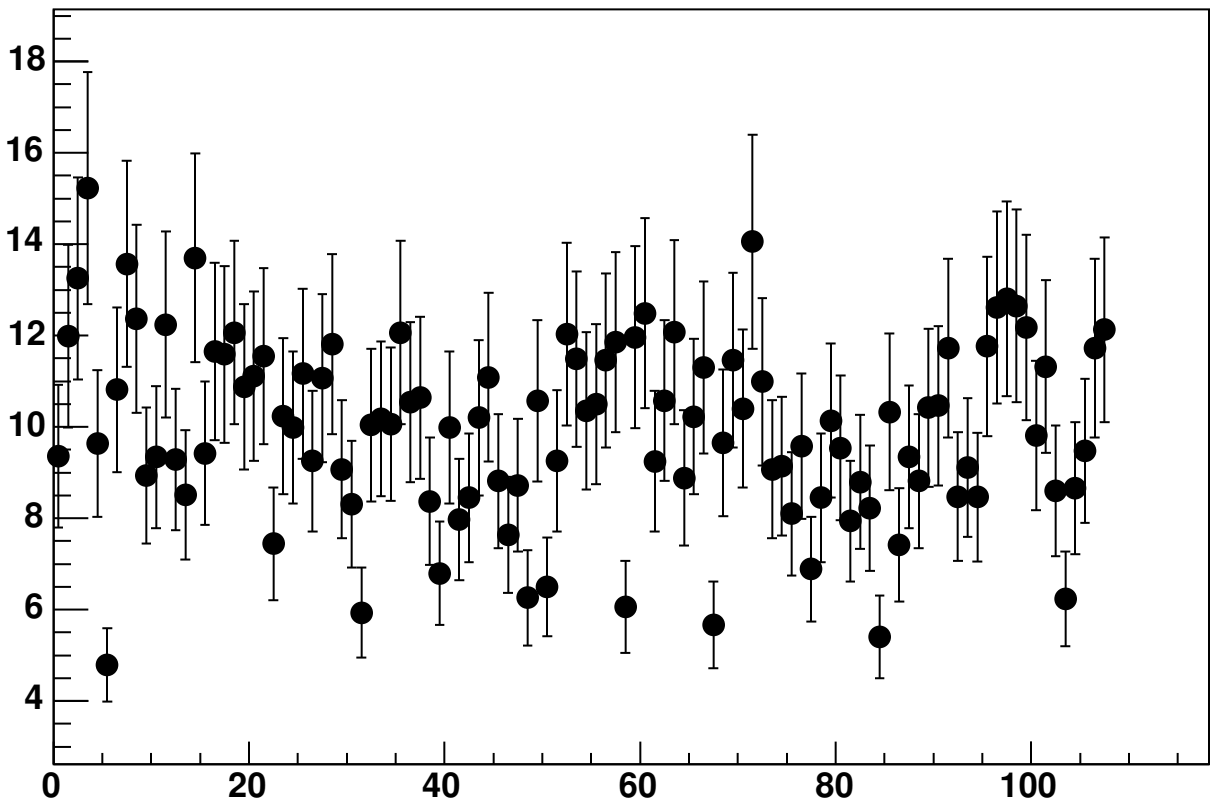
Enable 1, Hold=35, DAC=19200, ADC Noise vs 18*Chip+Chan



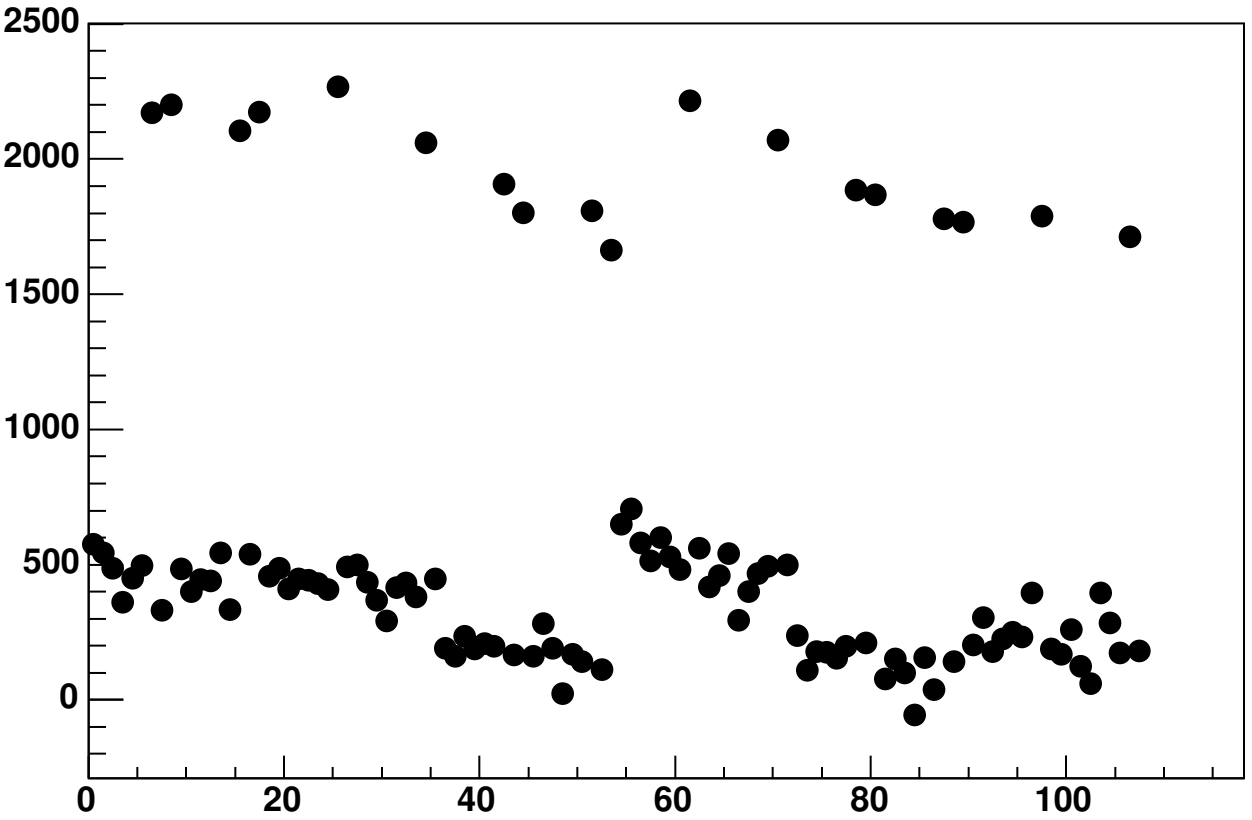
Enable 1, Hold=35, DAC=19600, ADC Mean vs 18*Chip+Chan



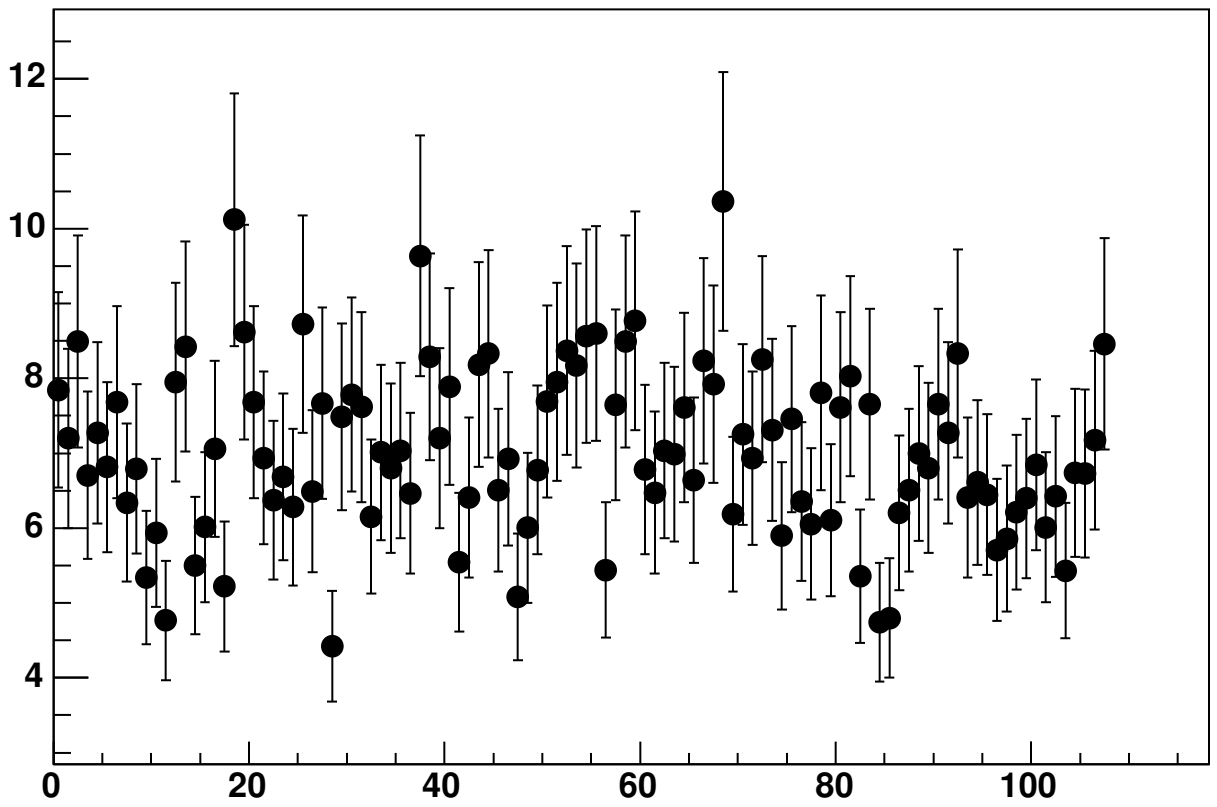
Enable 1, Hold=35, DAC=19600, ADC Noise vs 18*Chip+Chan



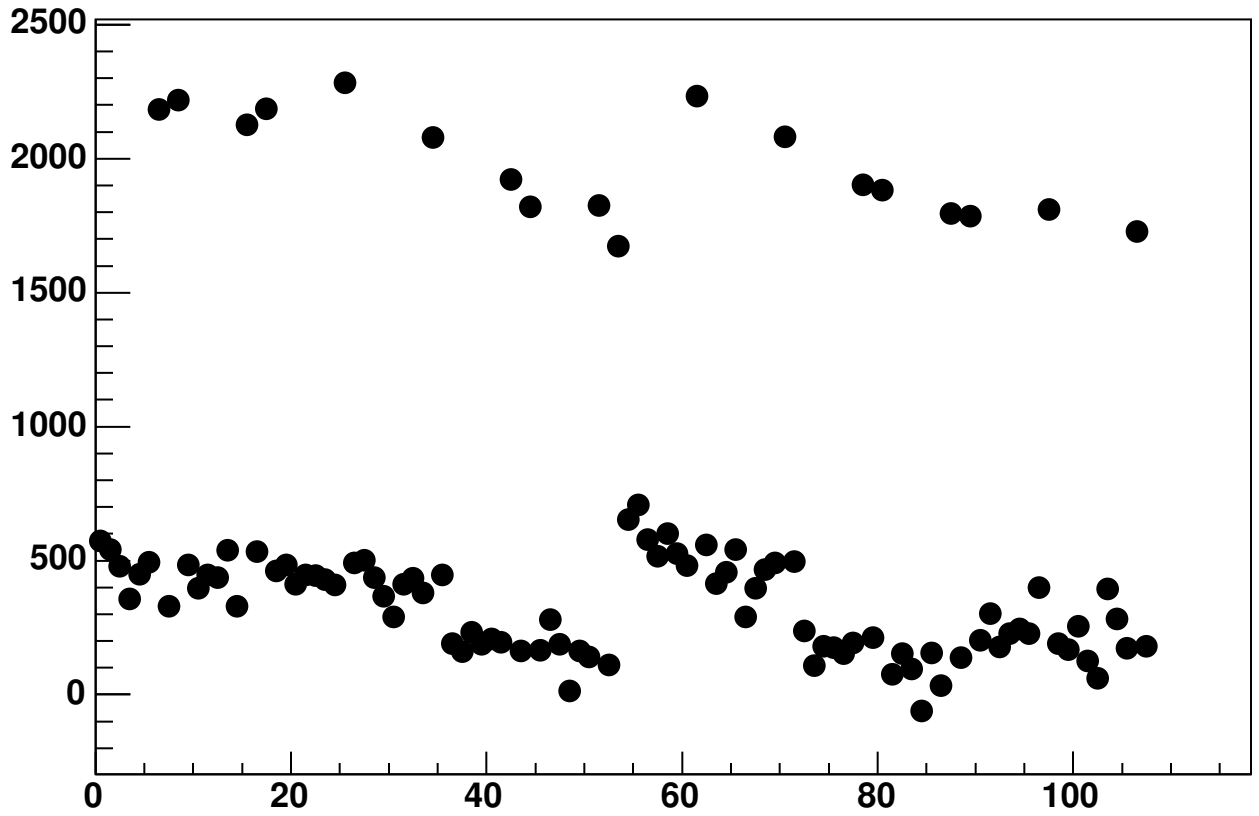
Enable 2, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



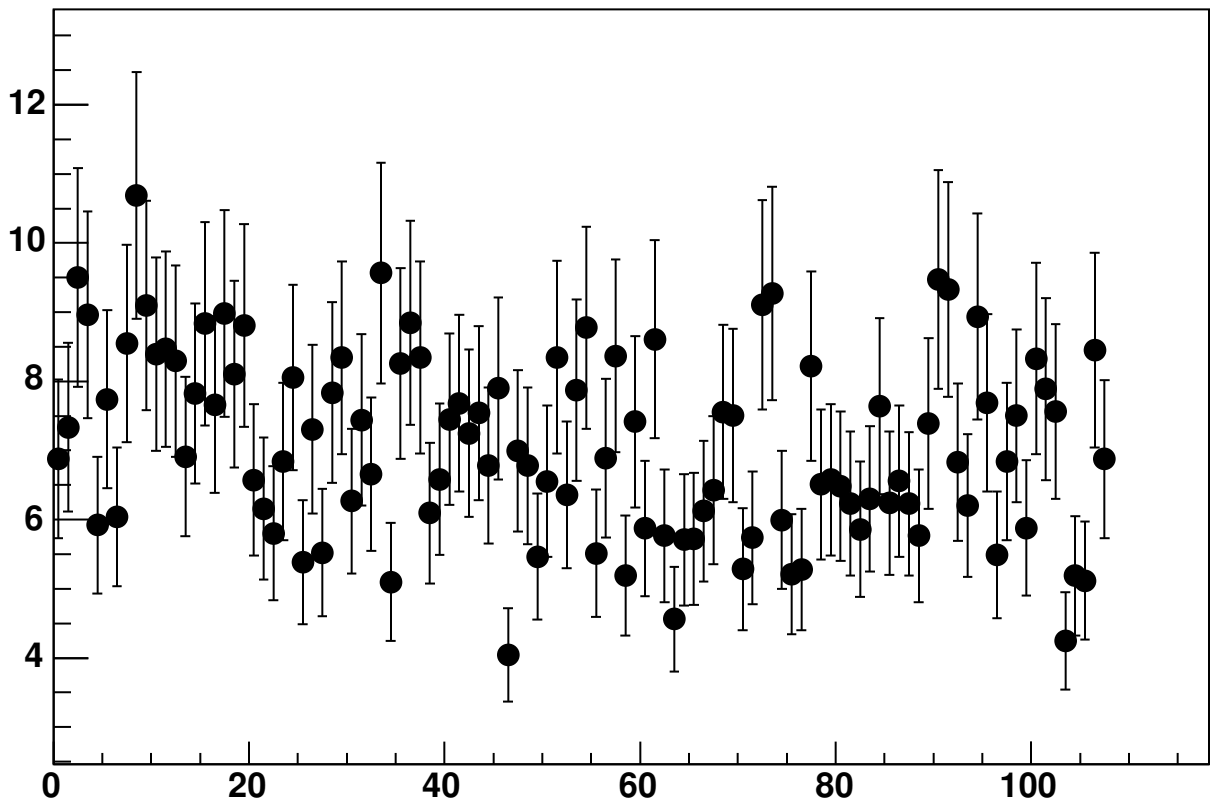
Enable 2, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



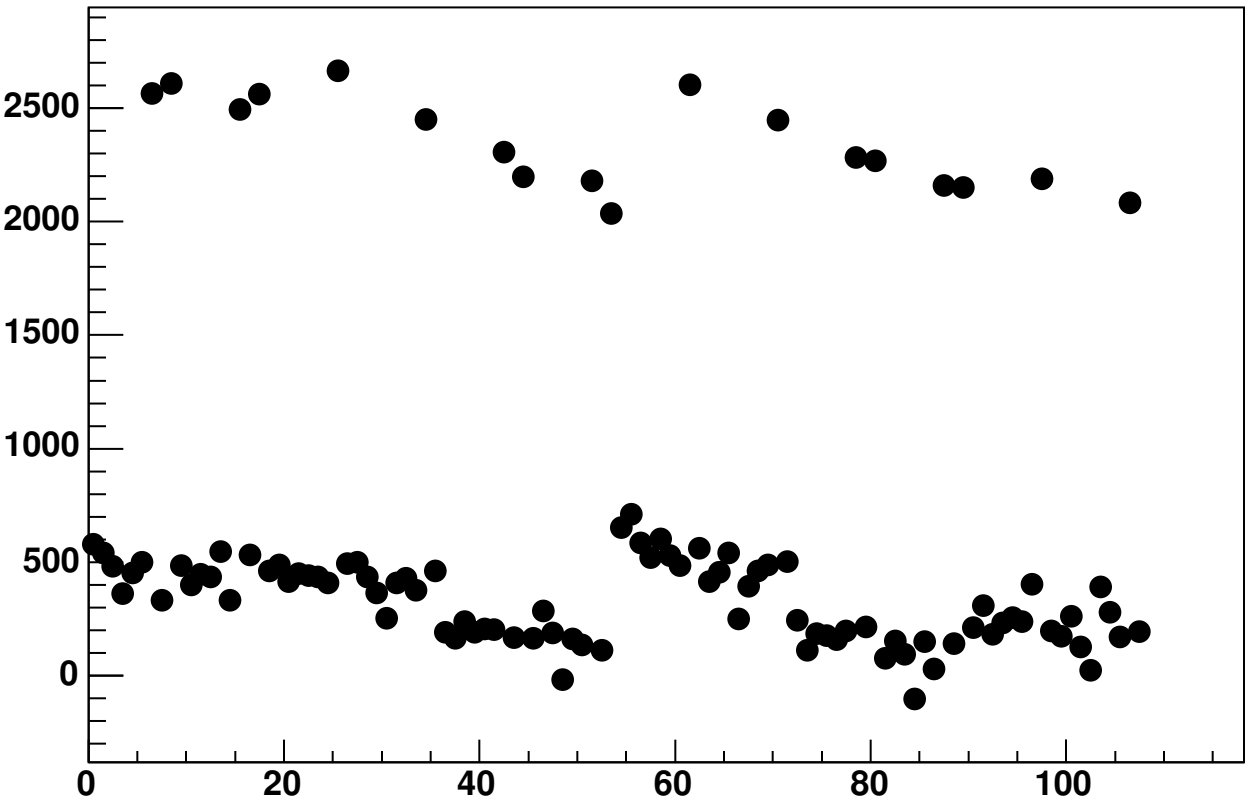
Enable 2, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



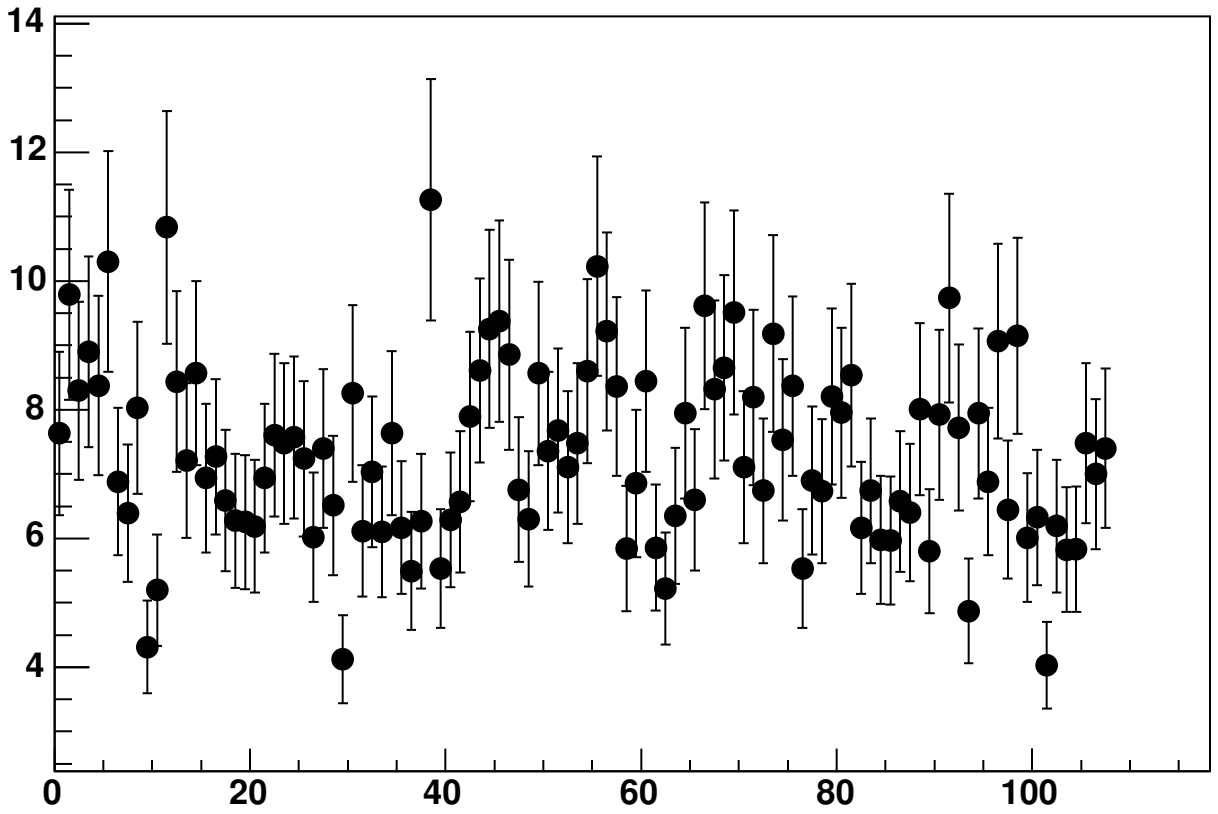
Enable 2, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



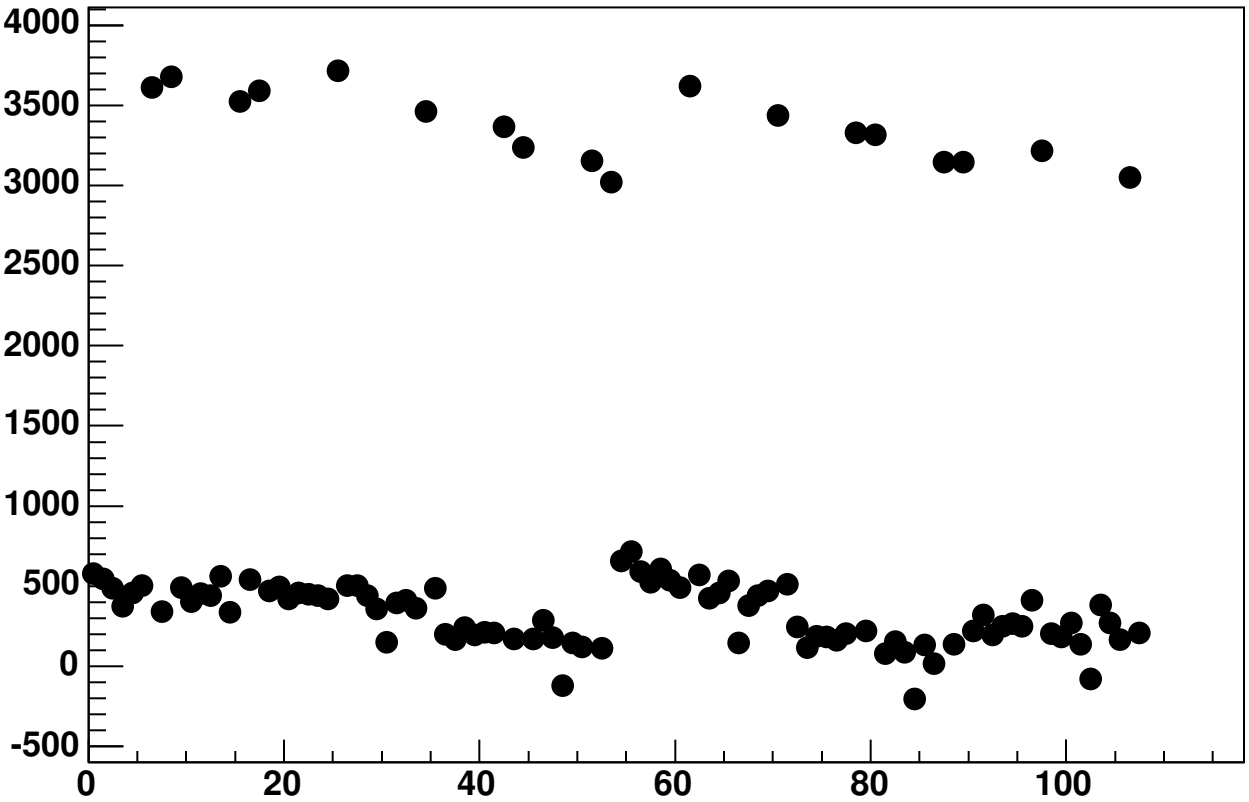
Enable 2, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



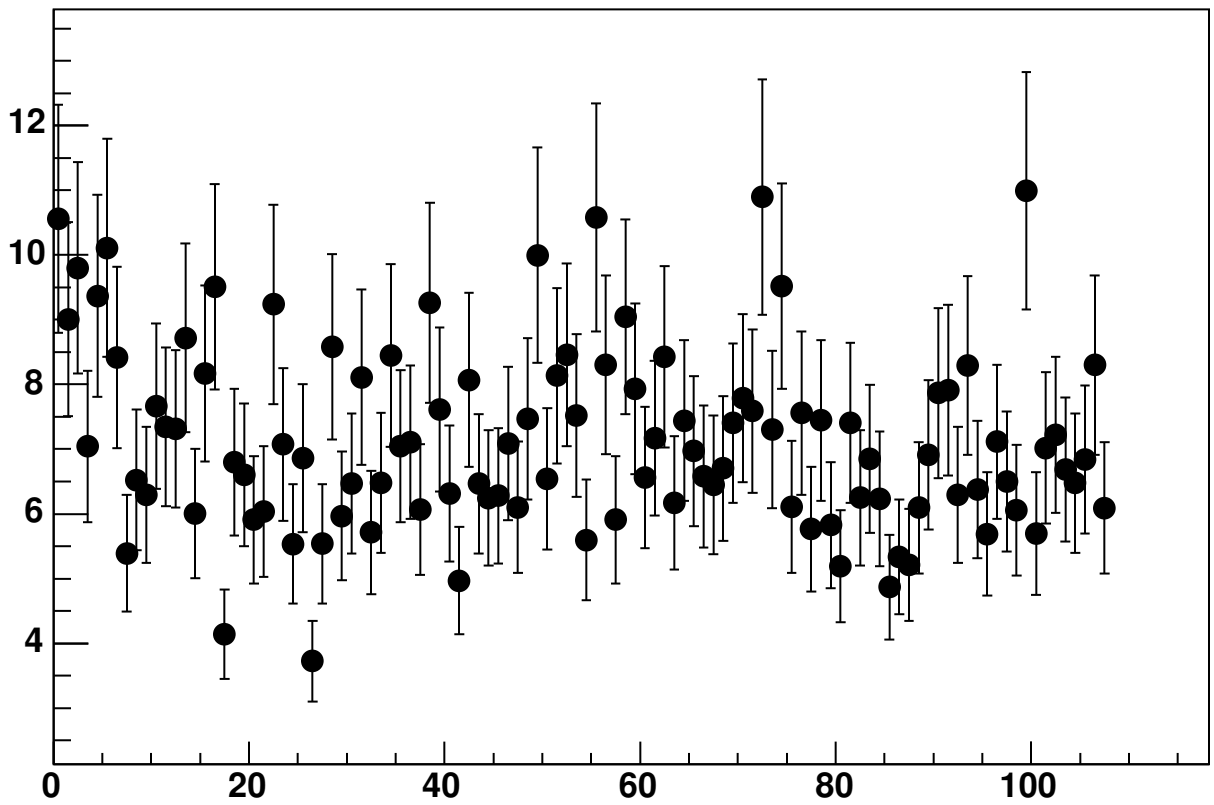
Enable 2, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



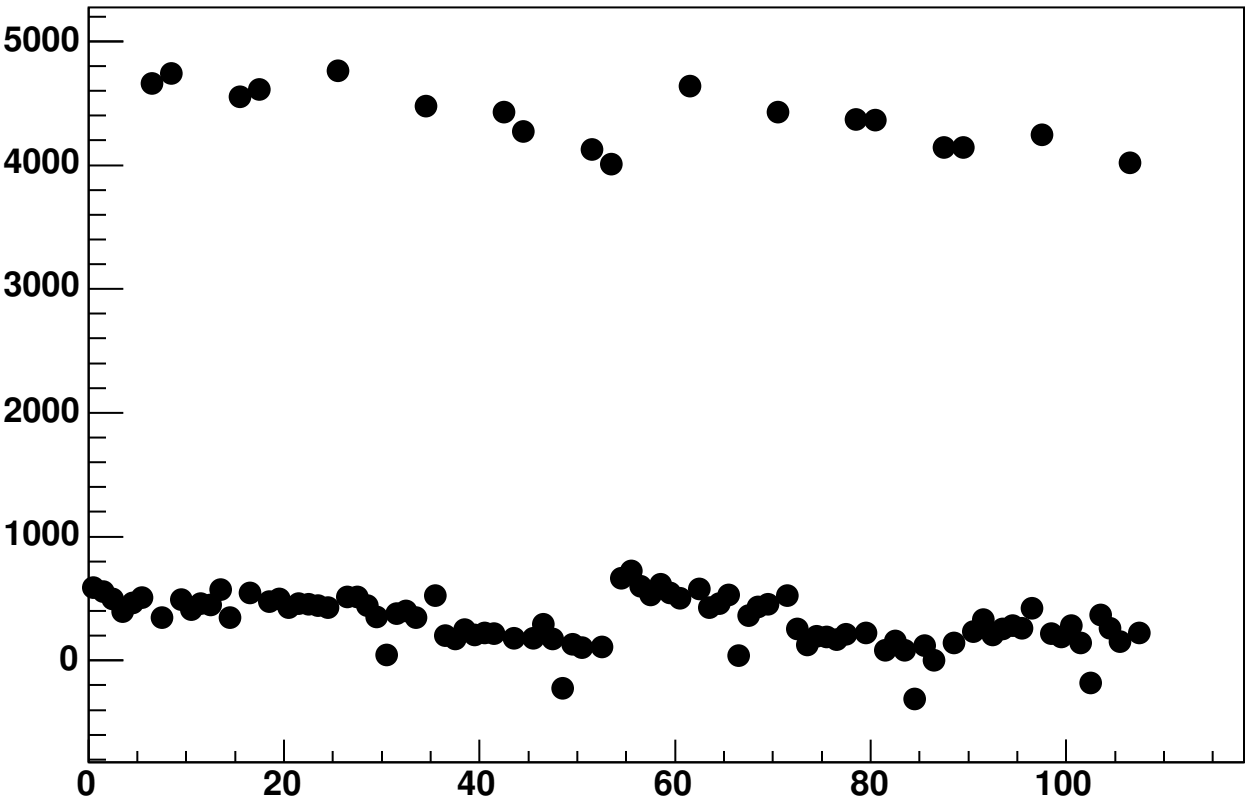
Enable 2, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



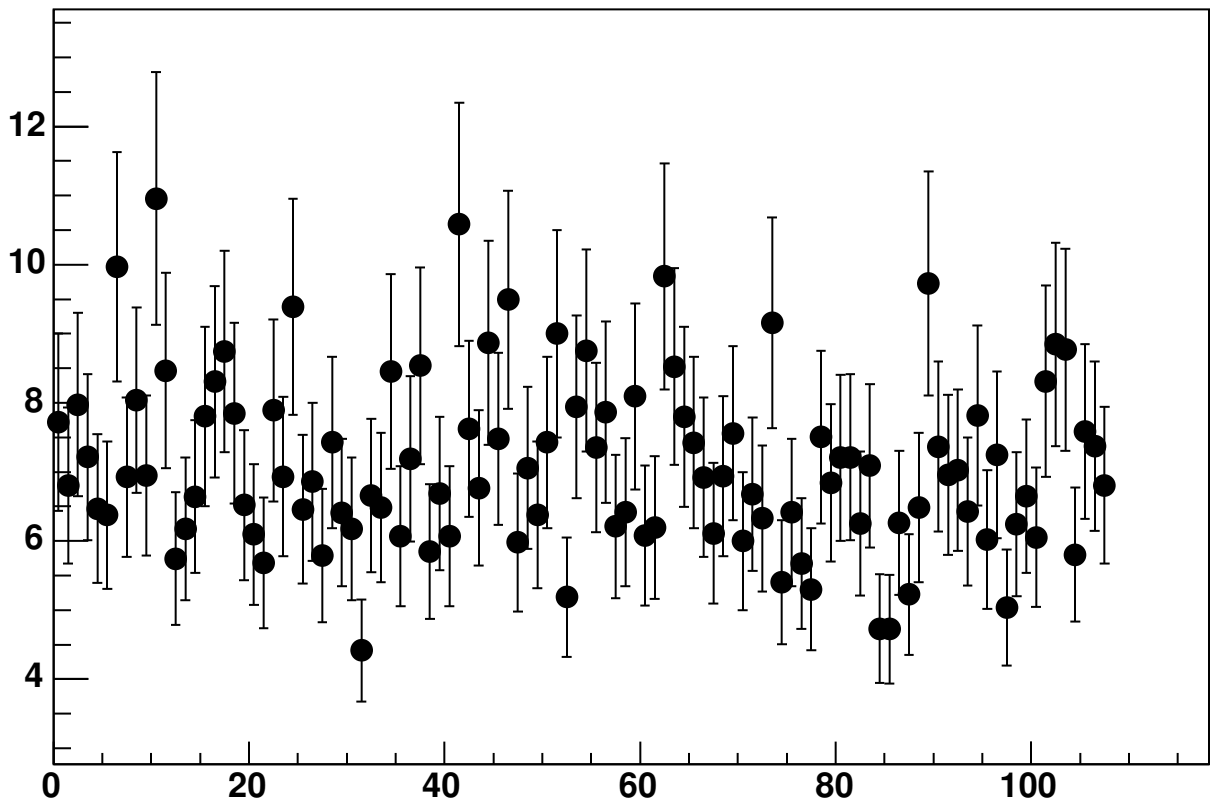
Enable 2, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



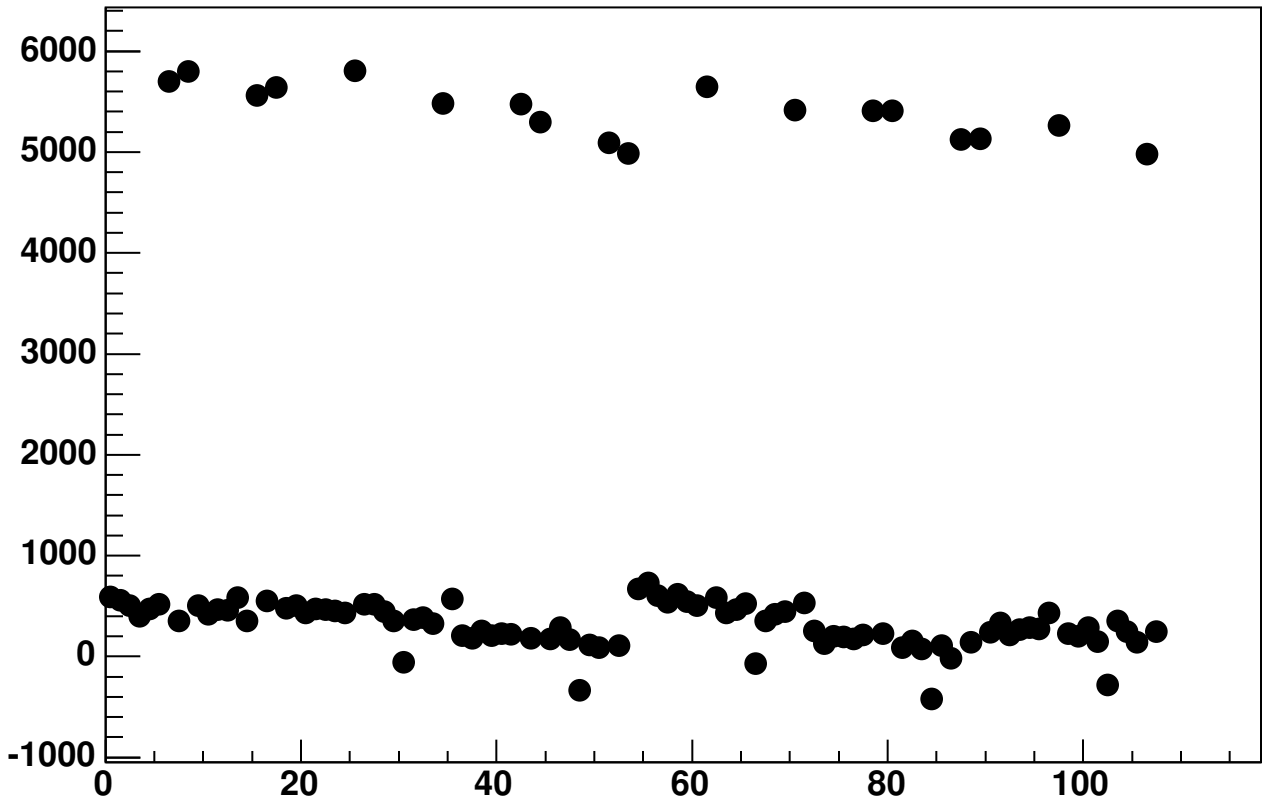
Enable 2, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



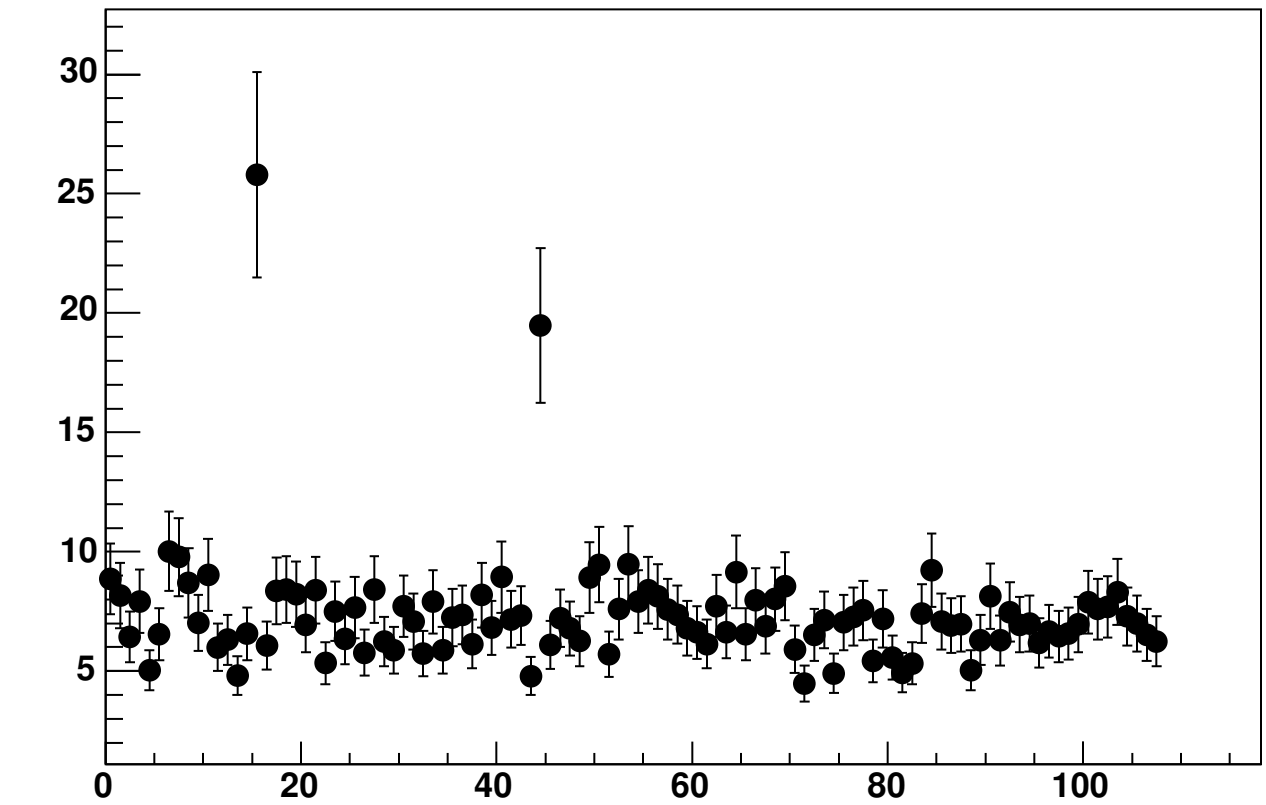
Enable 2, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



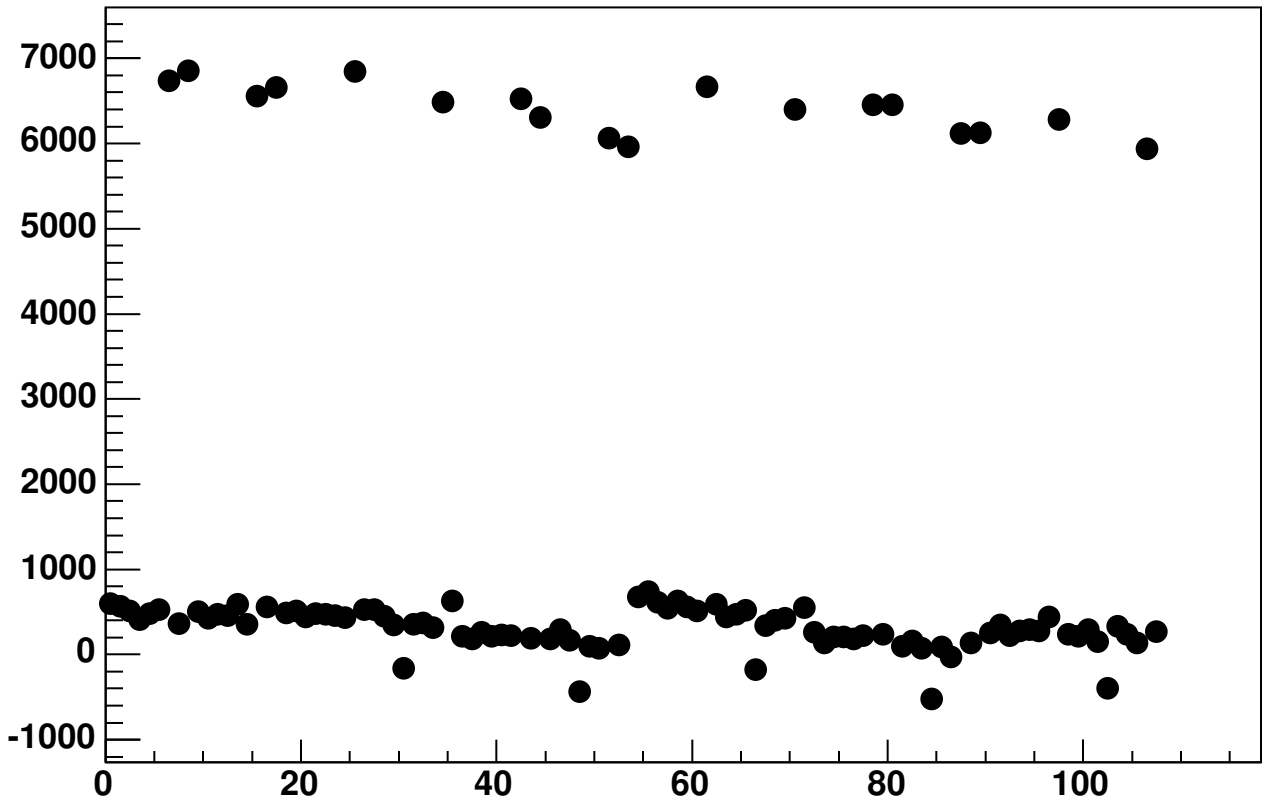
Enable 2, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



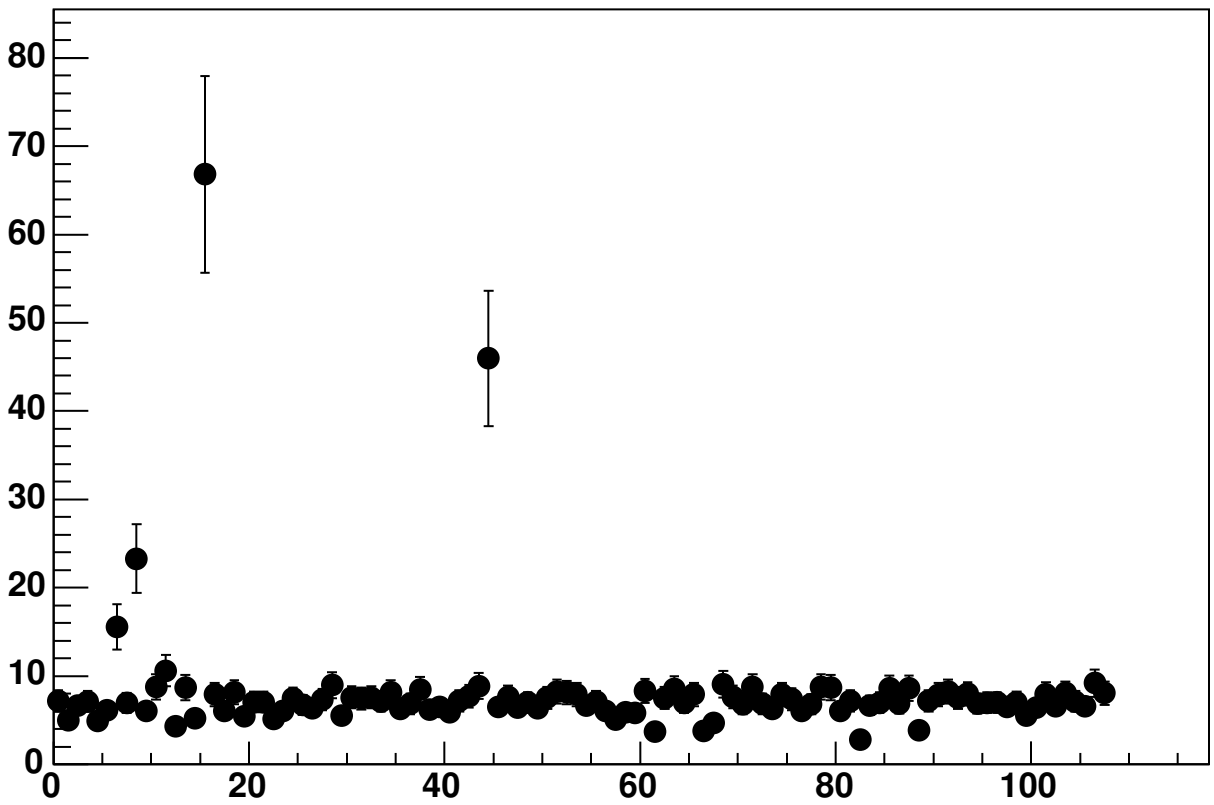
Enable 2, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



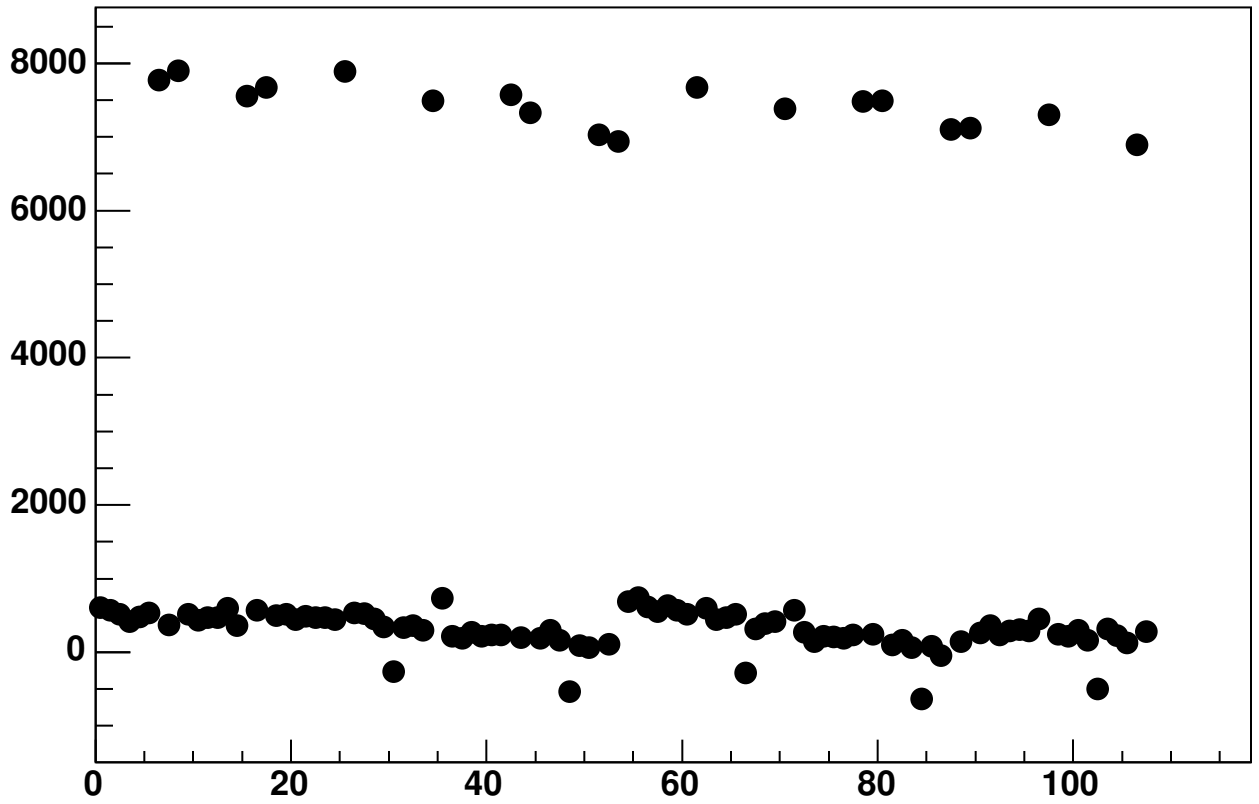
Enable 2, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



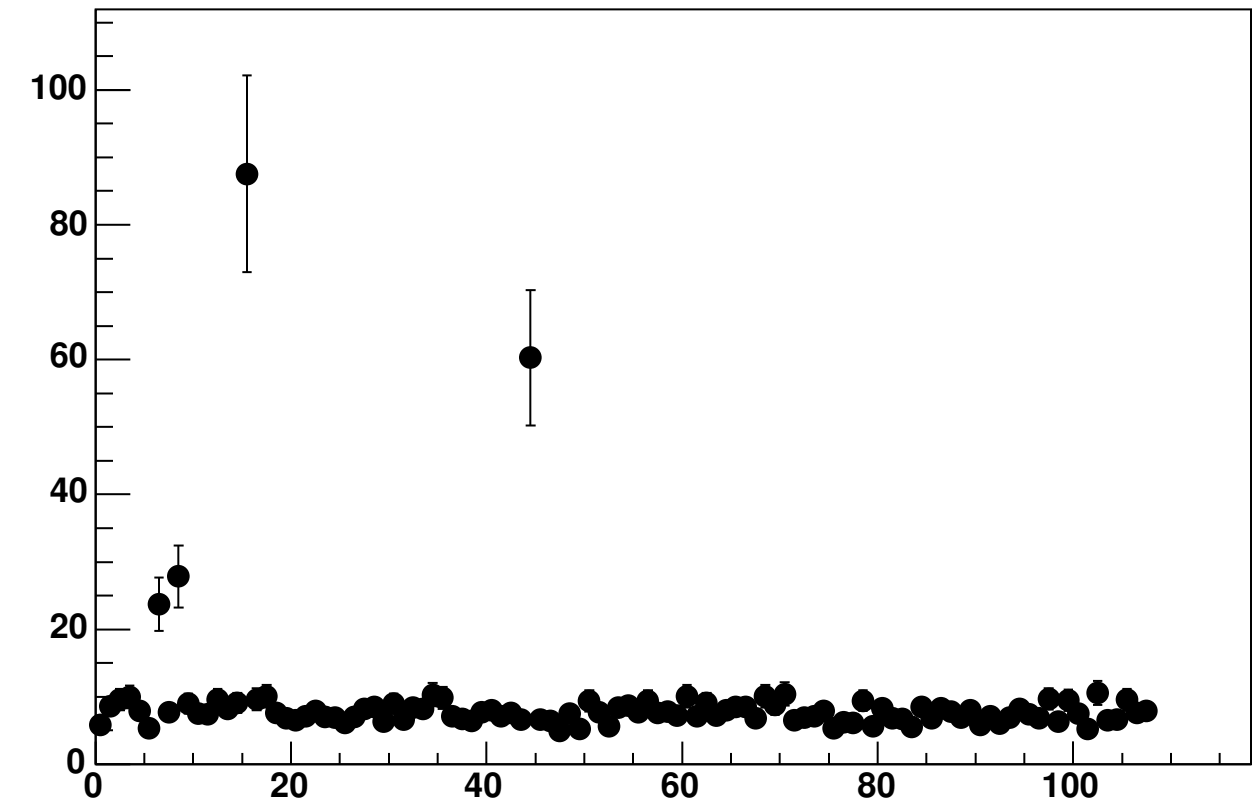
Enable 2, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



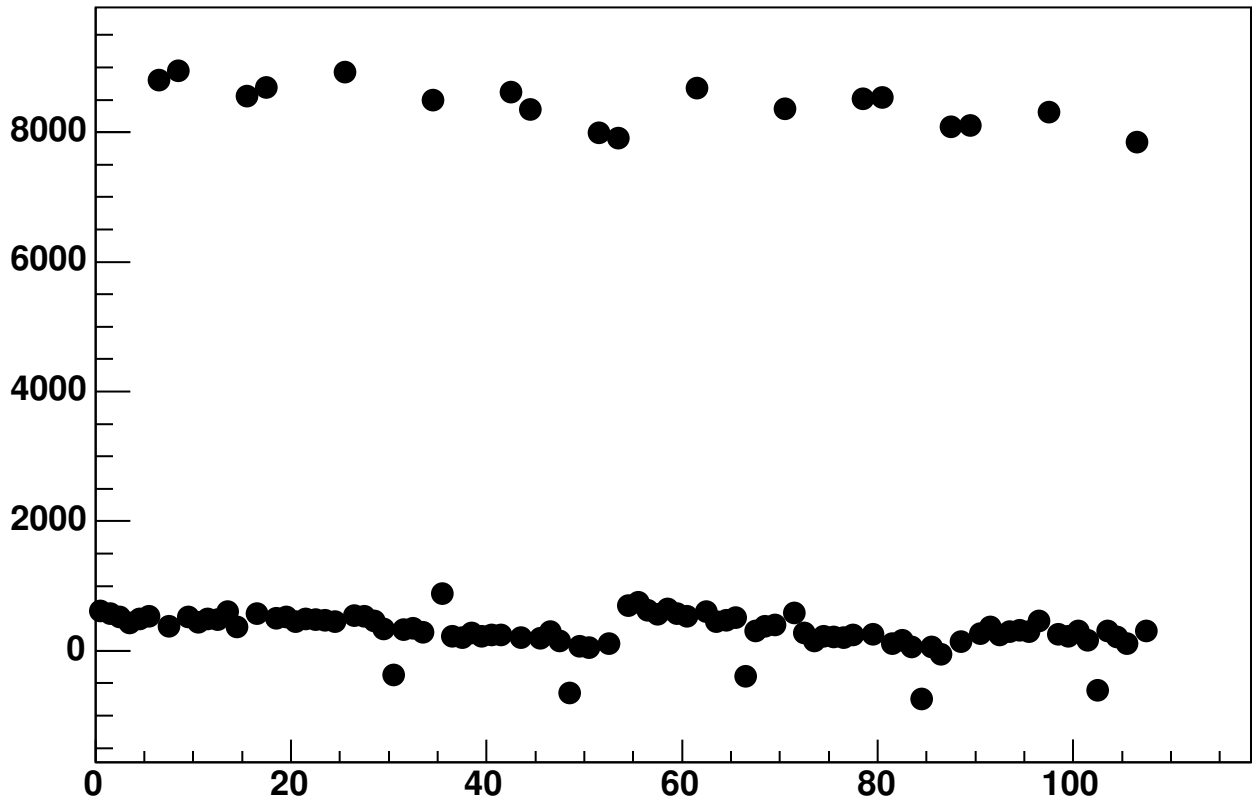
Enable 2, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



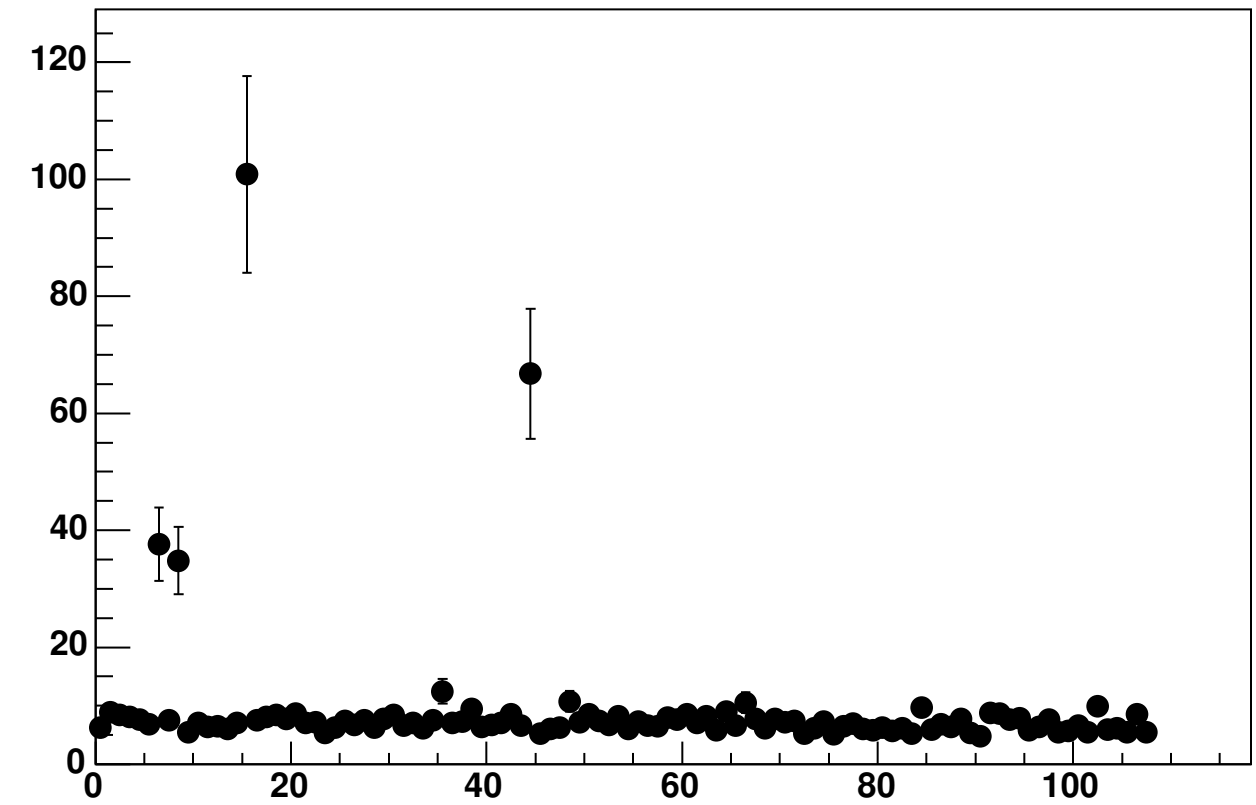
Enable 2, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



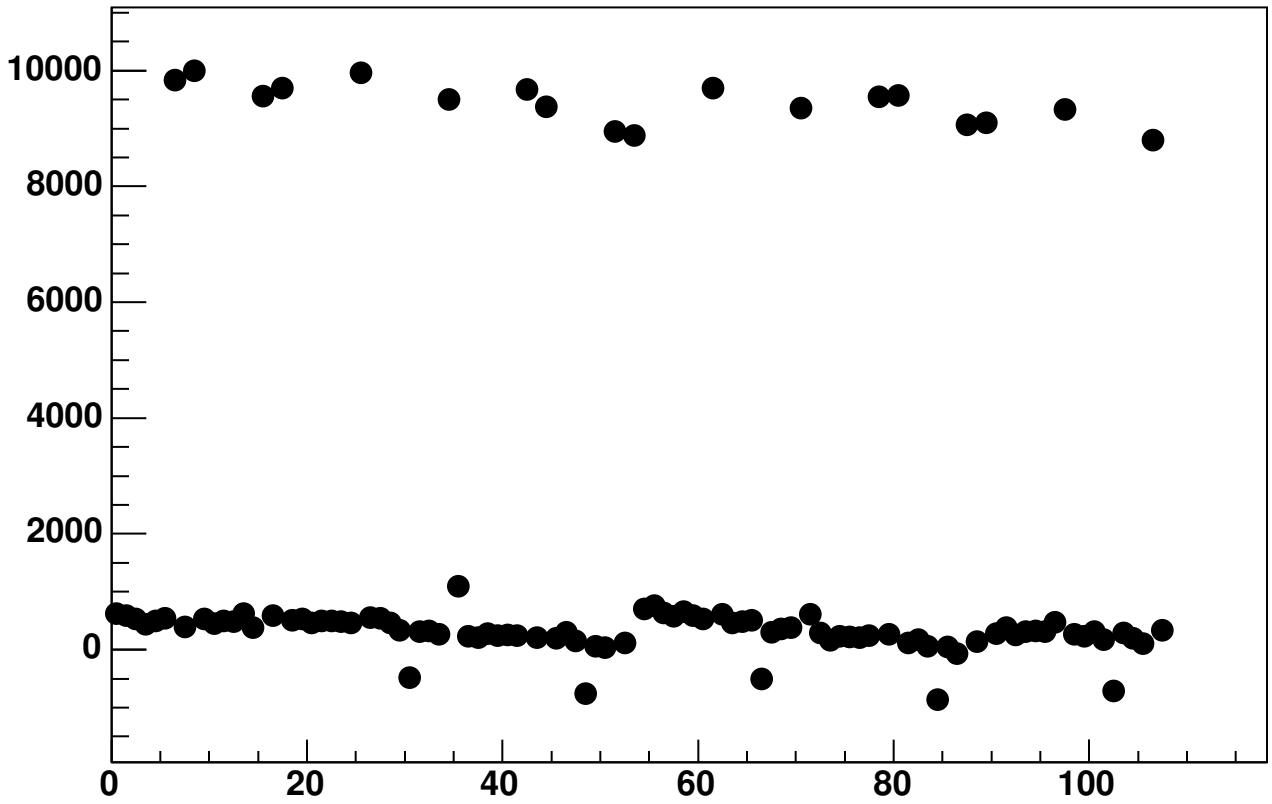
Enable 2, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



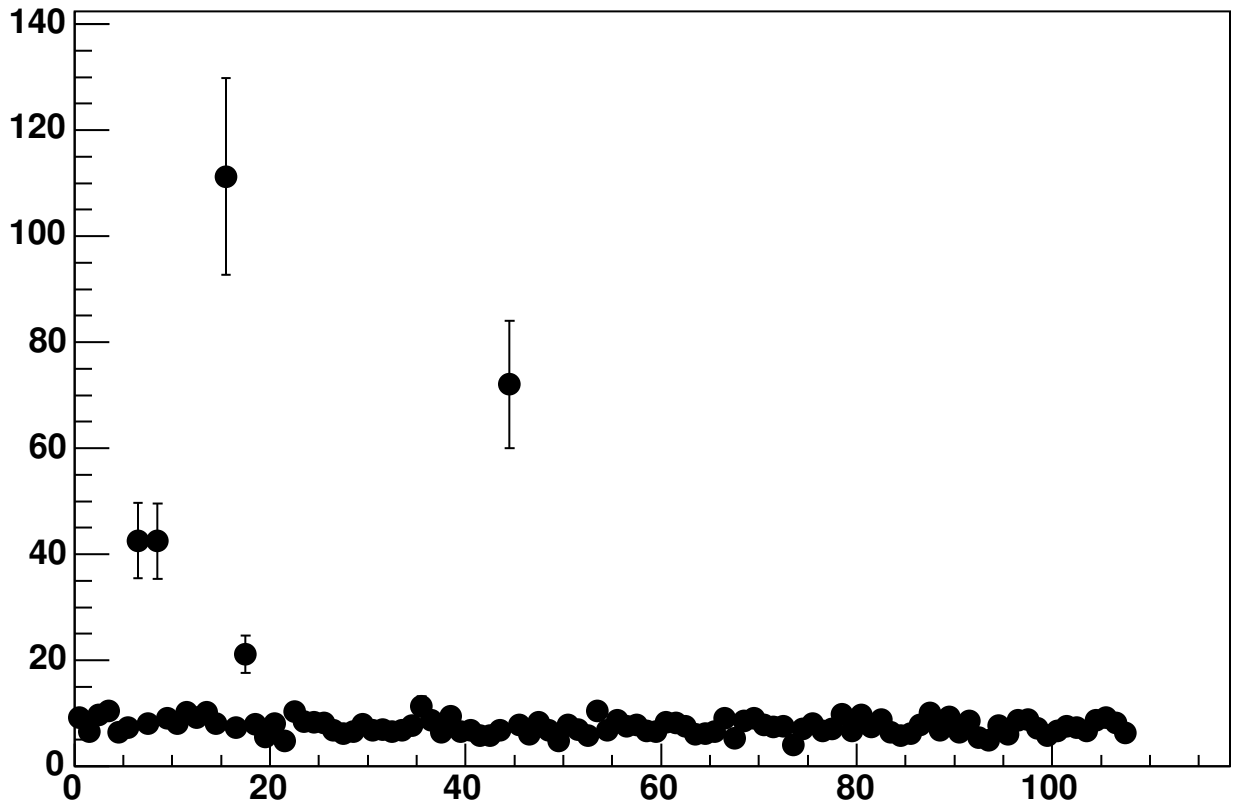
Enable 2, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



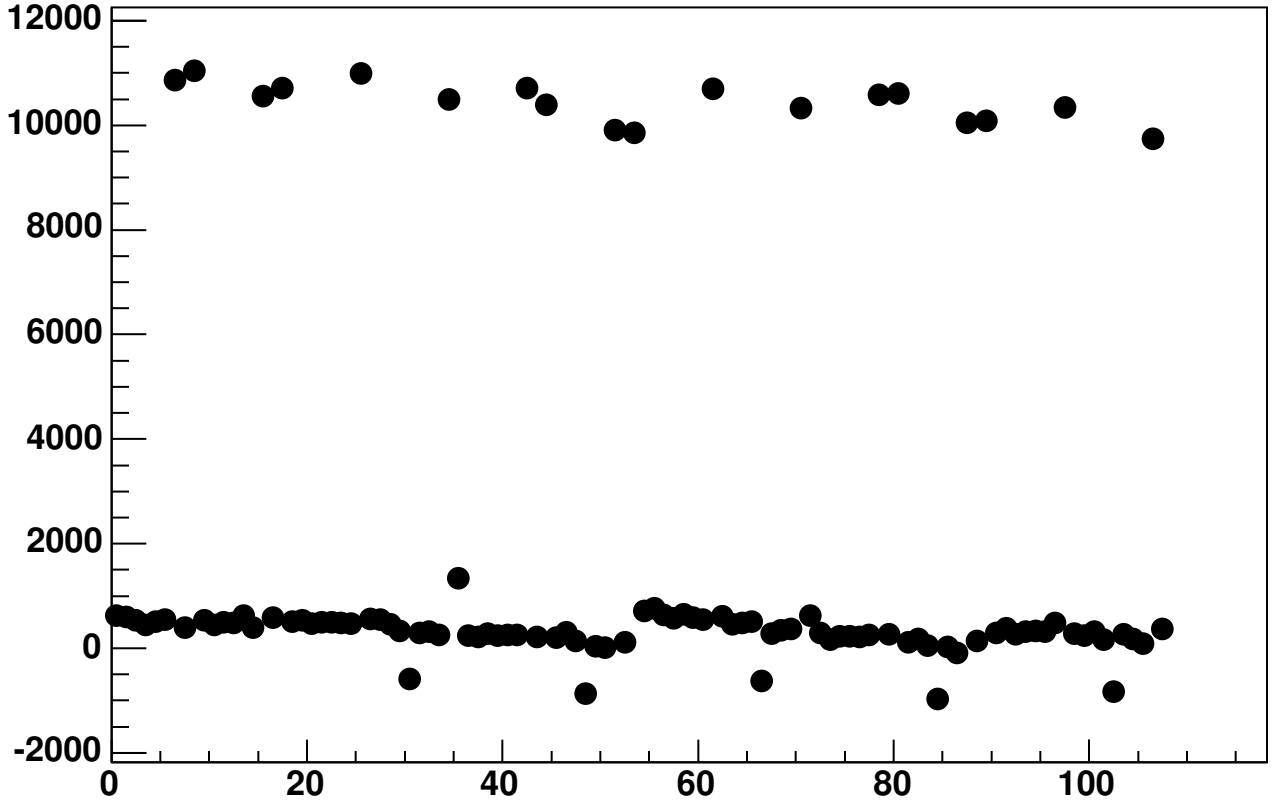
Enable 2, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



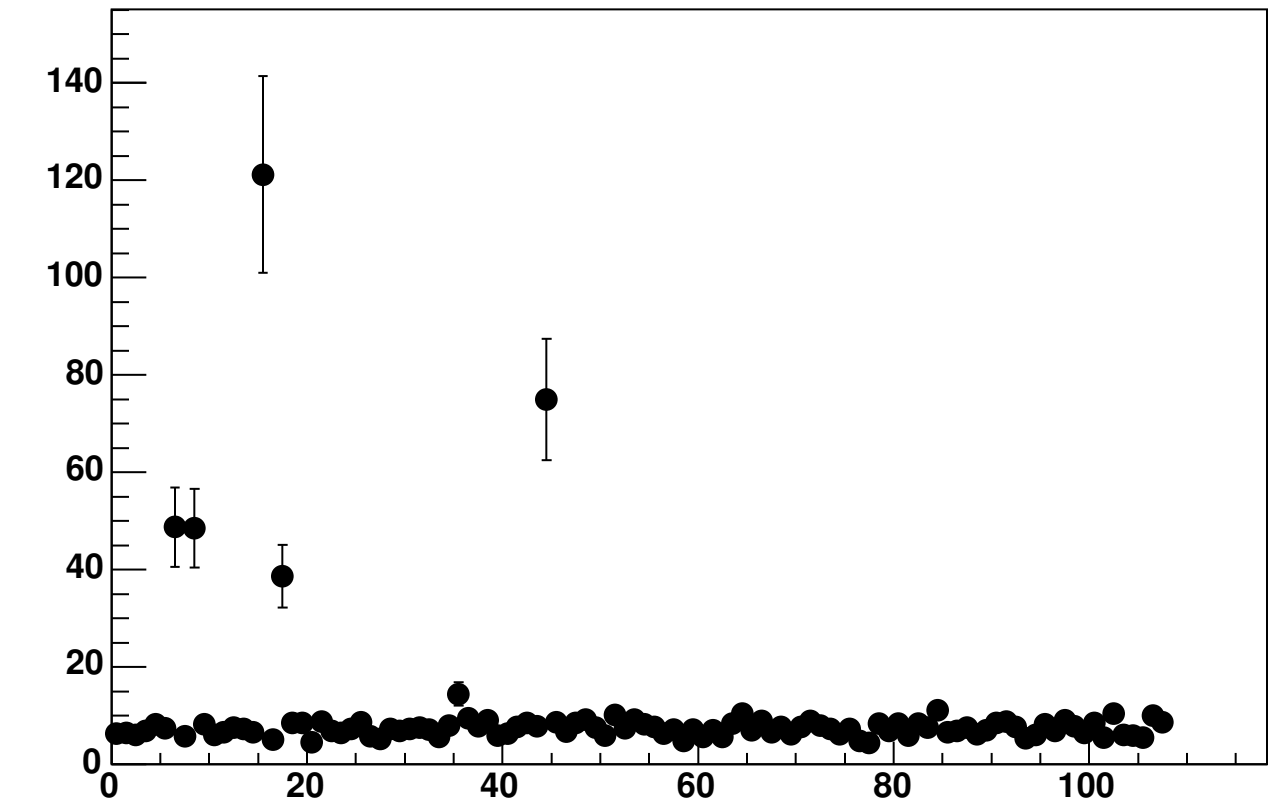
Enable 2, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



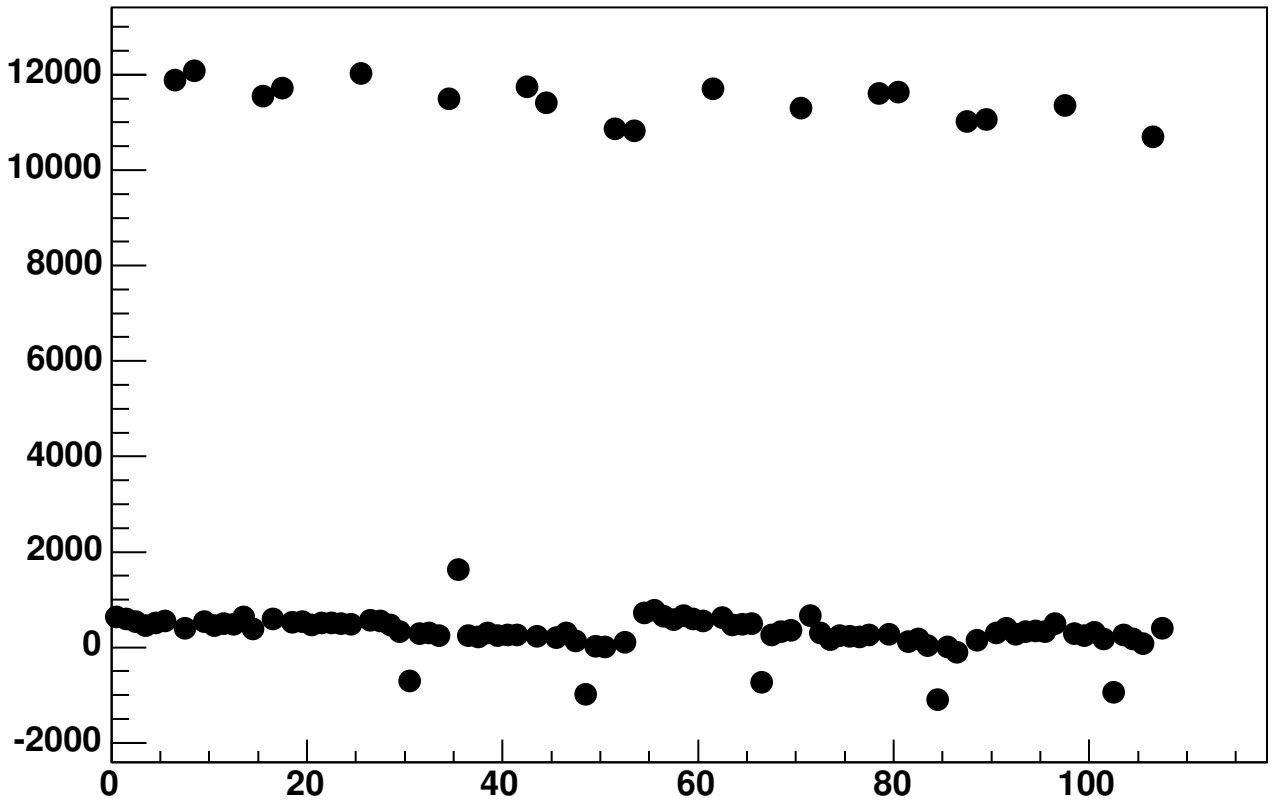
Enable 2, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



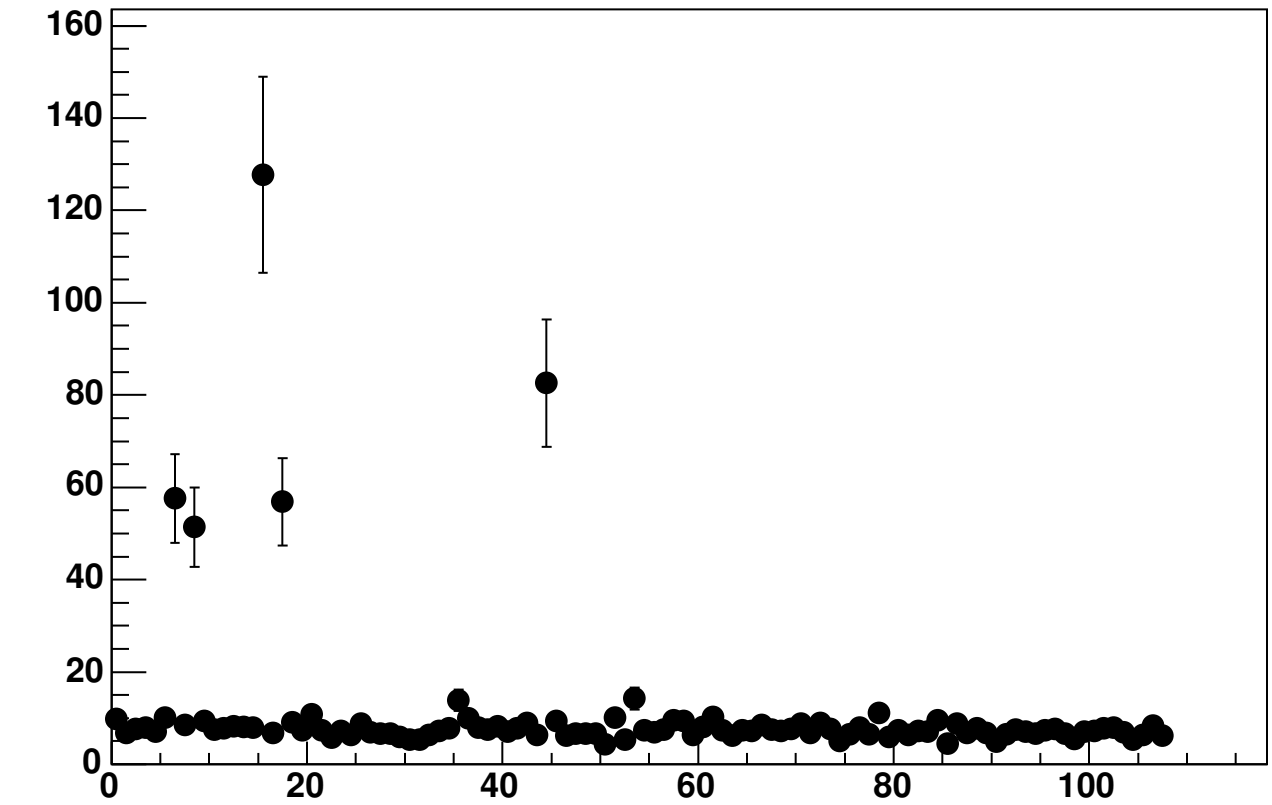
Enable 2, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



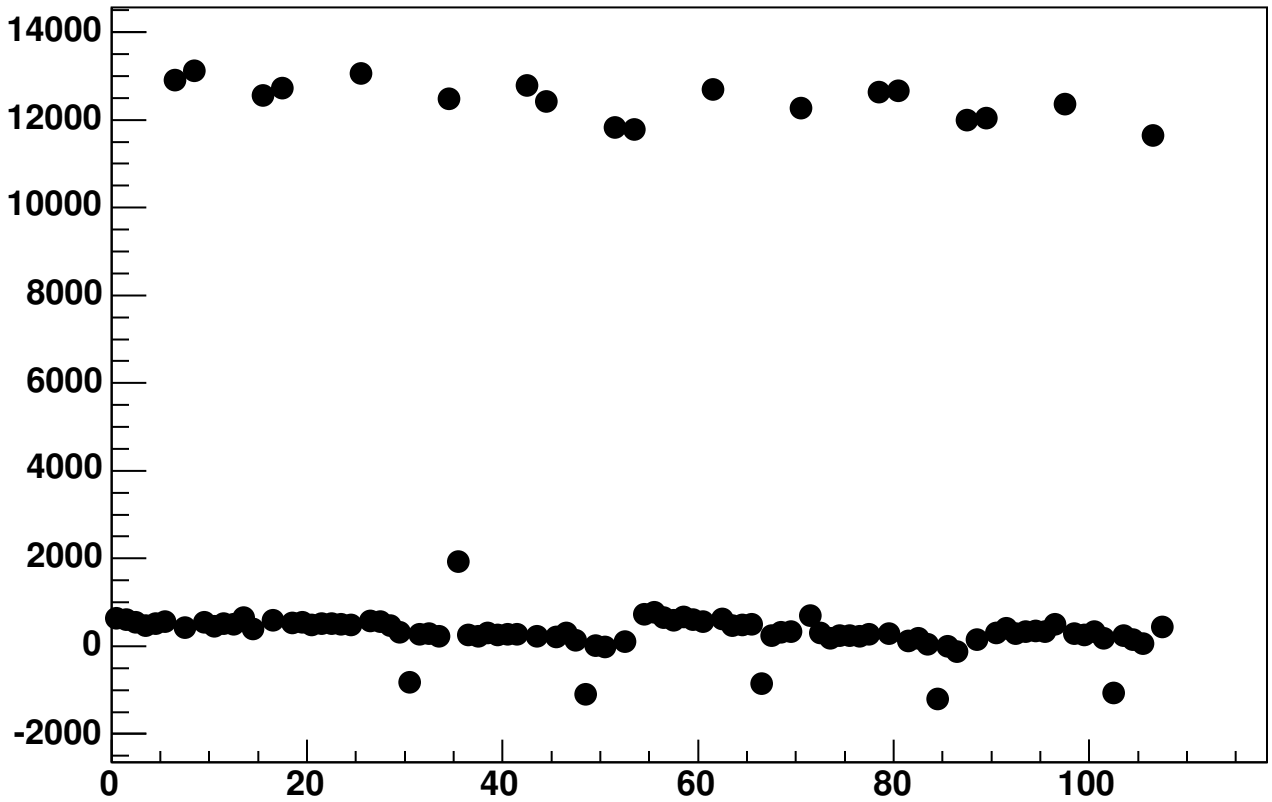
Enable 2, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



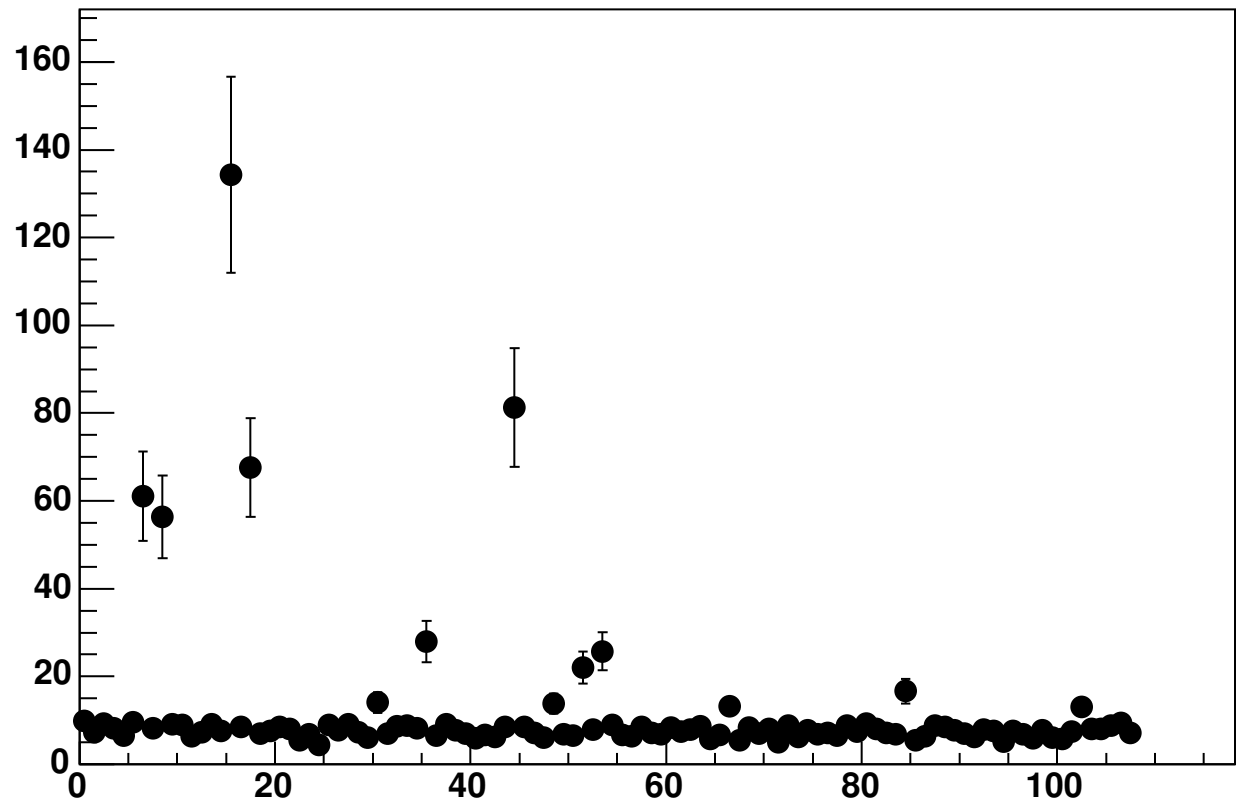
Enable 2, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



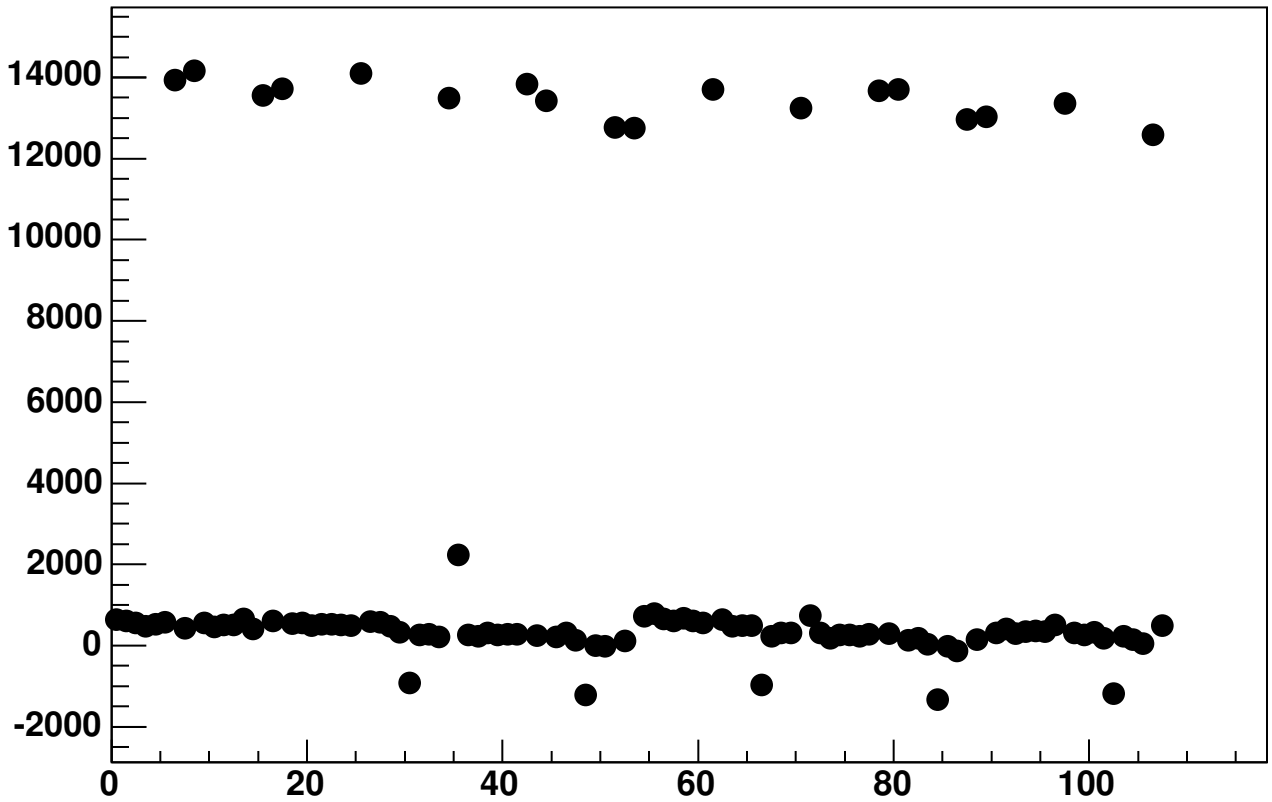
Enable 2, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



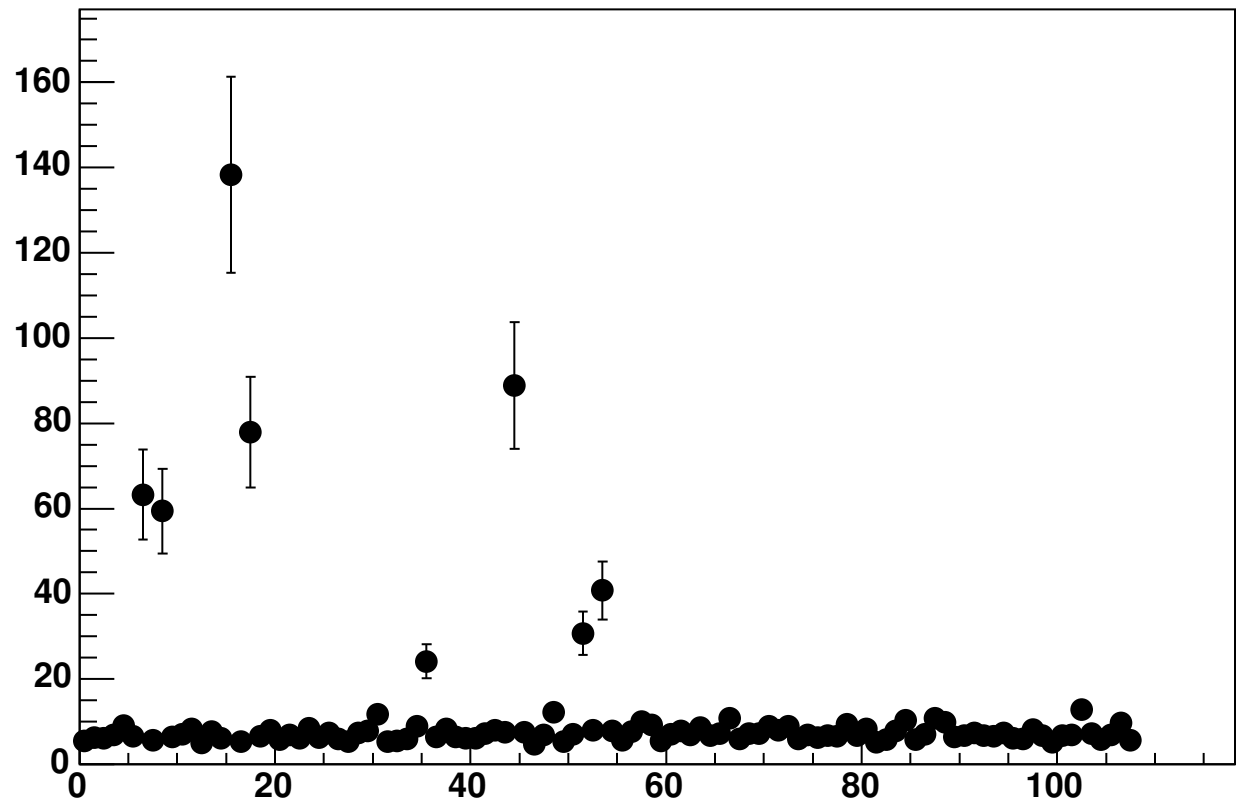
Enable 2, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



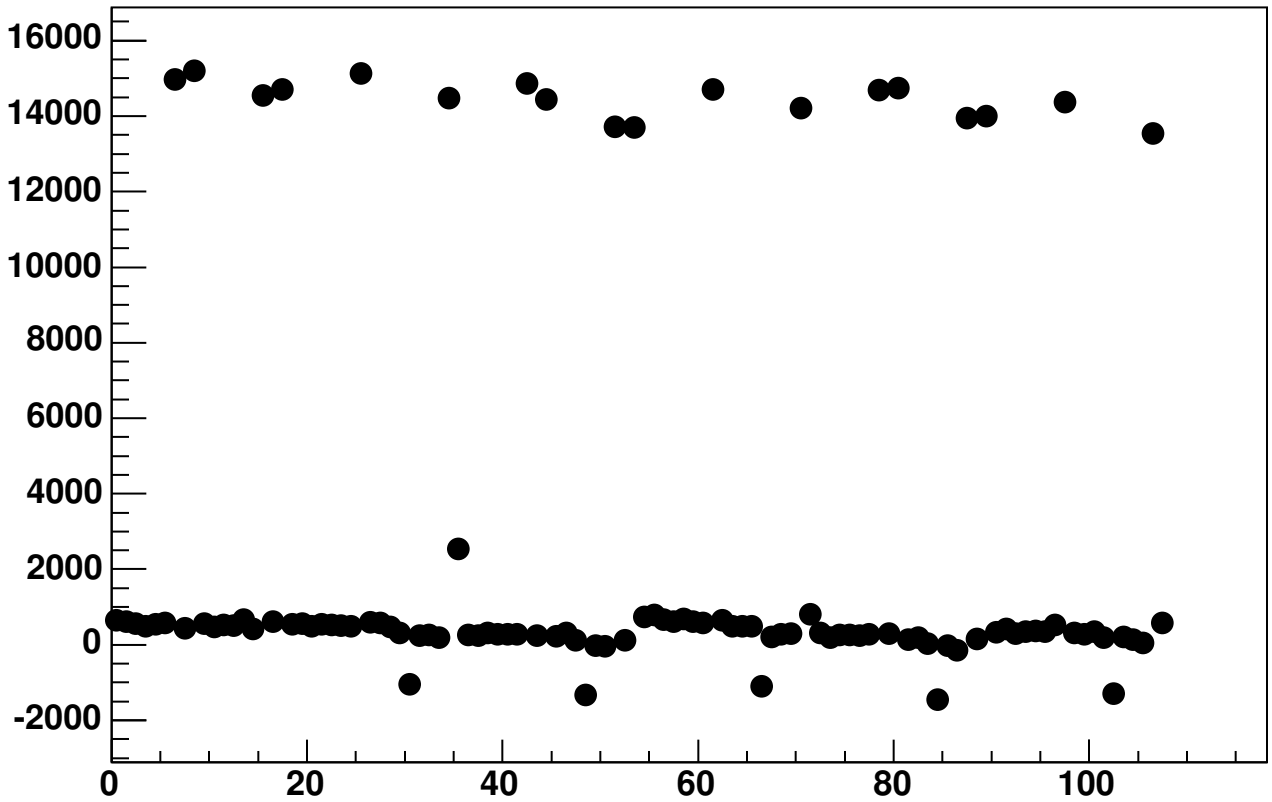
Enable 2, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



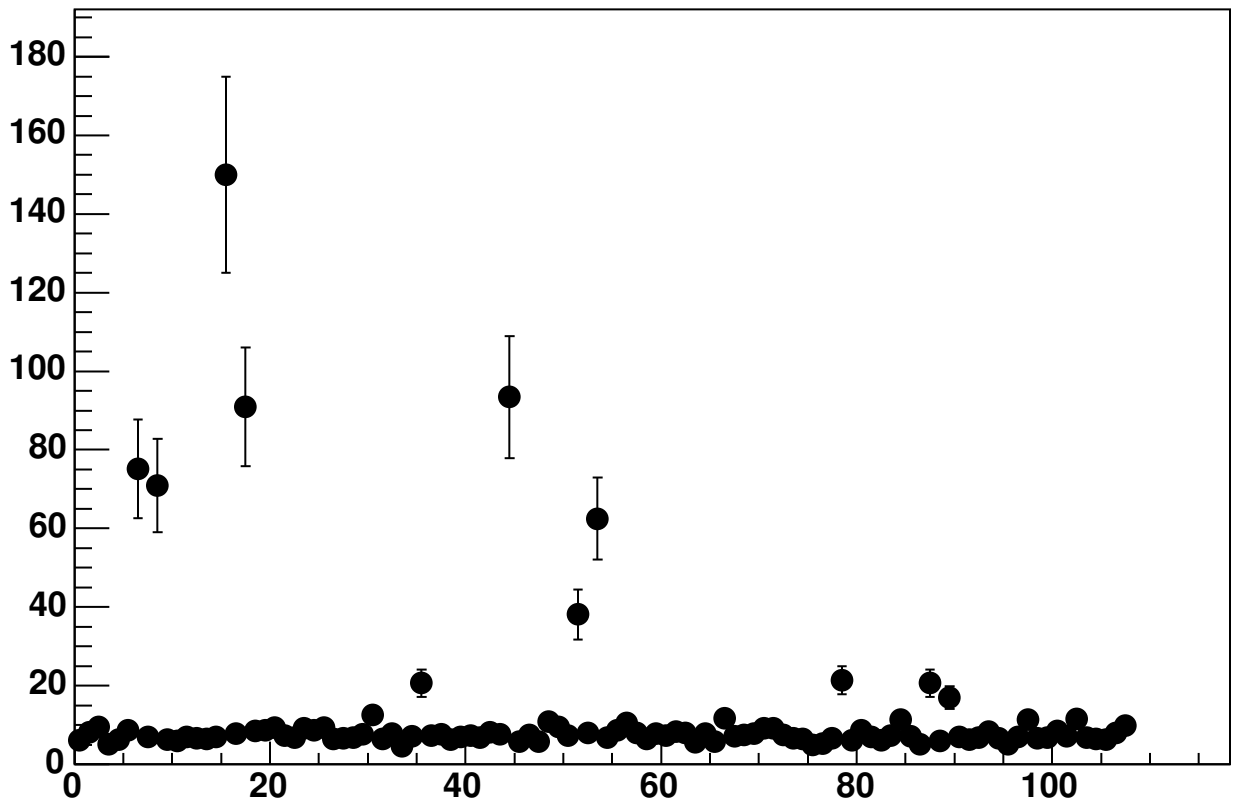
Enable 2, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



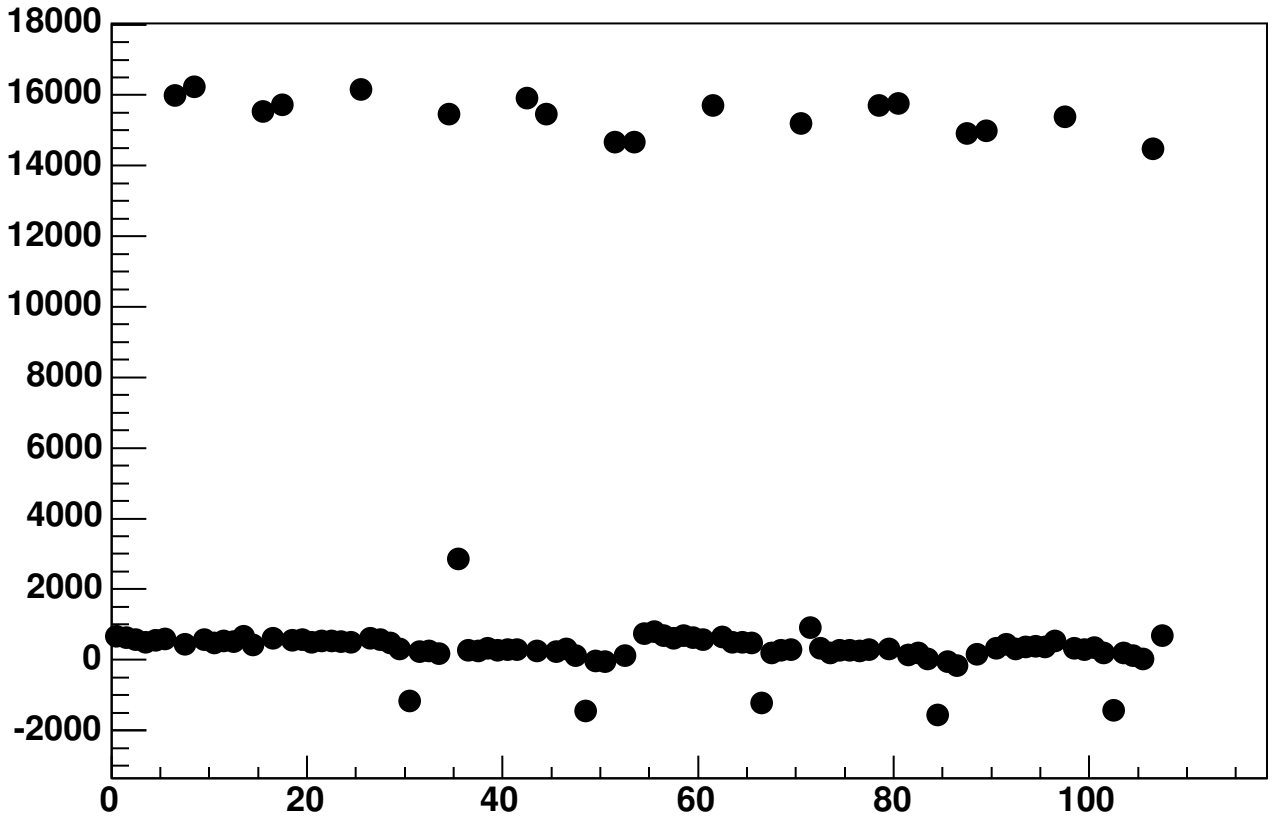
Enable 2, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



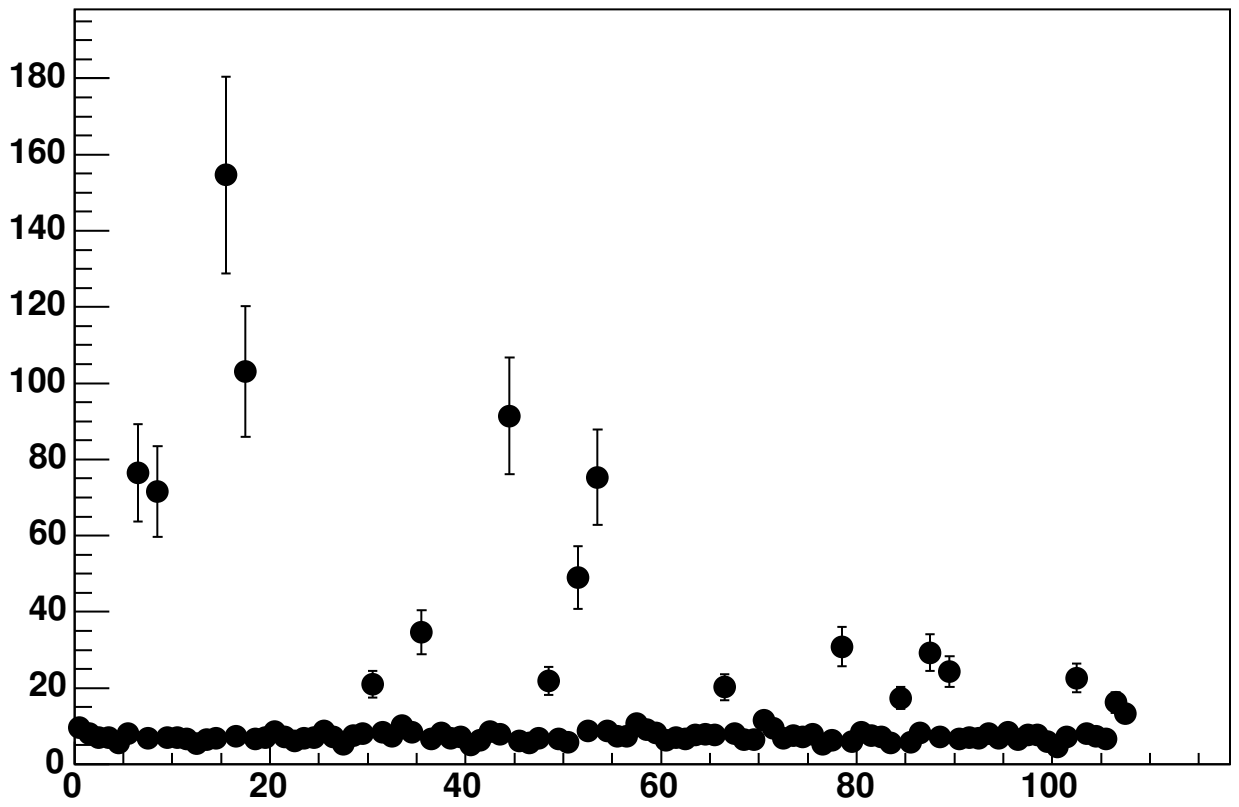
Enable 2, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



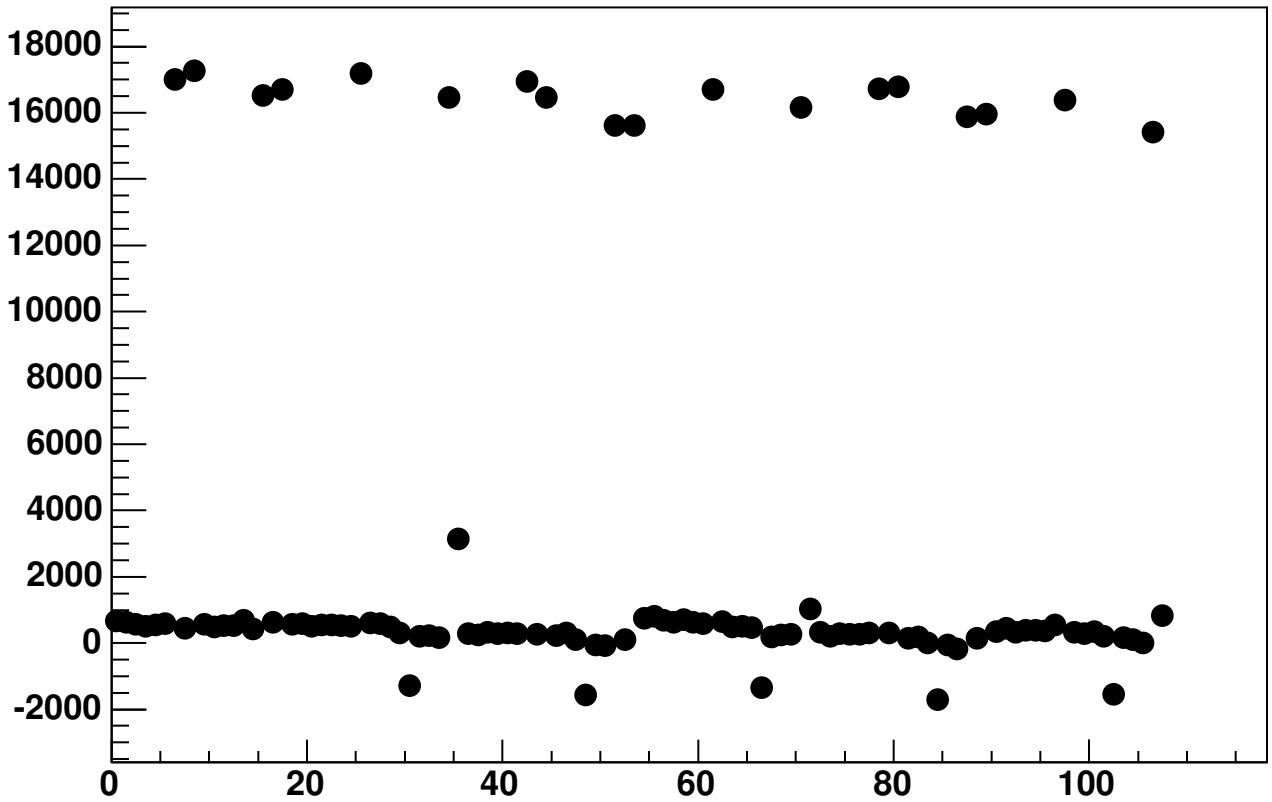
Enable 2, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



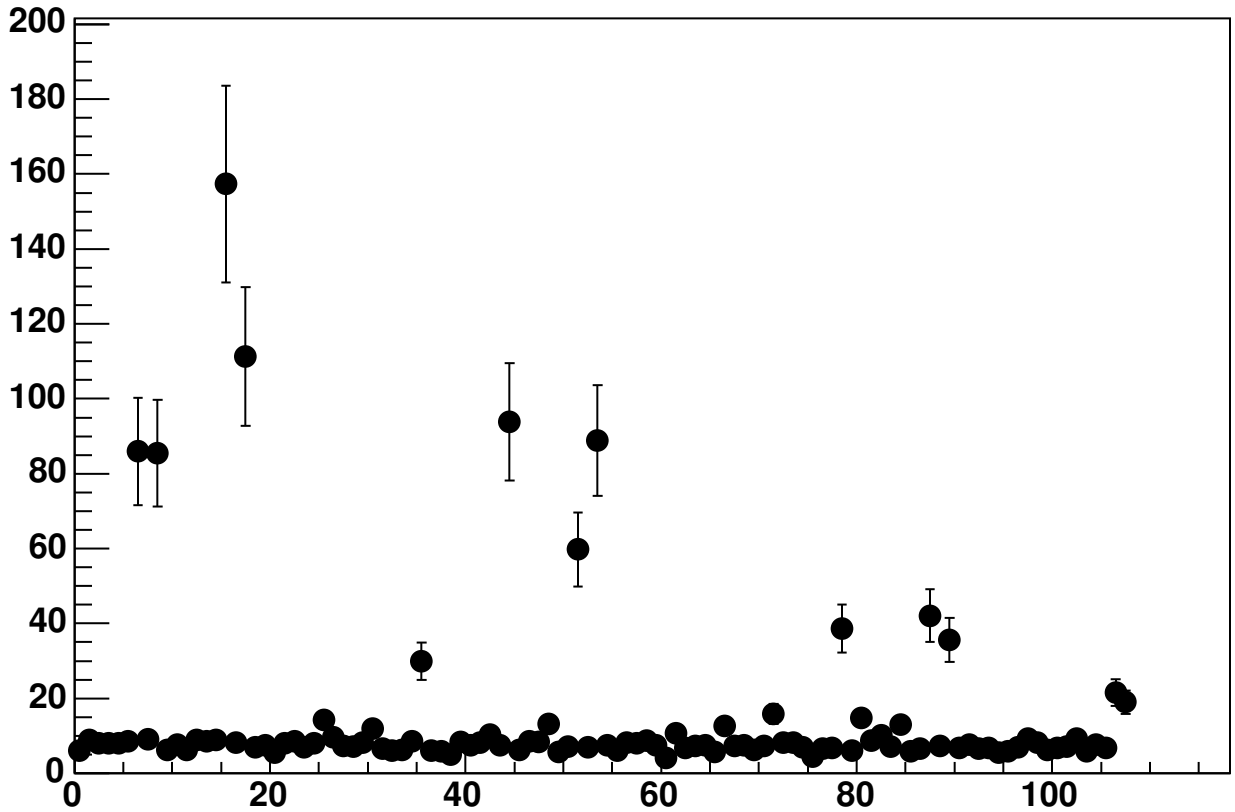
Enable 2, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



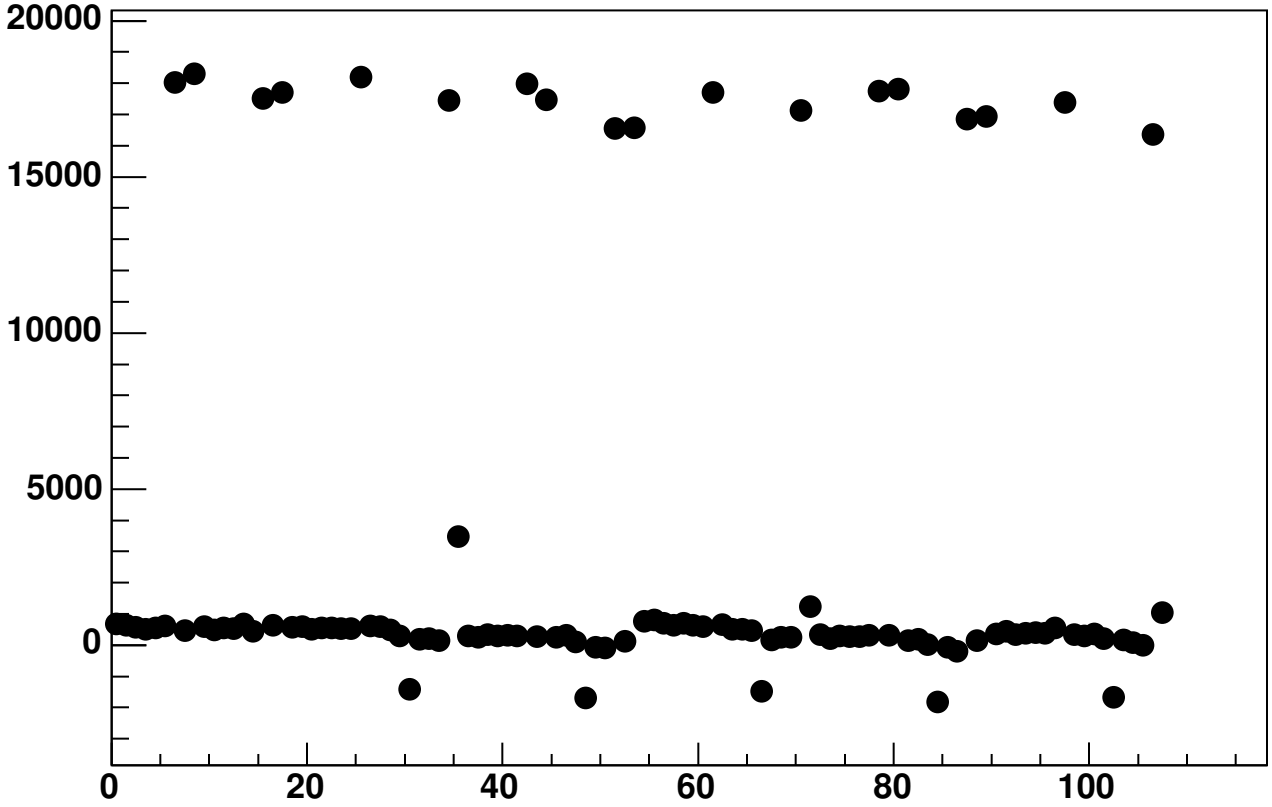
Enable 2, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



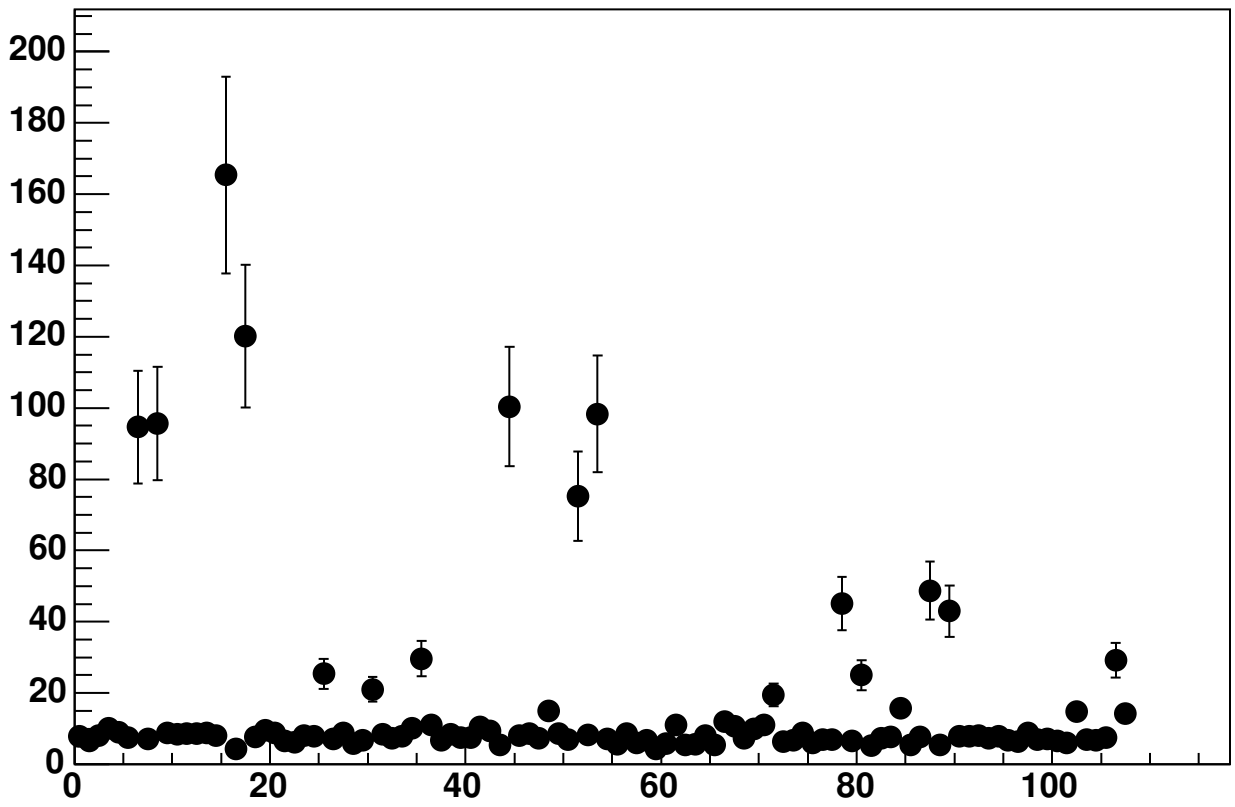
Enable 2, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



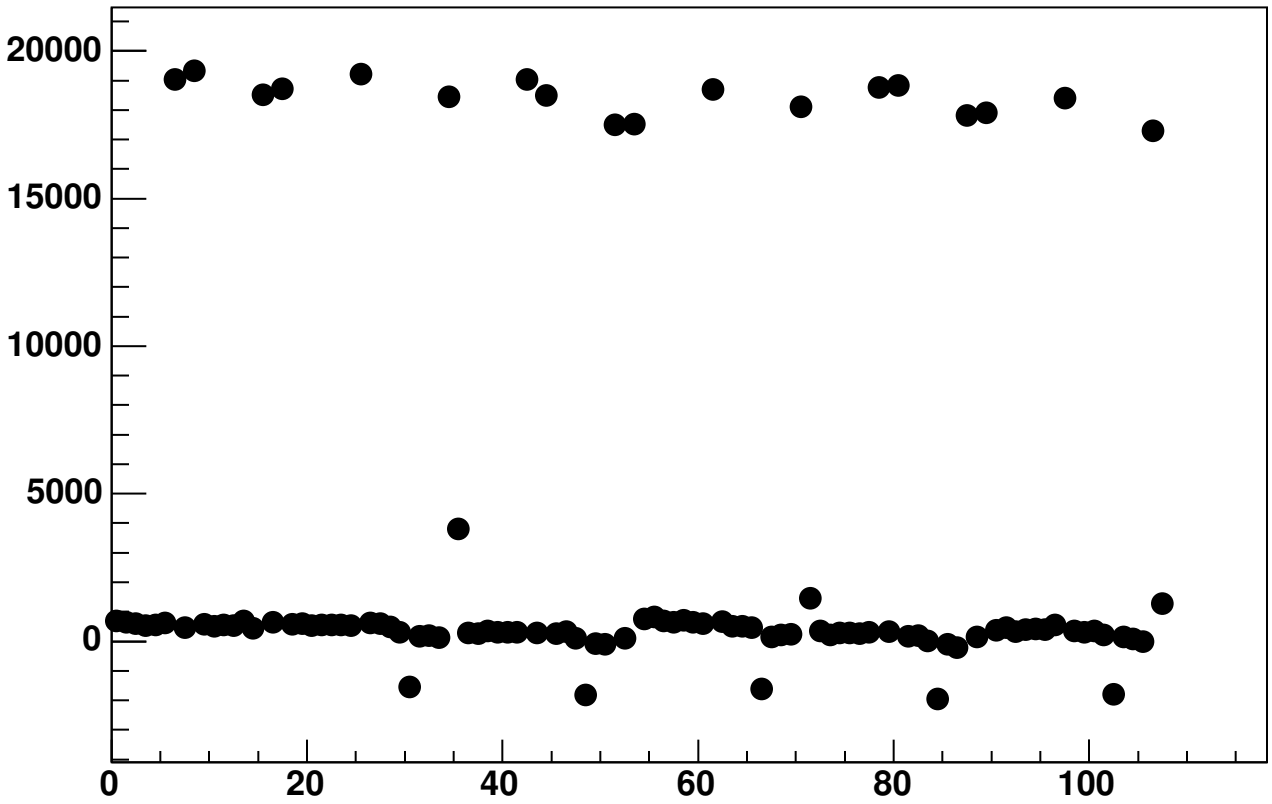
Enable 2, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



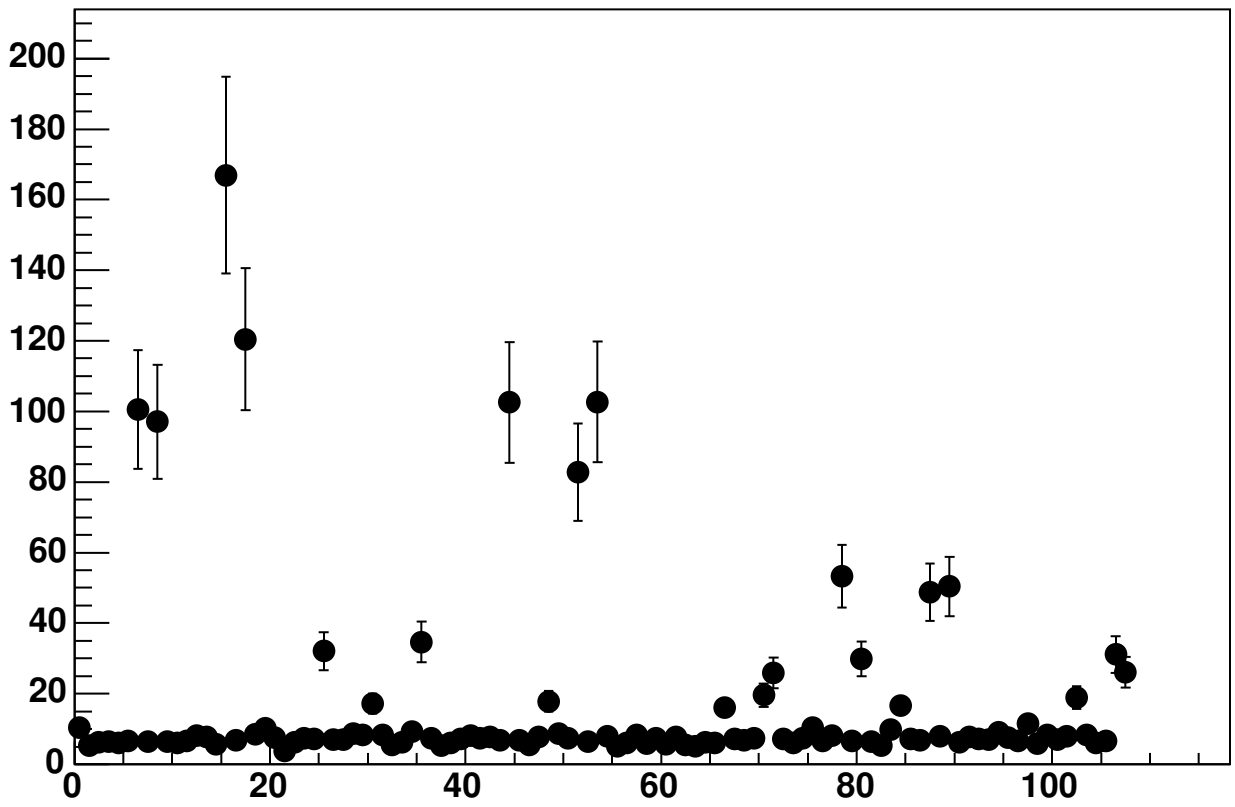
Enable 2, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



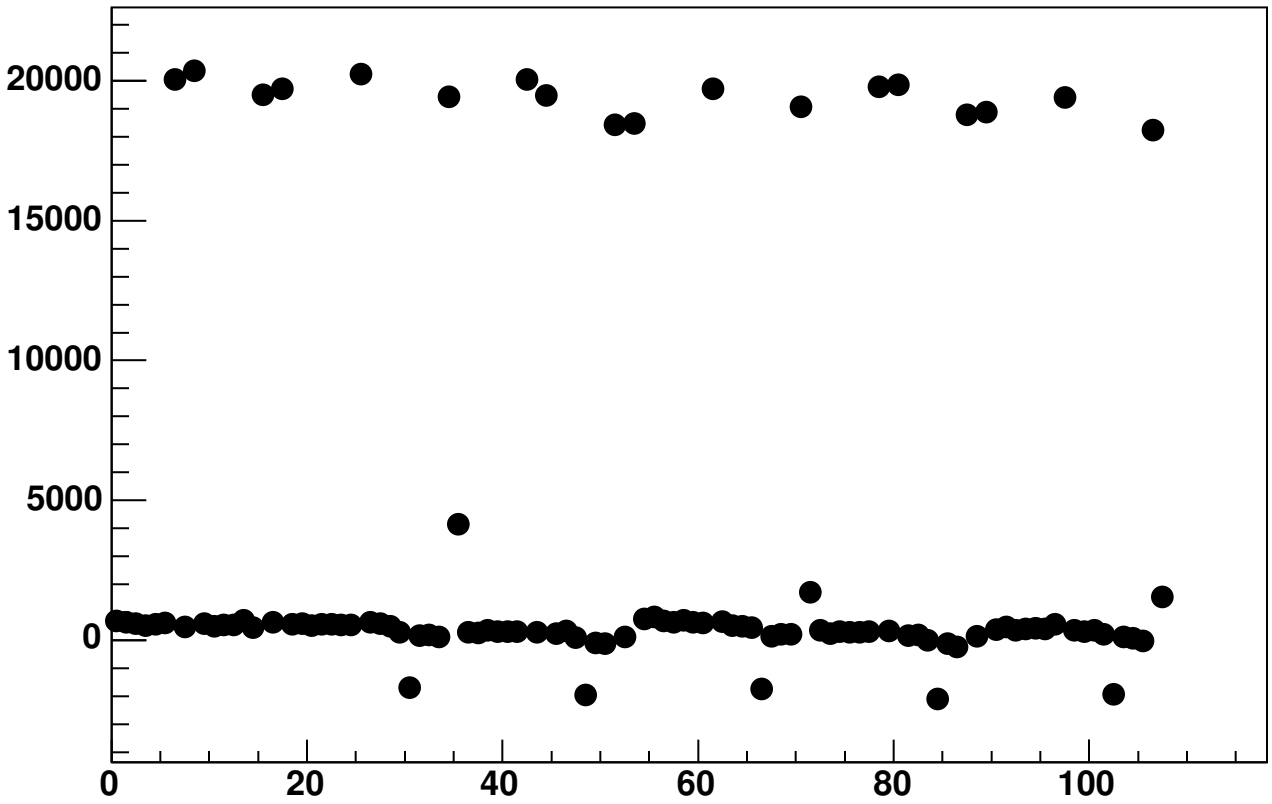
Enable 2, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



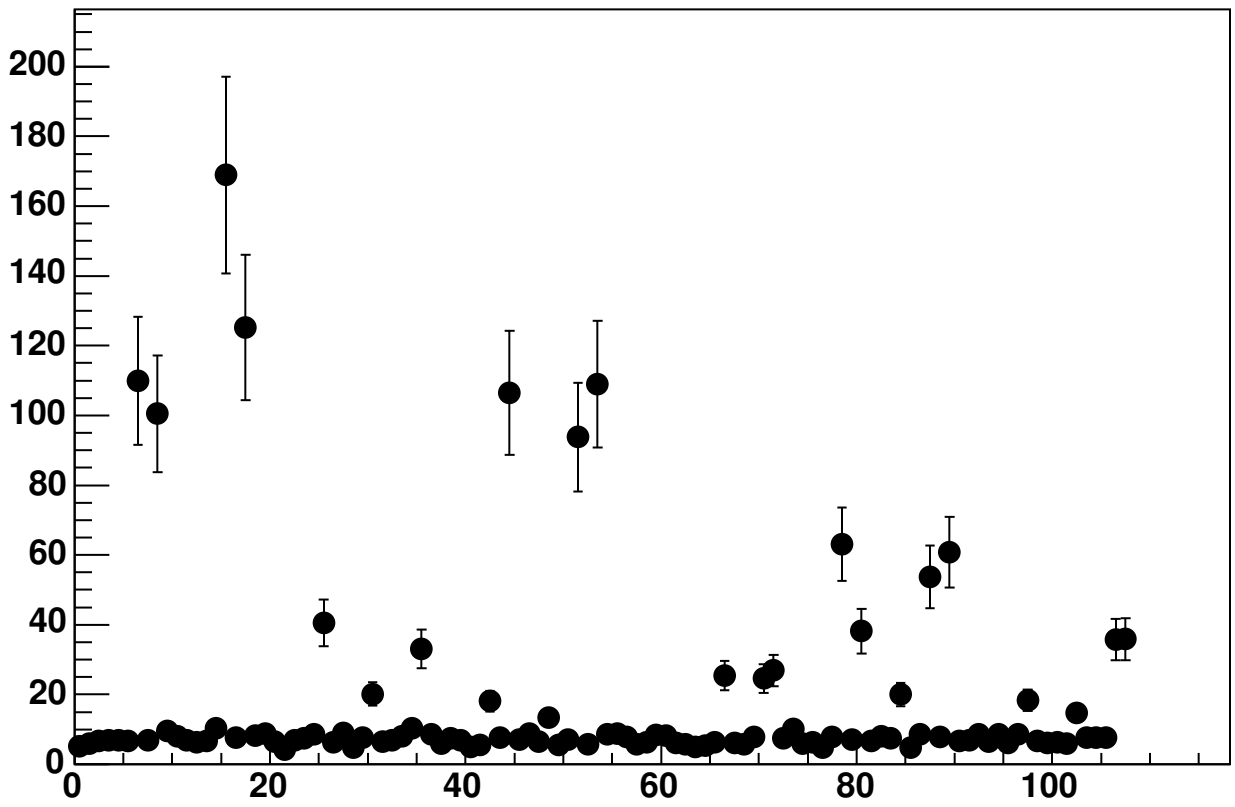
Enable 2, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



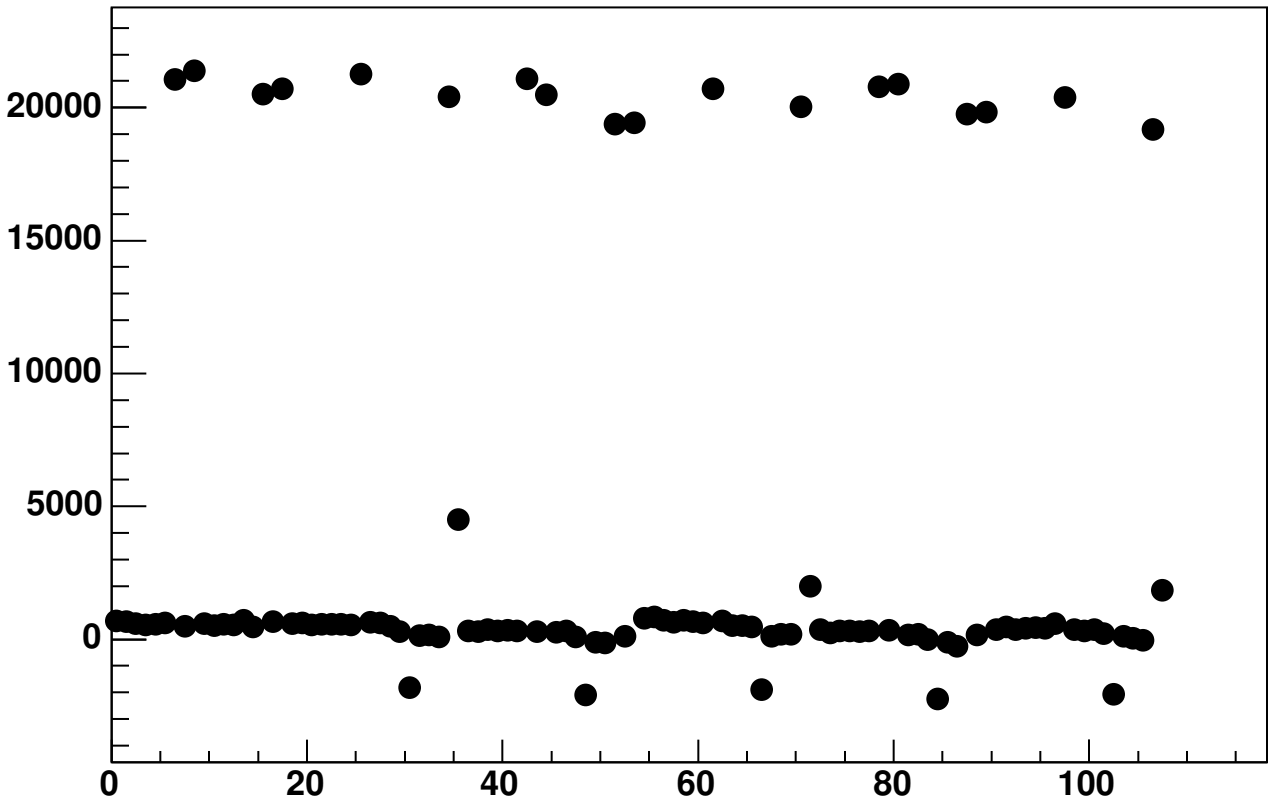
Enable 2, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



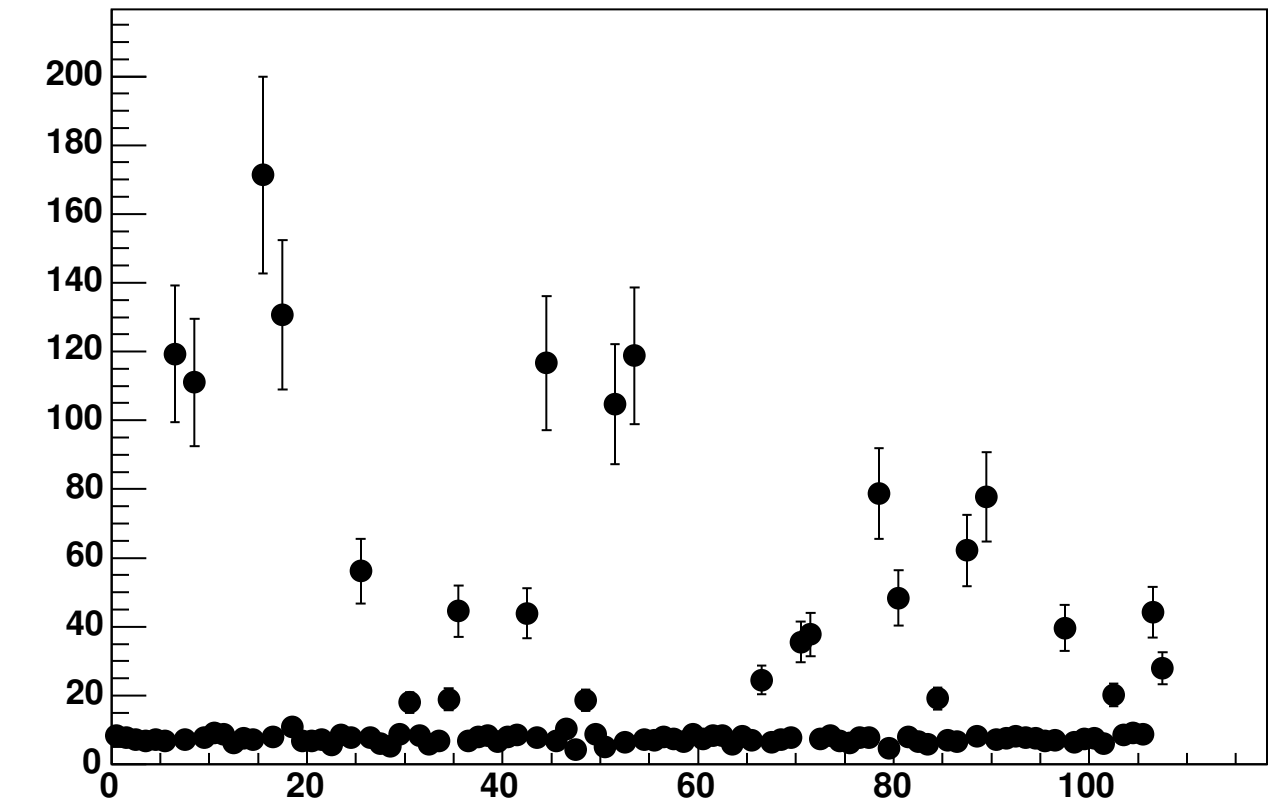
Enable 2, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



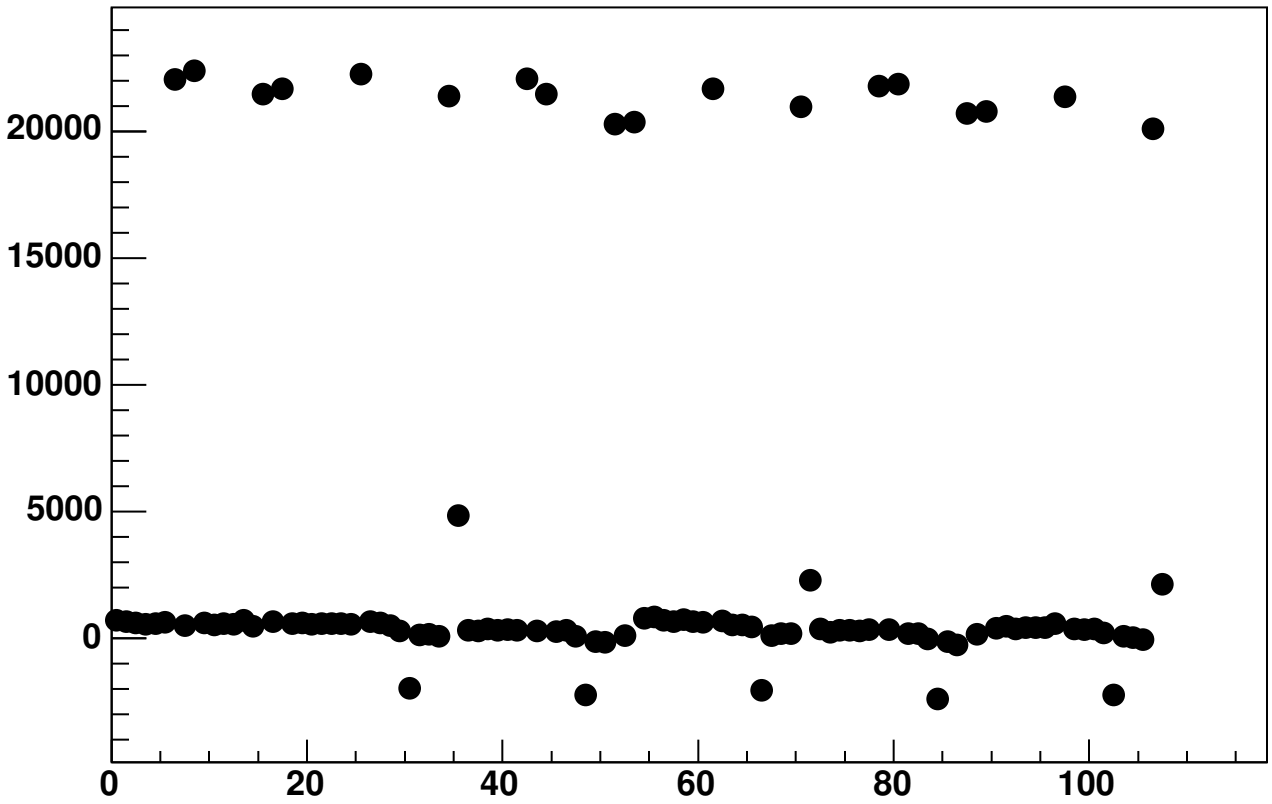
Enable 2, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



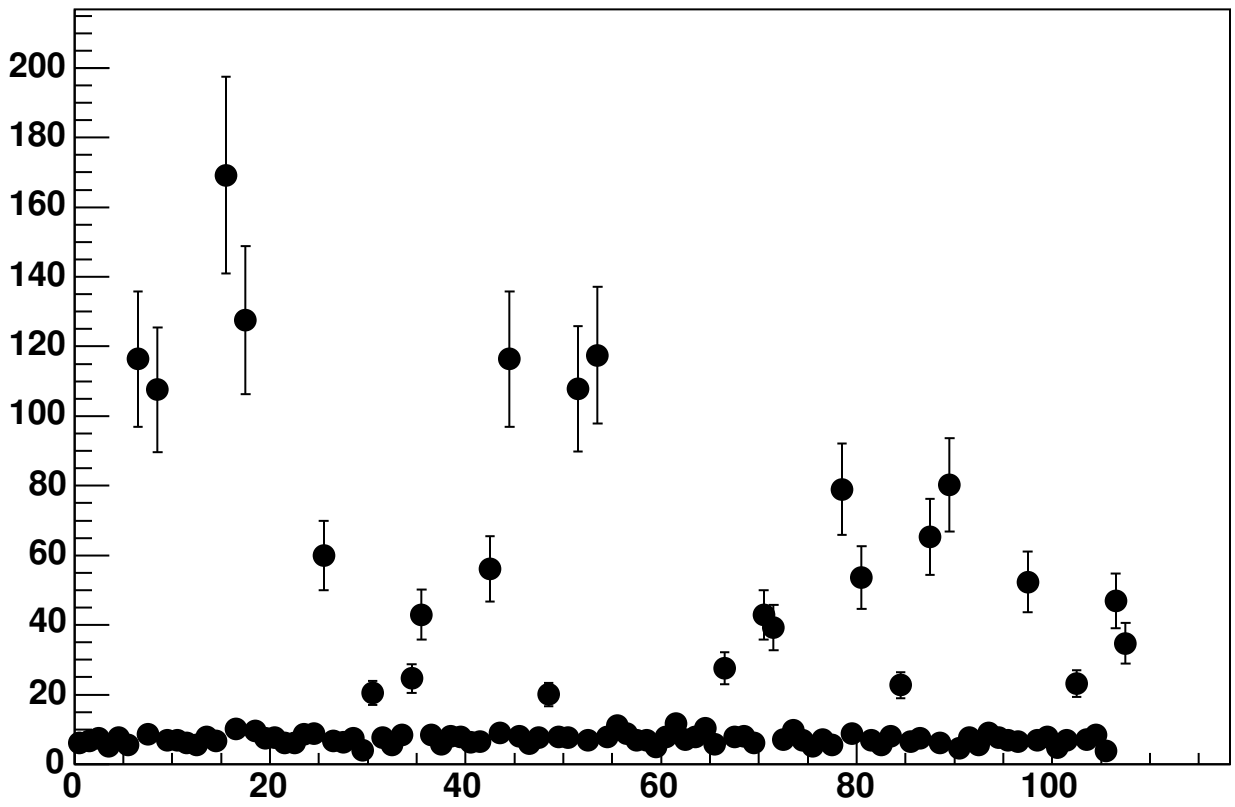
Enable 2, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



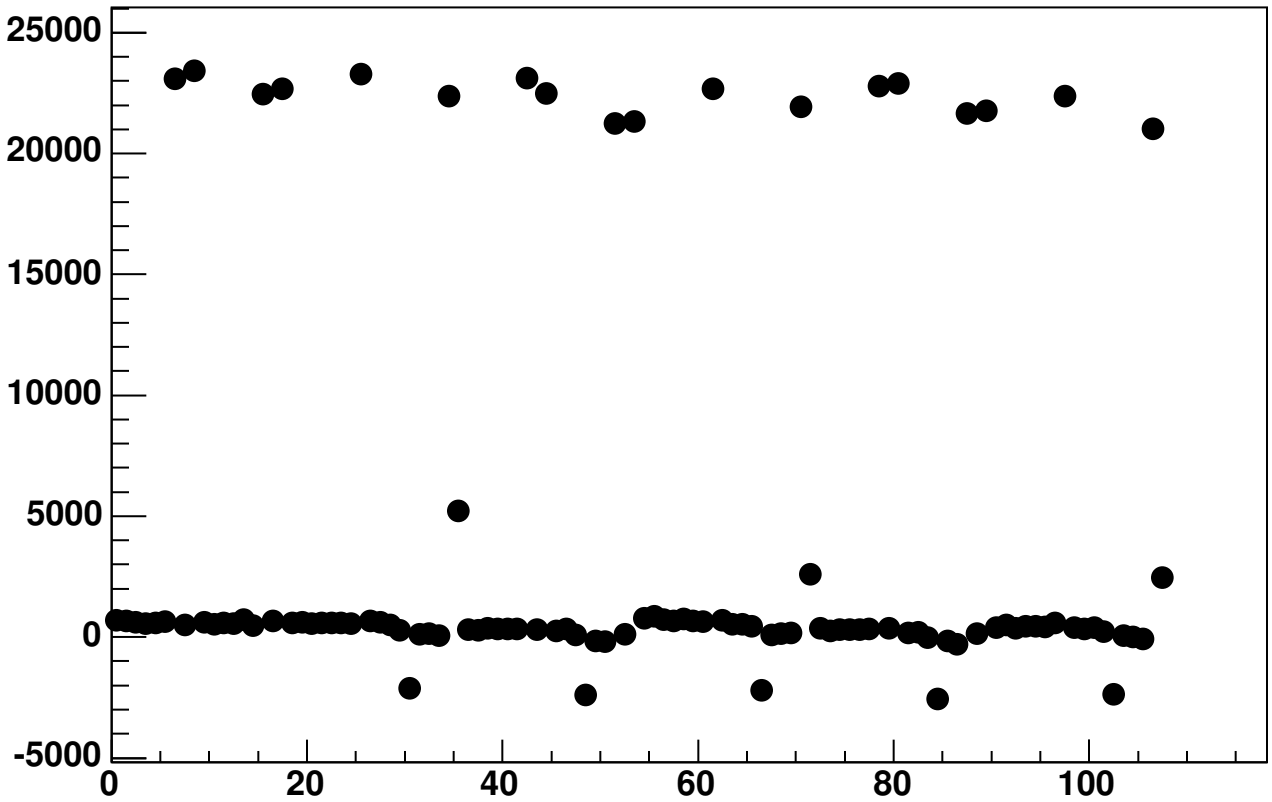
Enable 2, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



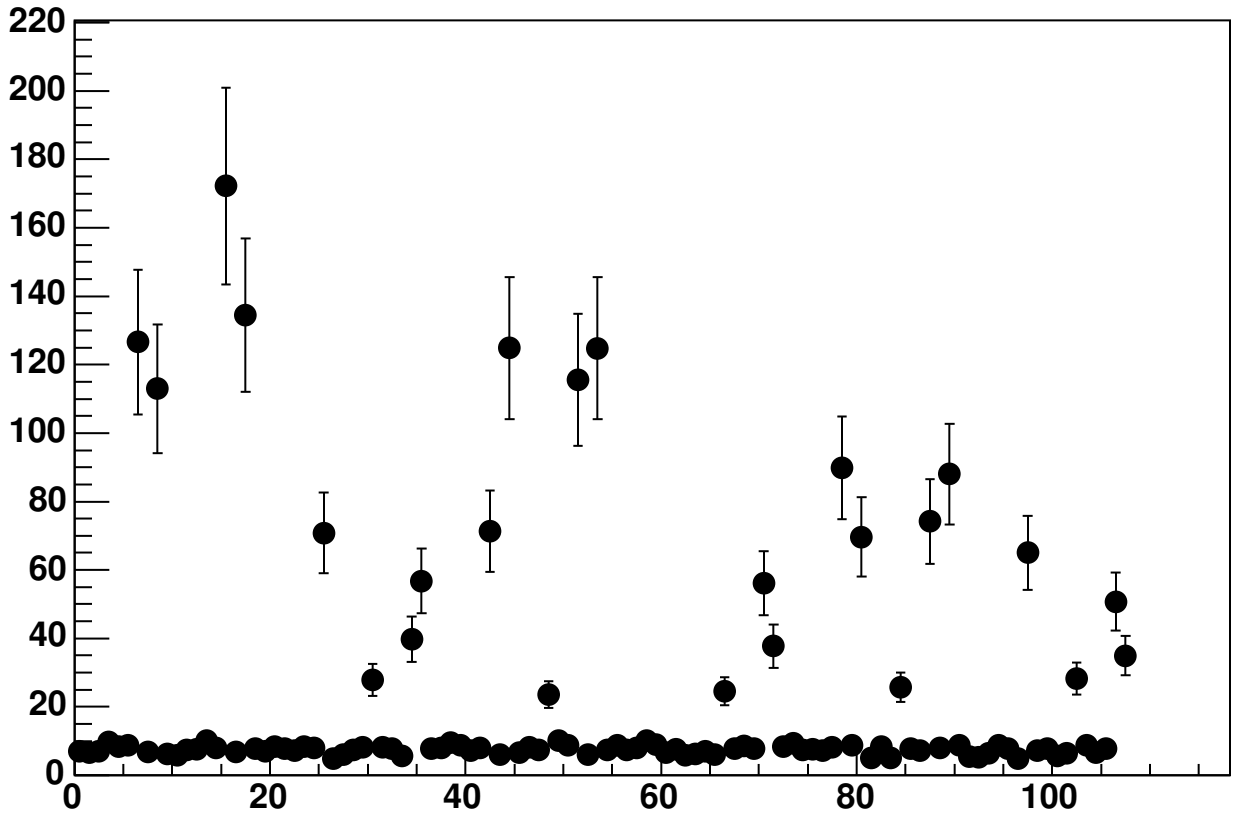
Enable 2, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



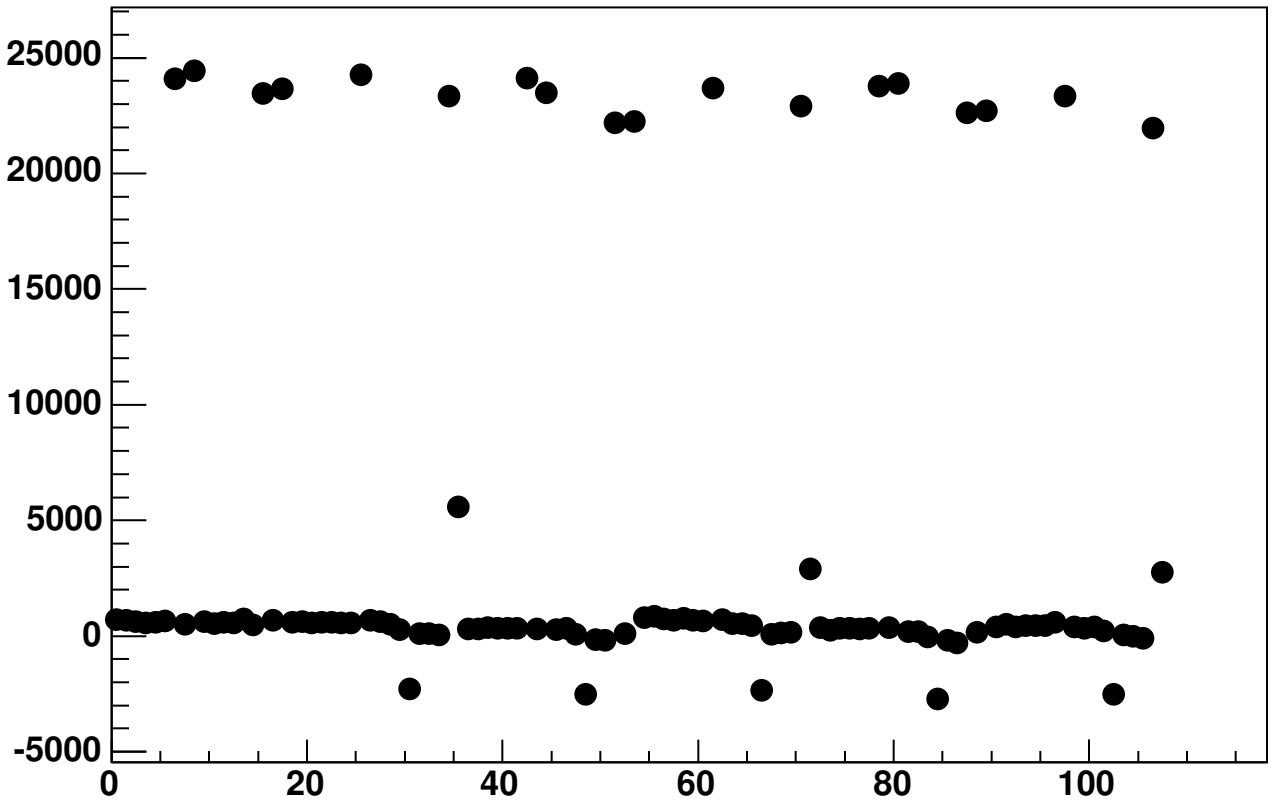
Enable 2, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



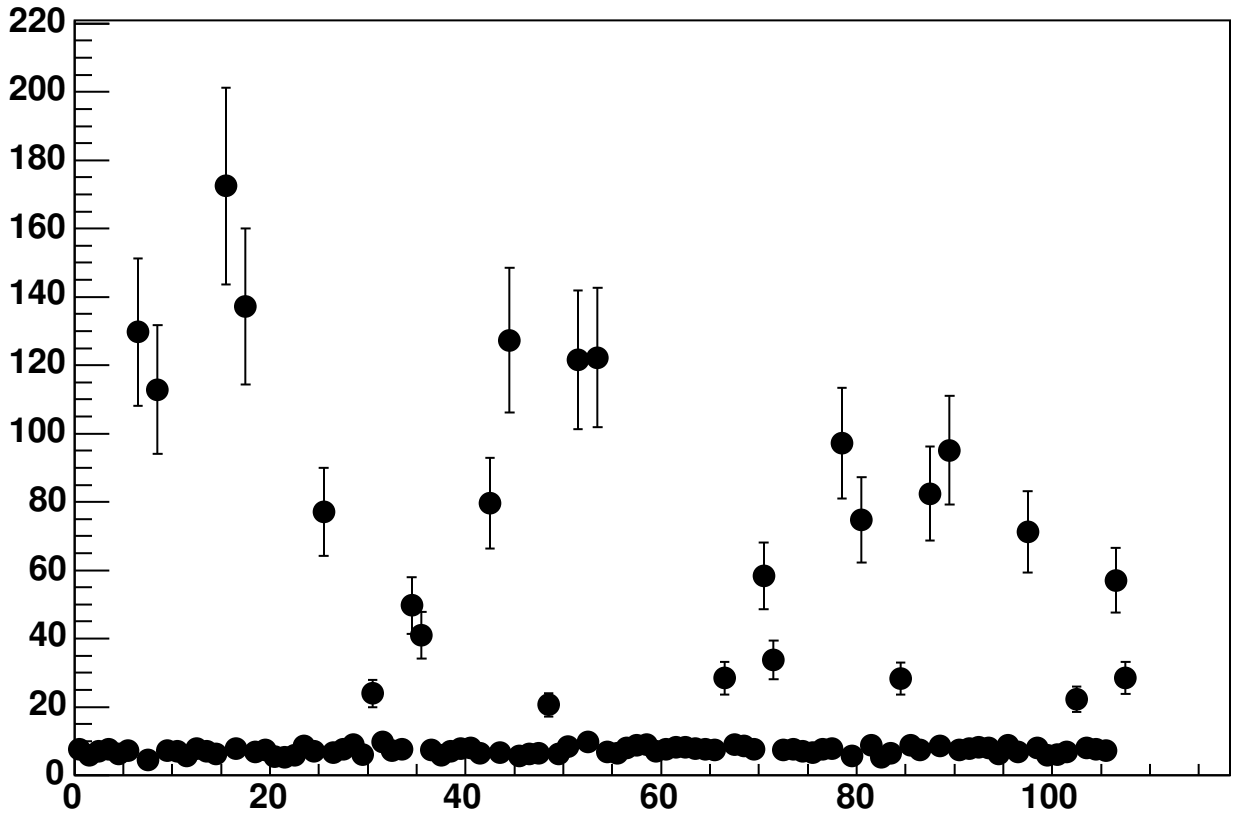
Enable 2, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



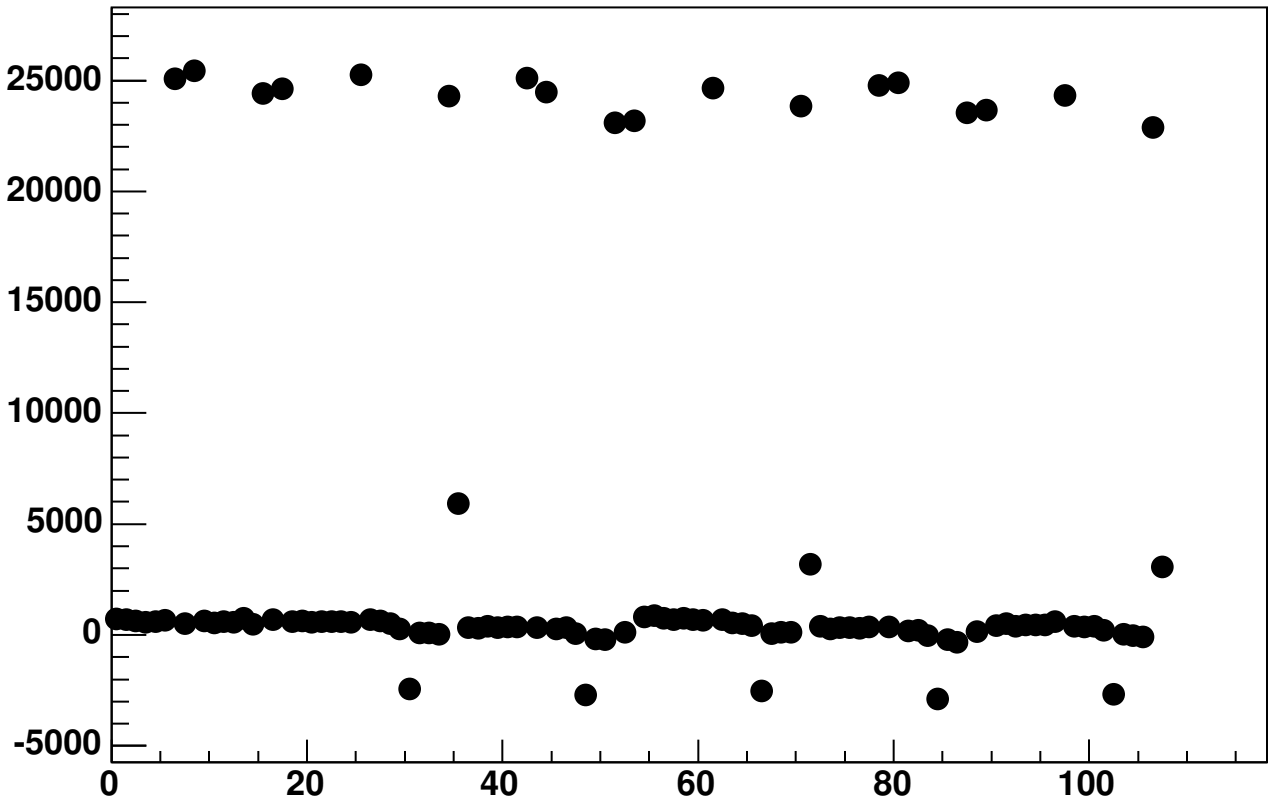
Enable 2, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



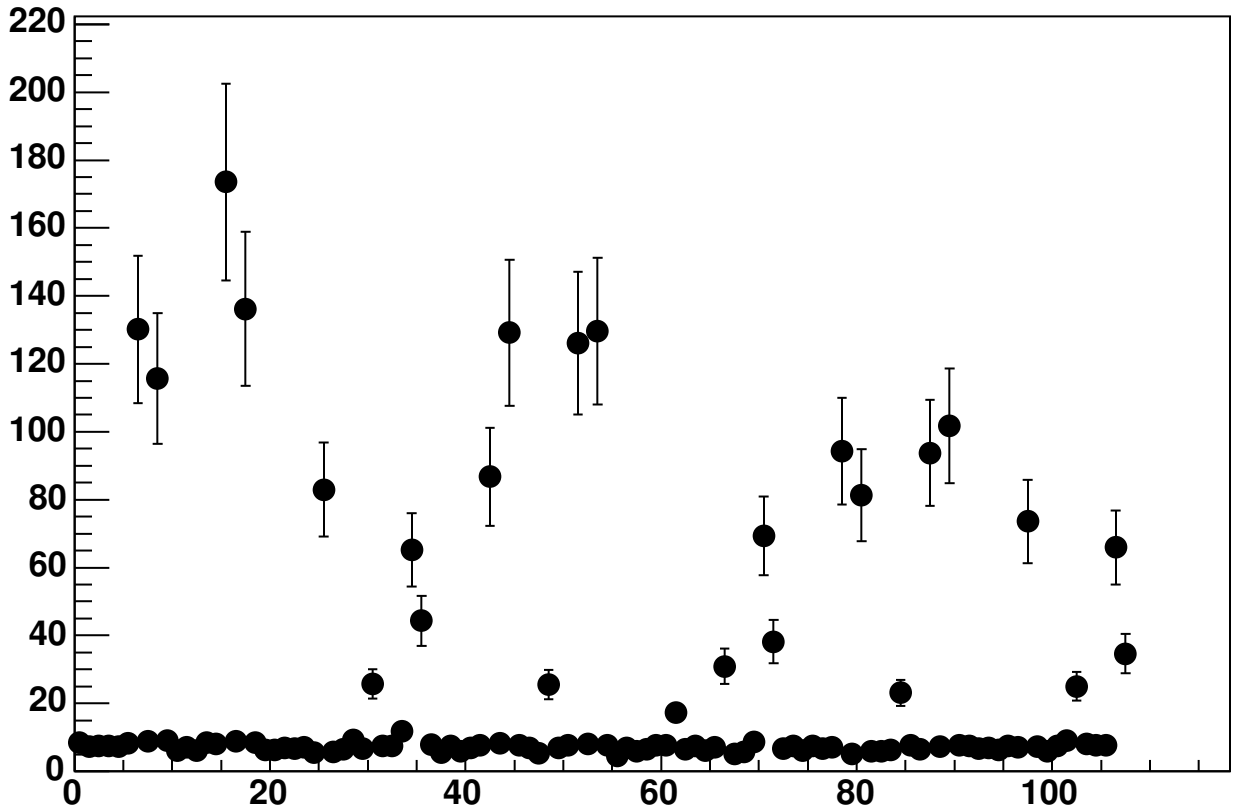
Enable 2, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



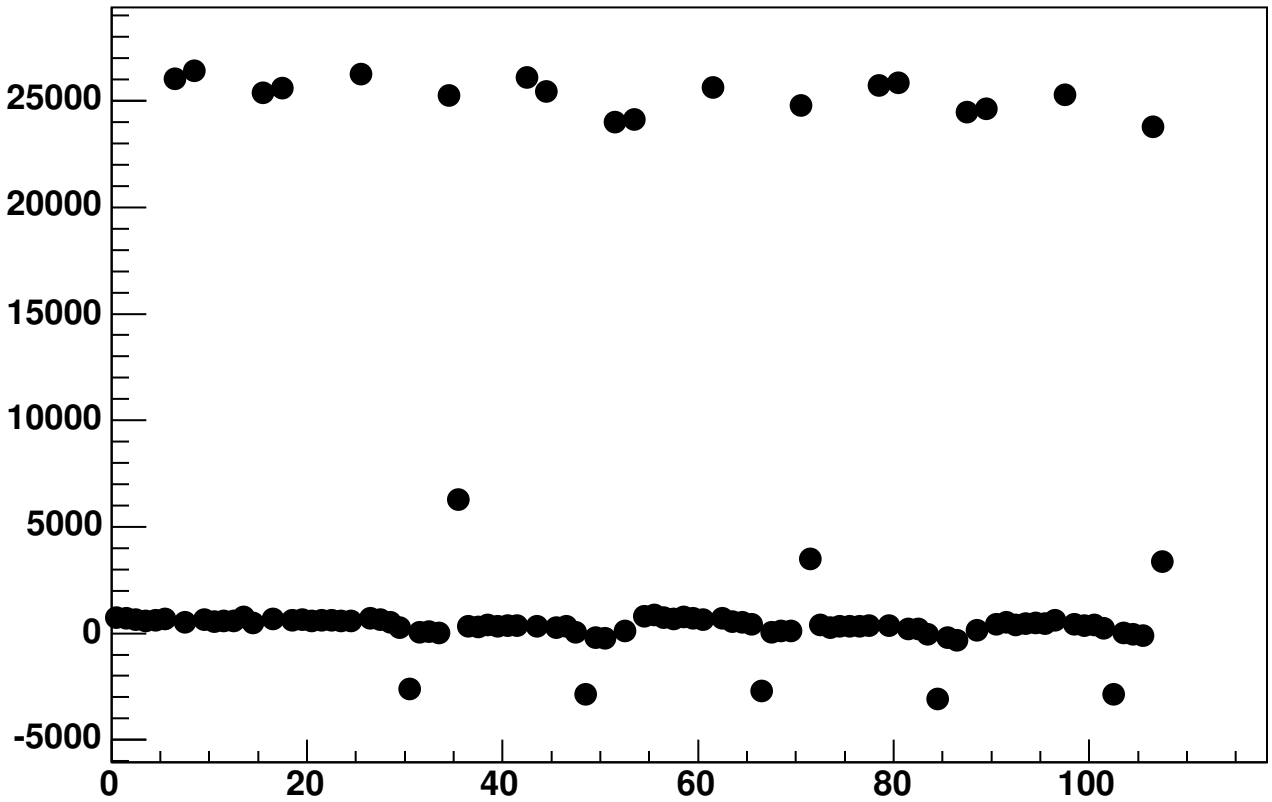
Enable 2, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



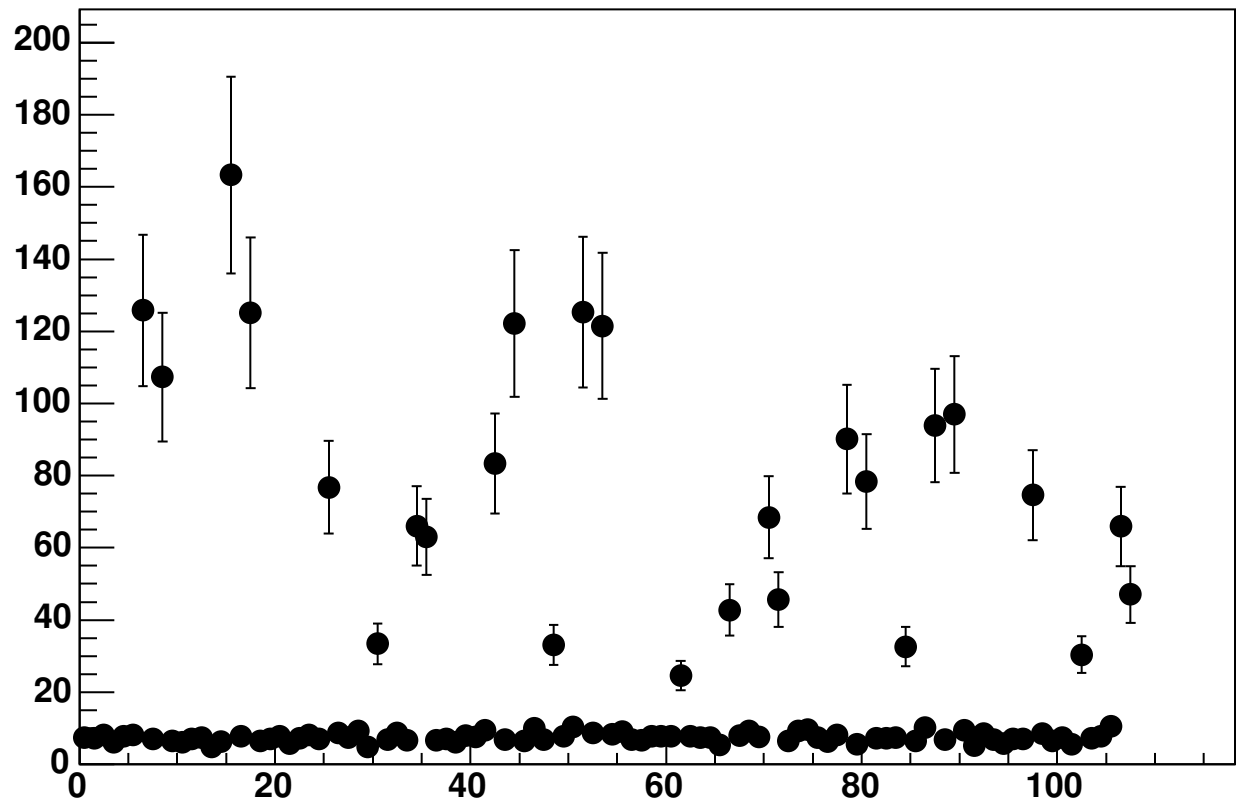
Enable 2, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



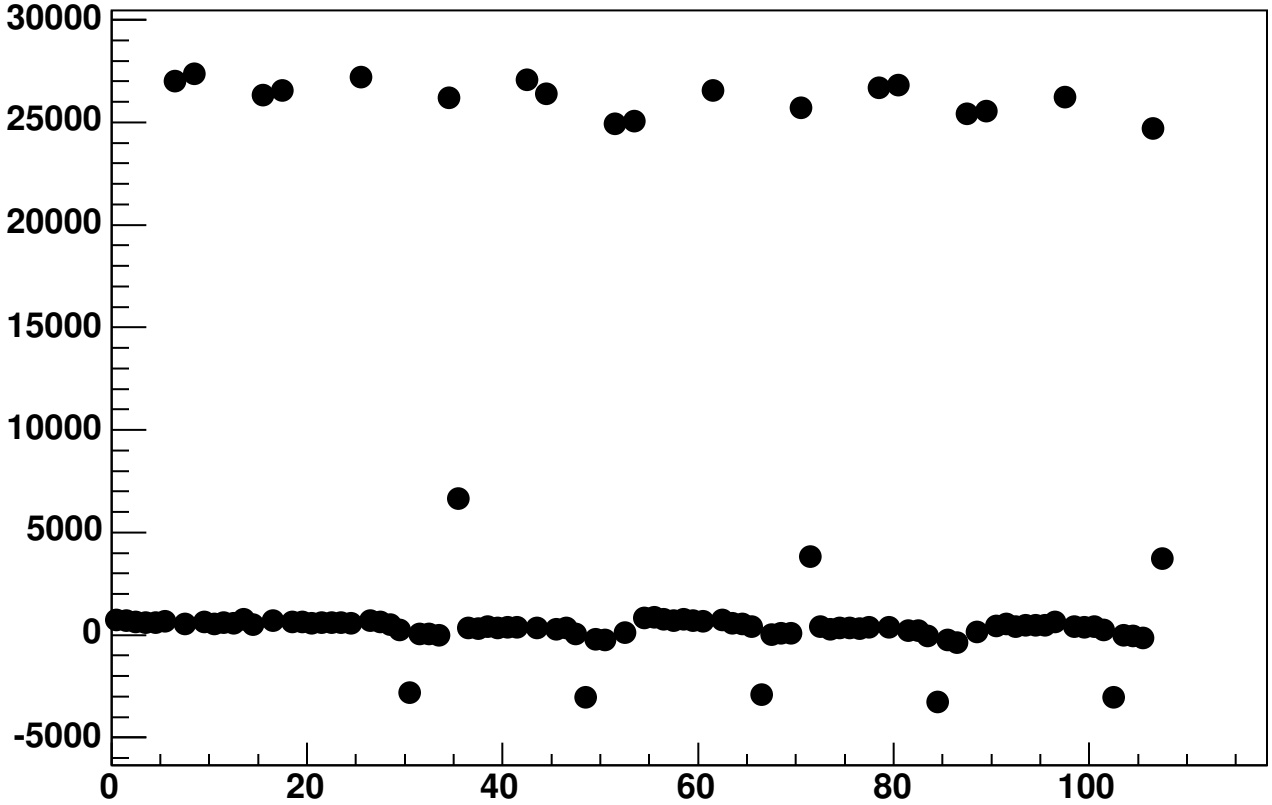
Enable 2, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



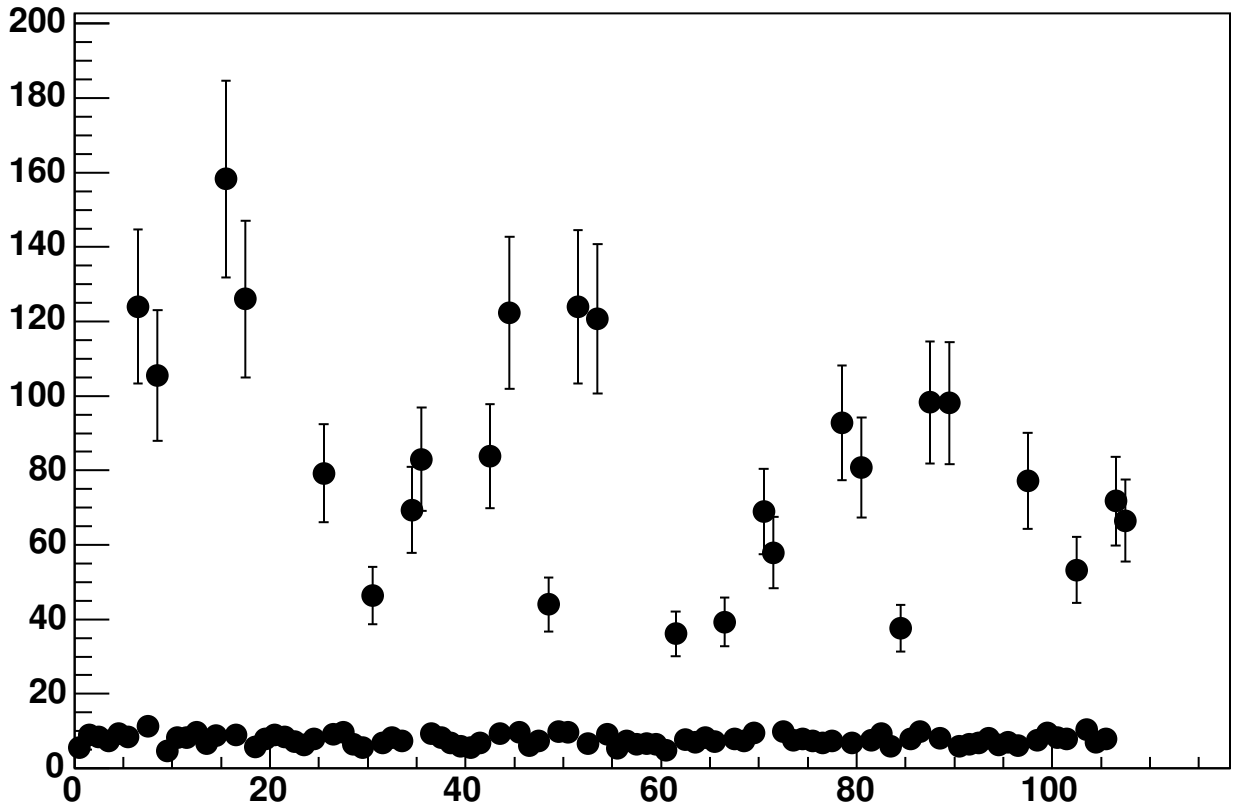
Enable 2, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



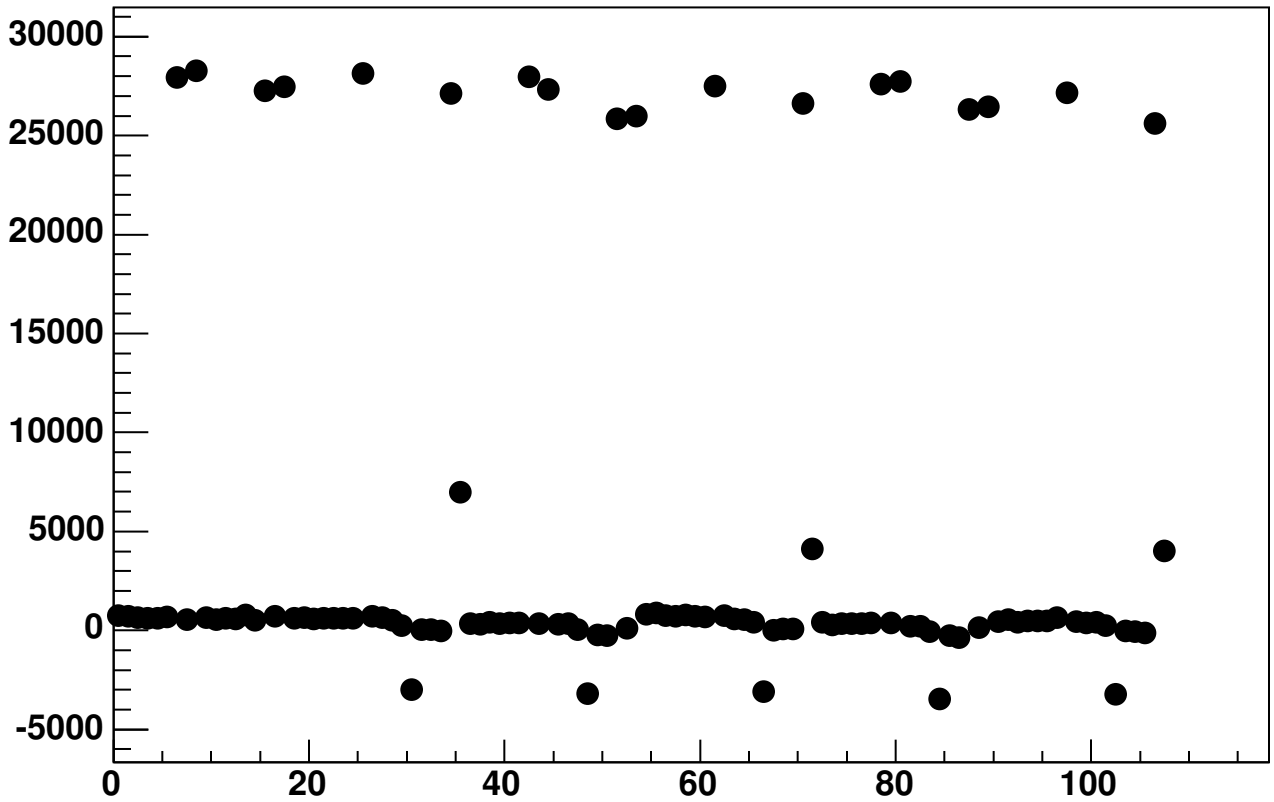
Enable 2, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



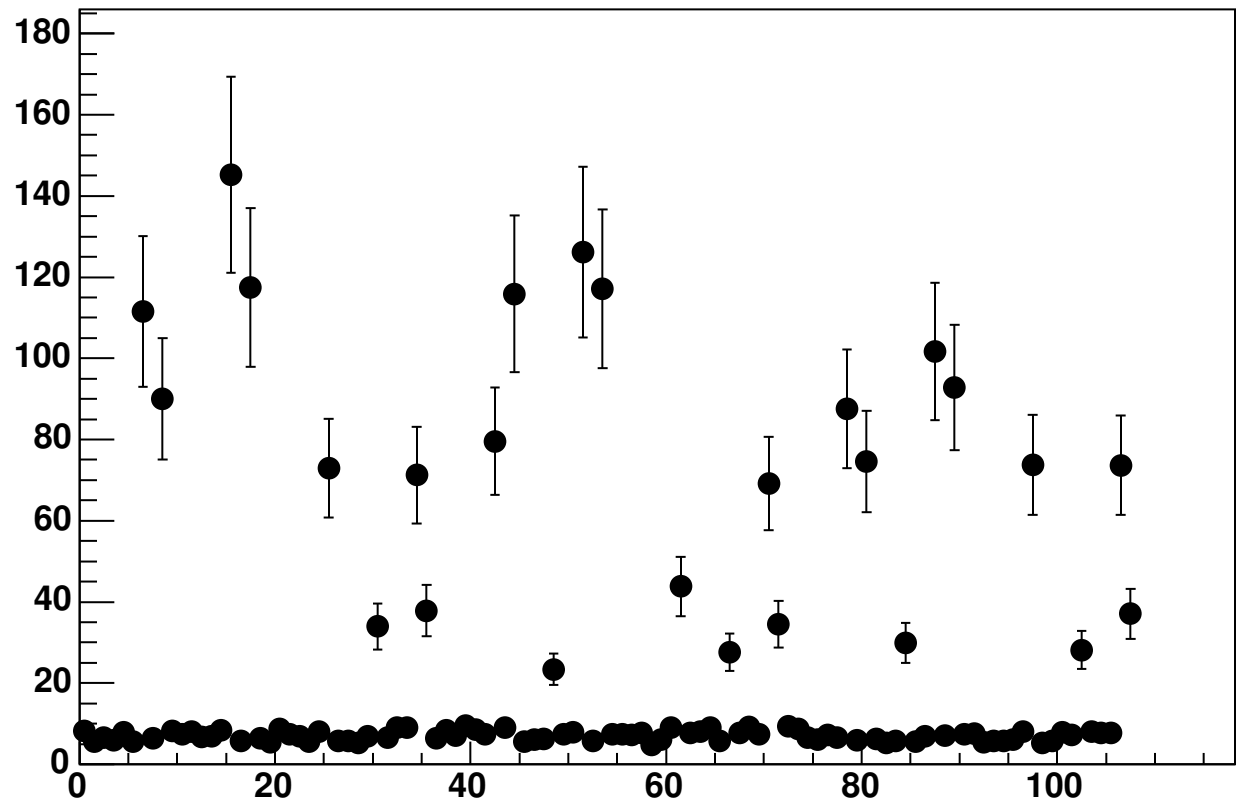
Enable 2, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



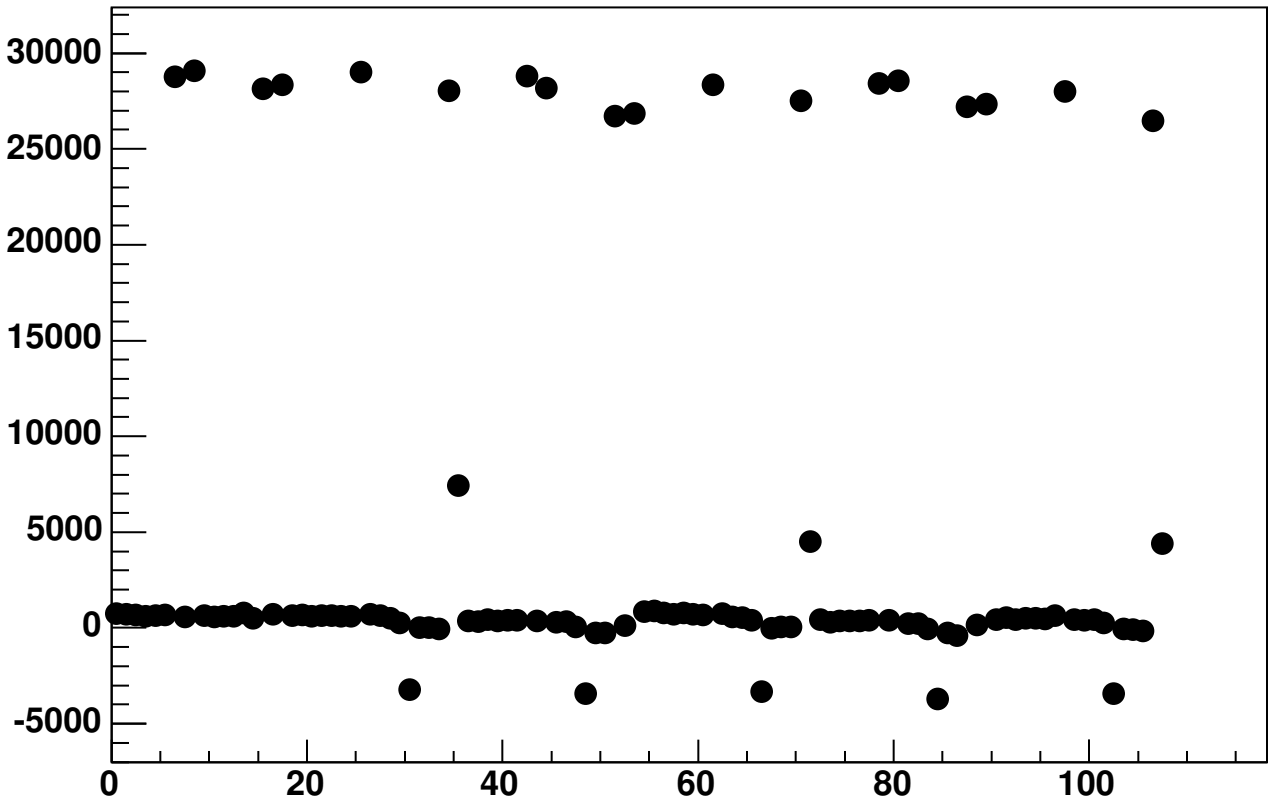
Enable 2, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



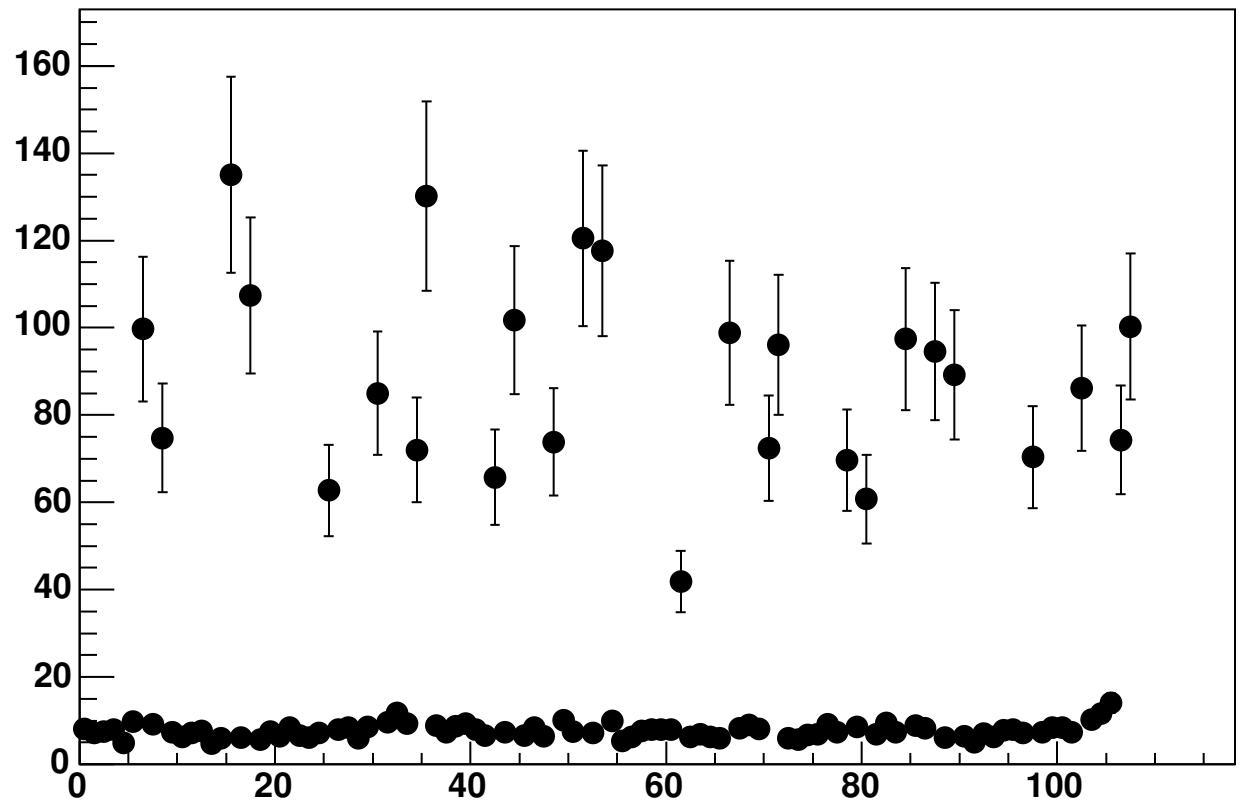
Enable 2, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



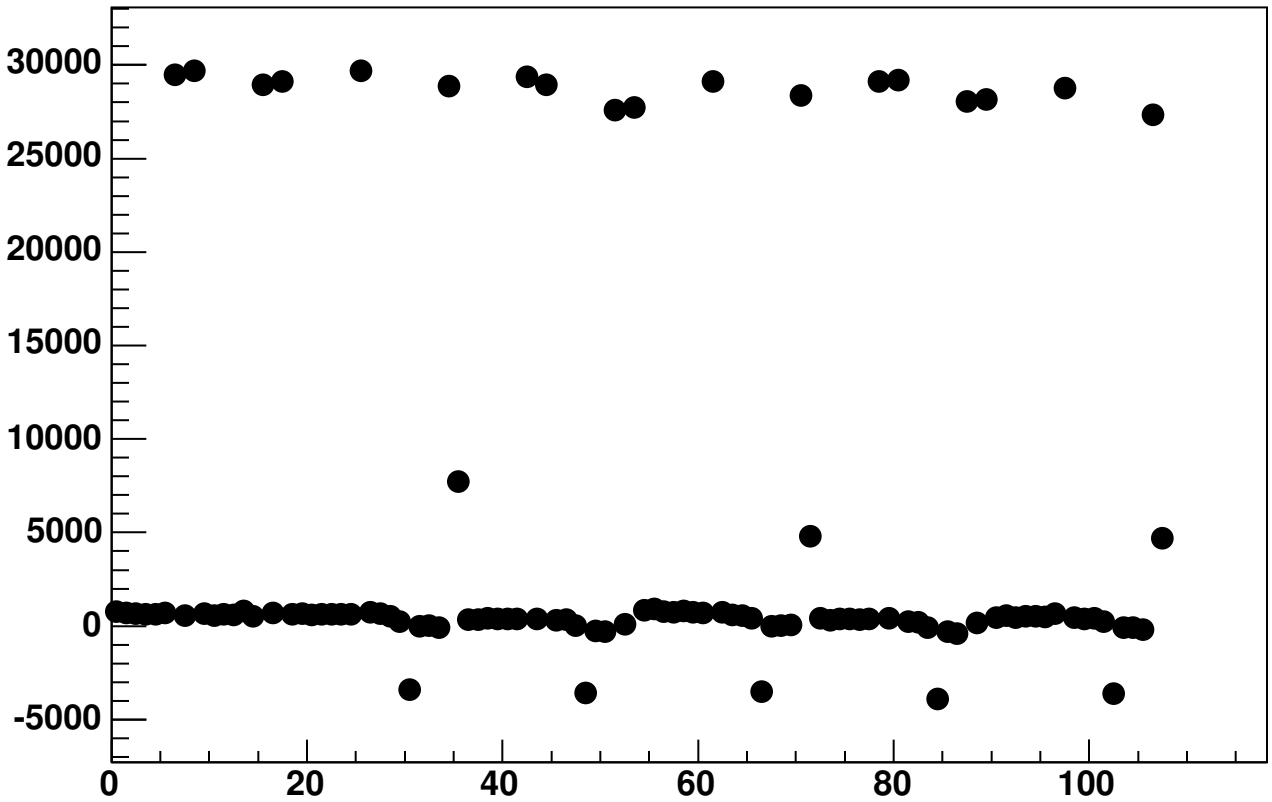
Enable 2, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



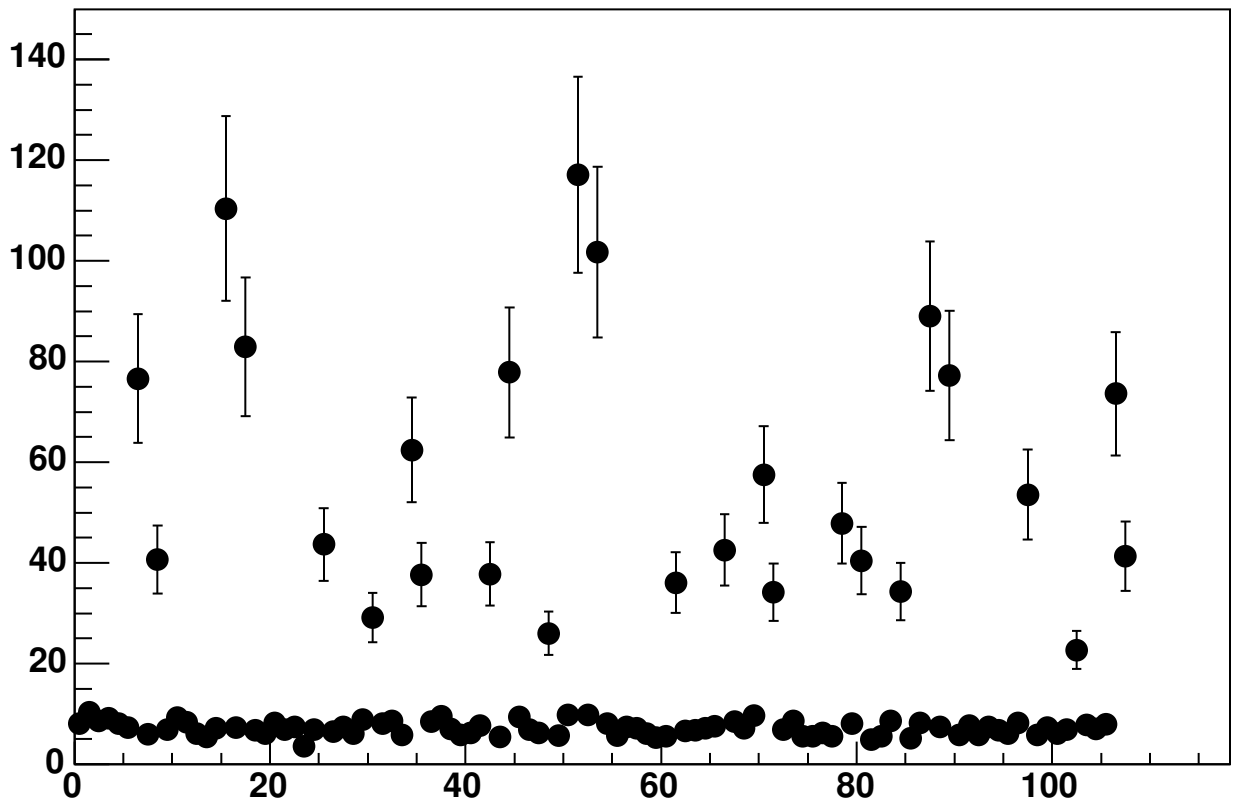
Enable 2, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



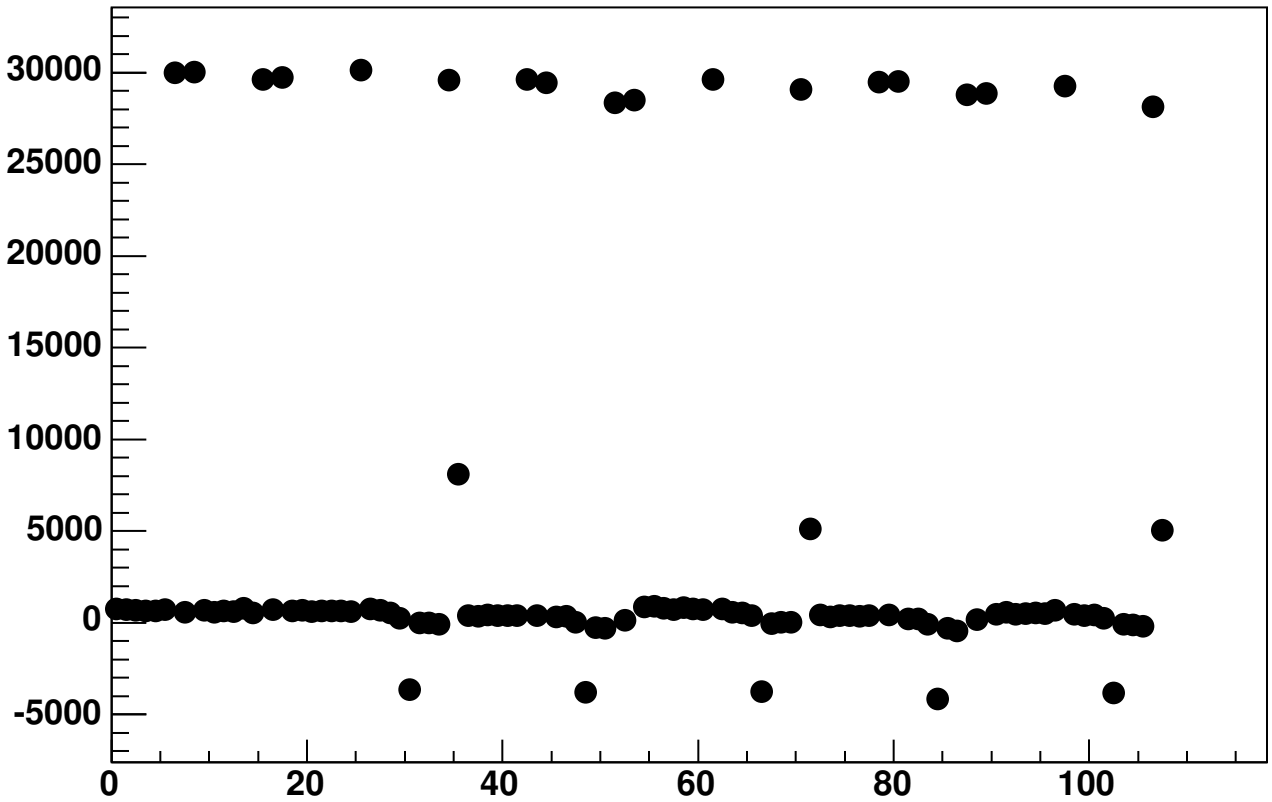
Enable 2, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



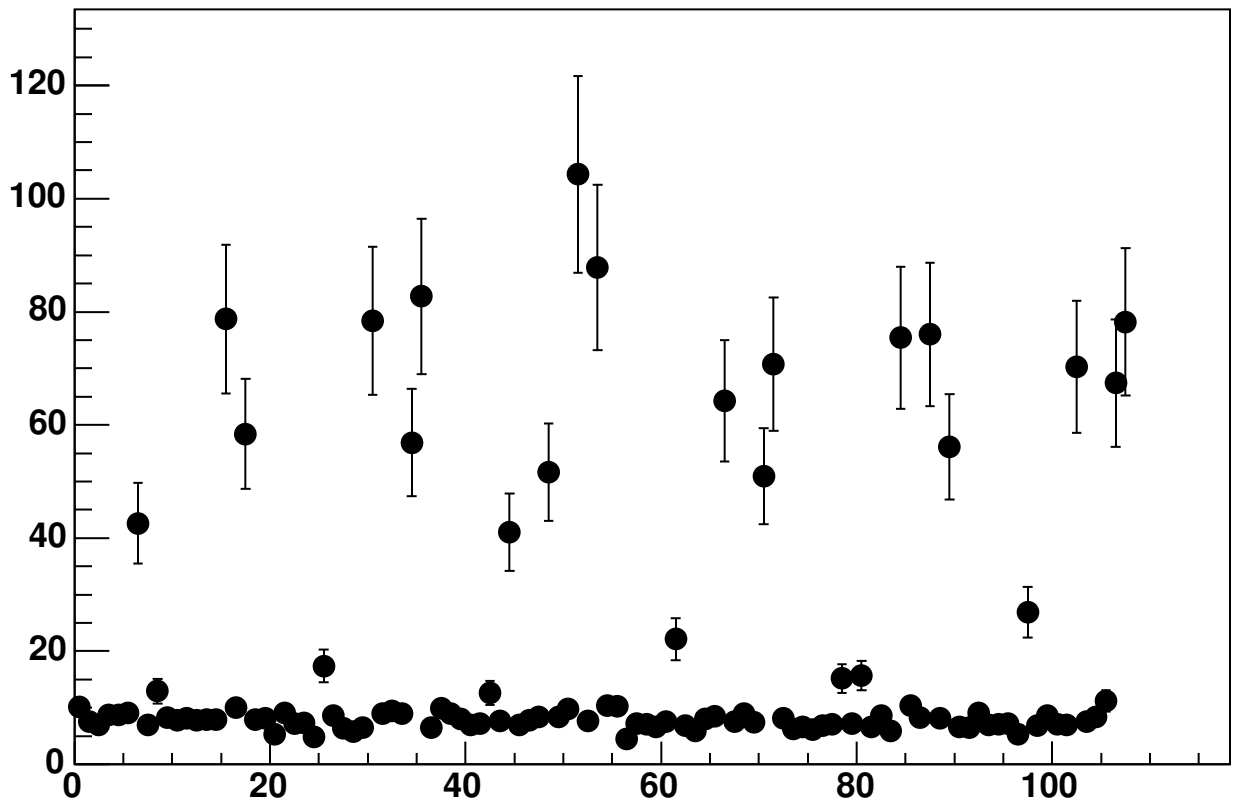
Enable 2, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



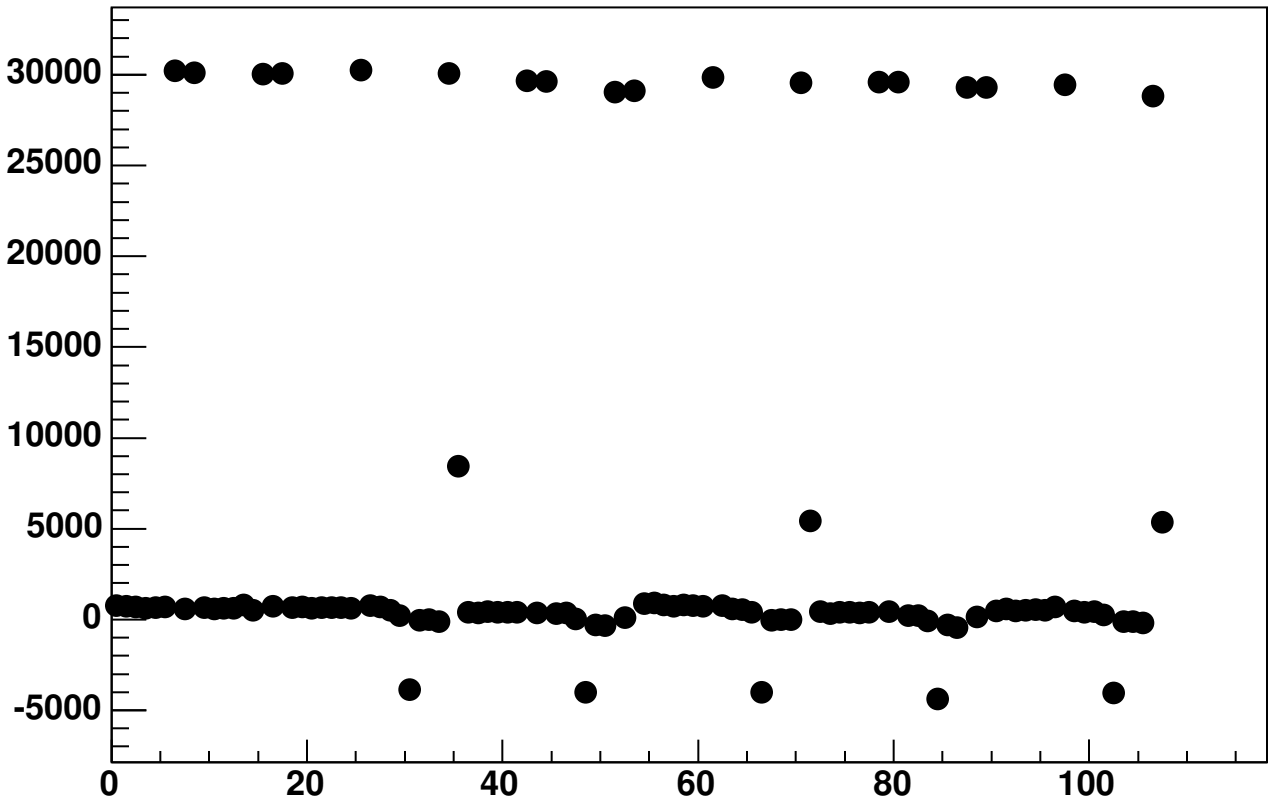
Enable 2, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



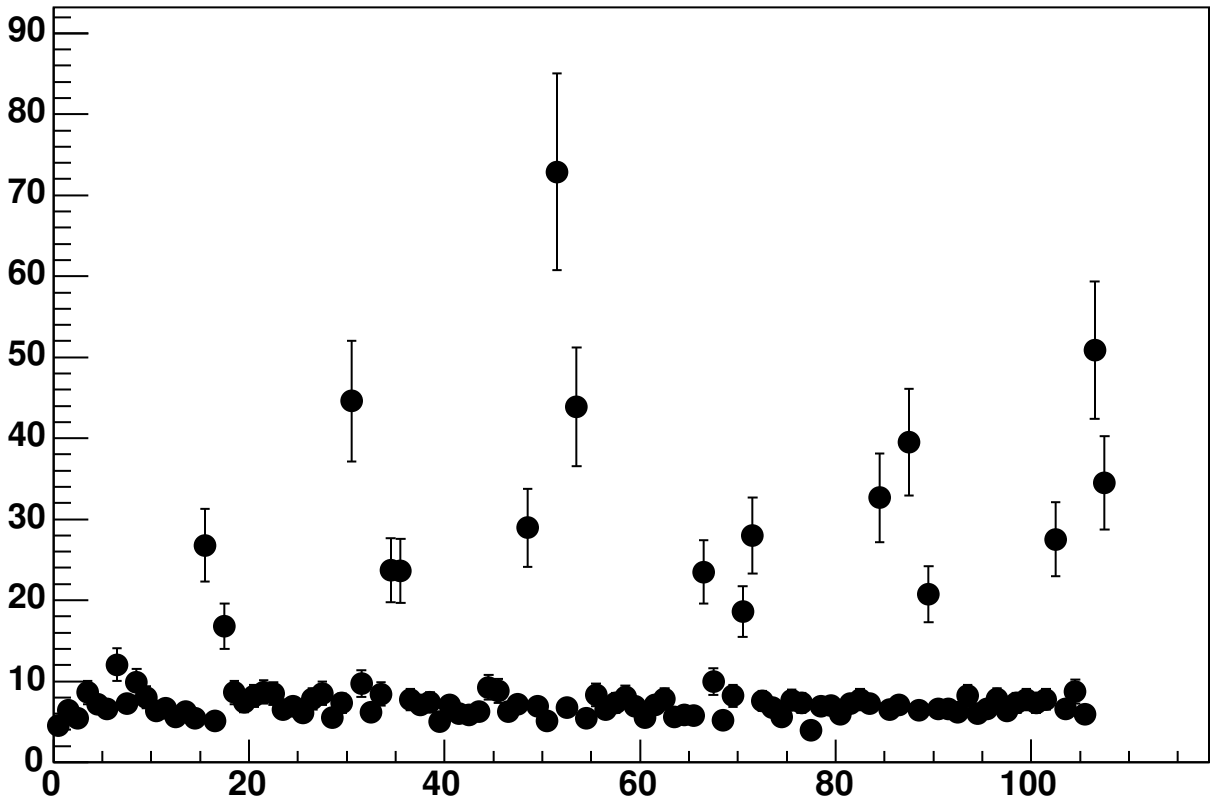
Enable 2, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



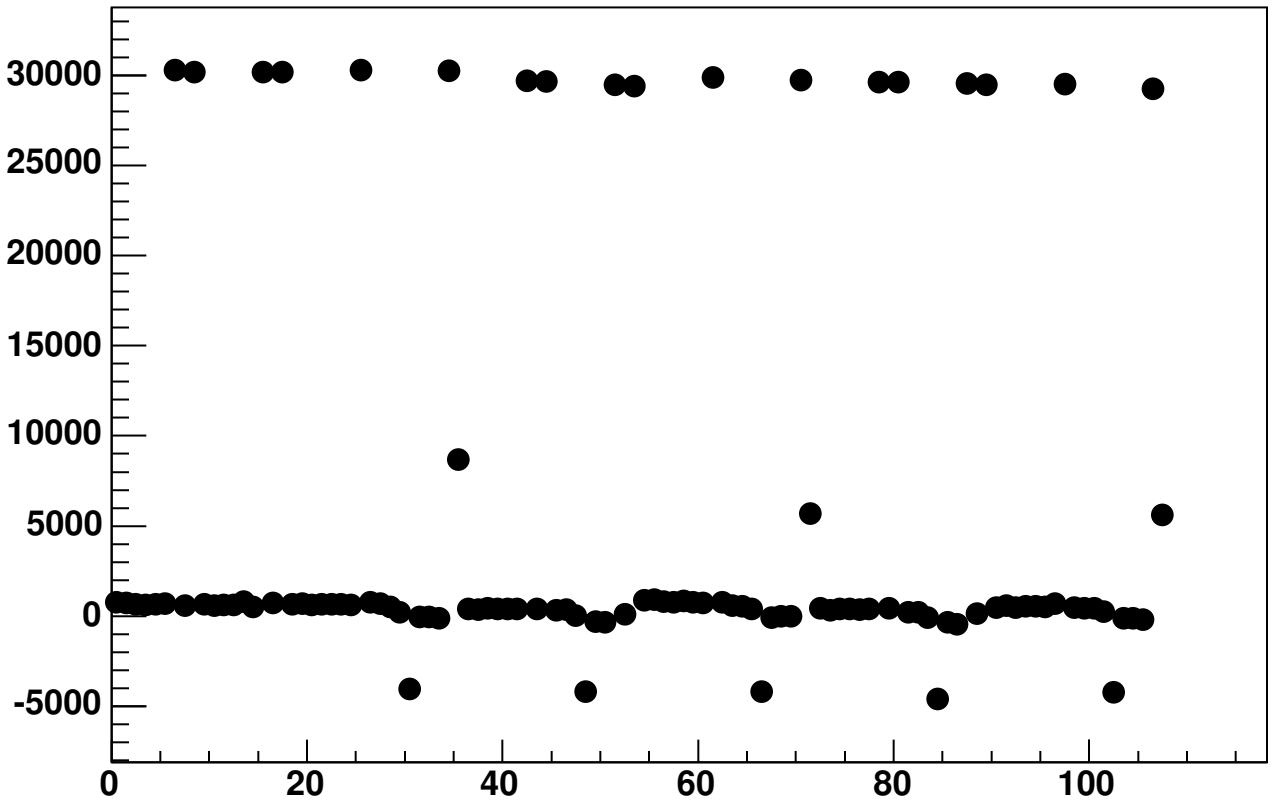
Enable 2, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



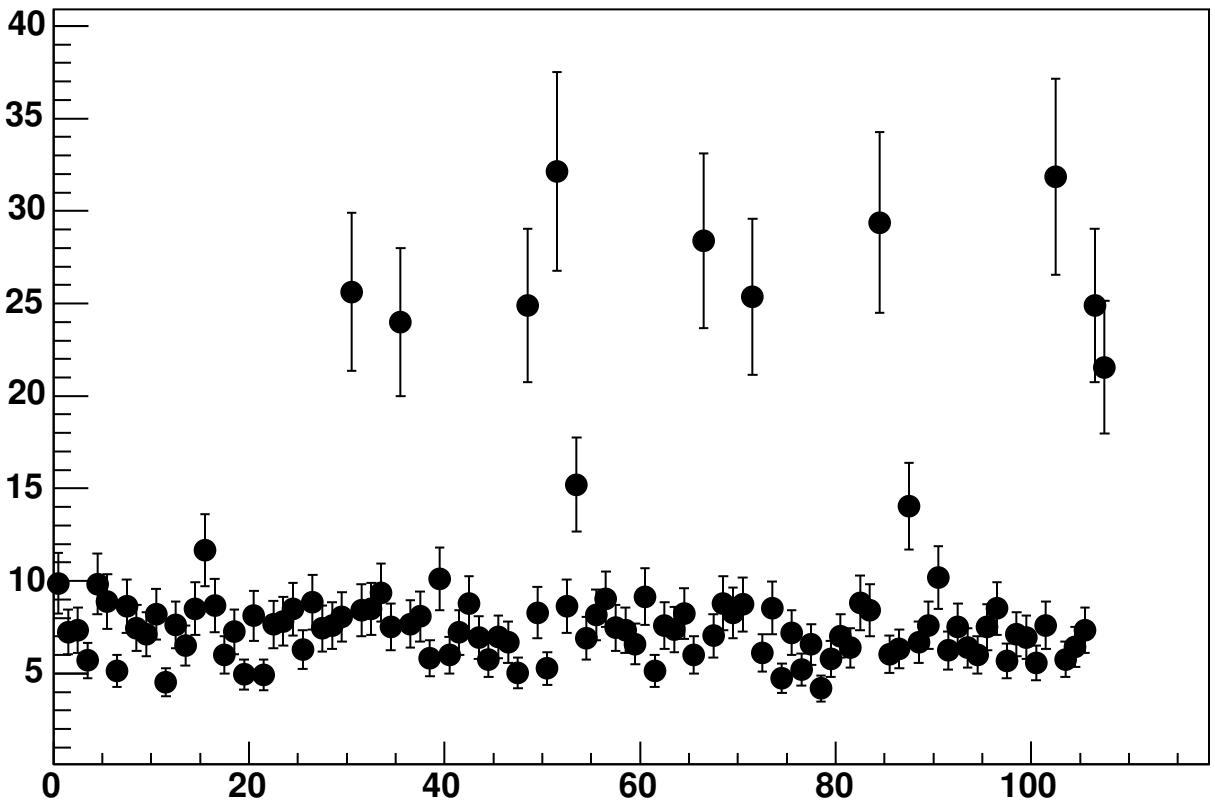
Enable 2, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



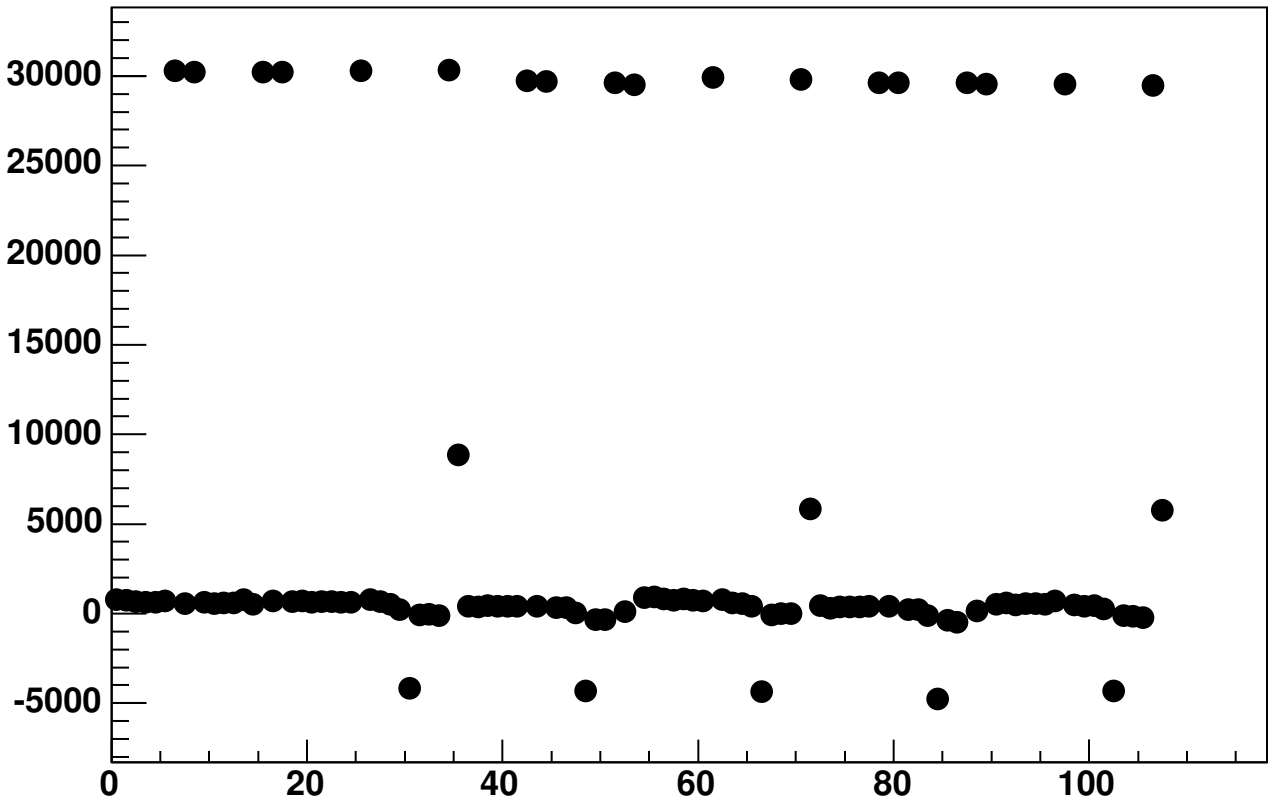
Enable 2, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



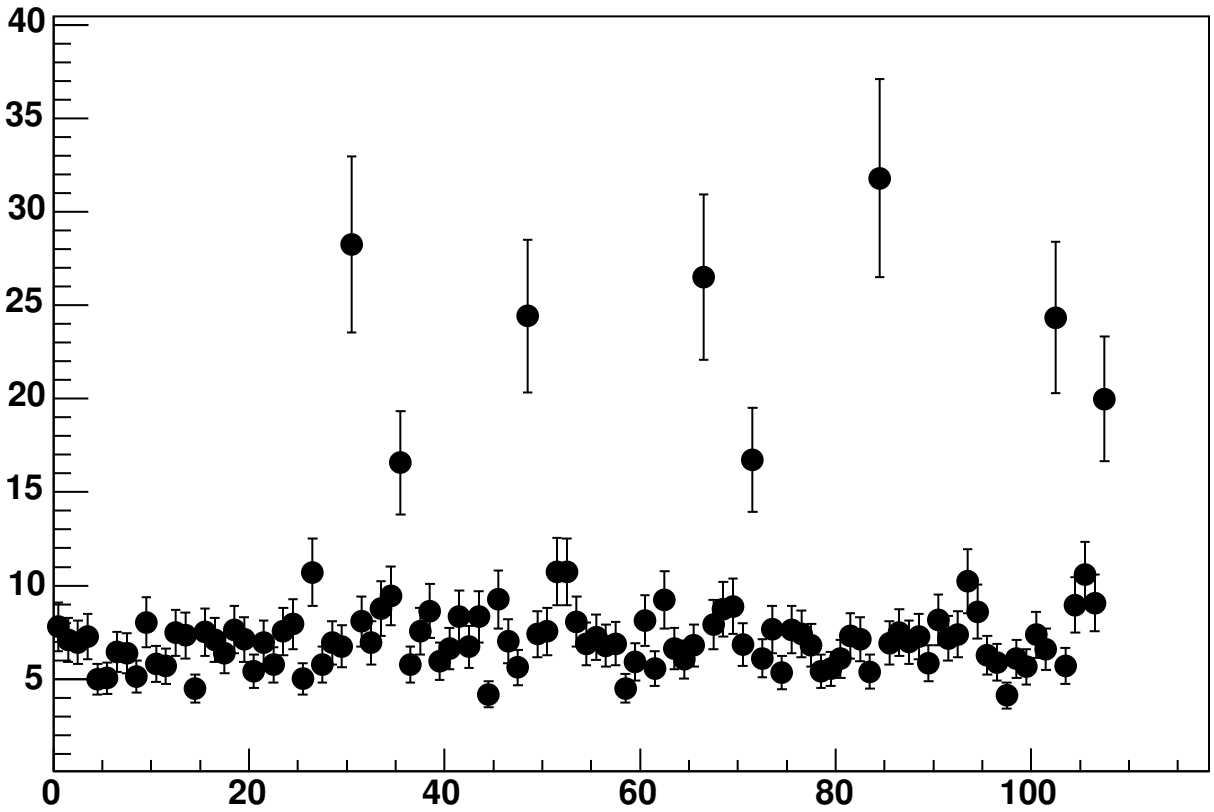
Enable 2, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



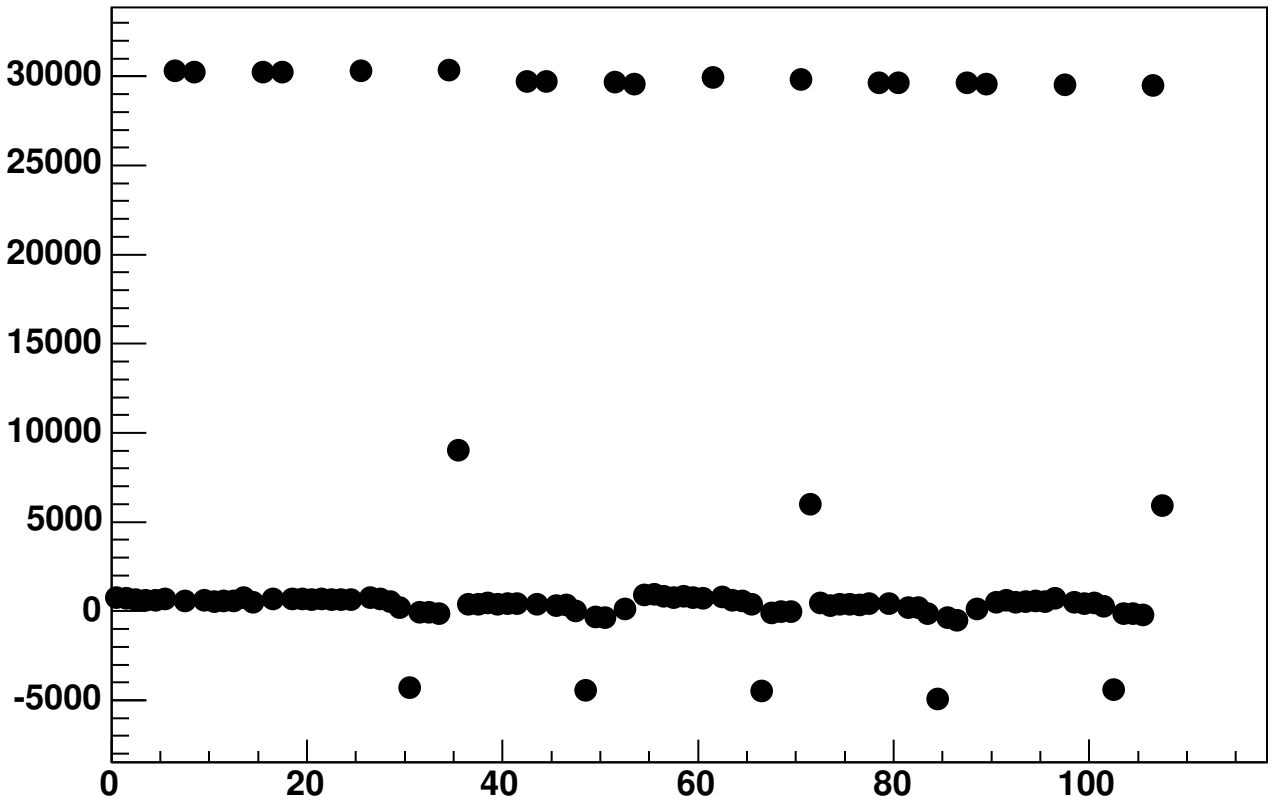
Enable 2, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



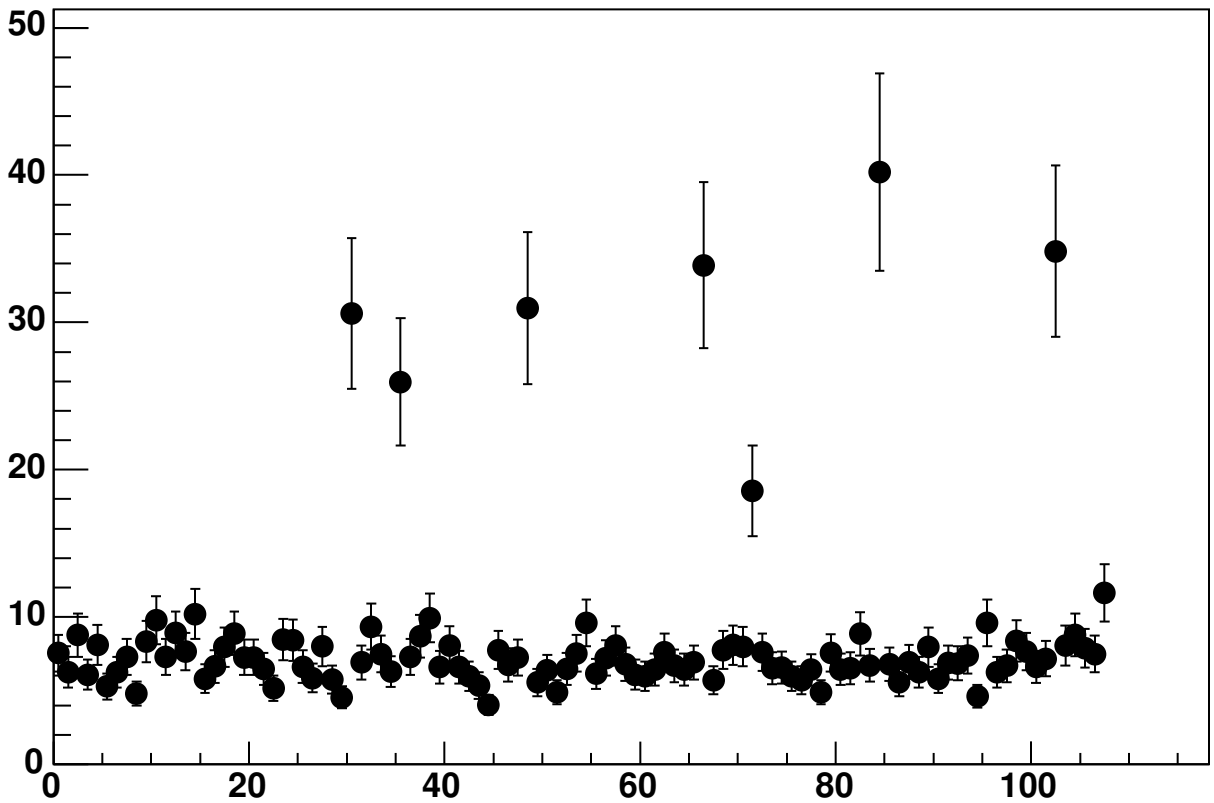
Enable 2, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



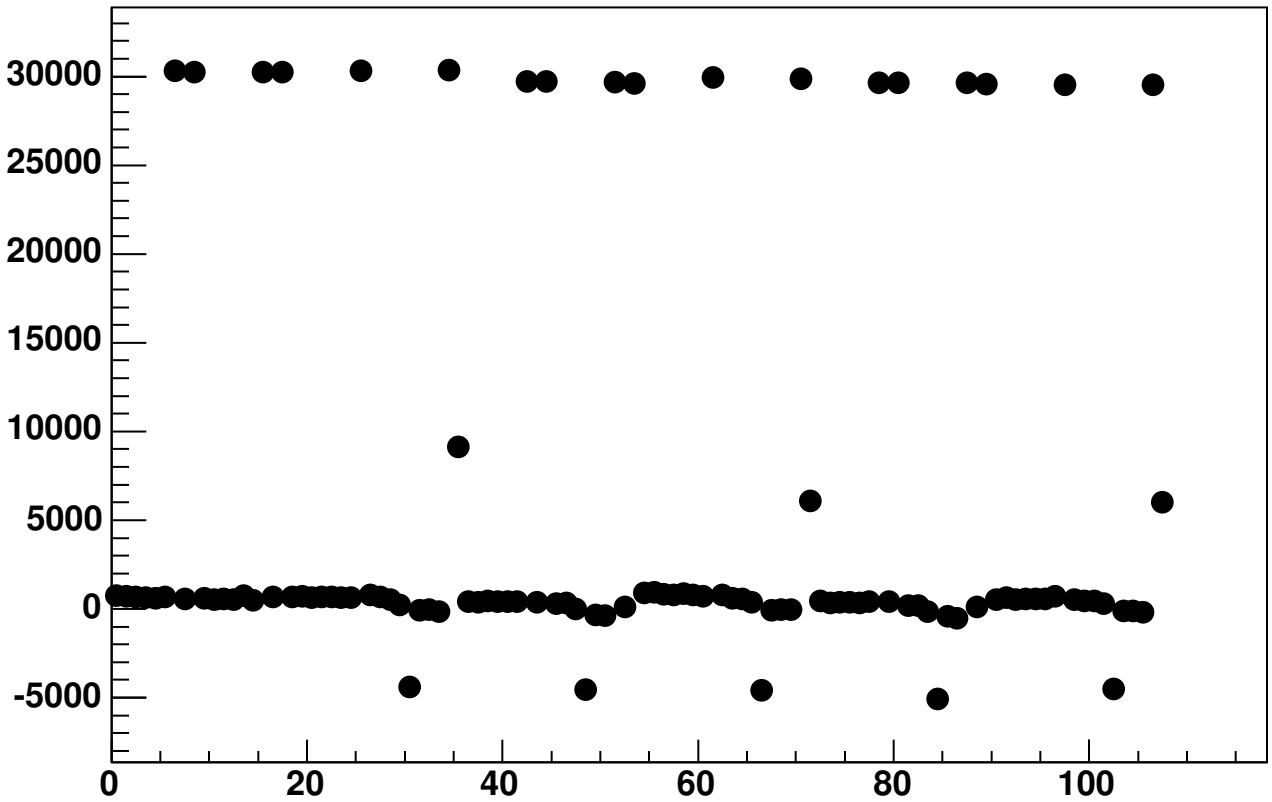
Enable 2, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



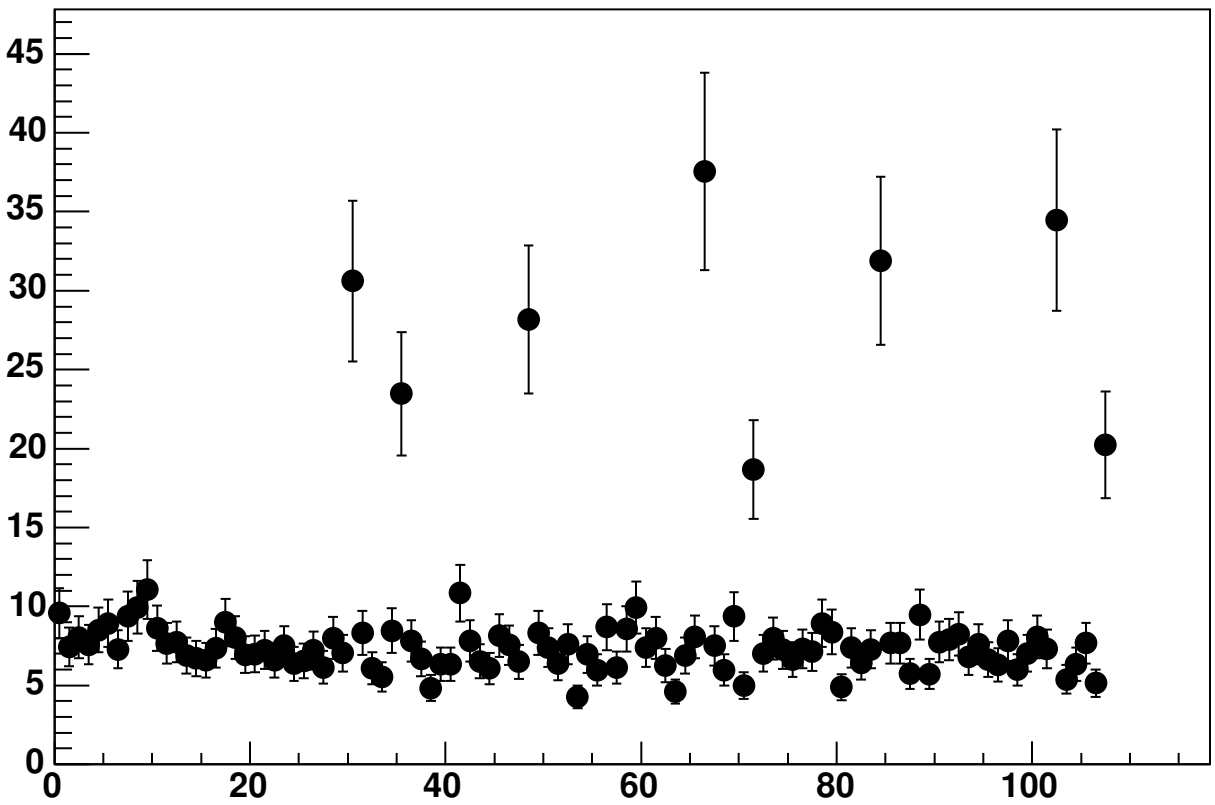
Enable 2, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



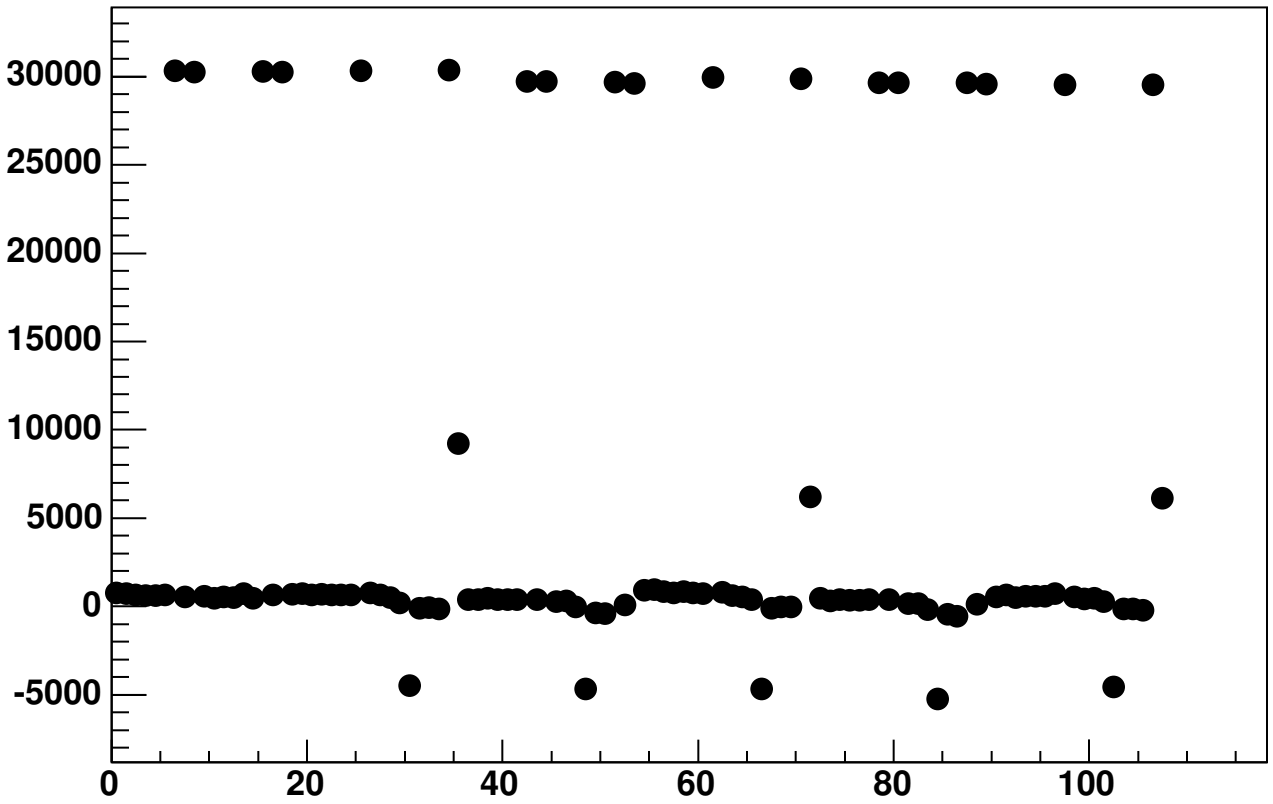
Enable 2, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



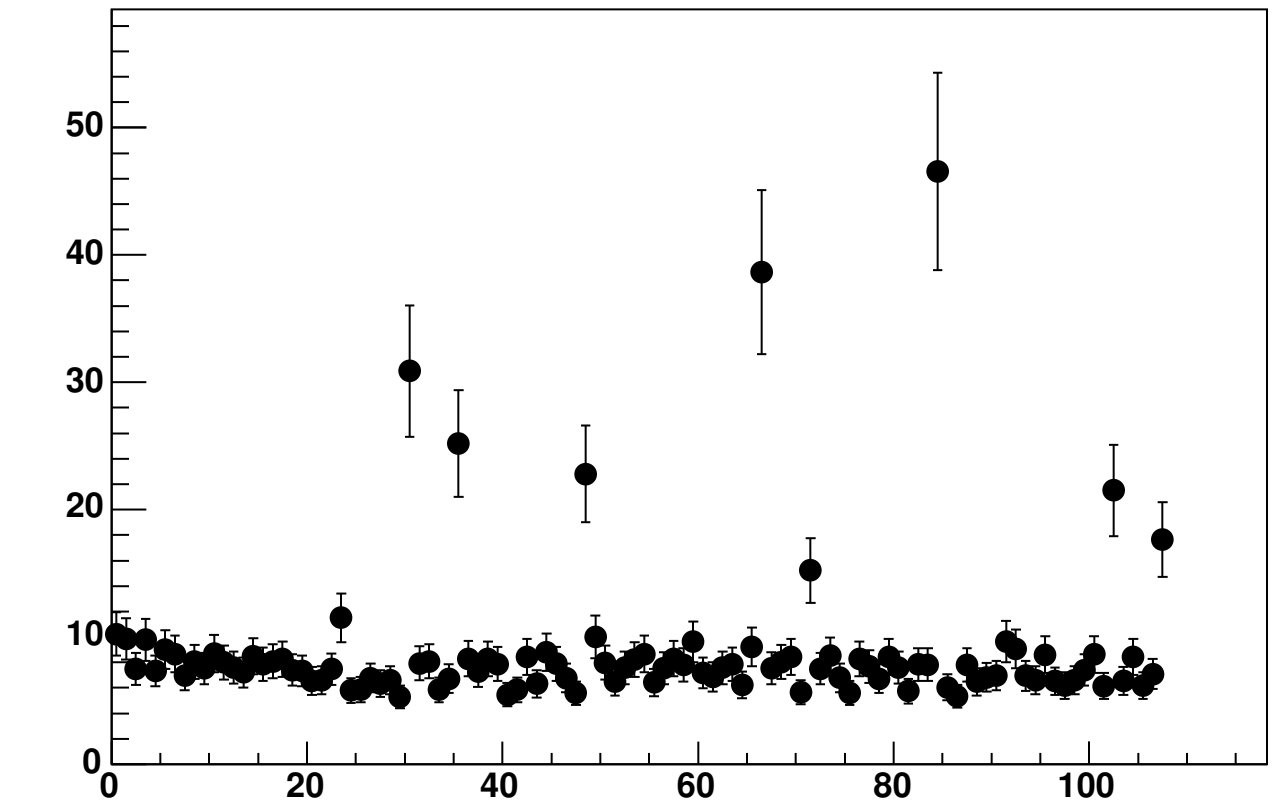
Enable 2, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



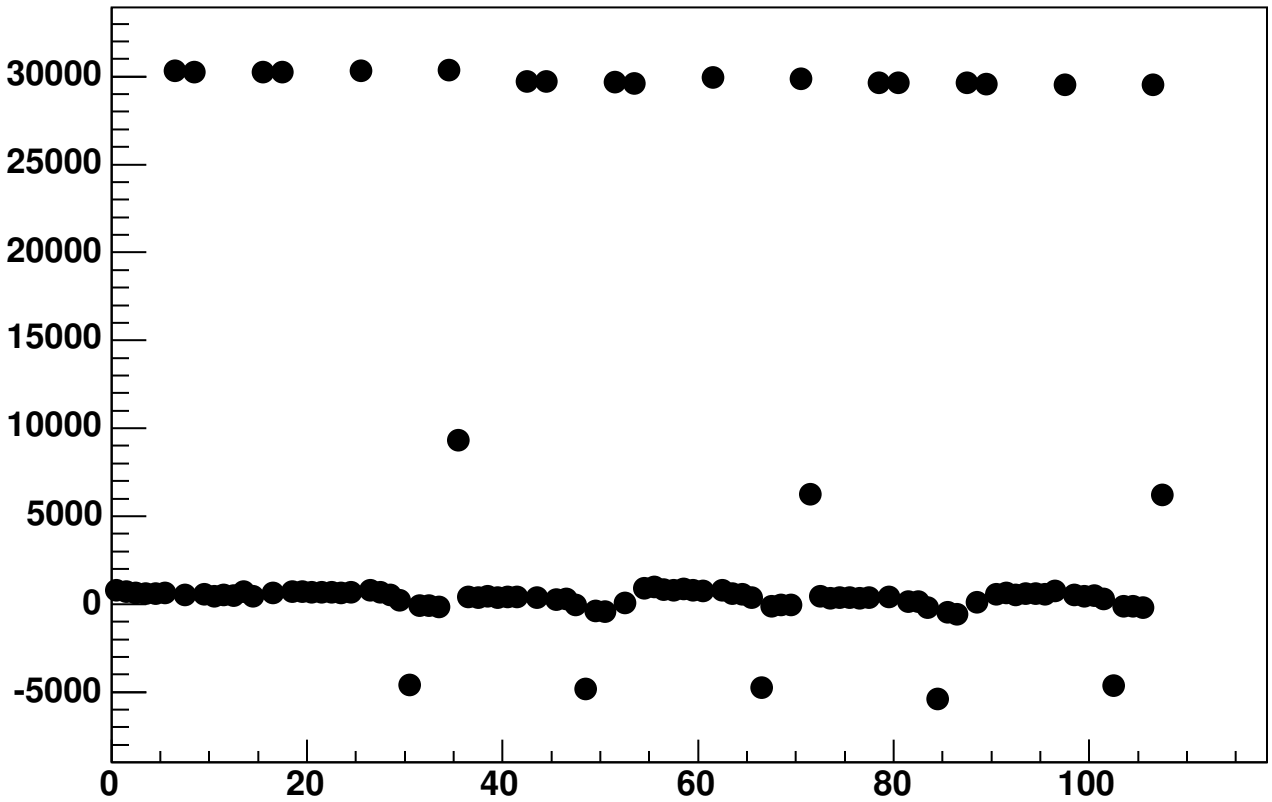
Enable 2, Hold=35, DAC=14400, ADC Mean vs 18*Chip+Chan



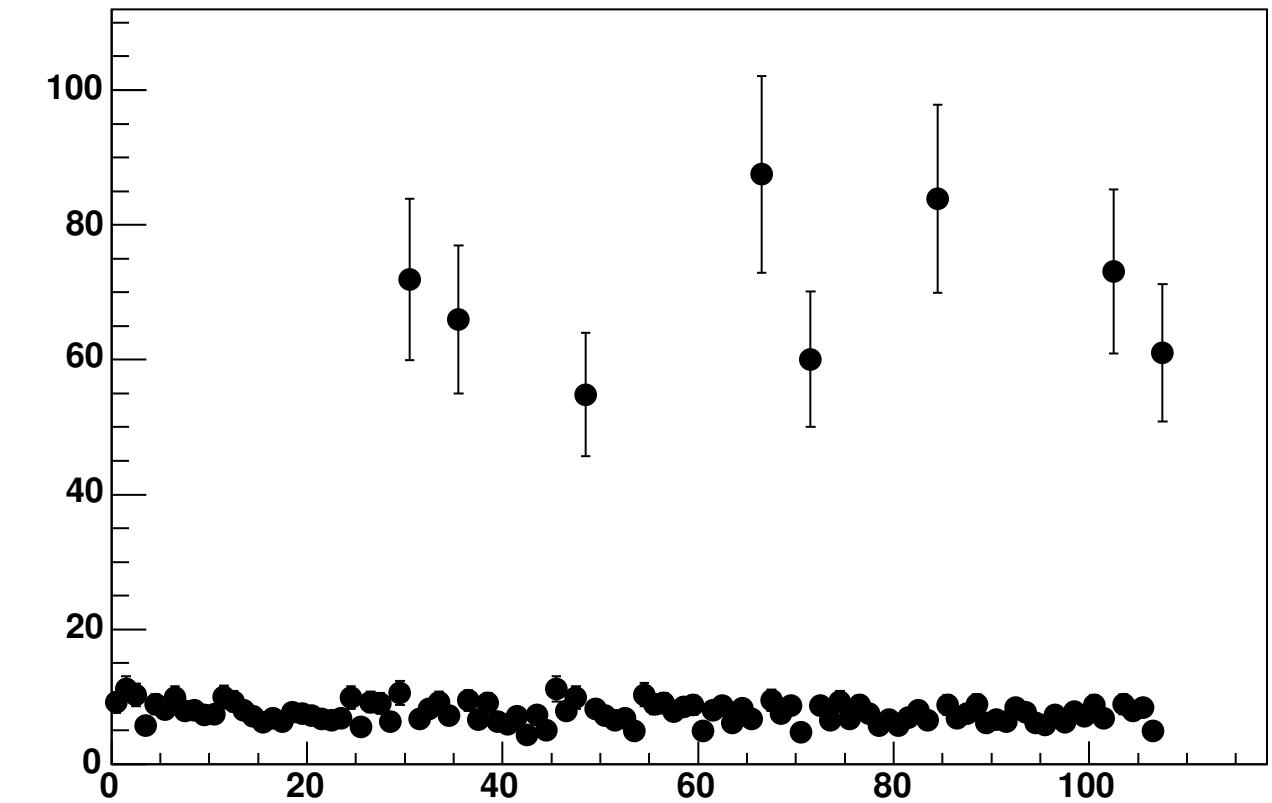
Enable 2, Hold=35, DAC=14400, ADC Noise vs 18*Chip+Chan



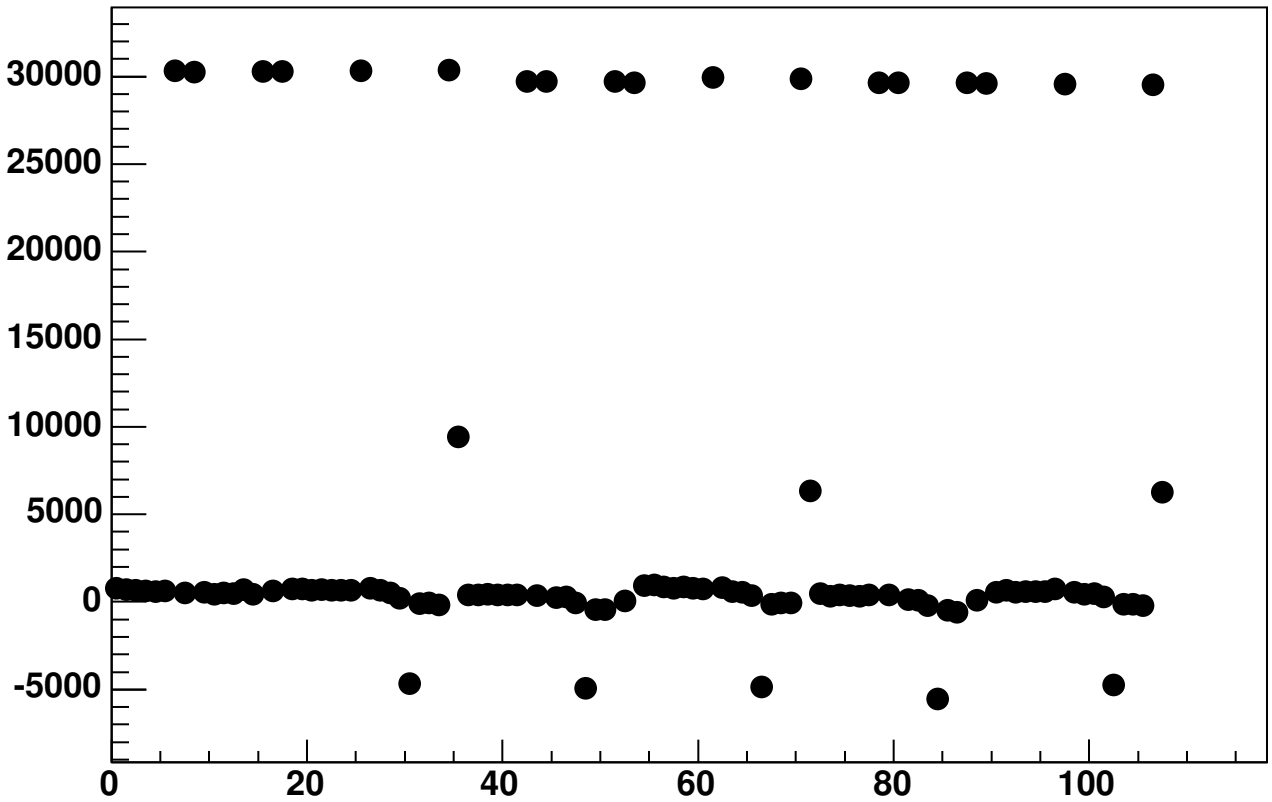
Enable 2, Hold=35, DAC=14800, ADC Mean vs 18*Chip+Chan



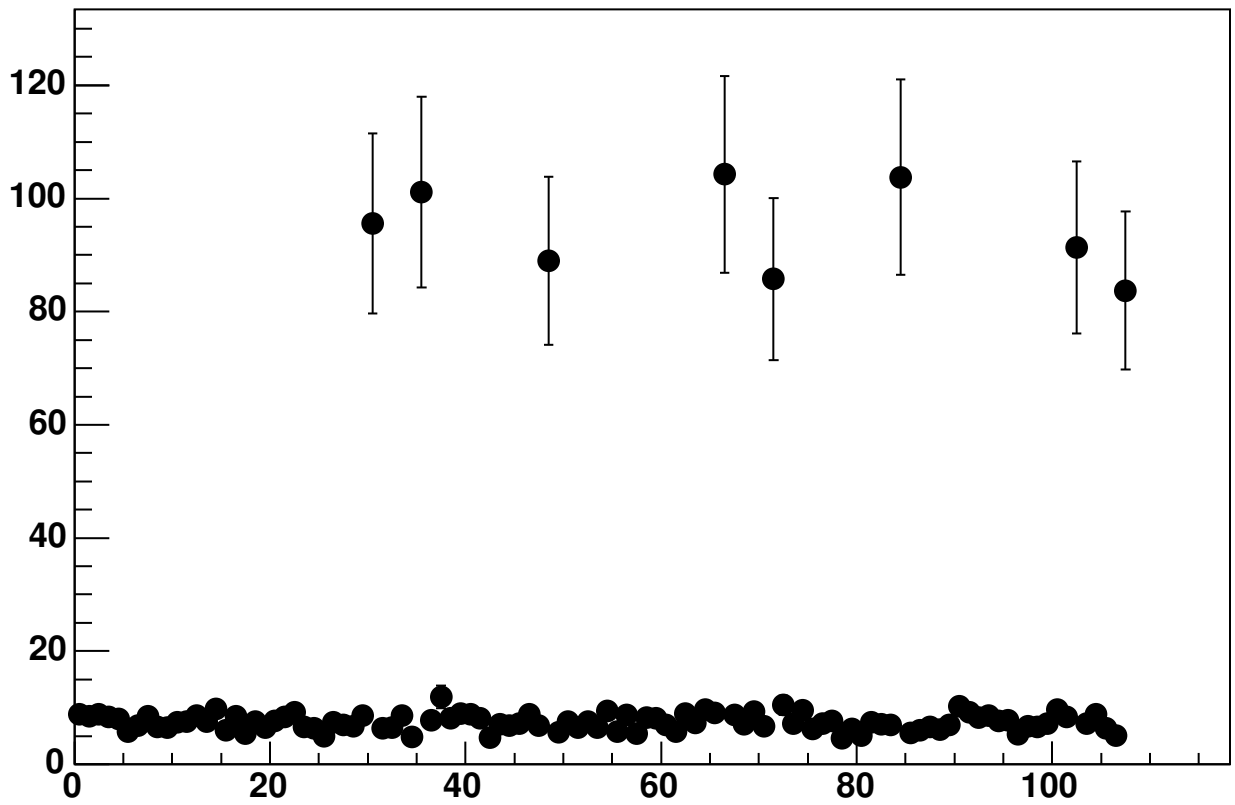
Enable 2, Hold=35, DAC=14800, ADC Noise vs 18*Chip+Chan



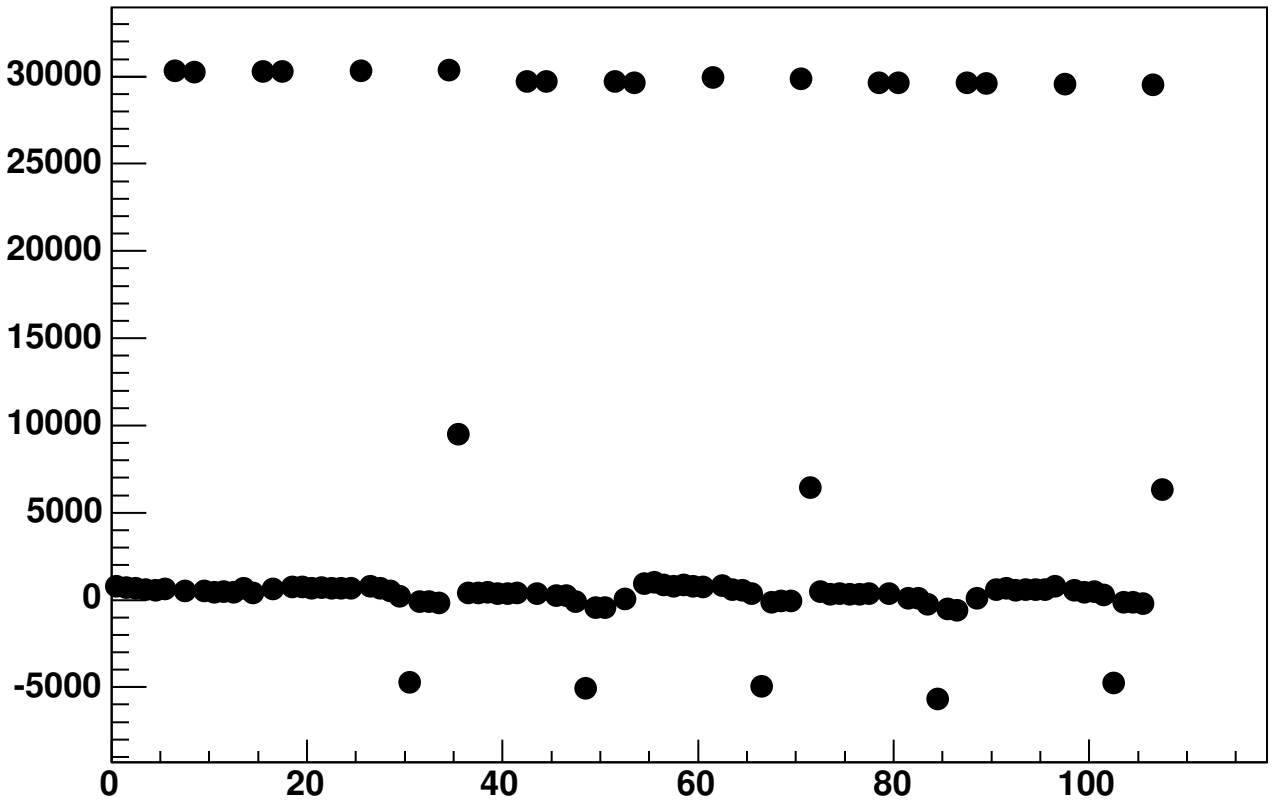
Enable 2, Hold=35, DAC=15200, ADC Mean vs 18*Chip+Chan



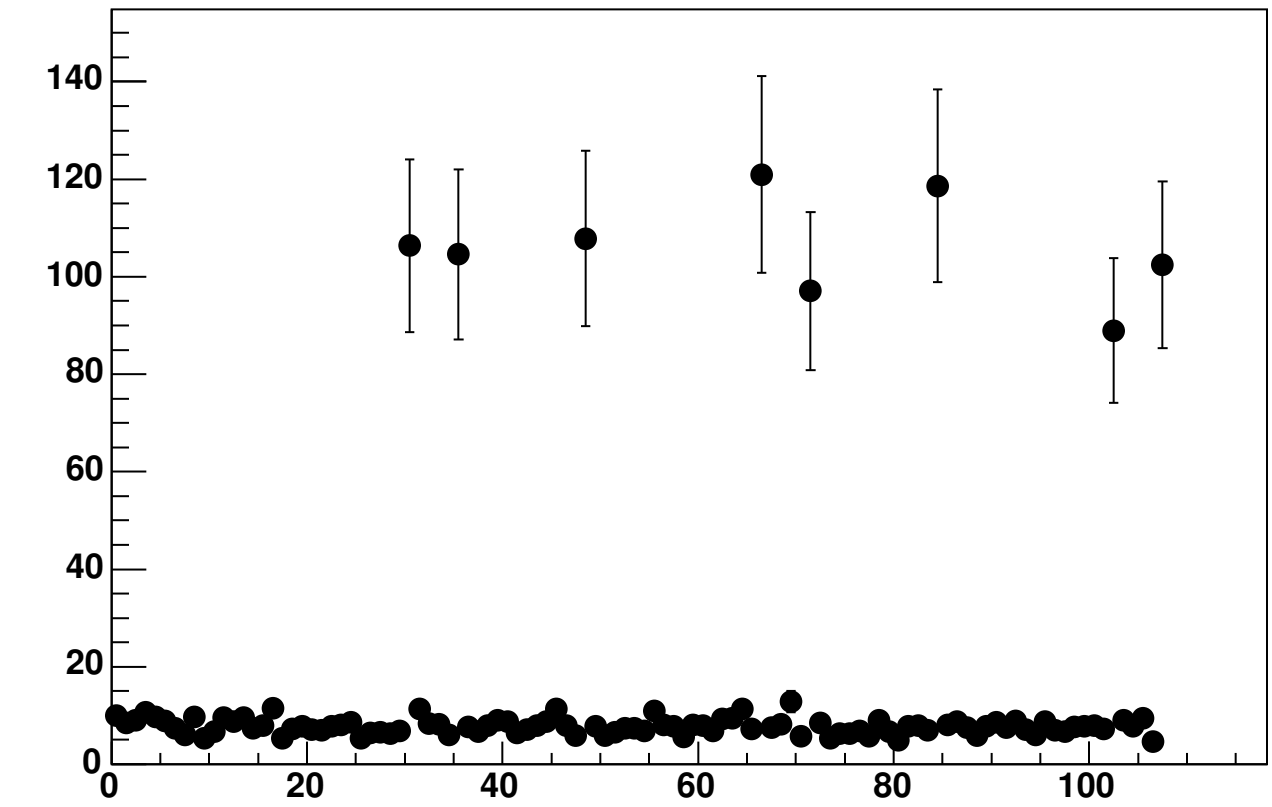
Enable 2, Hold=35, DAC=15200, ADC Noise vs 18*Chip+Chan



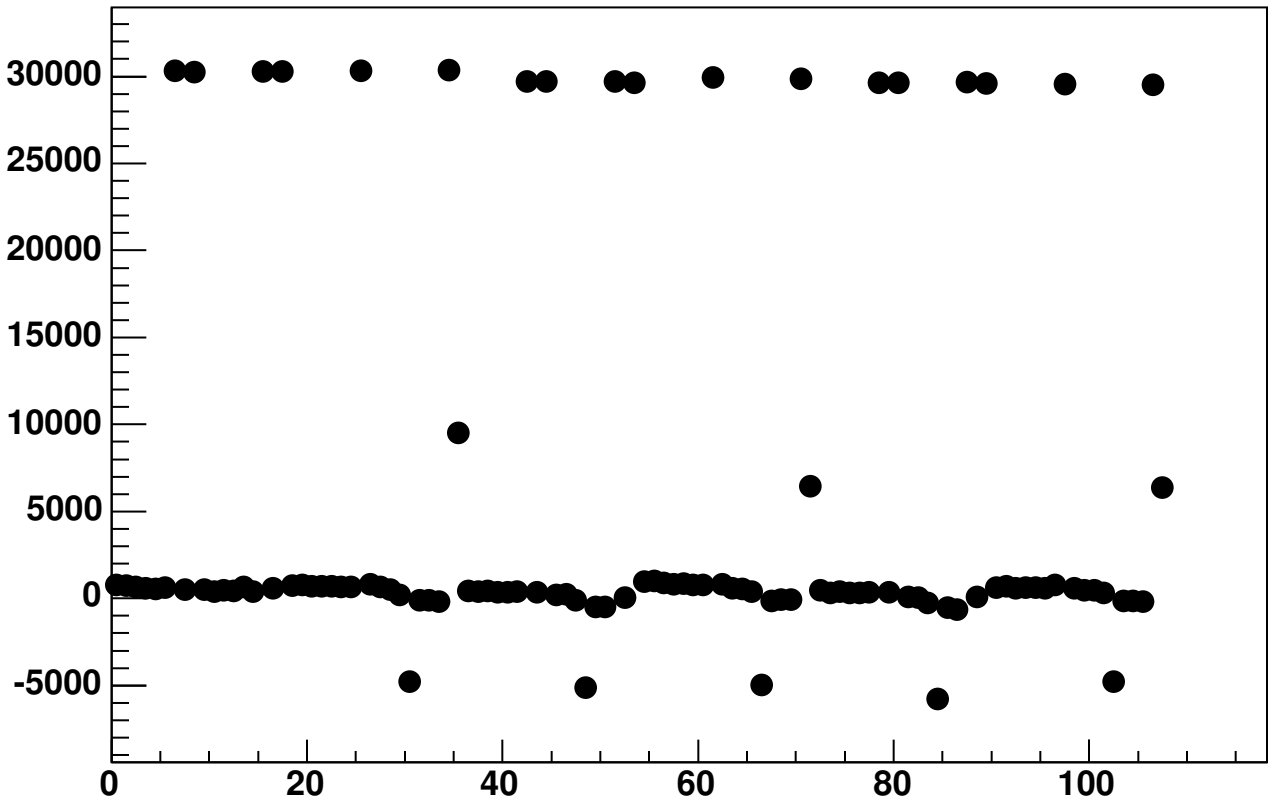
Enable 2, Hold=35, DAC=15600, ADC Mean vs 18*Chip+Chan



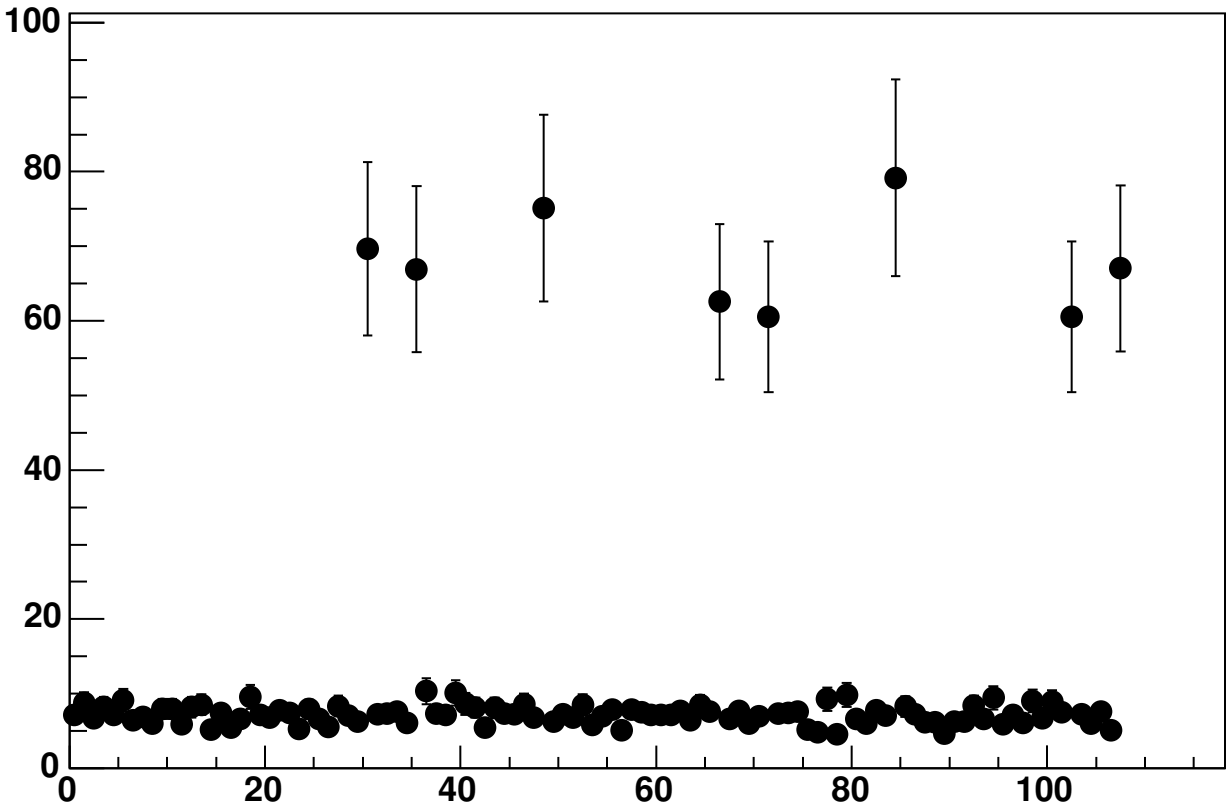
Enable 2, Hold=35, DAC=15600, ADC Noise vs 18*Chip+Chan



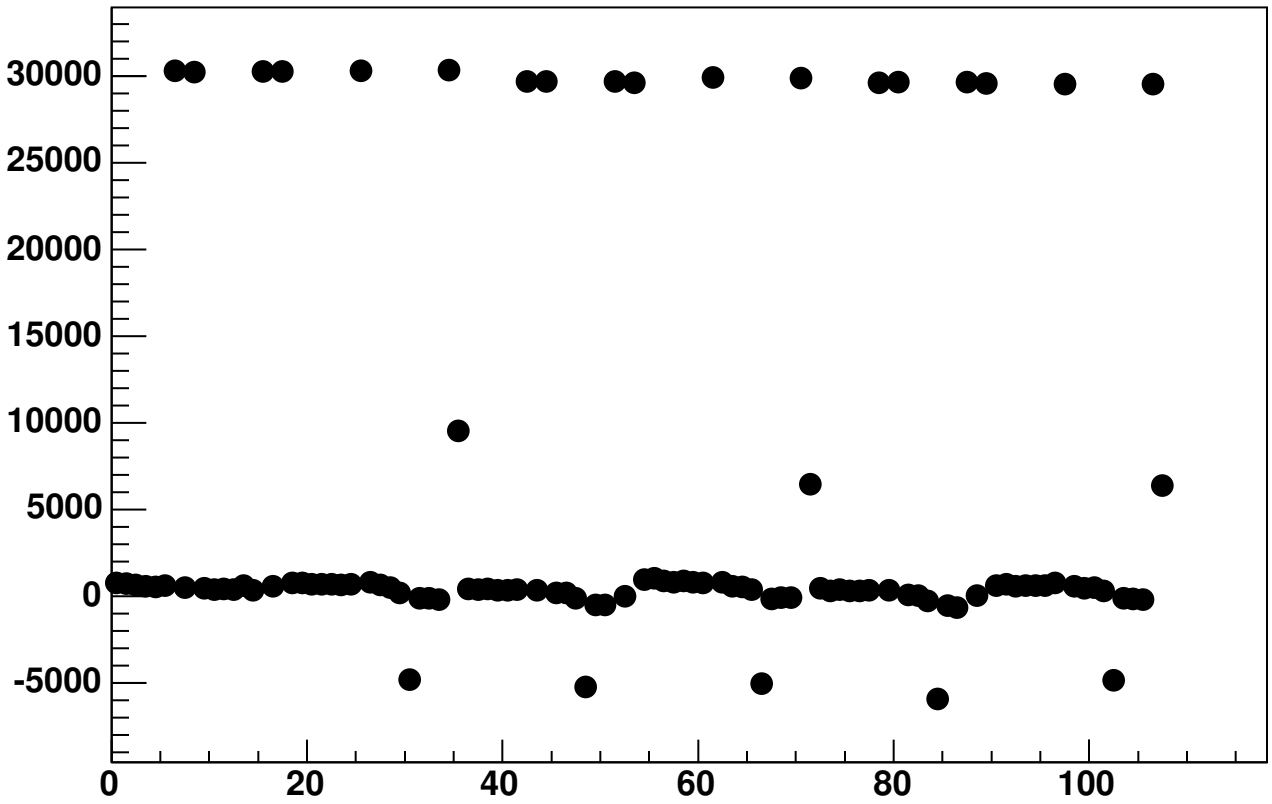
Enable 2, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



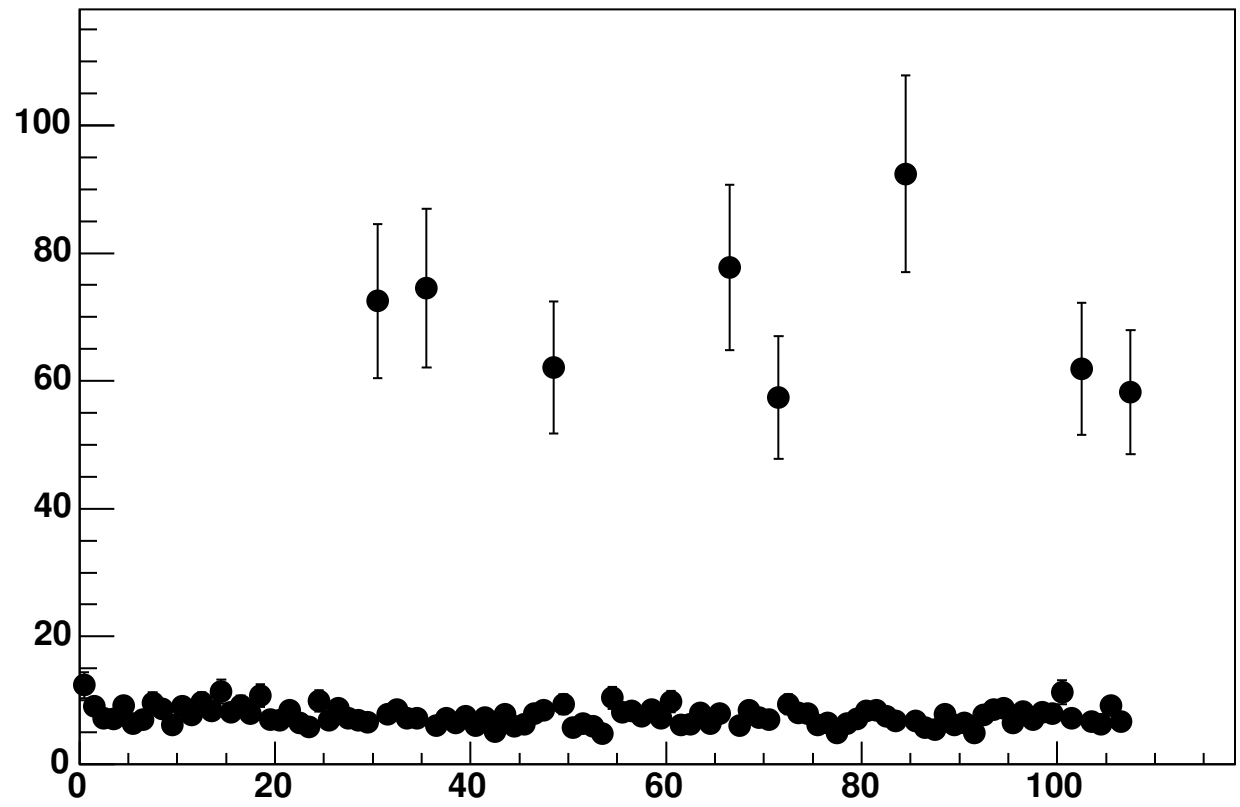
Enable 2, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



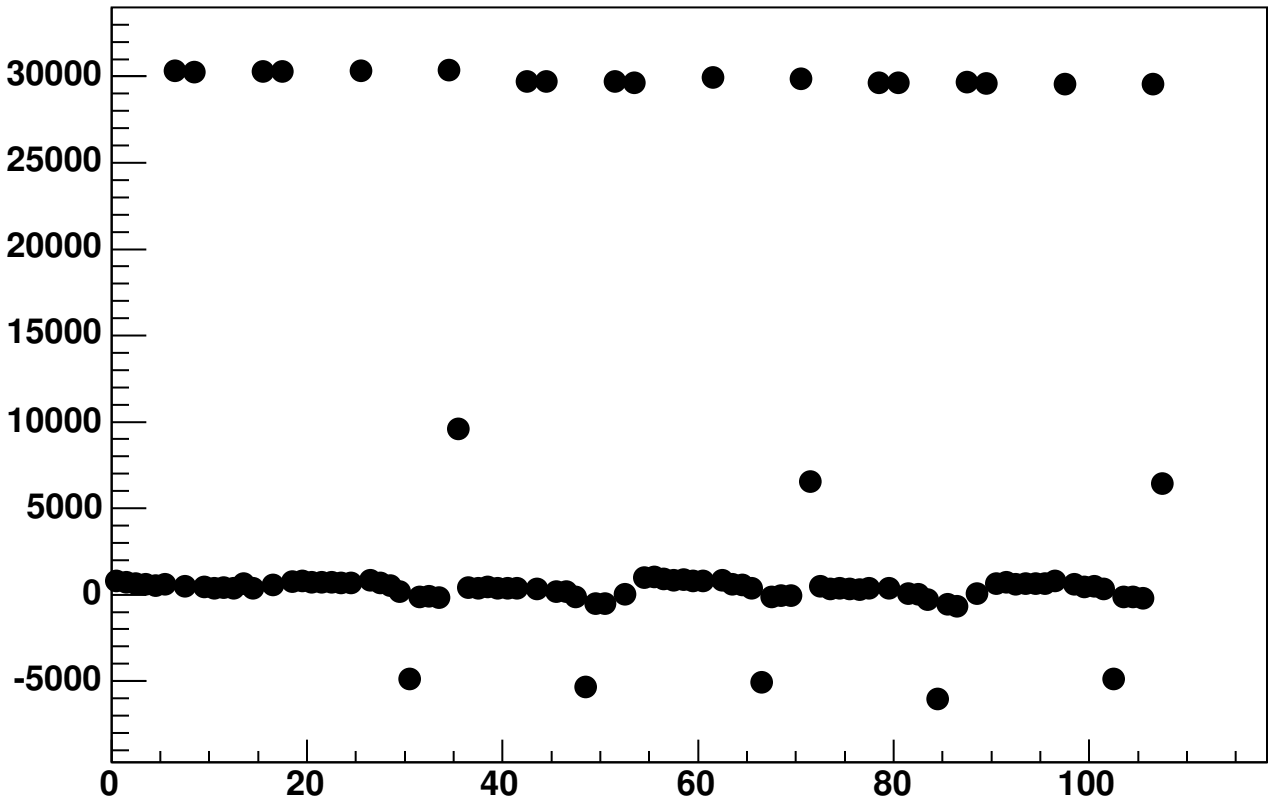
Enable 2, Hold=35, DAC=16400, ADC Mean vs 18*Chip+Chan



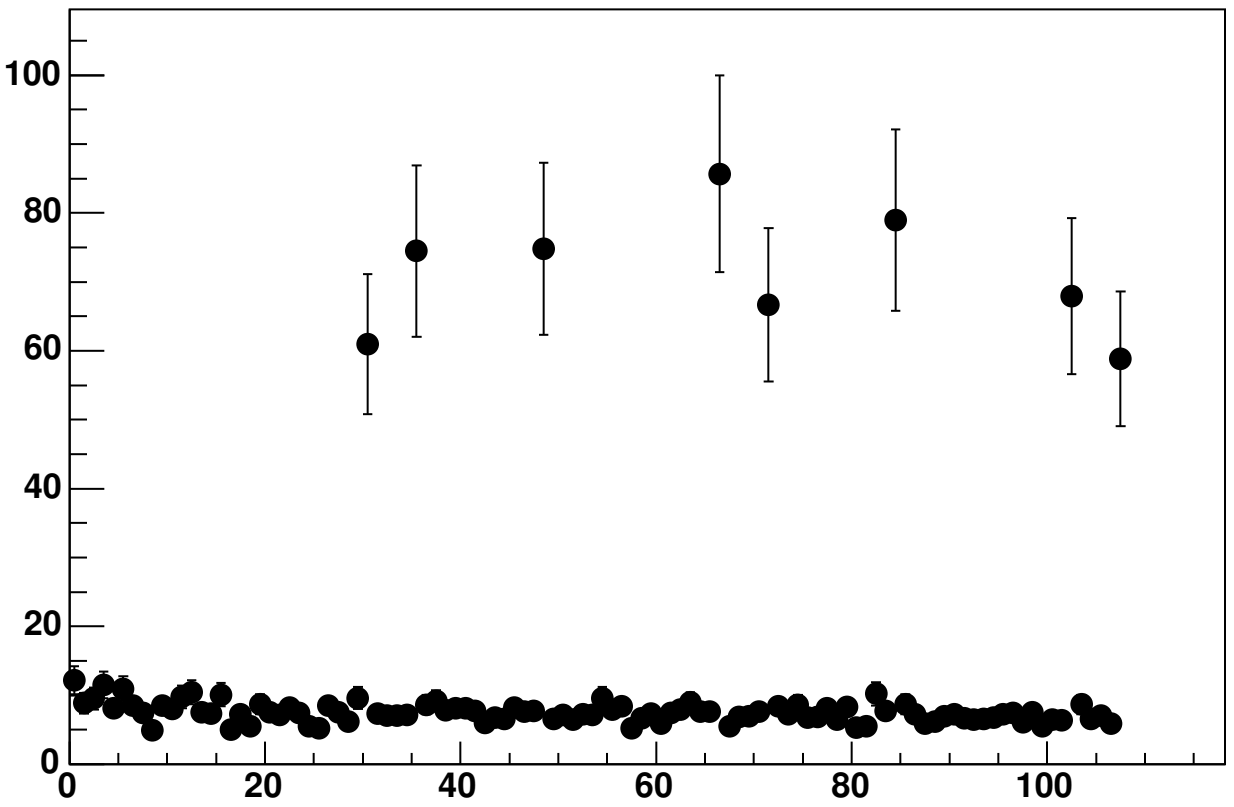
Enable 2, Hold=35, DAC=16400, ADC Noise vs 18*Chip+Chan



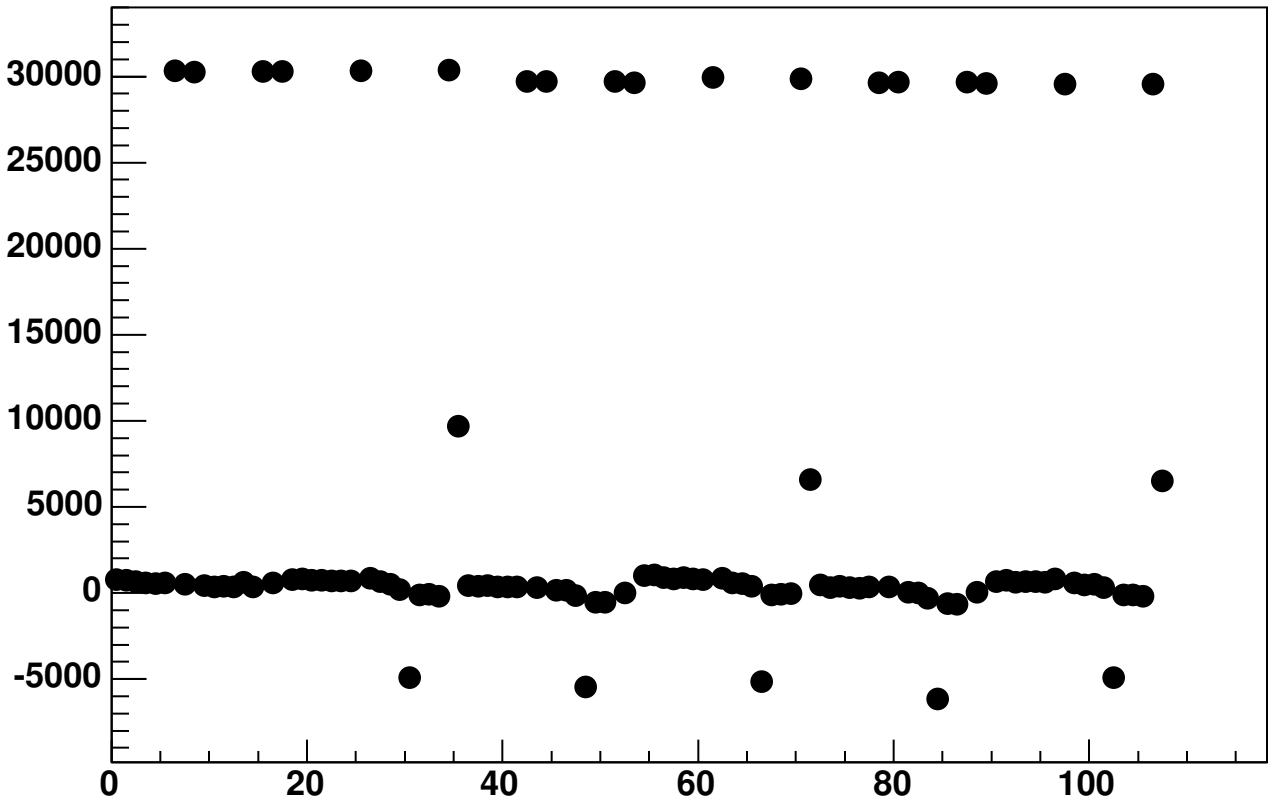
Enable 2, Hold=35, DAC=16800, ADC Mean vs 18*Chip+Chan



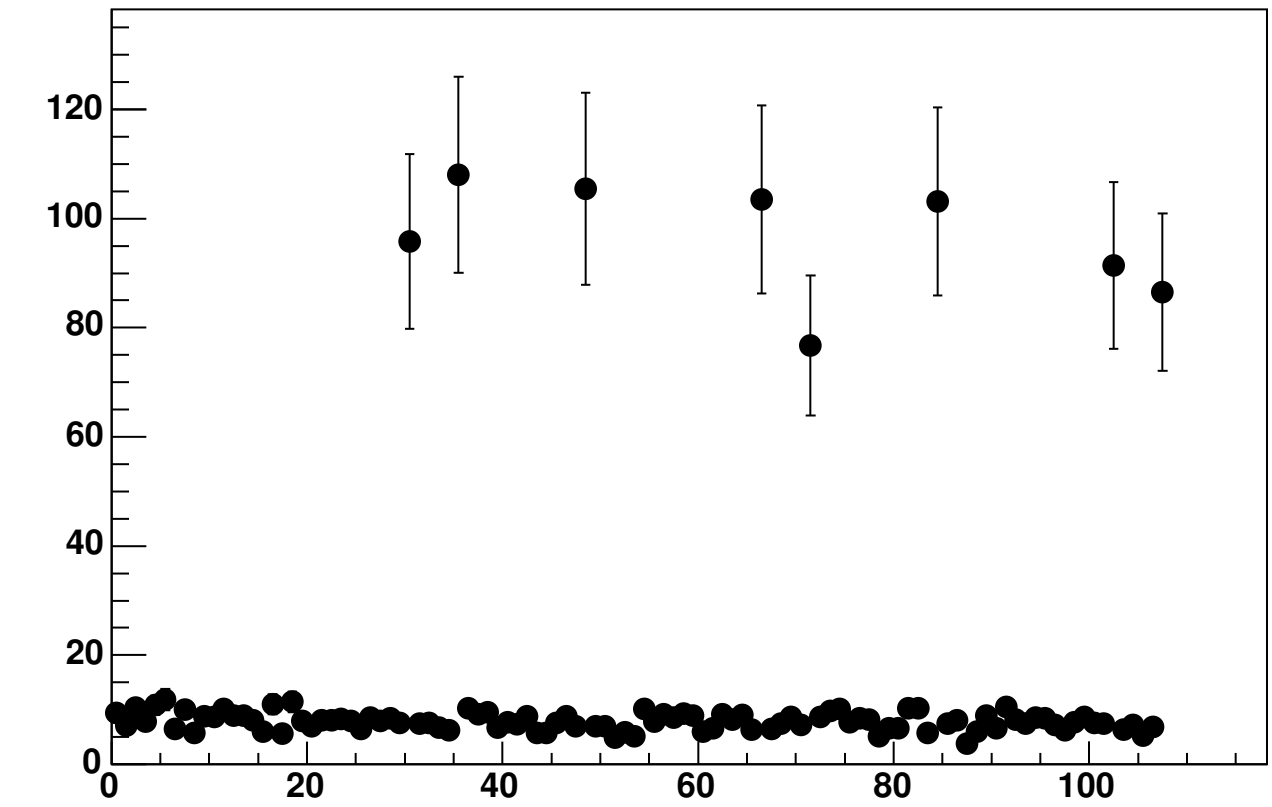
Enable 2, Hold=35, DAC=16800, ADC Noise vs 18*Chip+Chan



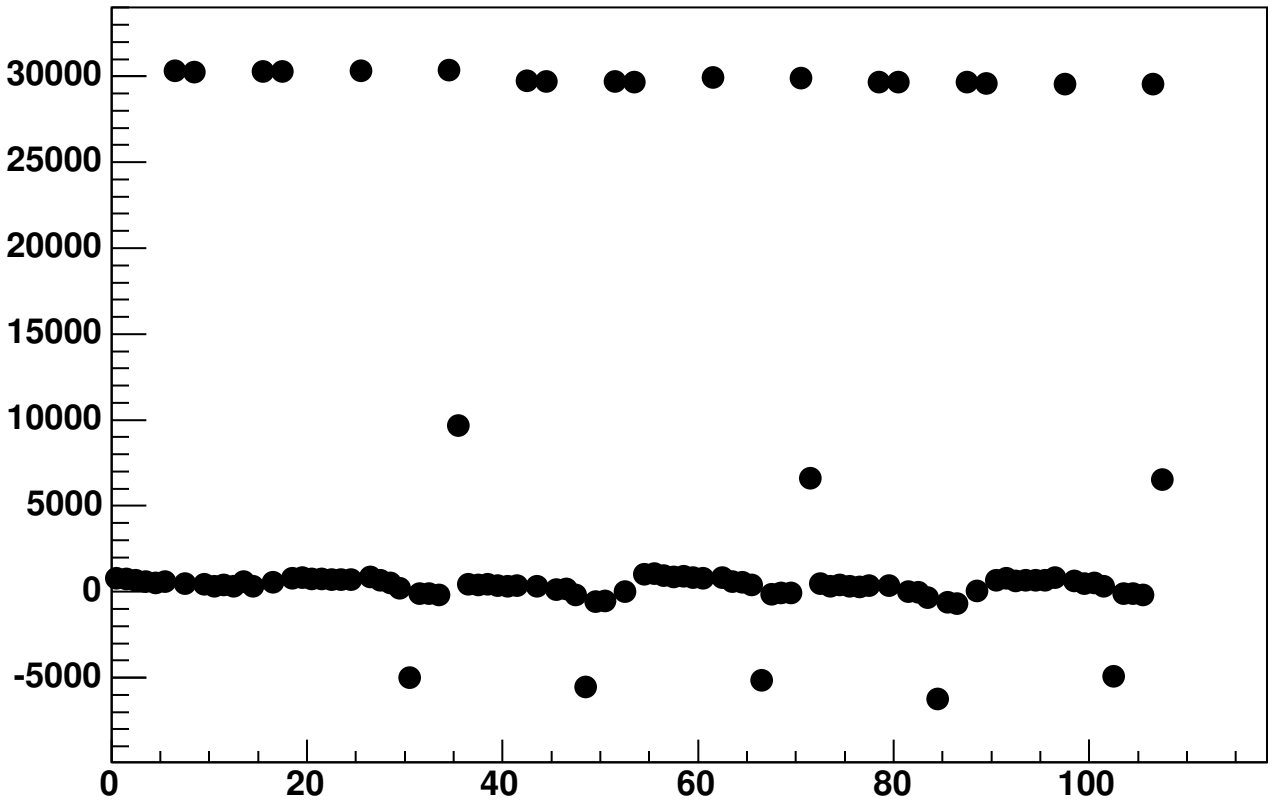
Enable 2, Hold=35, DAC=17200, ADC Mean vs 18*Chip+Chan



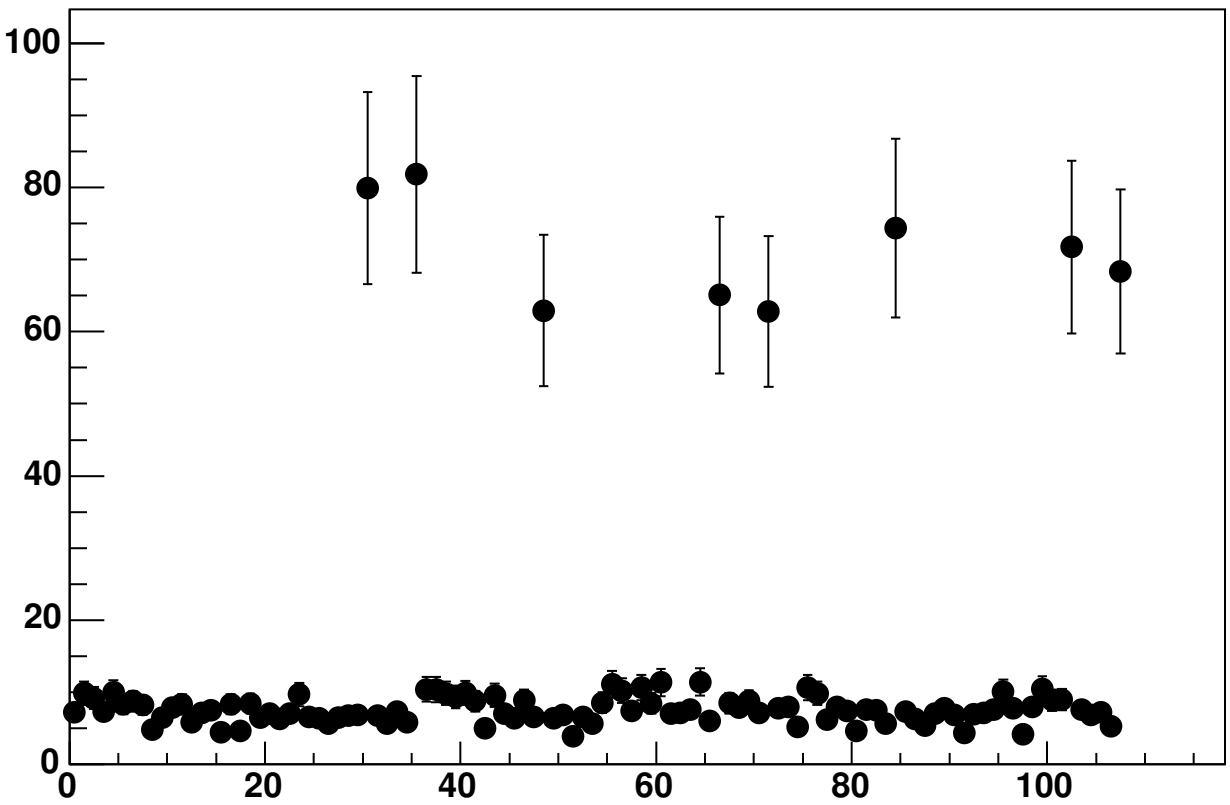
Enable 2, Hold=35, DAC=17200, ADC Noise vs 18*Chip+Chan



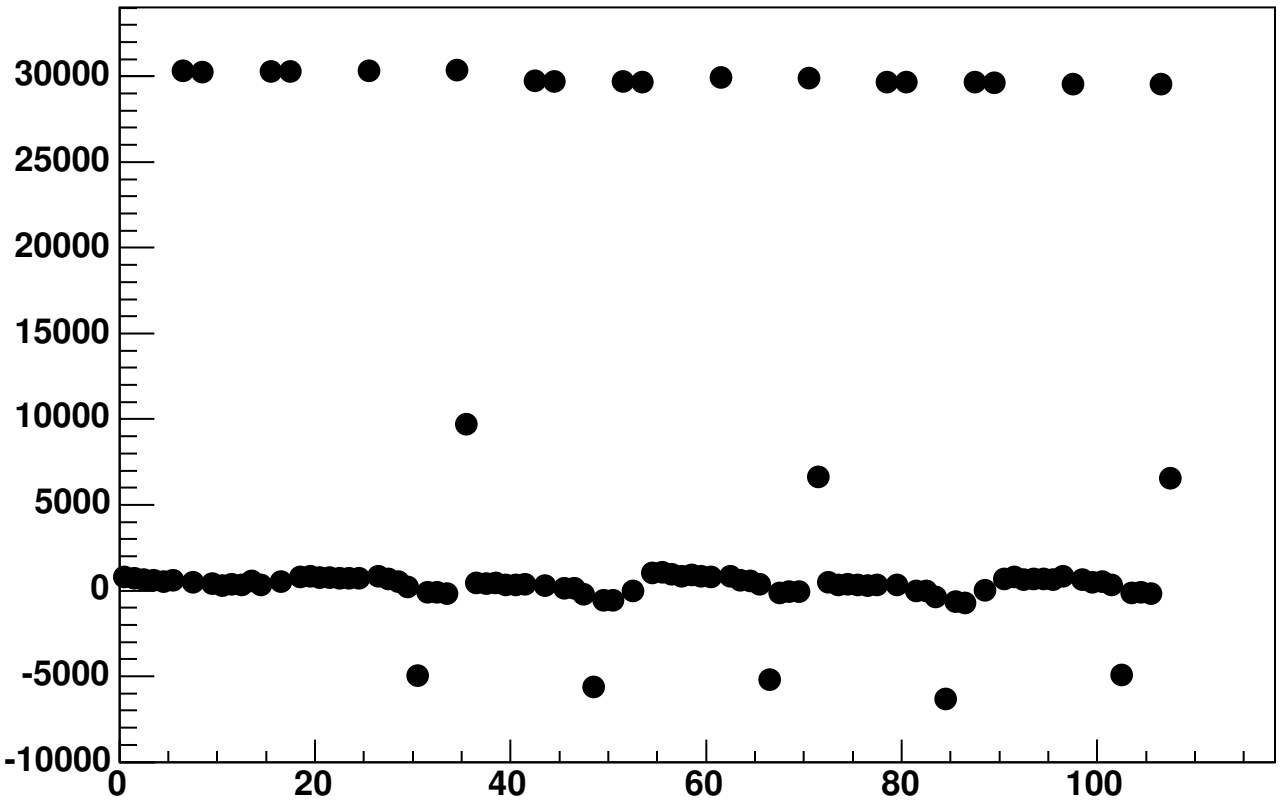
Enable 2, Hold=35, DAC=17600, ADC Mean vs 18*Chip+Chan



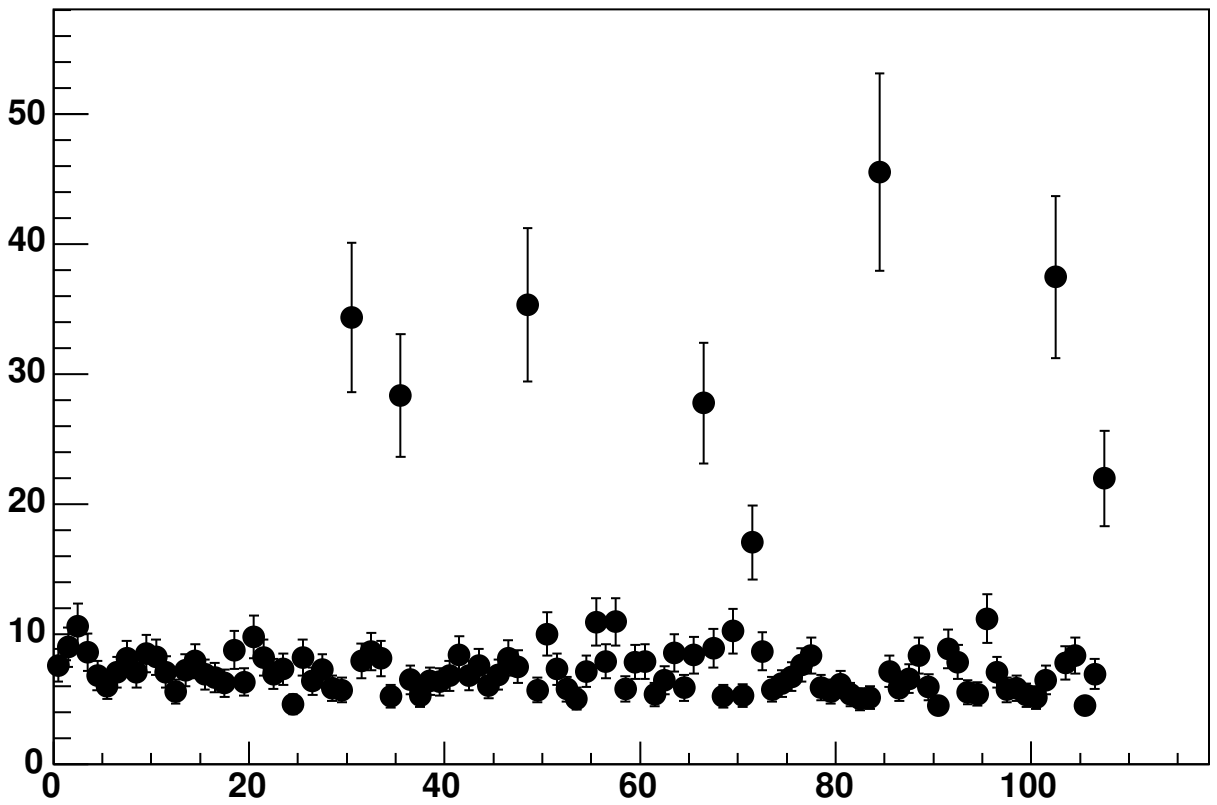
Enable 2, Hold=35, DAC=17600, ADC Noise vs 18*Chip+Chan



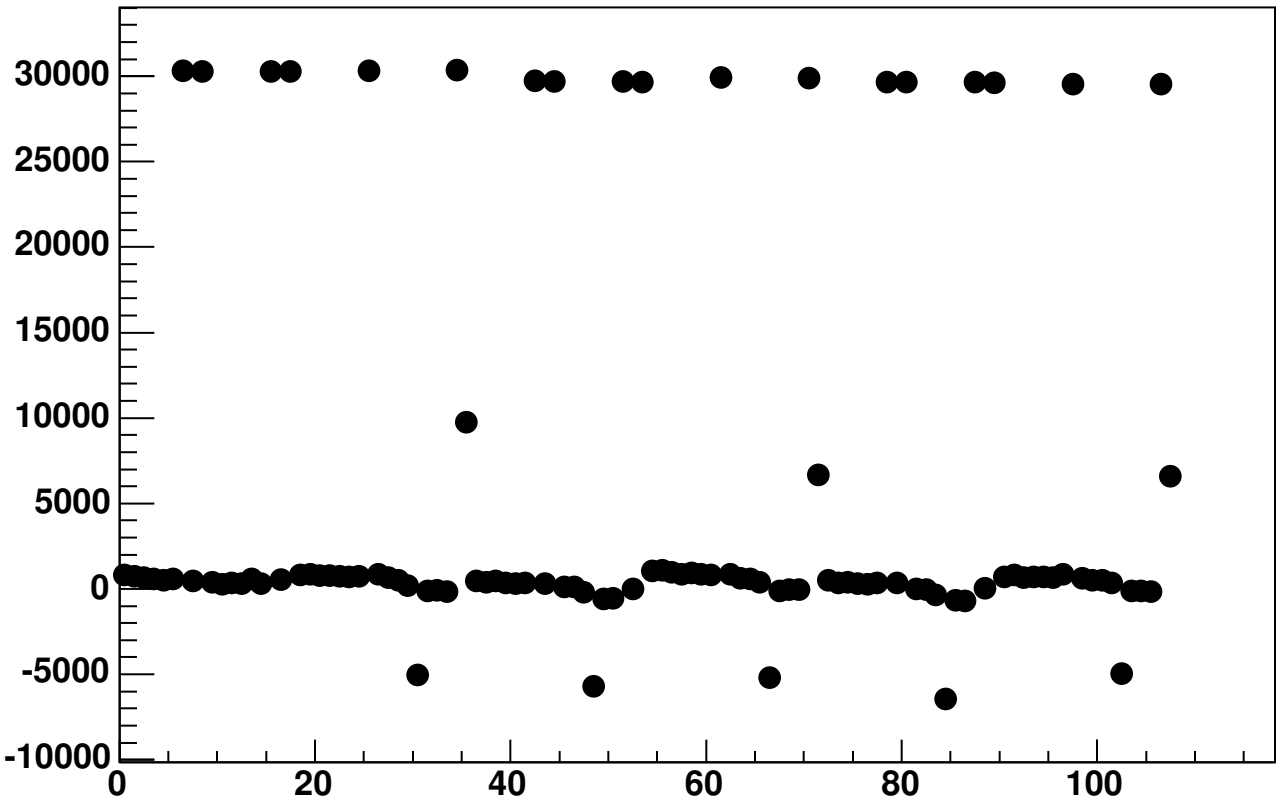
Enable 2, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



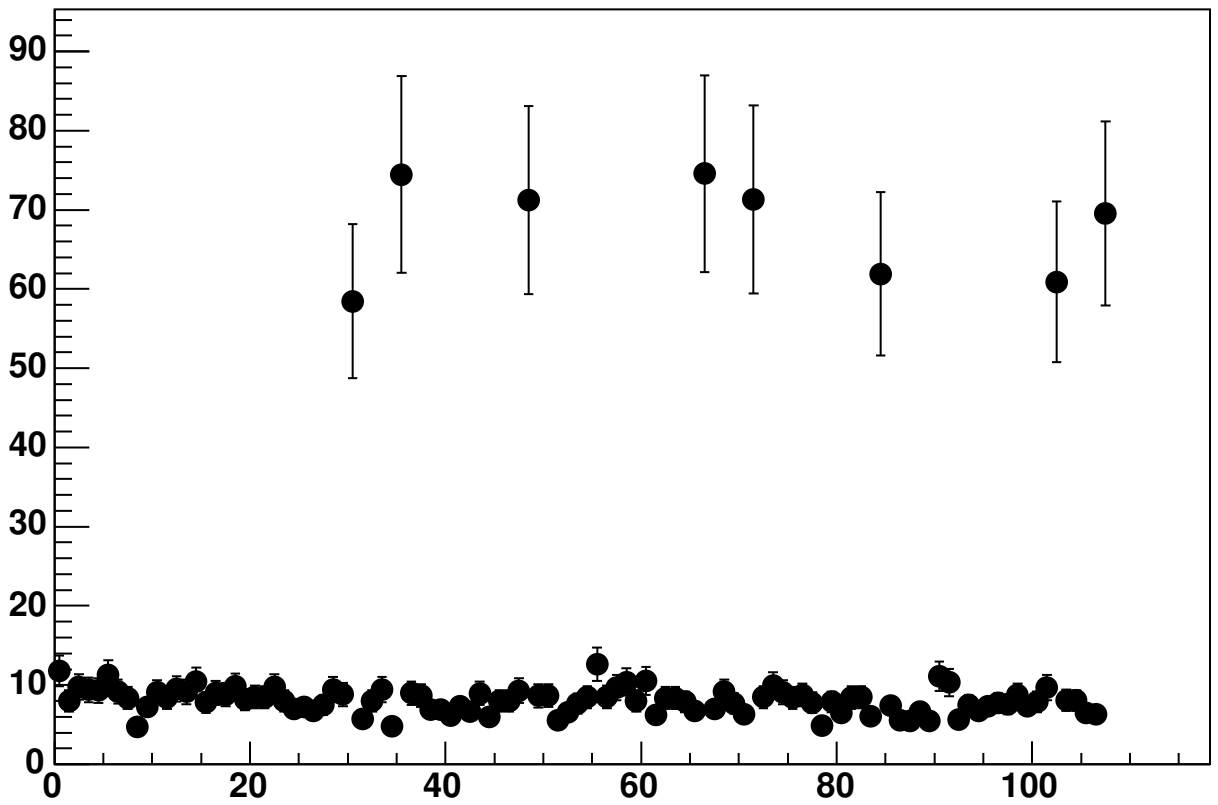
Enable 2, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



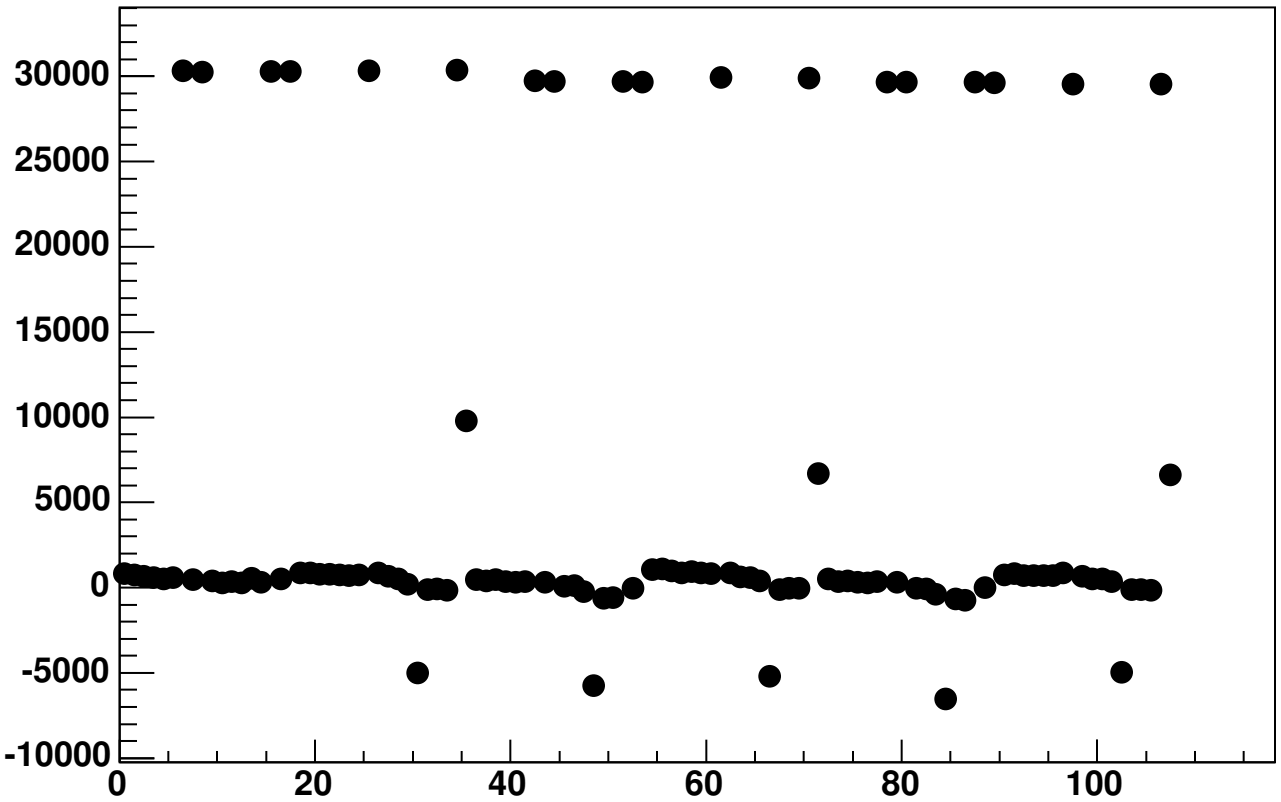
Enable 2, Hold=35, DAC=18400, ADC Mean vs 18*Chip+Chan



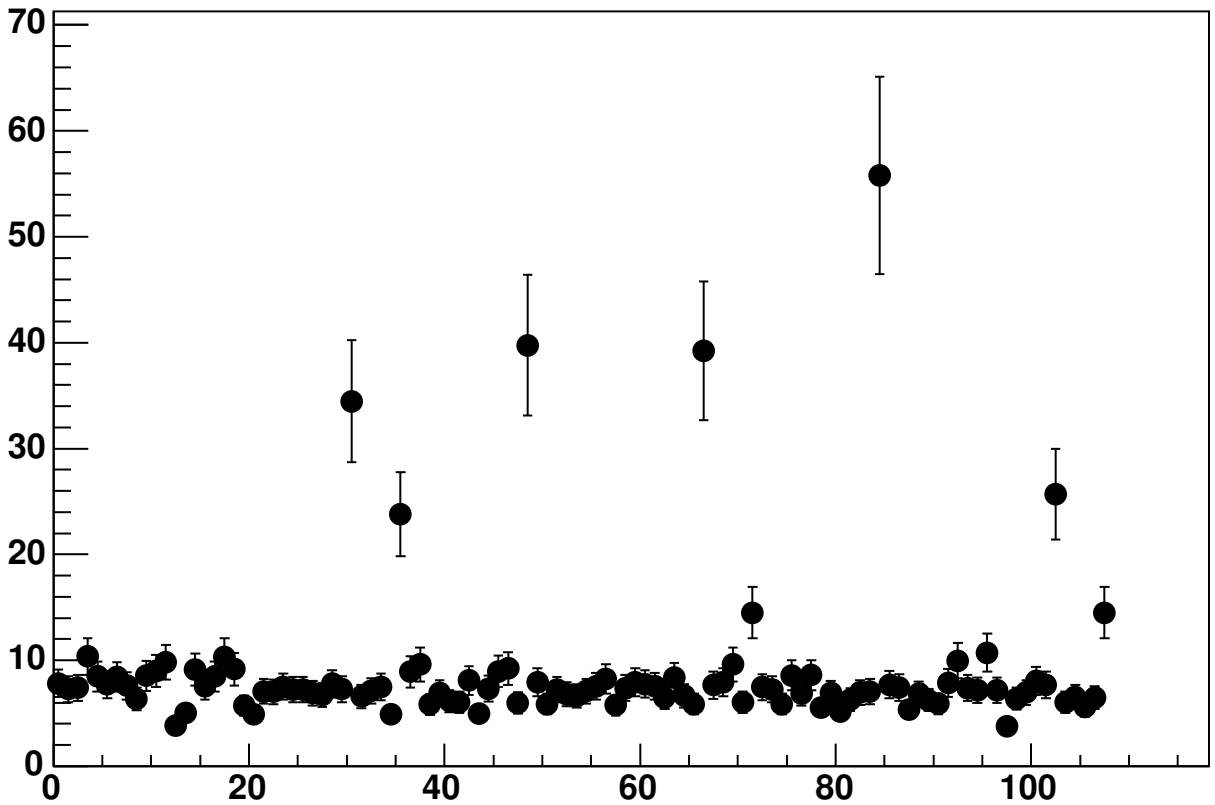
Enable 2, Hold=35, DAC=18400, ADC Noise vs 18*Chip+Chan



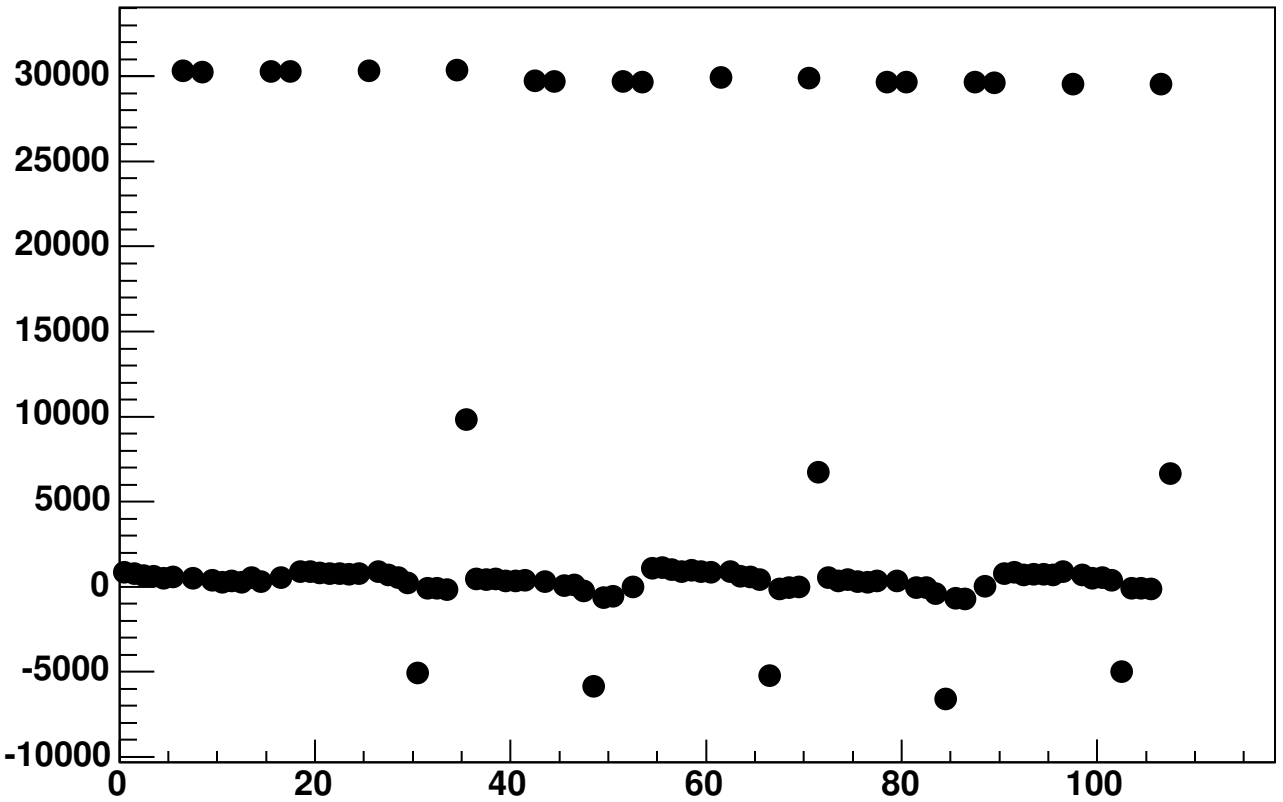
Enable 2, Hold=35, DAC=18800, ADC Mean vs 18*Chip+Chan



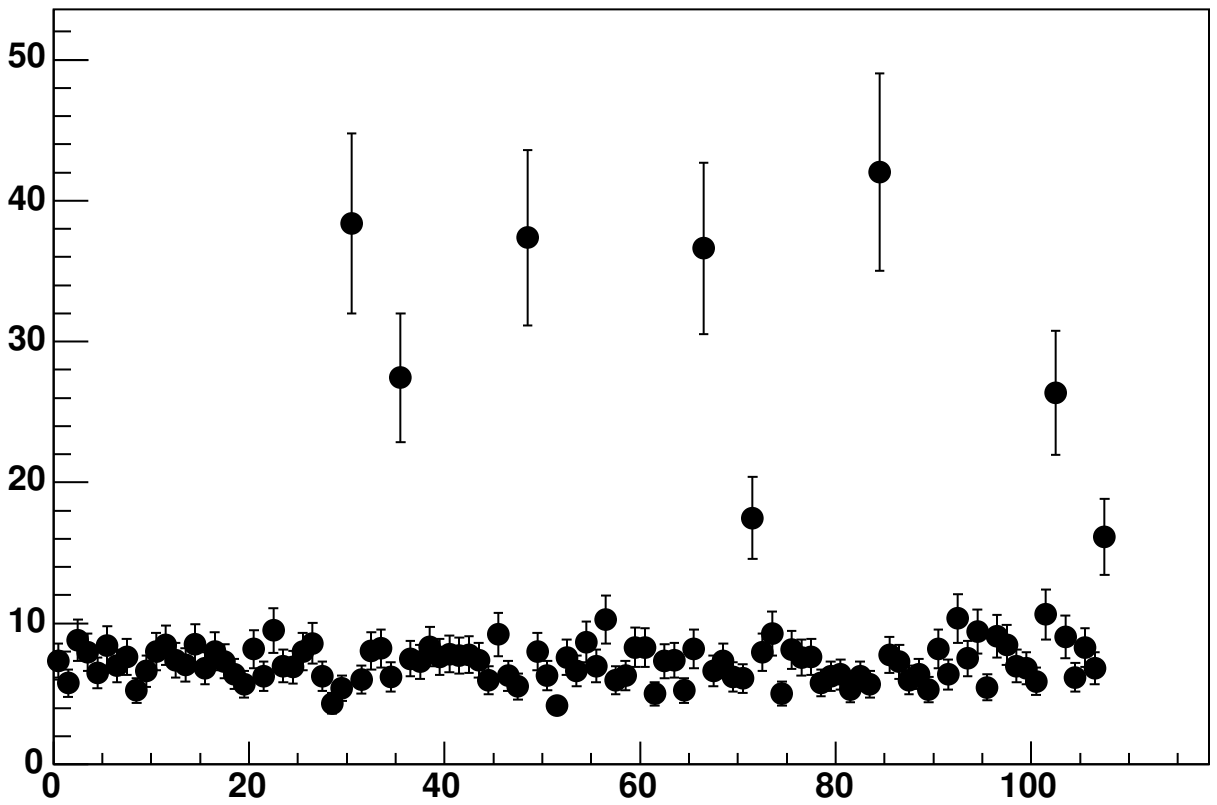
Enable 2, Hold=35, DAC=18800, ADC Noise vs 18*Chip+Chan



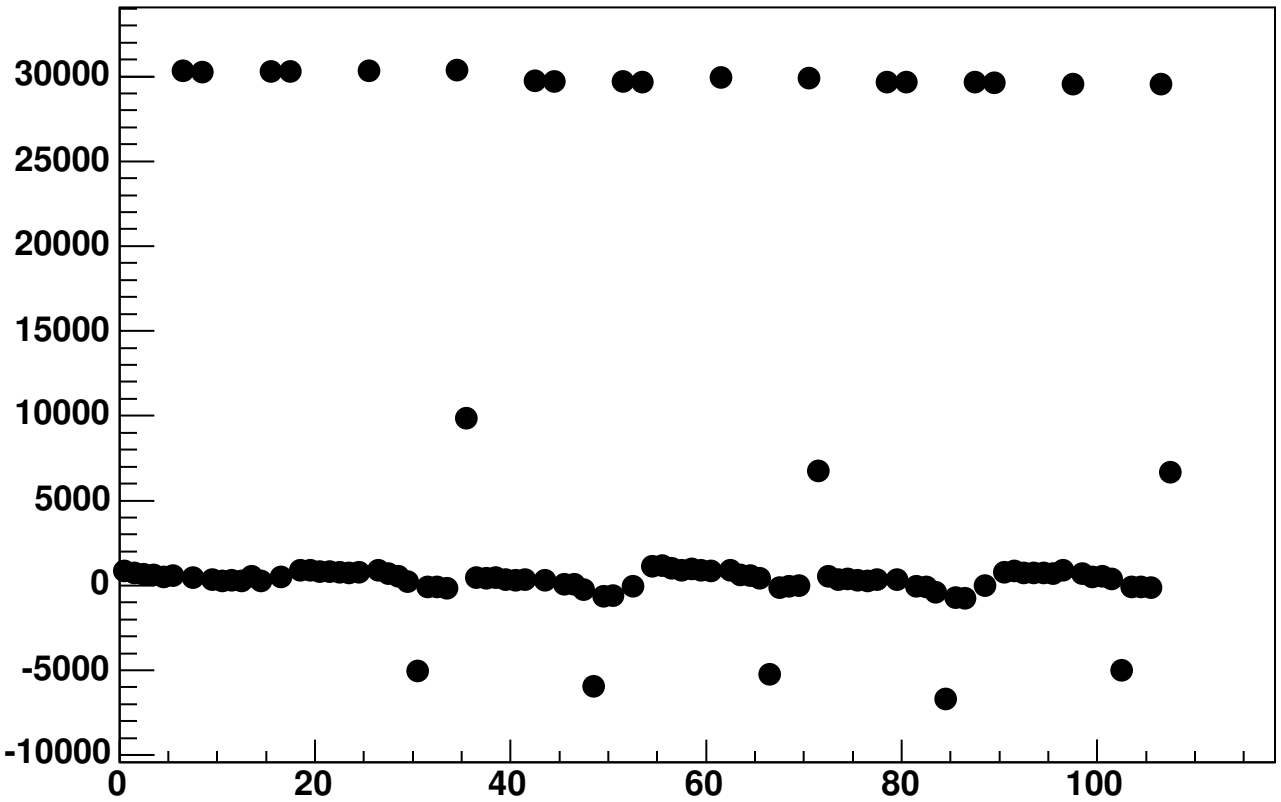
Enable 2, Hold=35, DAC=19200, ADC Mean vs 18*Chip+Chan



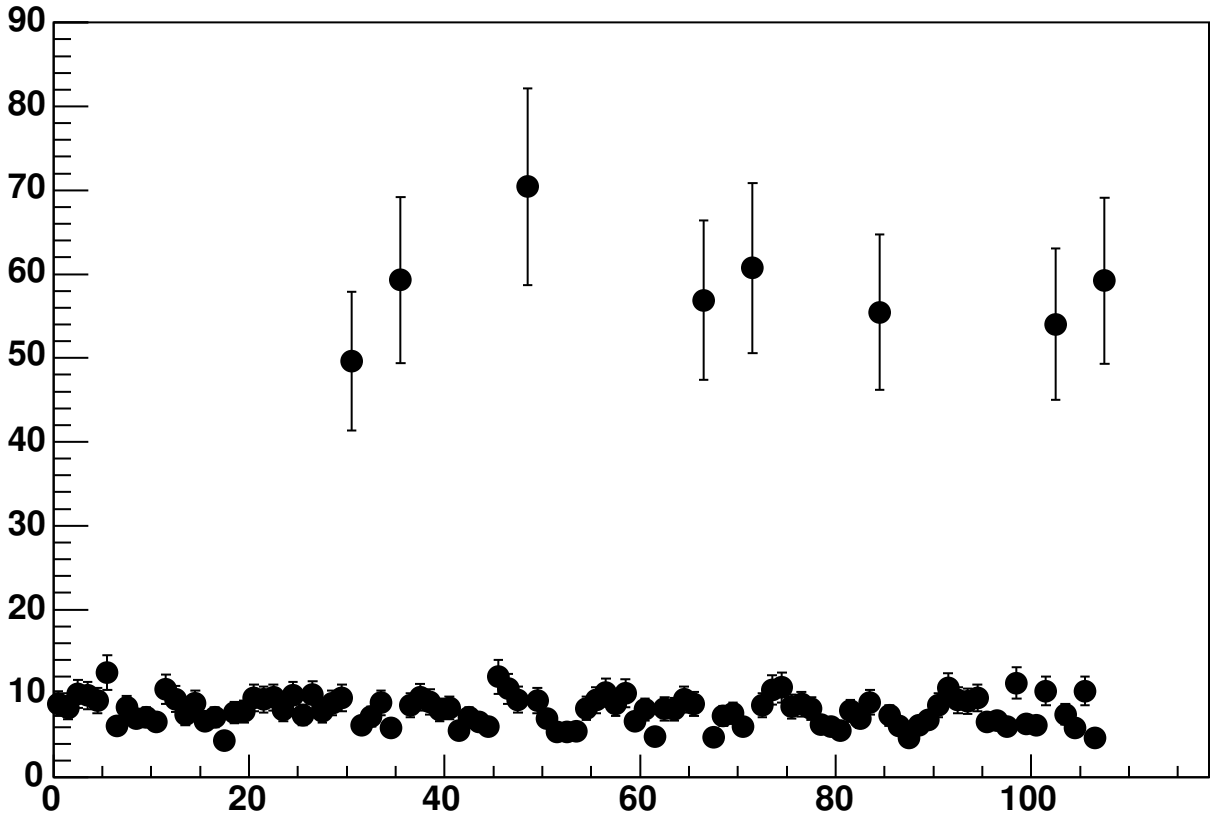
Enable 2, Hold=35, DAC=19200, ADC Noise vs 18*Chip+Chan



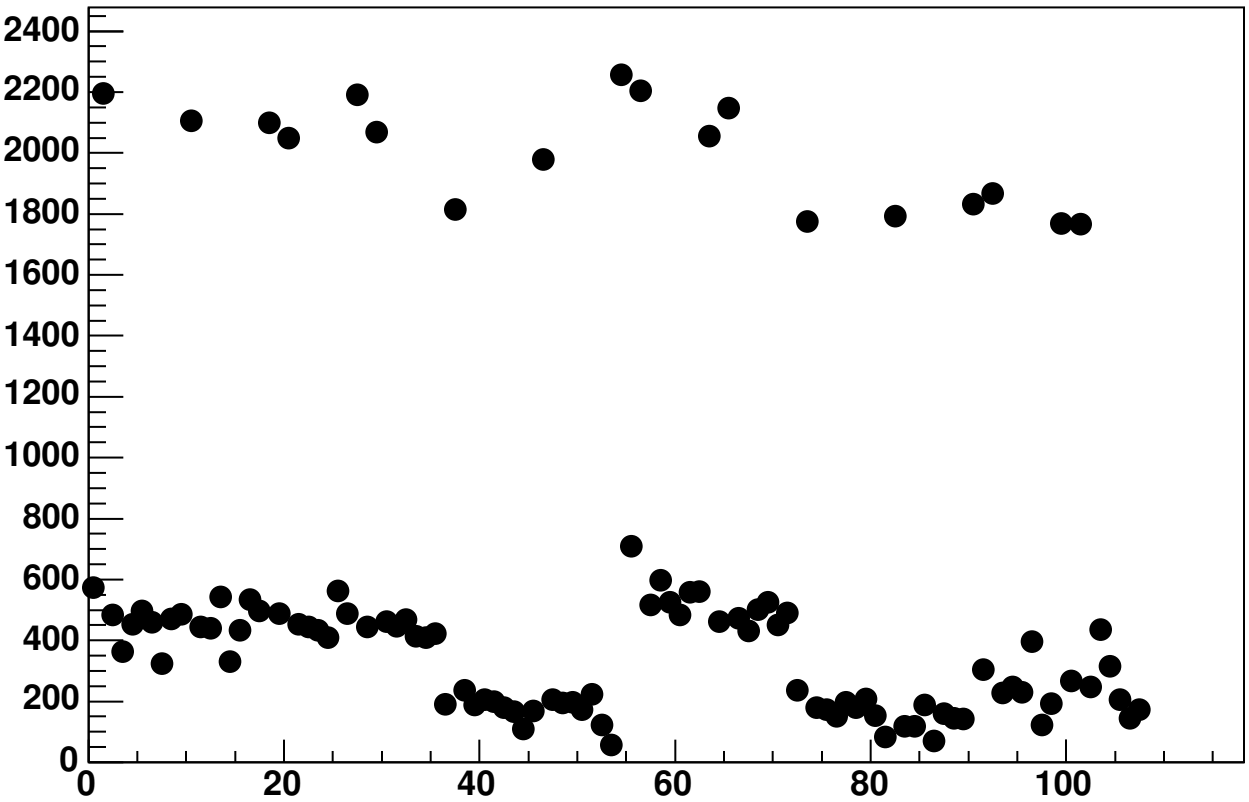
Enable 2, Hold=35, DAC=19600, ADC Mean vs 18*Chip+Chan



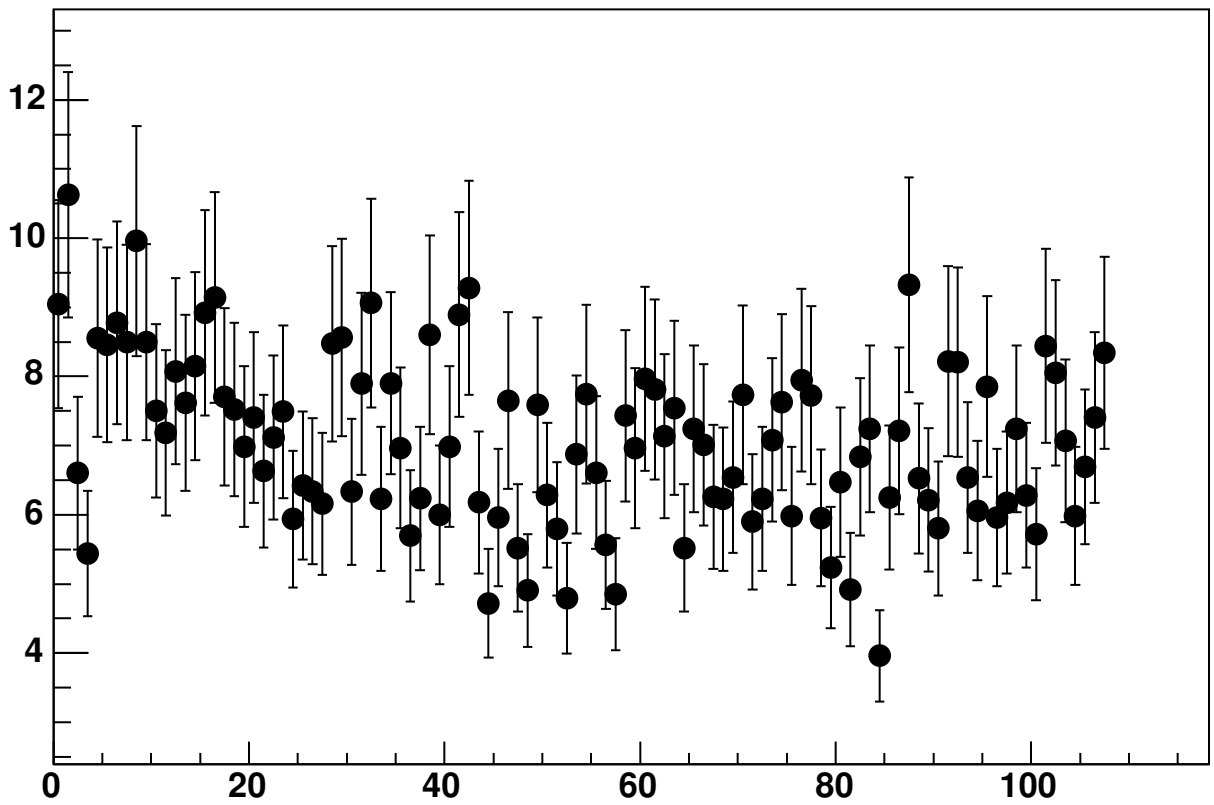
Enable 2, Hold=35, DAC=19600, ADC Noise vs 18*Chip+Chan



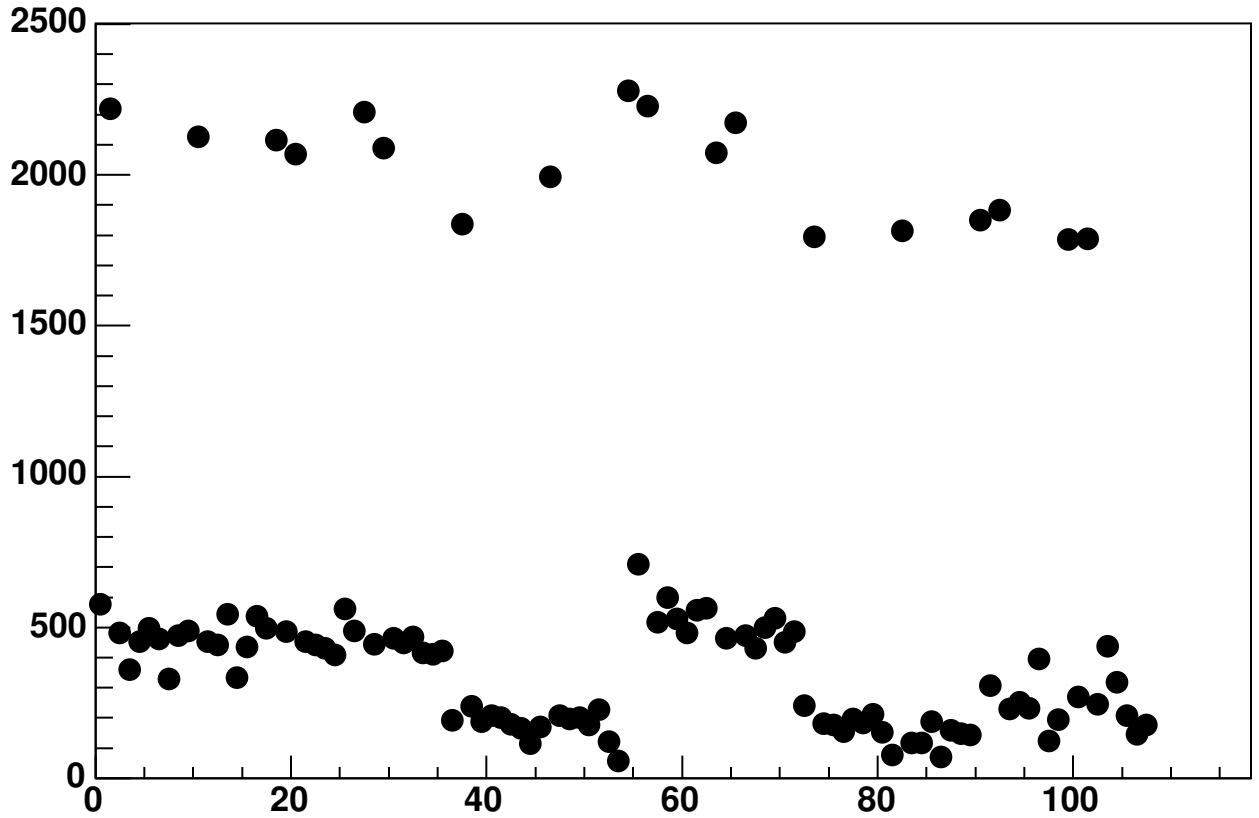
Enable 3, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



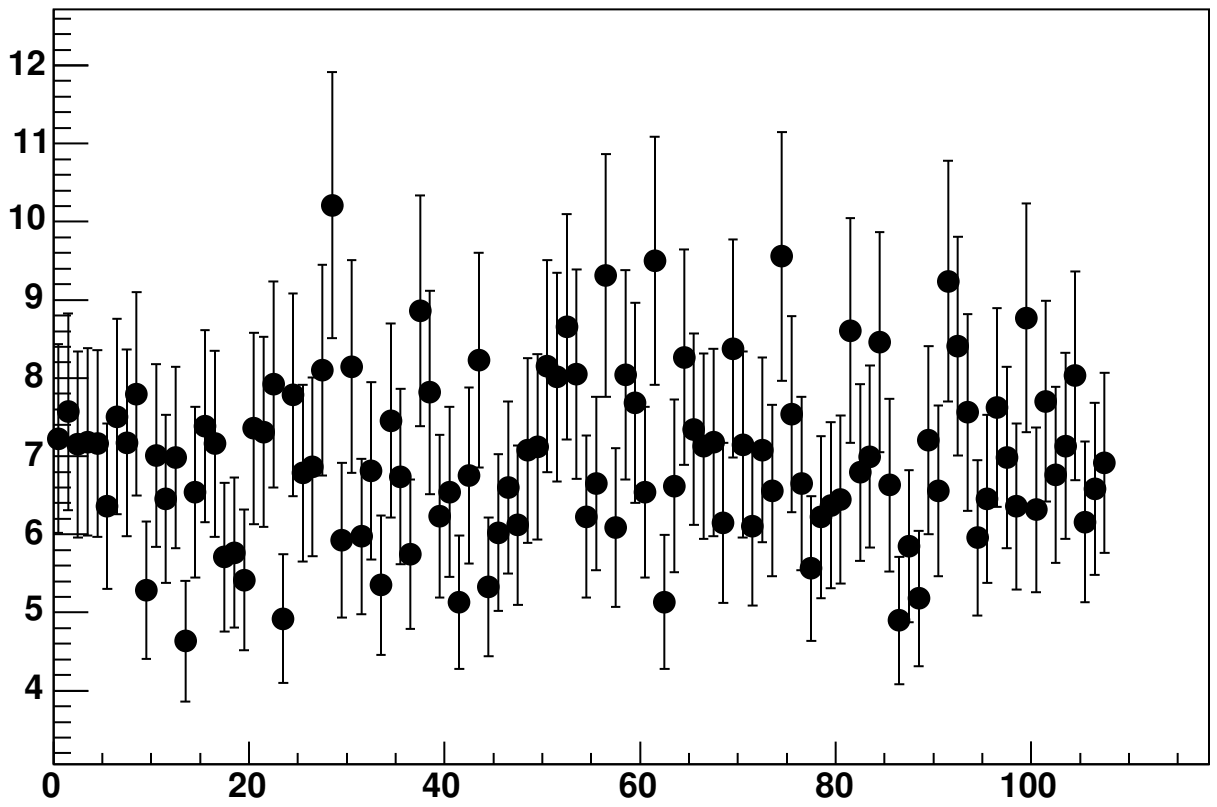
Enable 3, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



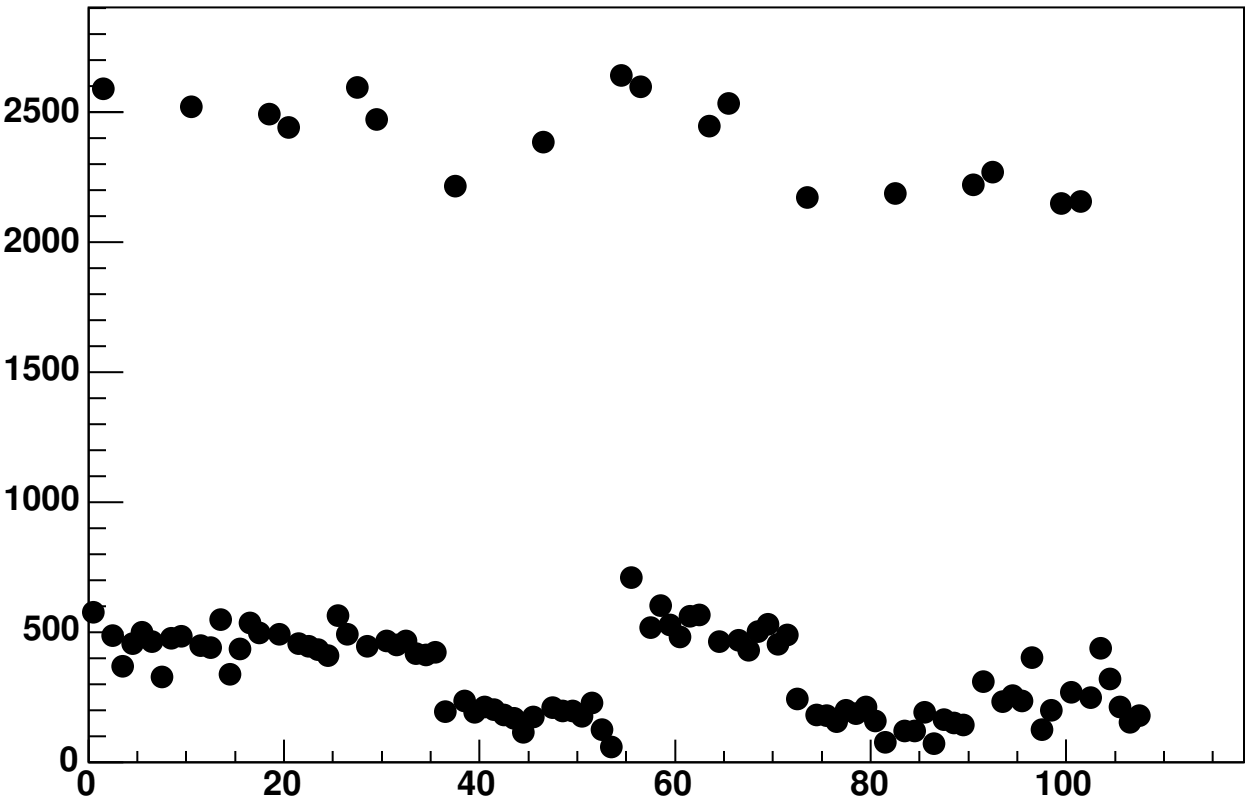
Enable 3, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



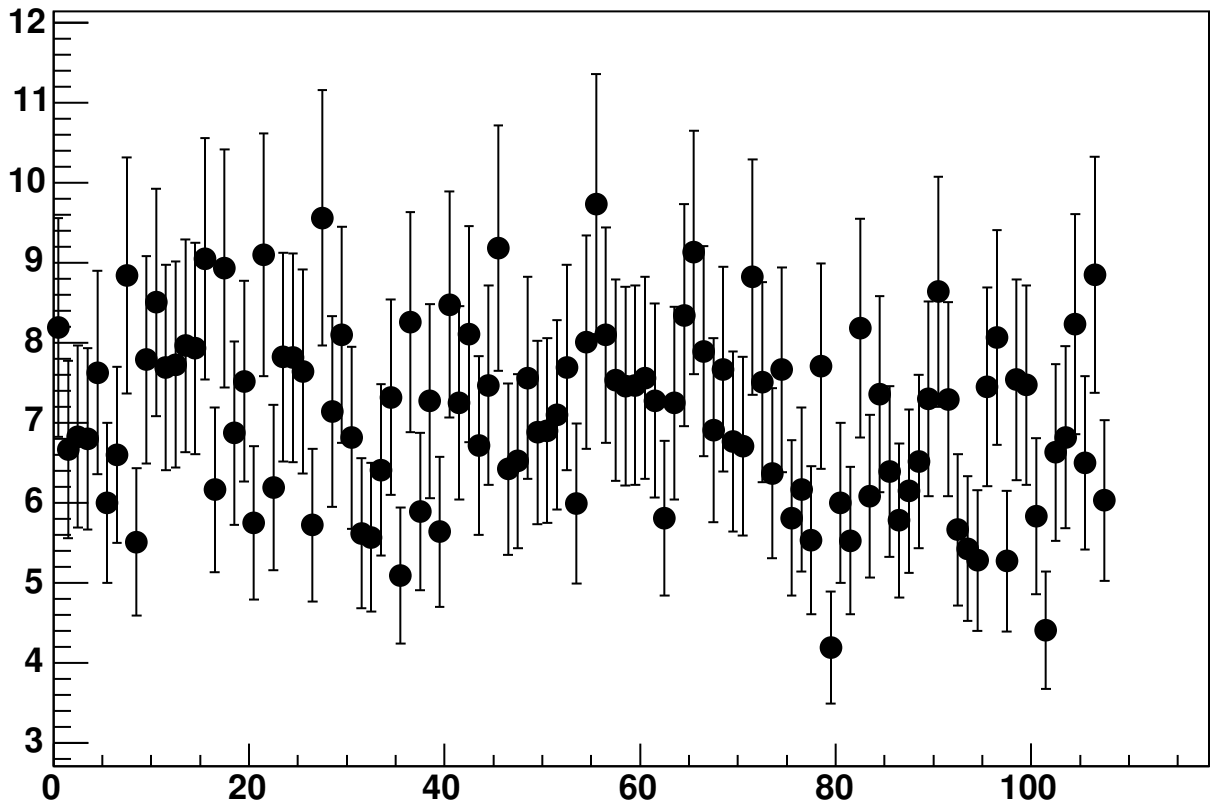
Enable 3, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



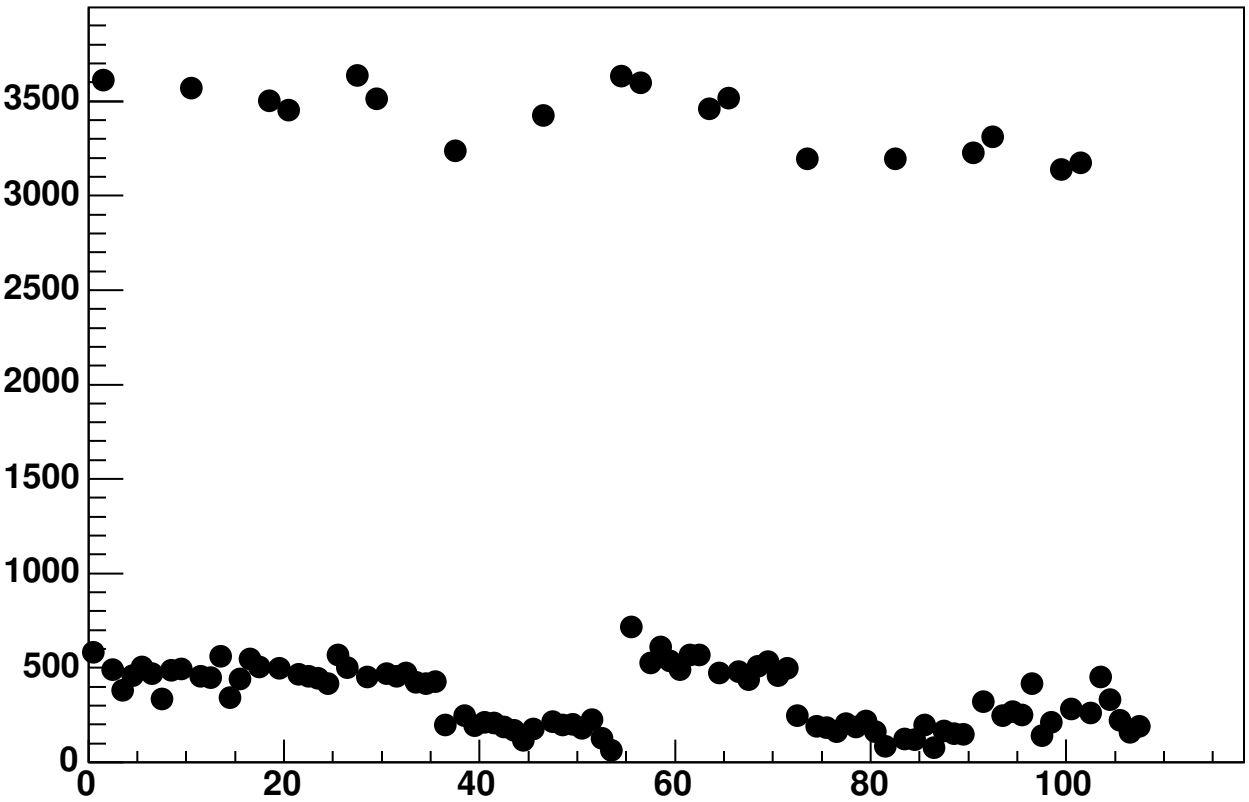
Enable 3, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



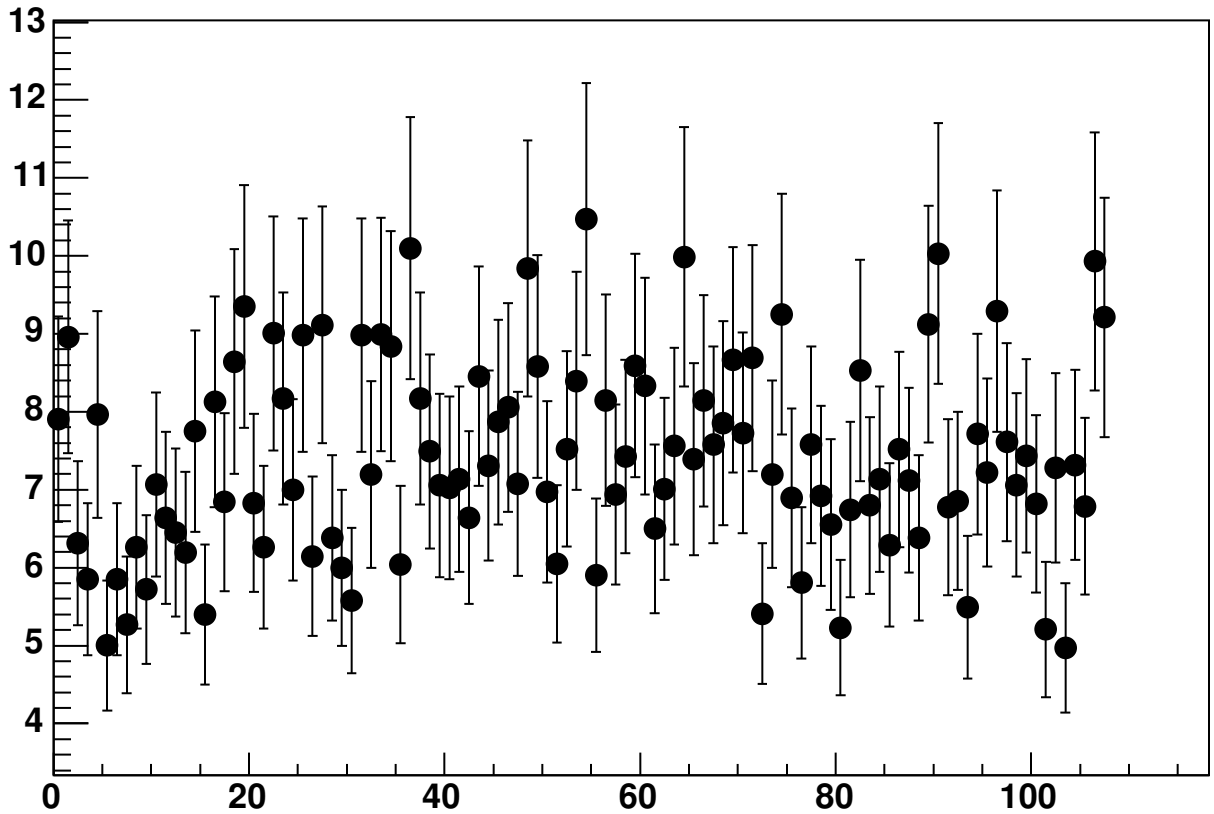
Enable 3, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



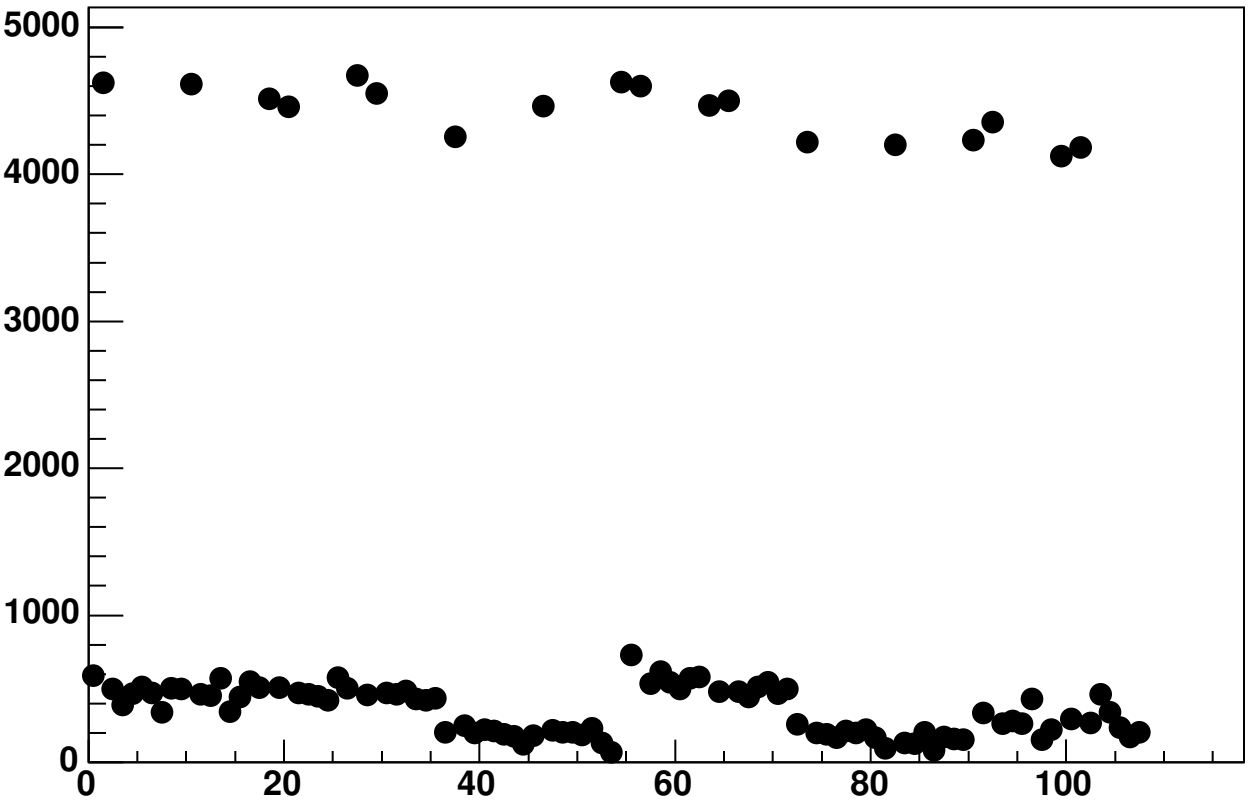
Enable 3, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



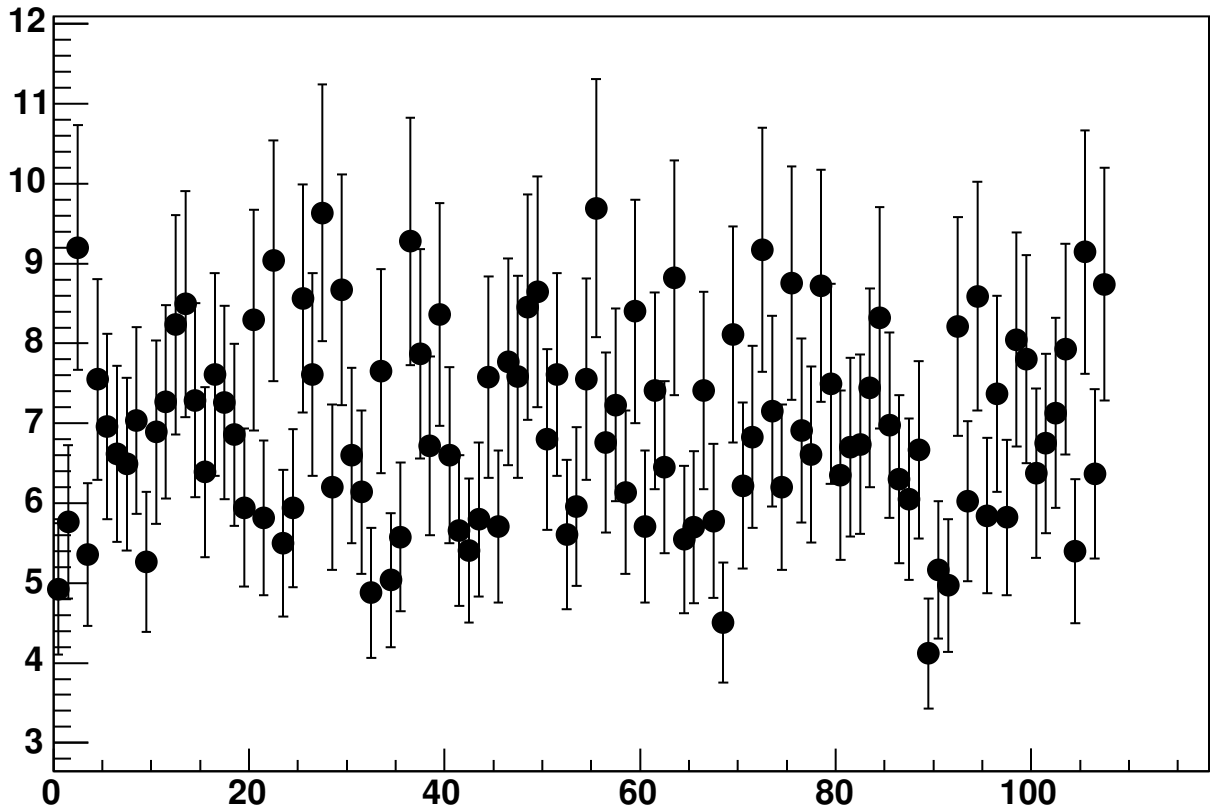
Enable 3, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



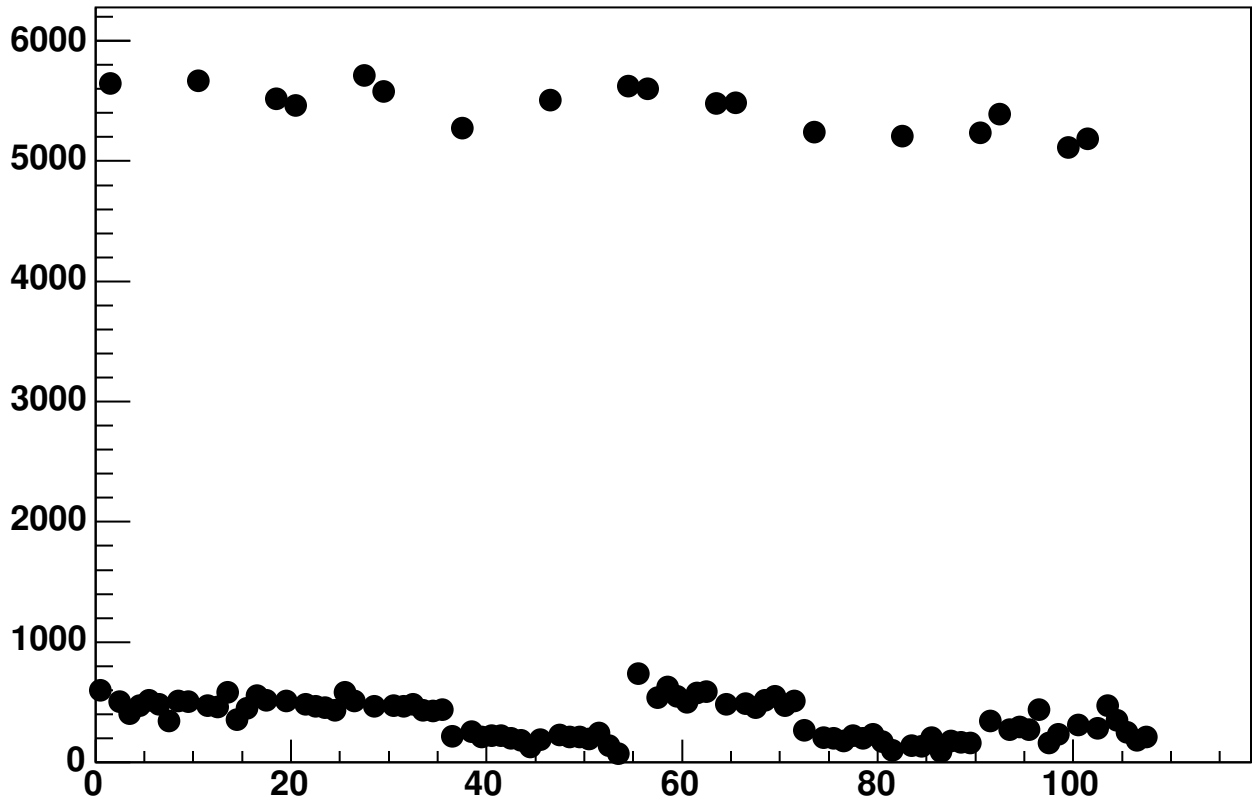
Enable 3, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



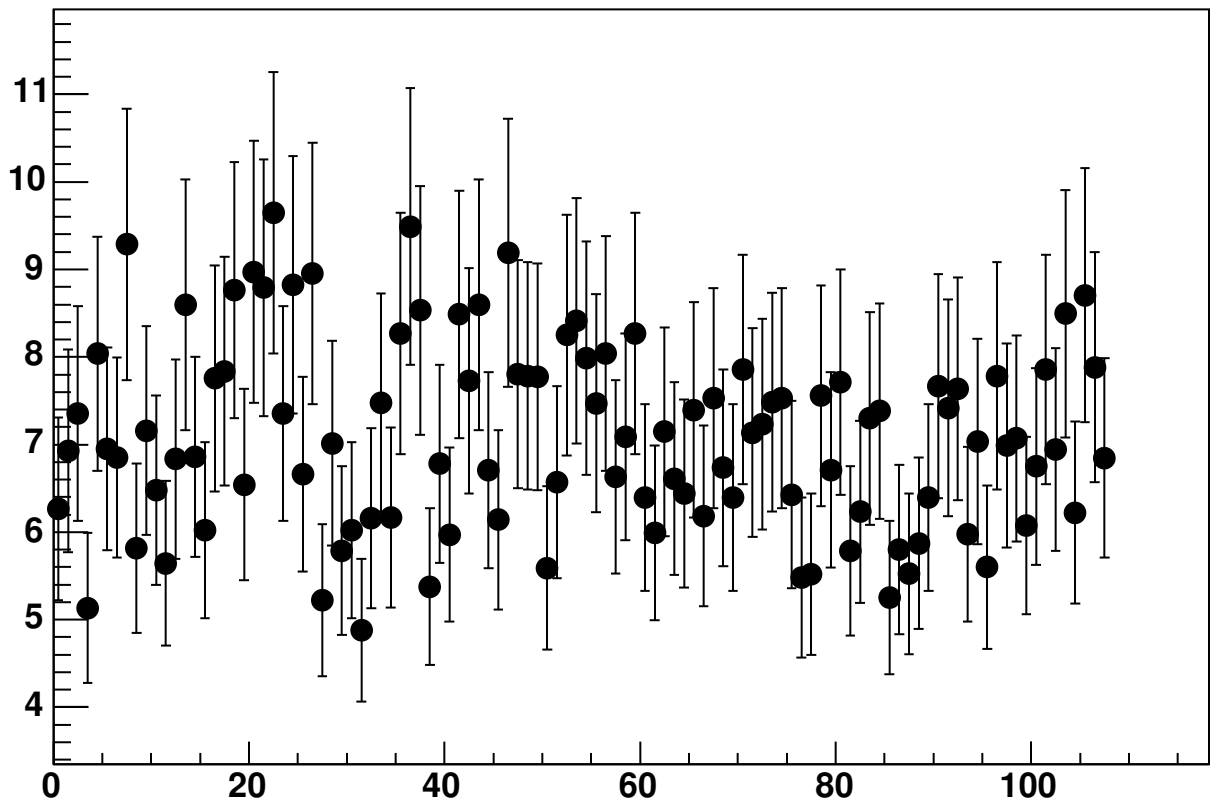
Enable 3, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



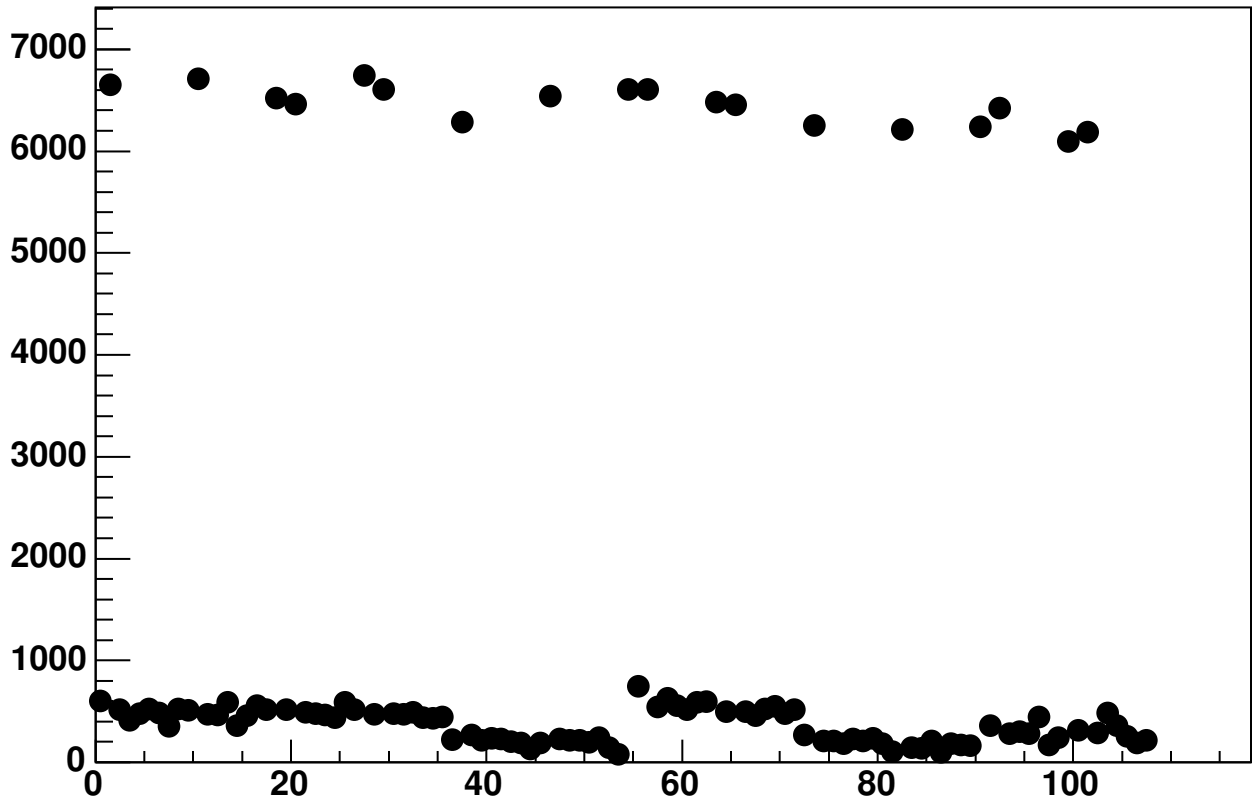
Enable 3, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



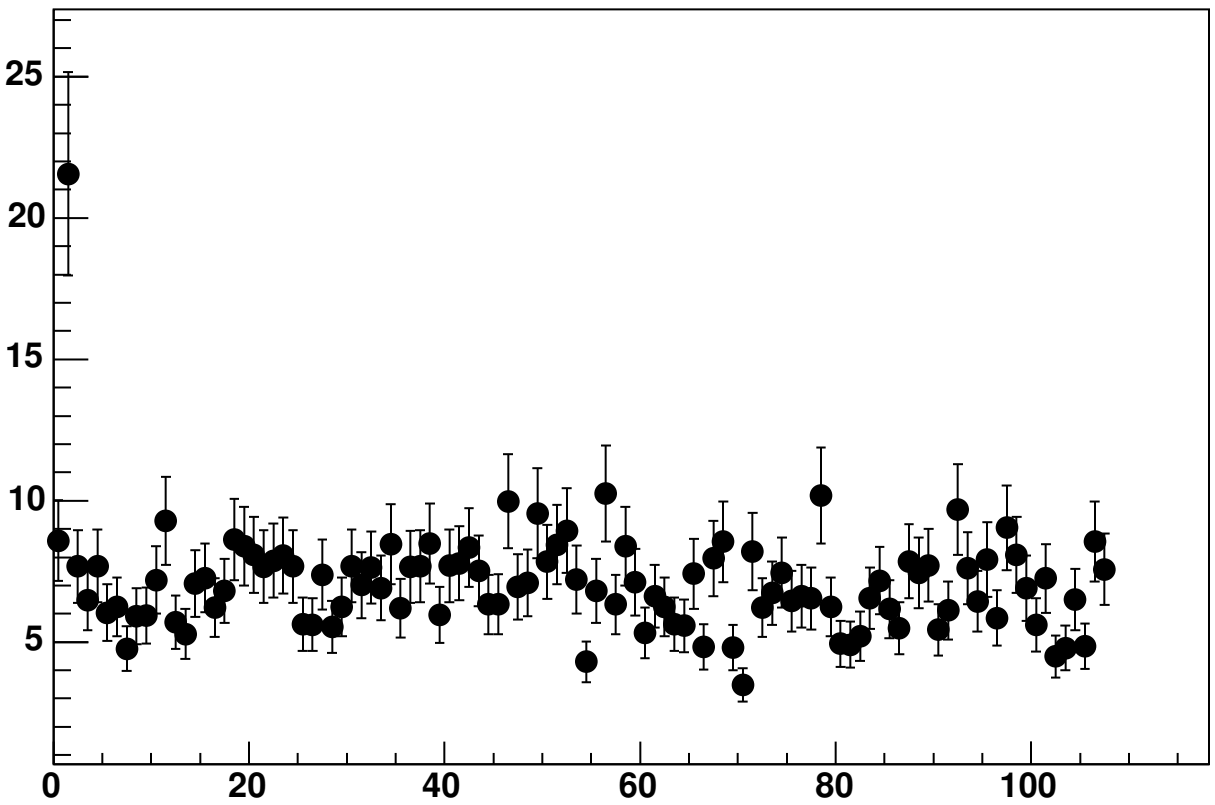
Enable 3, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



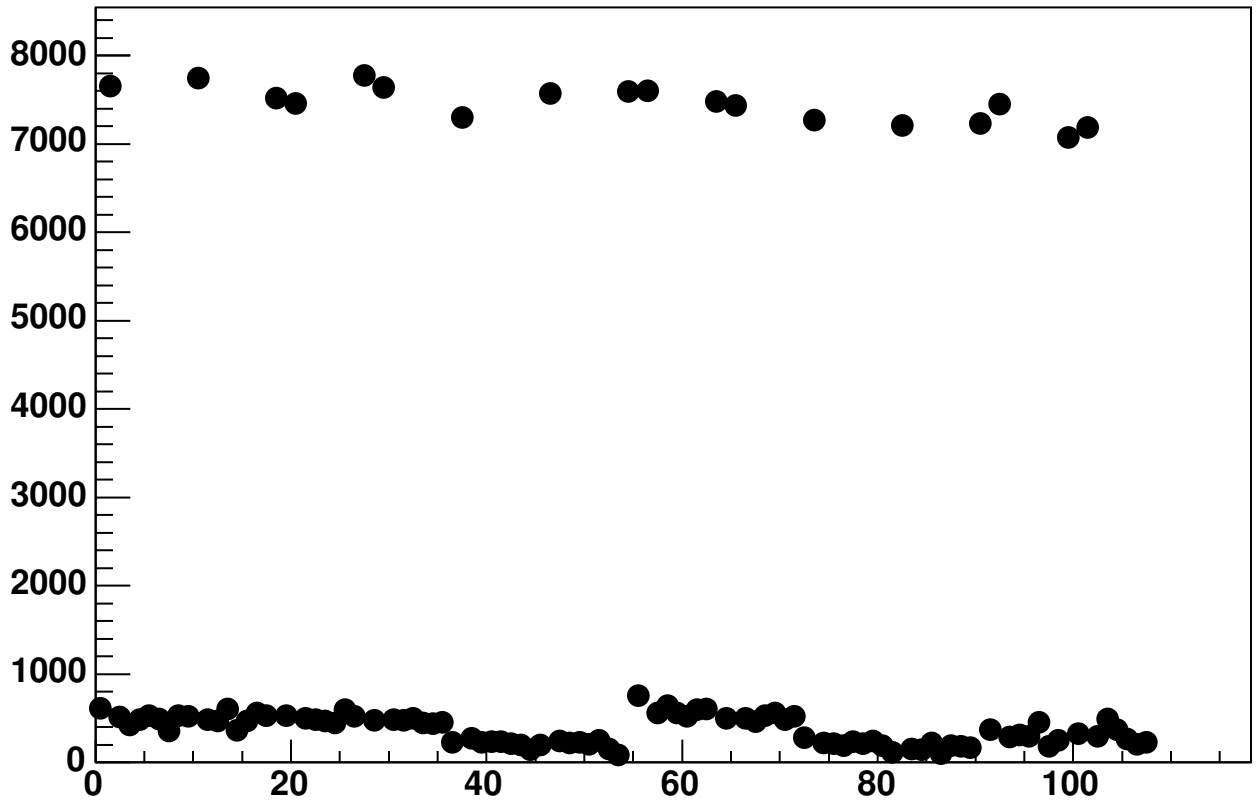
Enable 3, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



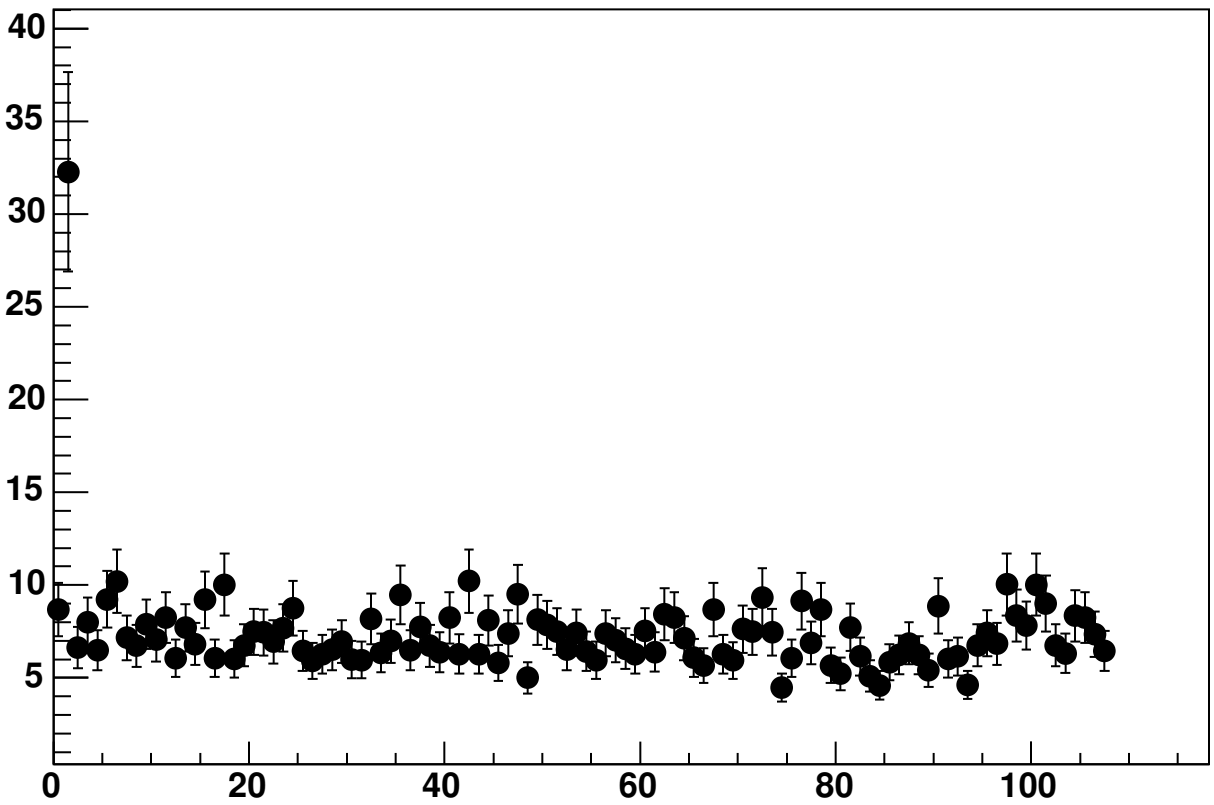
Enable 3, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



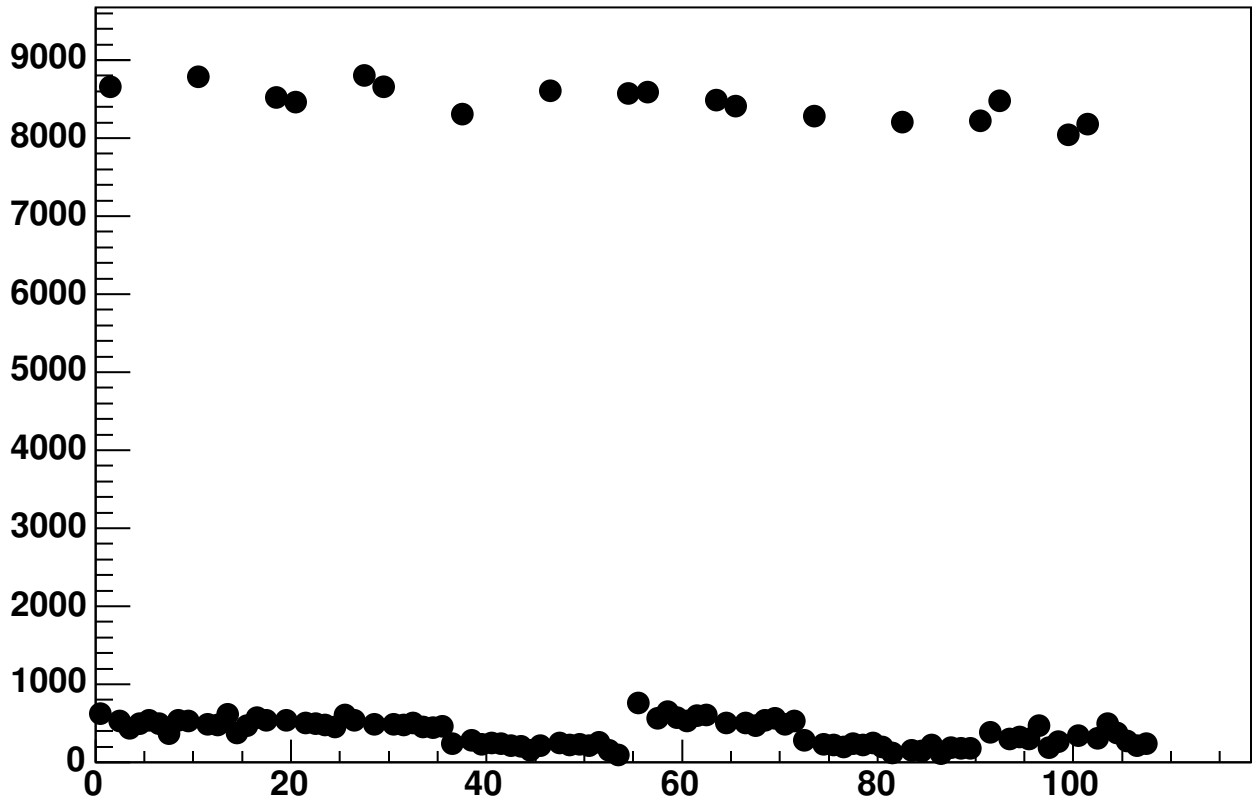
Enable 3, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



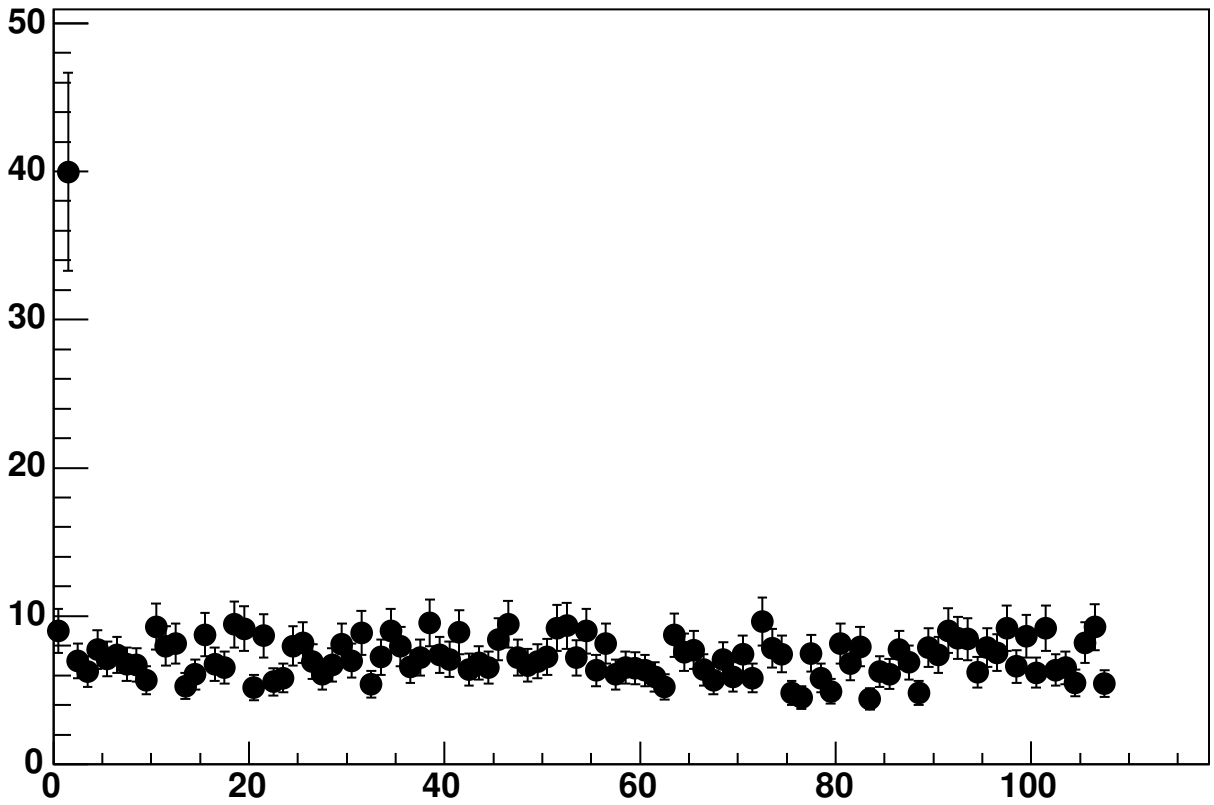
Enable 3, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



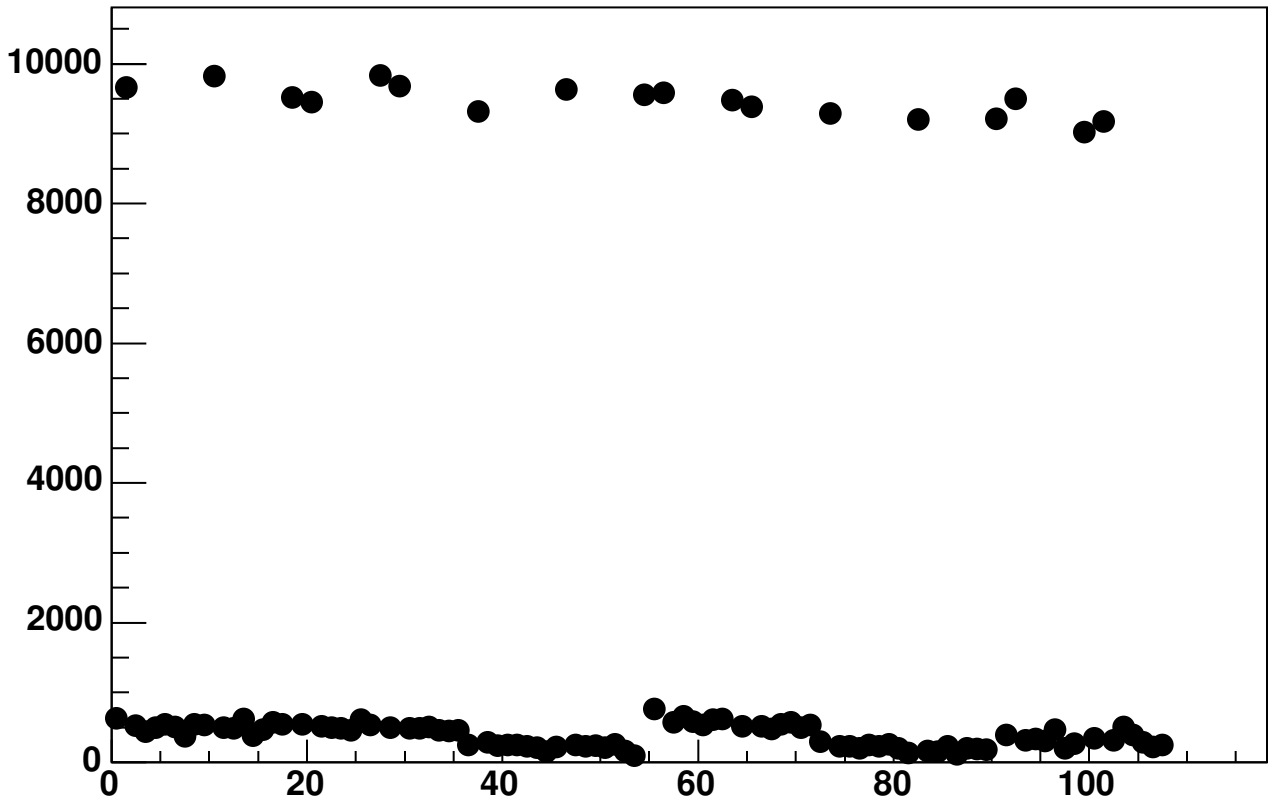
Enable 3, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



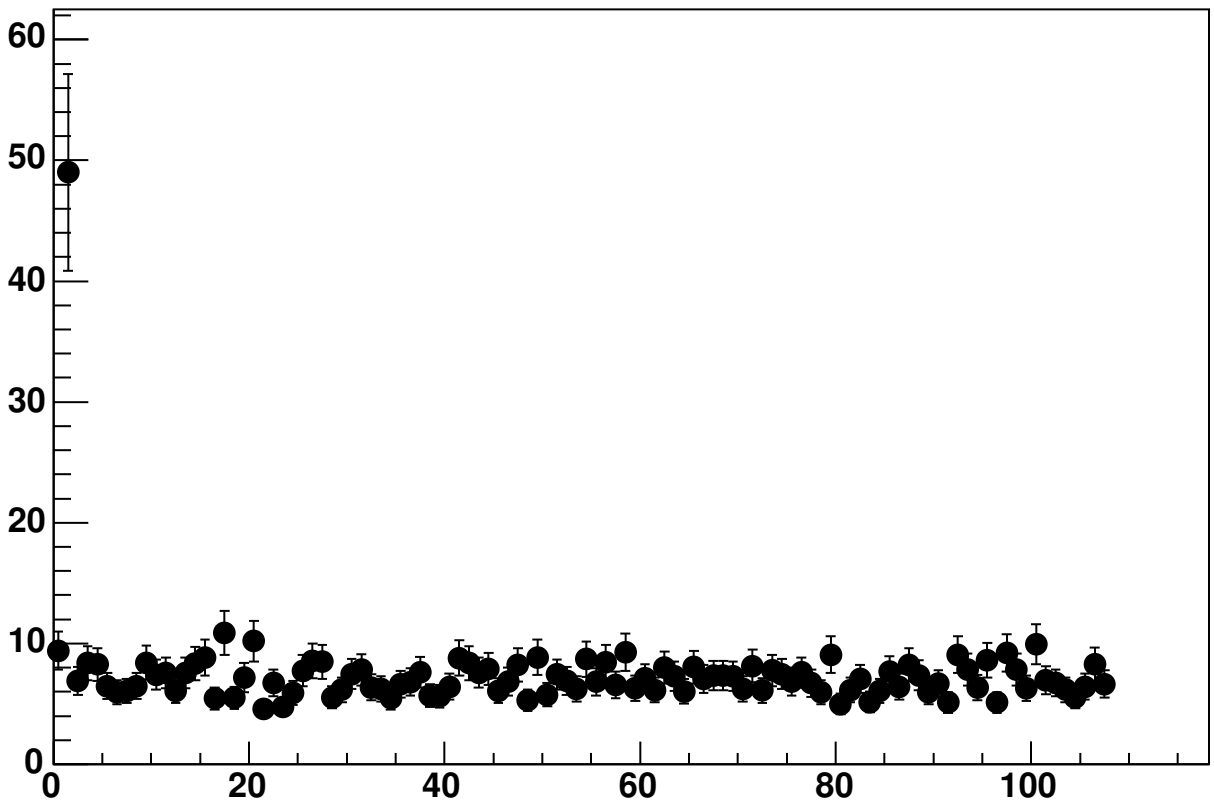
Enable 3, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



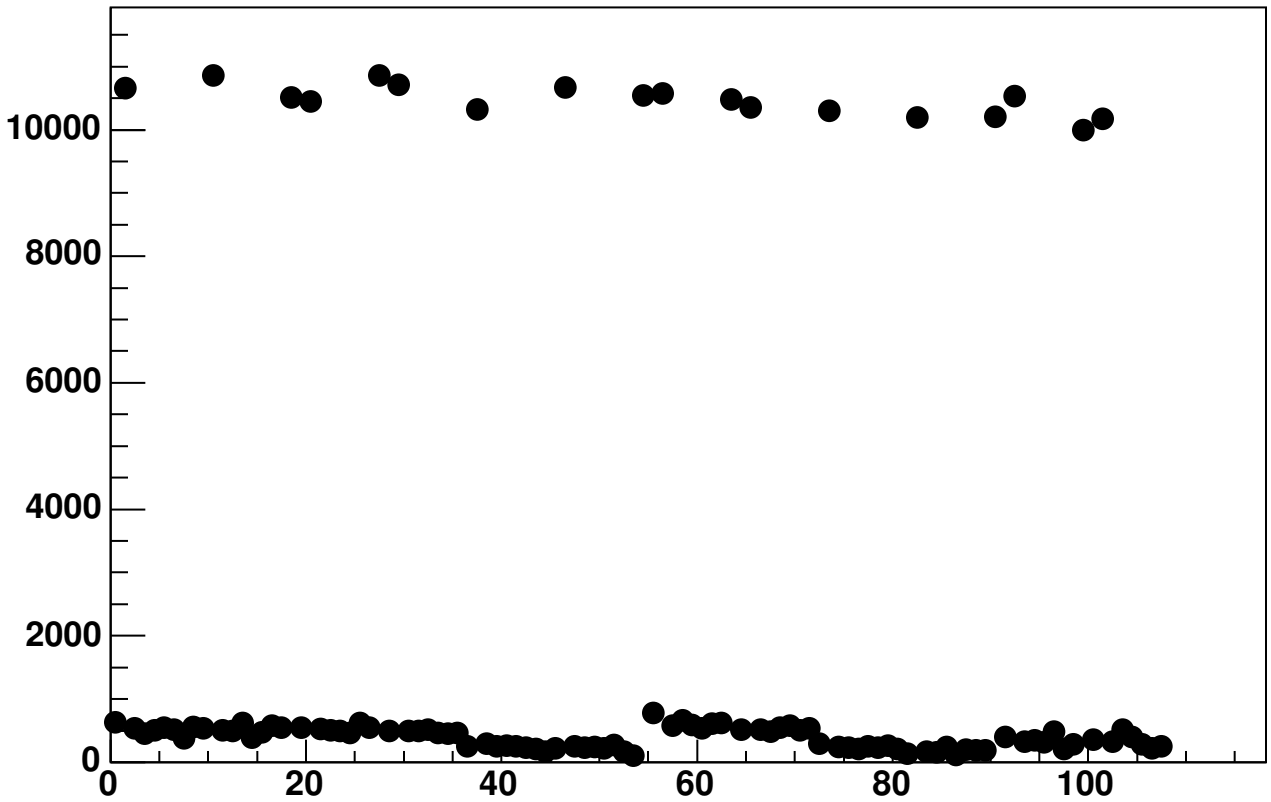
Enable 3, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



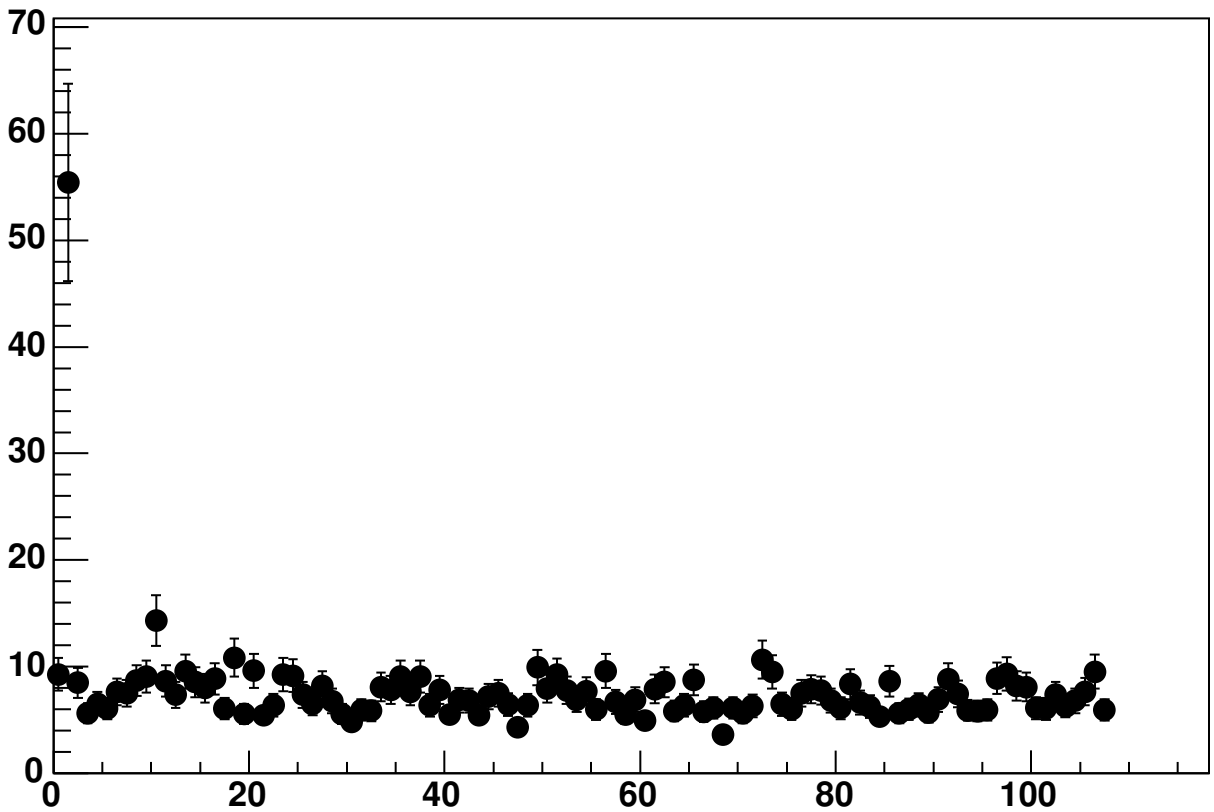
Enable 3, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



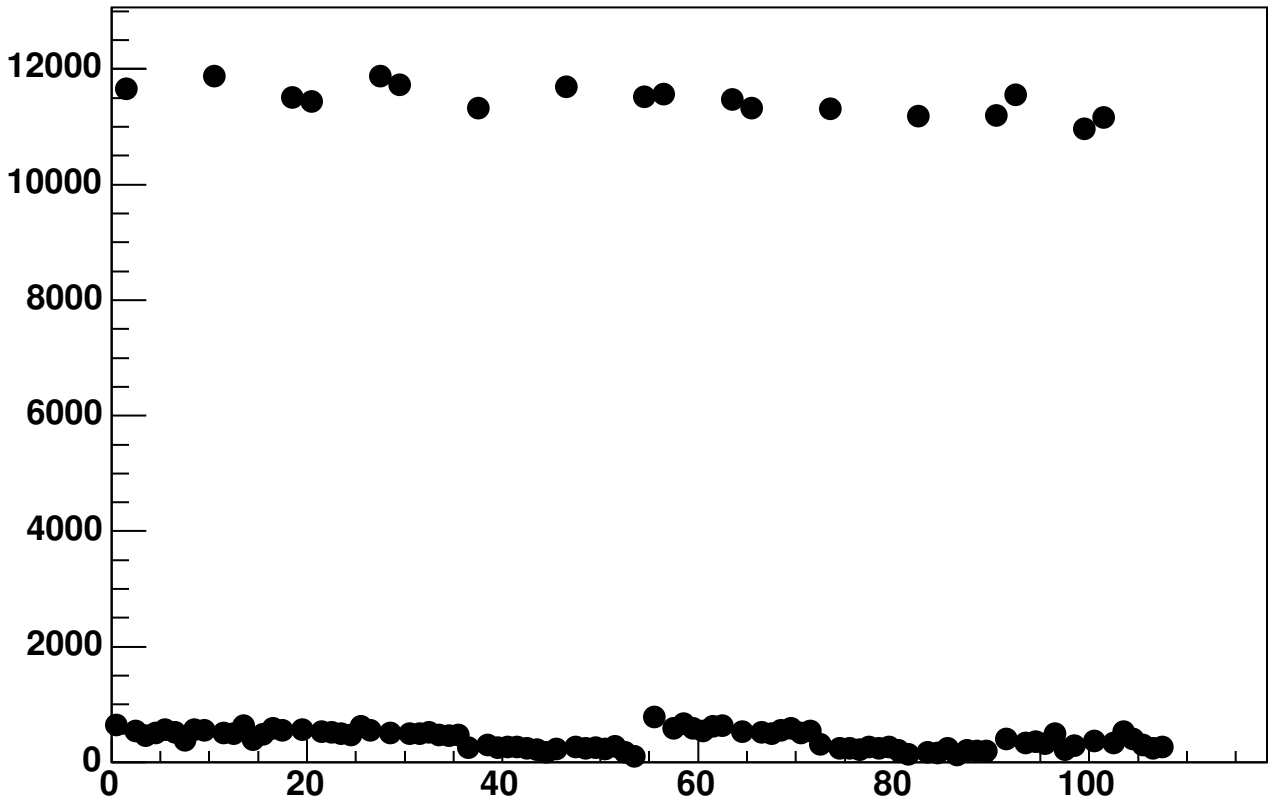
Enable 3, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



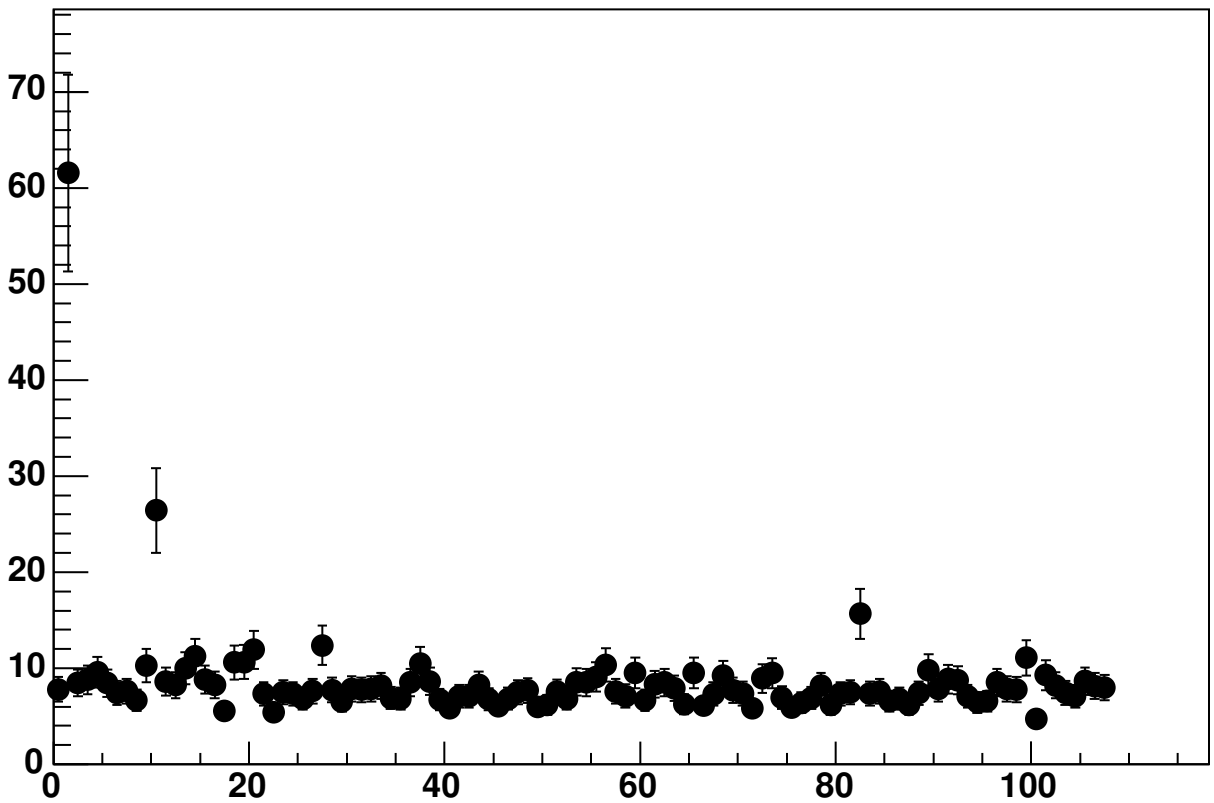
Enable 3, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



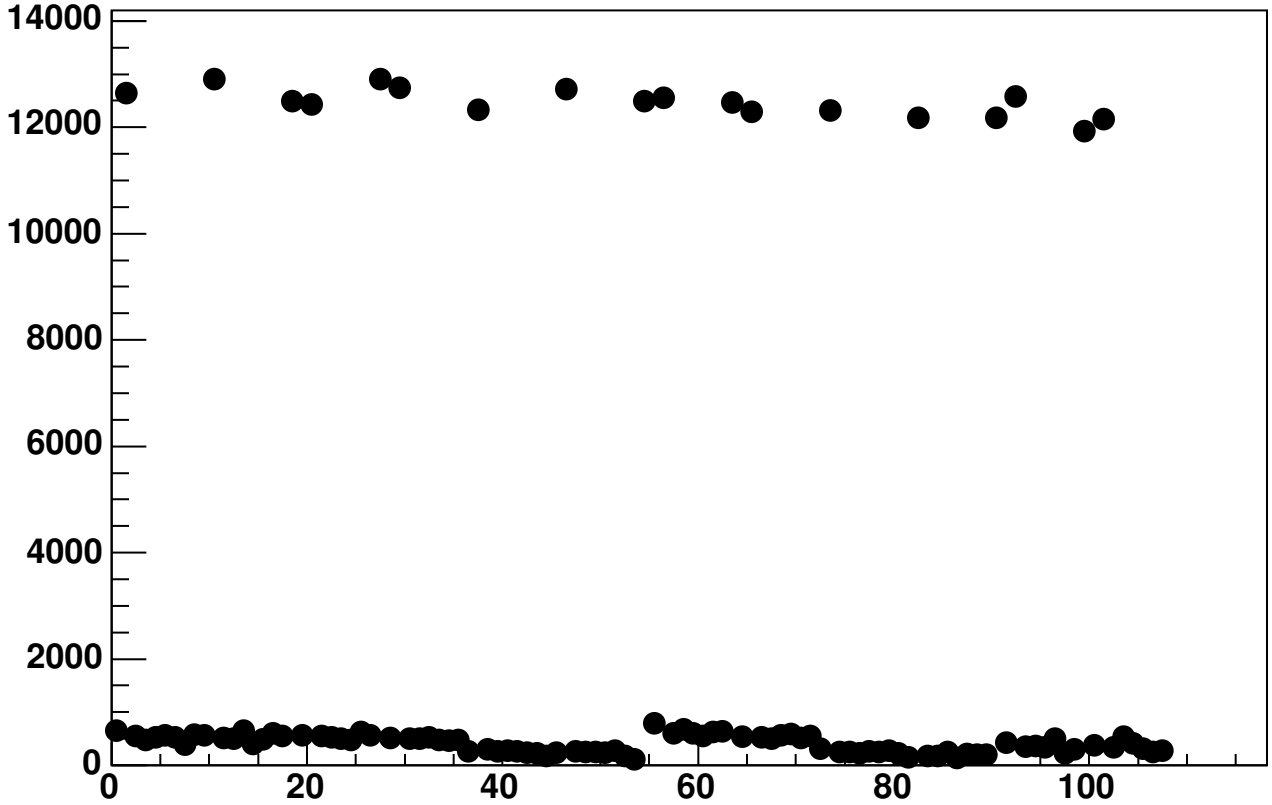
Enable 3, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



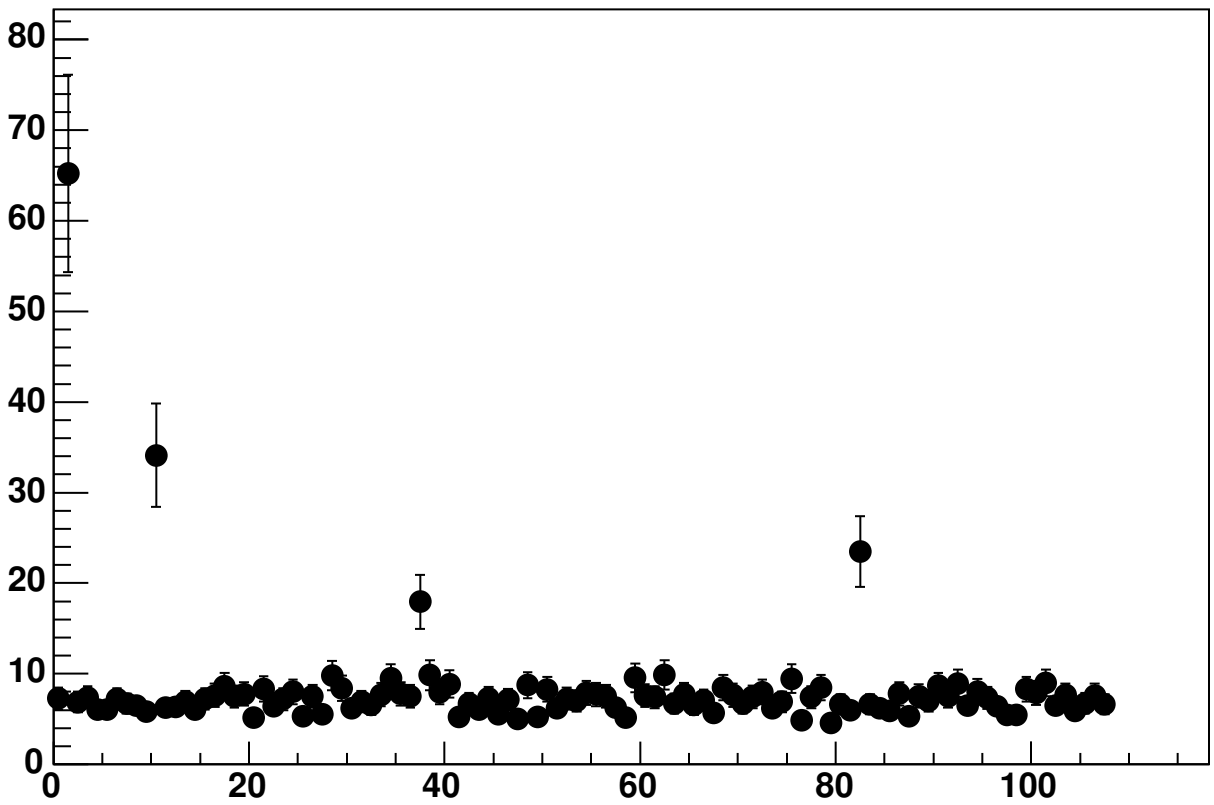
Enable 3, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



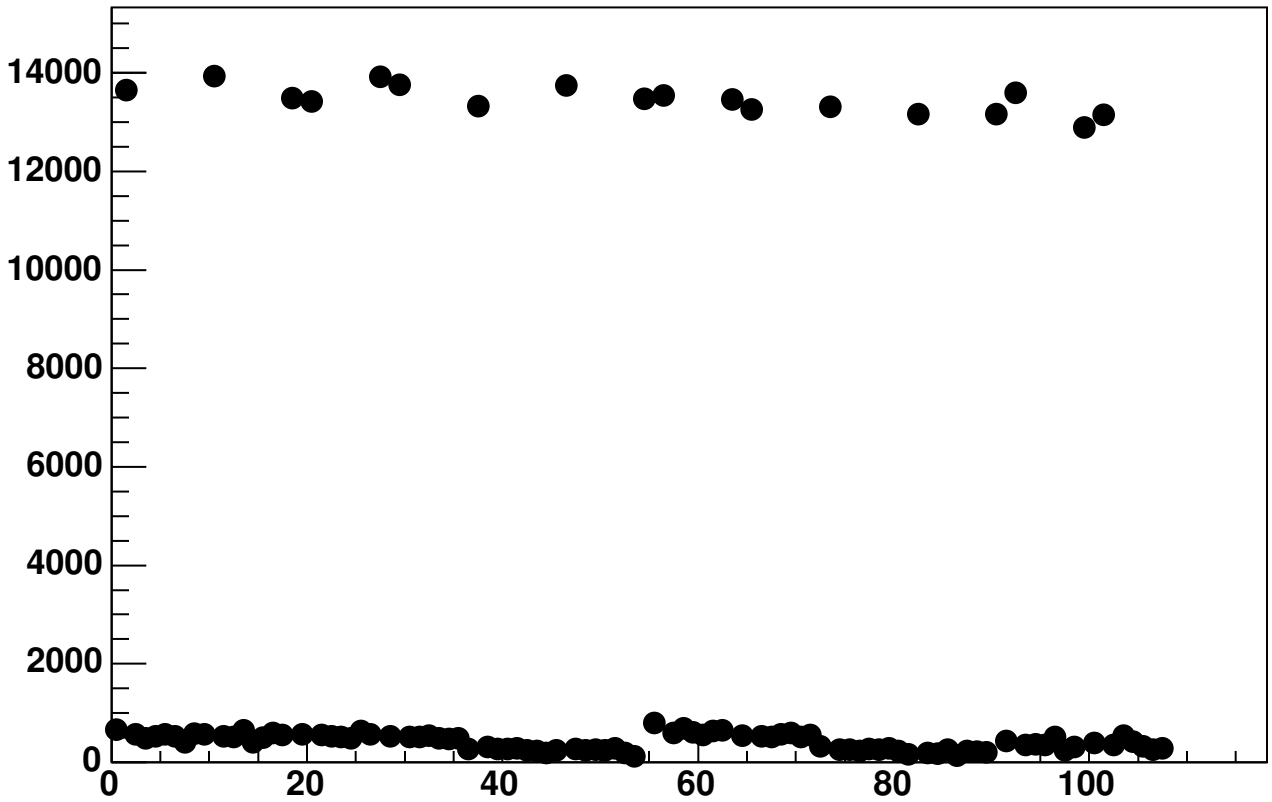
Enable 3, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



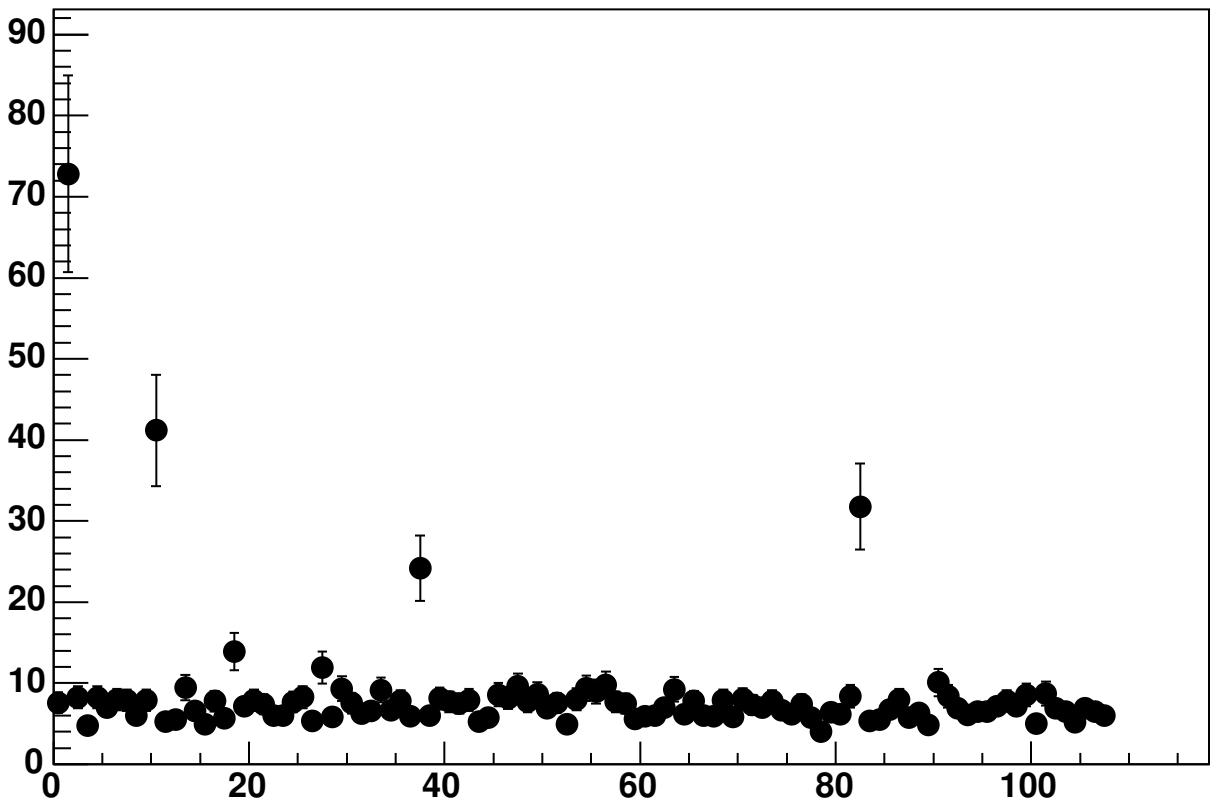
Enable 3, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



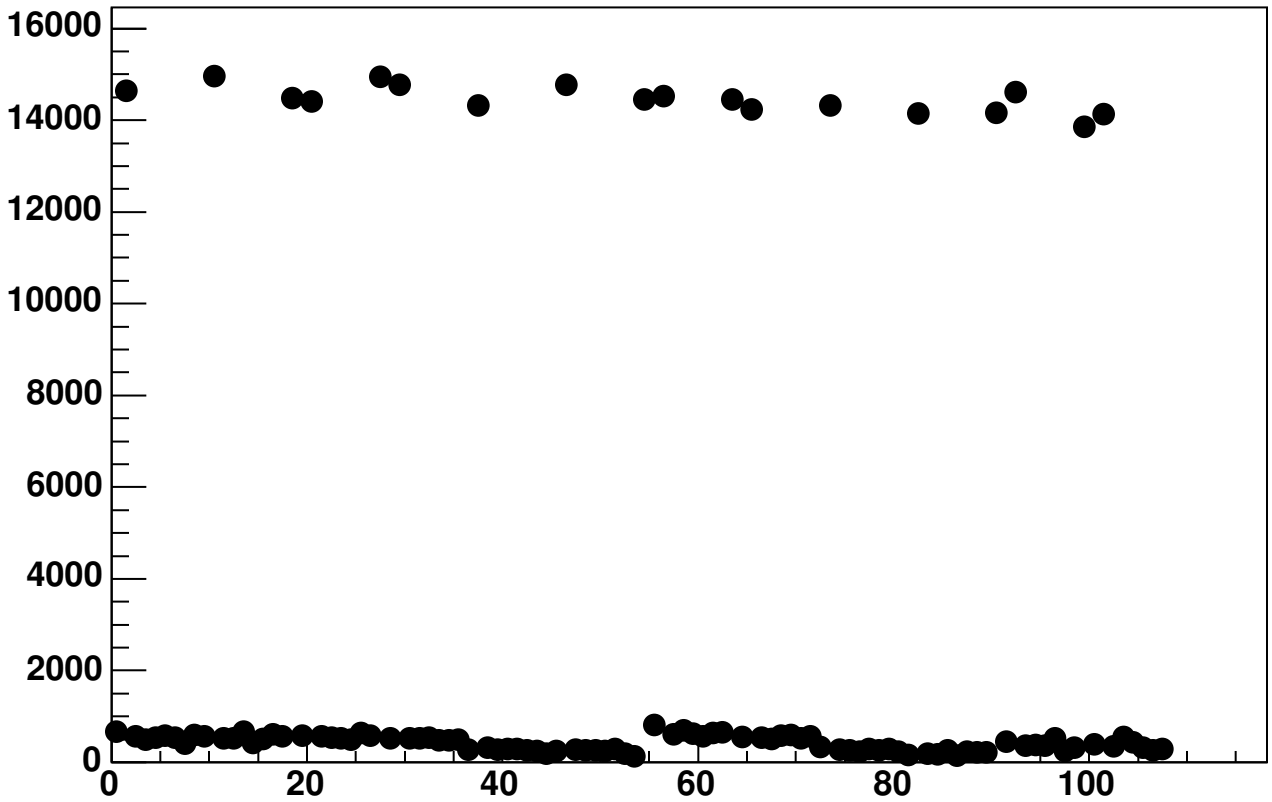
Enable 3, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



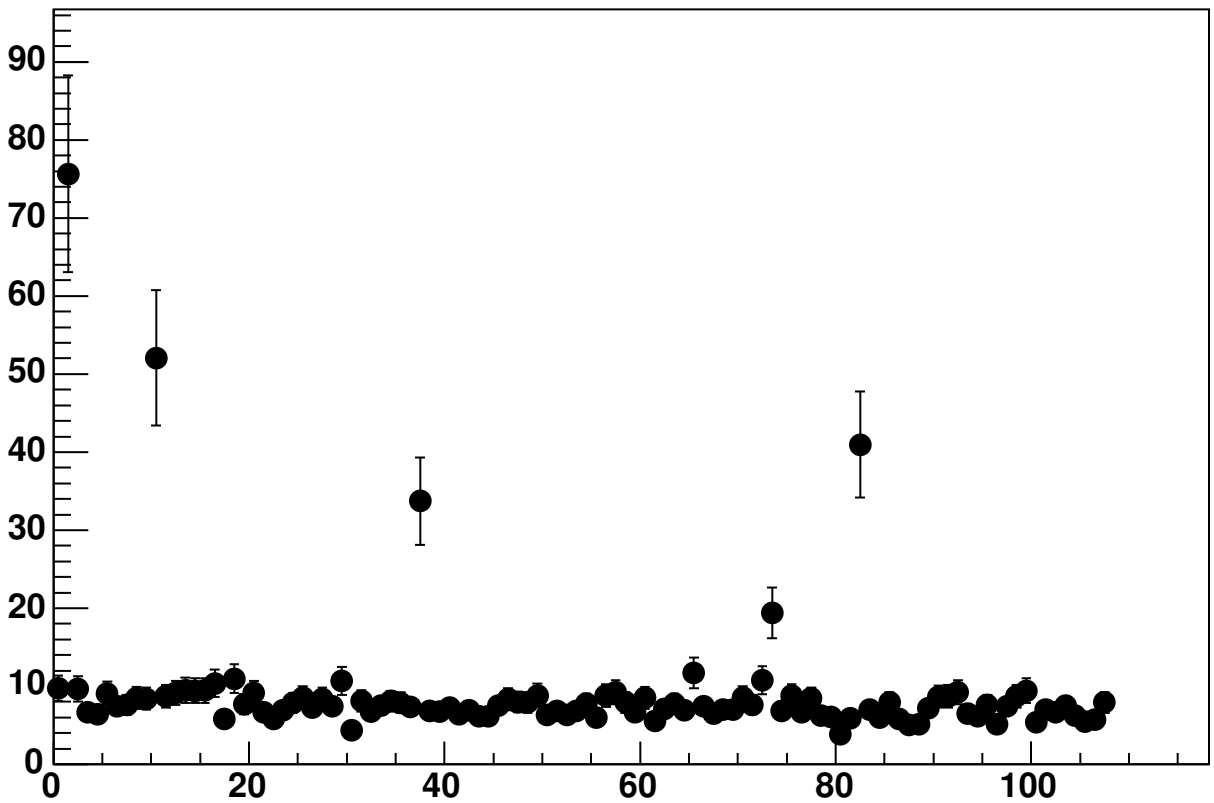
Enable 3, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



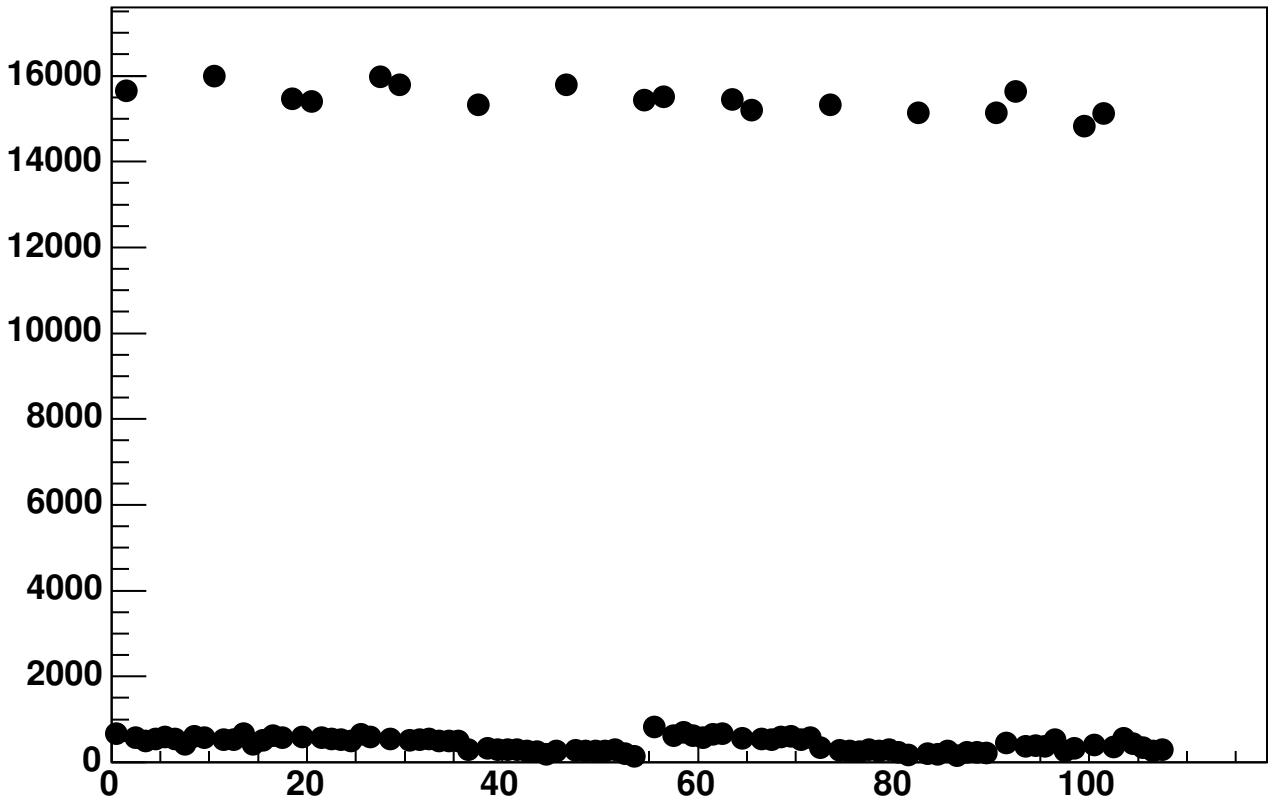
Enable 3, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



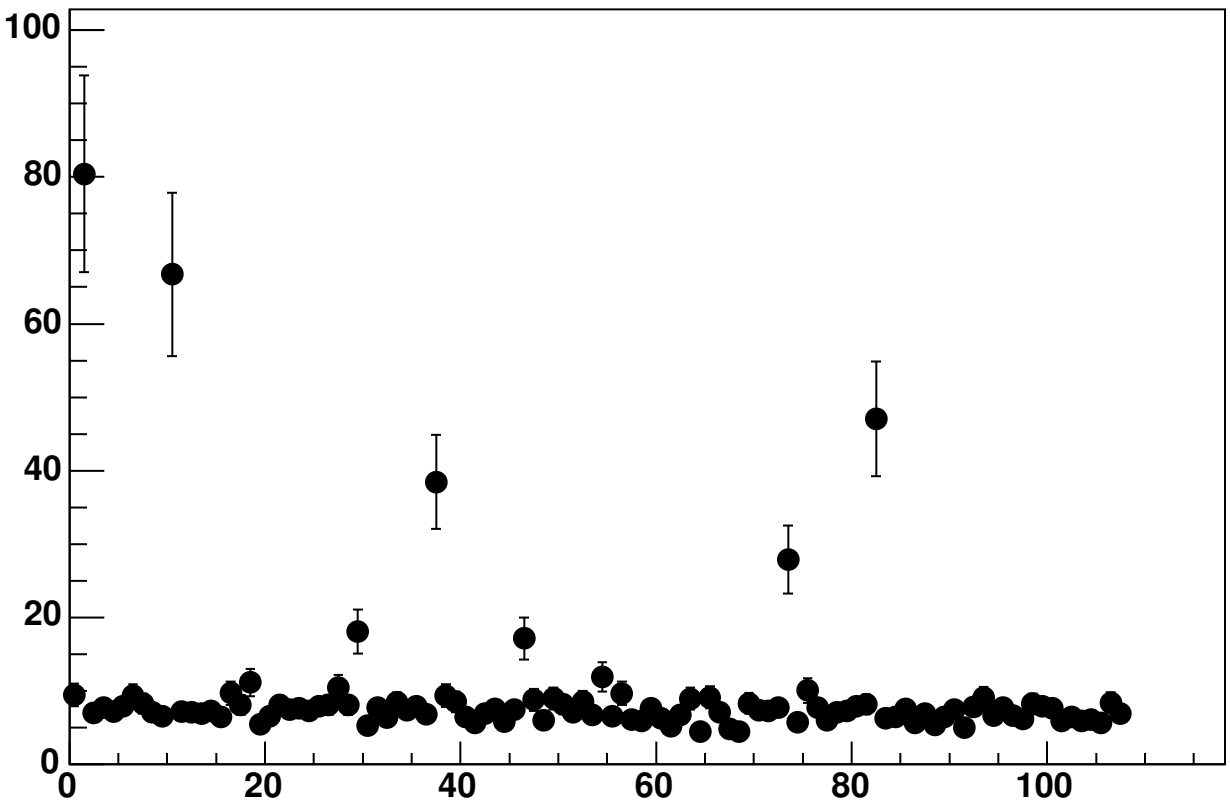
Enable 3, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



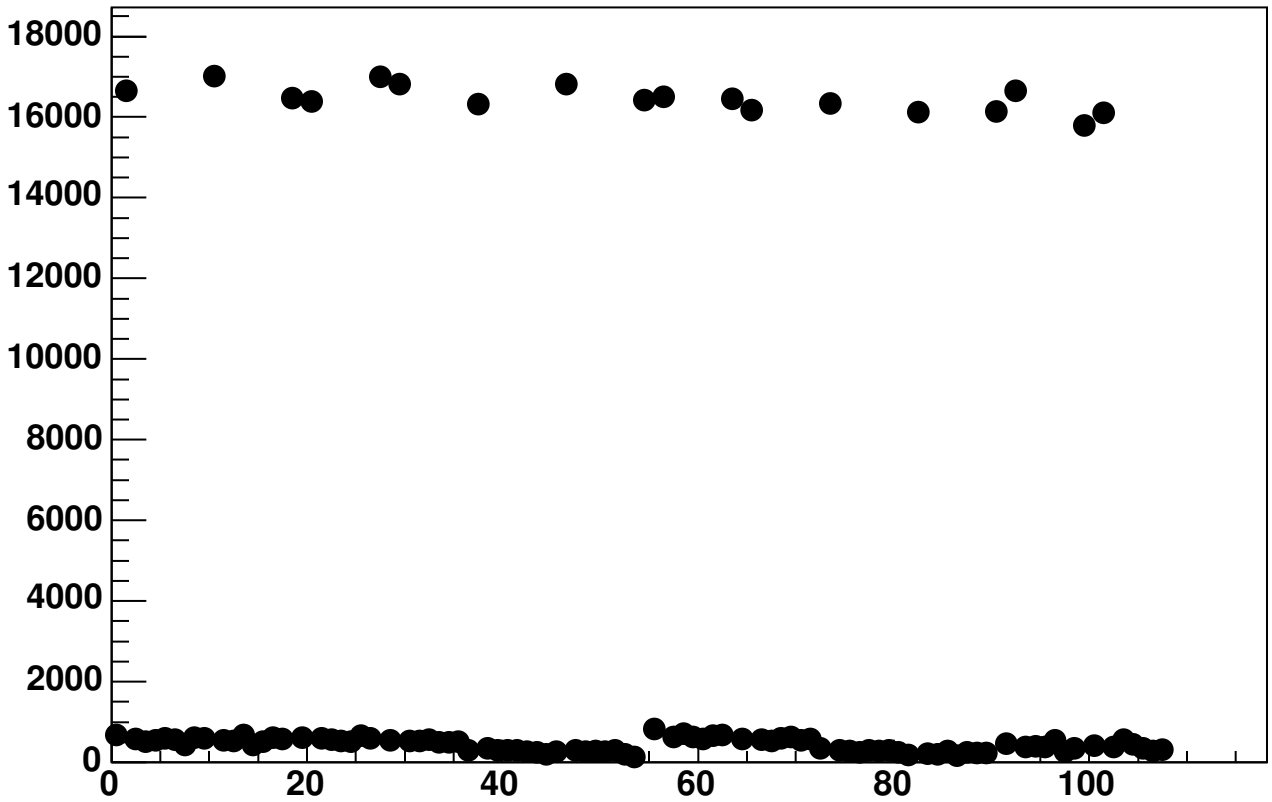
Enable 3, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



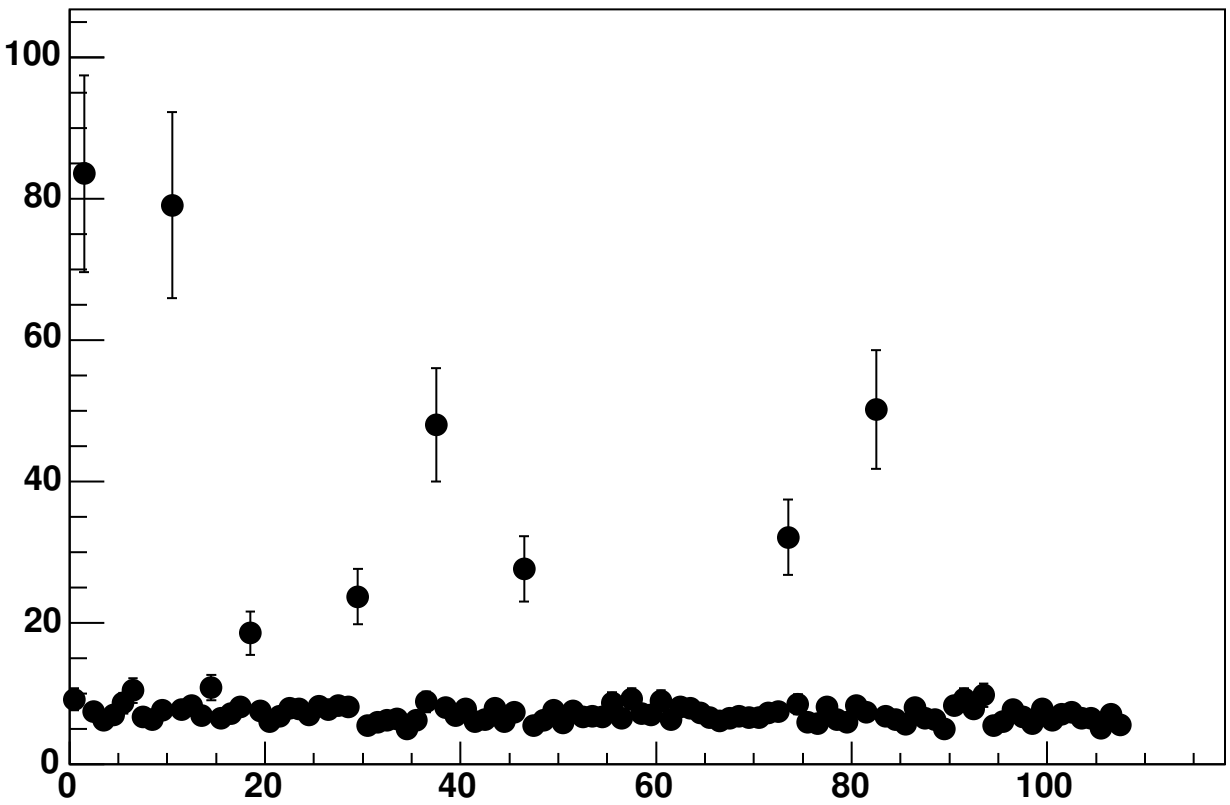
Enable 3, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



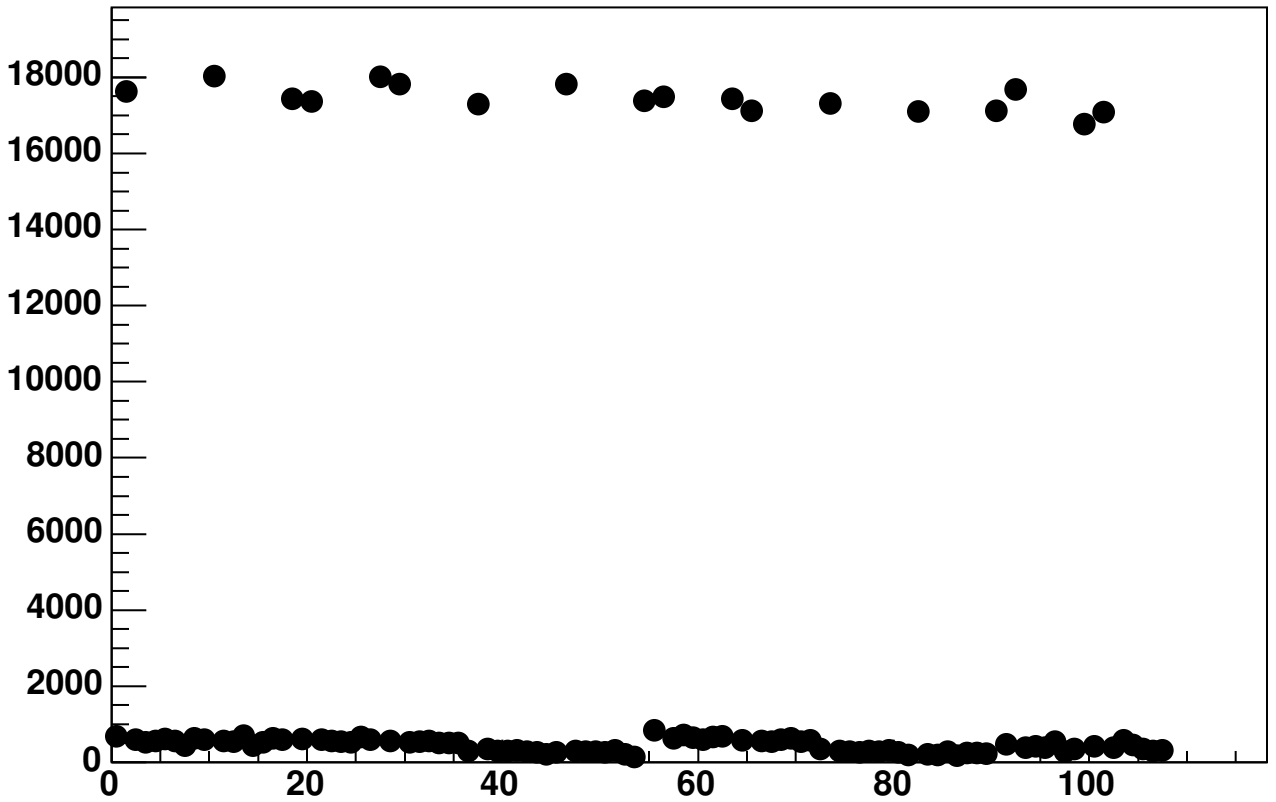
Enable 3, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



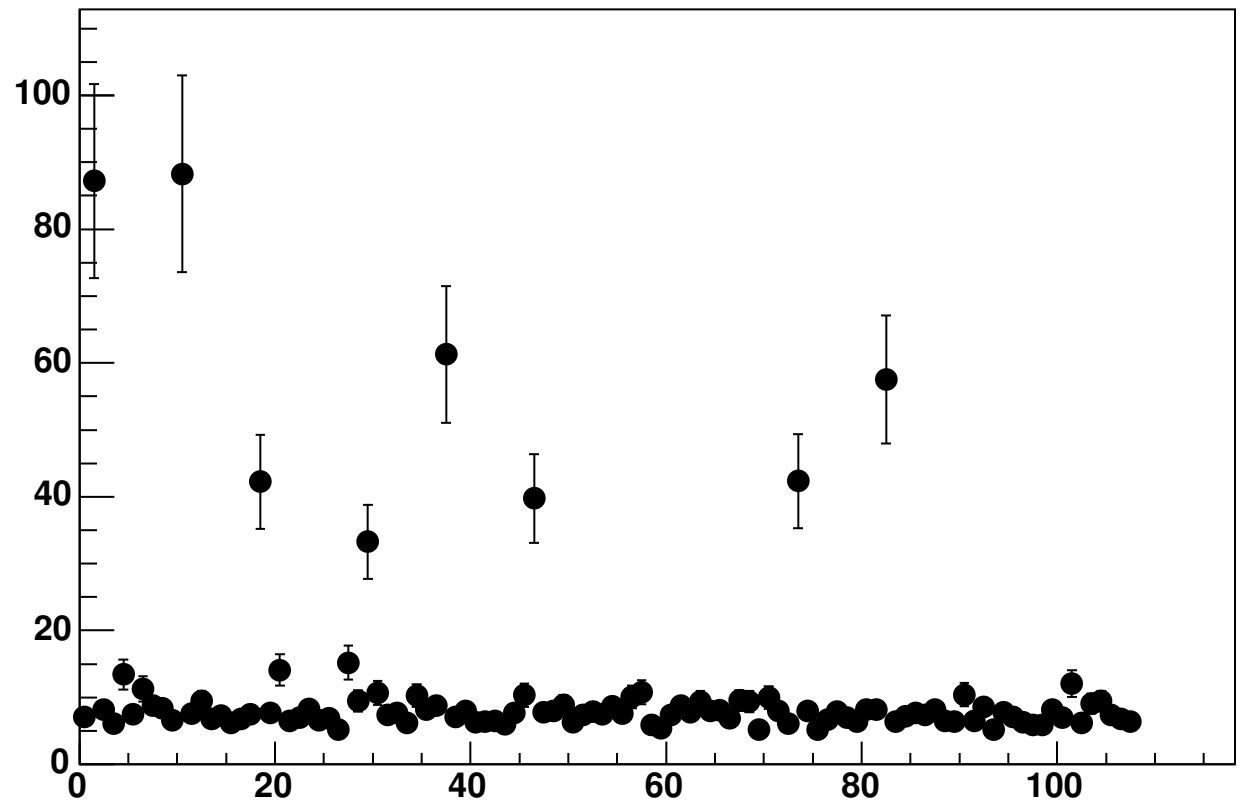
Enable 3, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



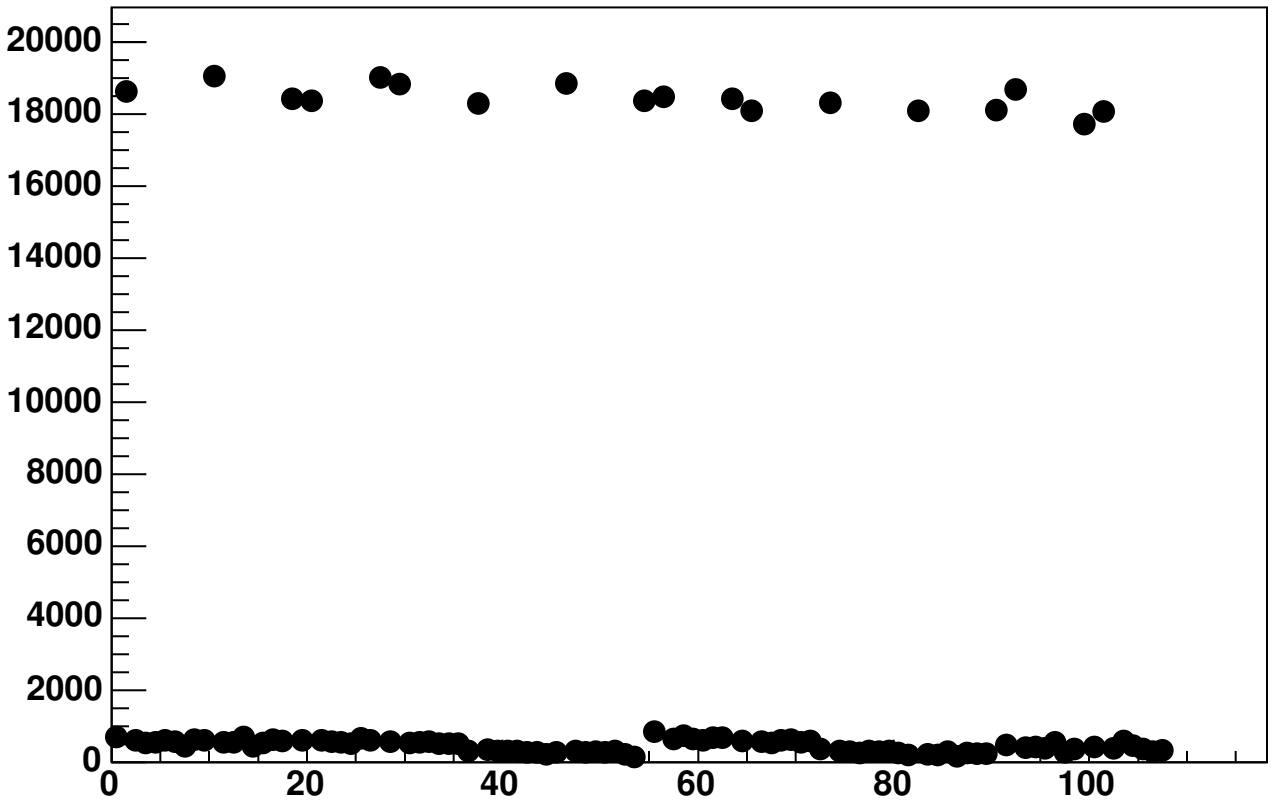
Enable 3, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



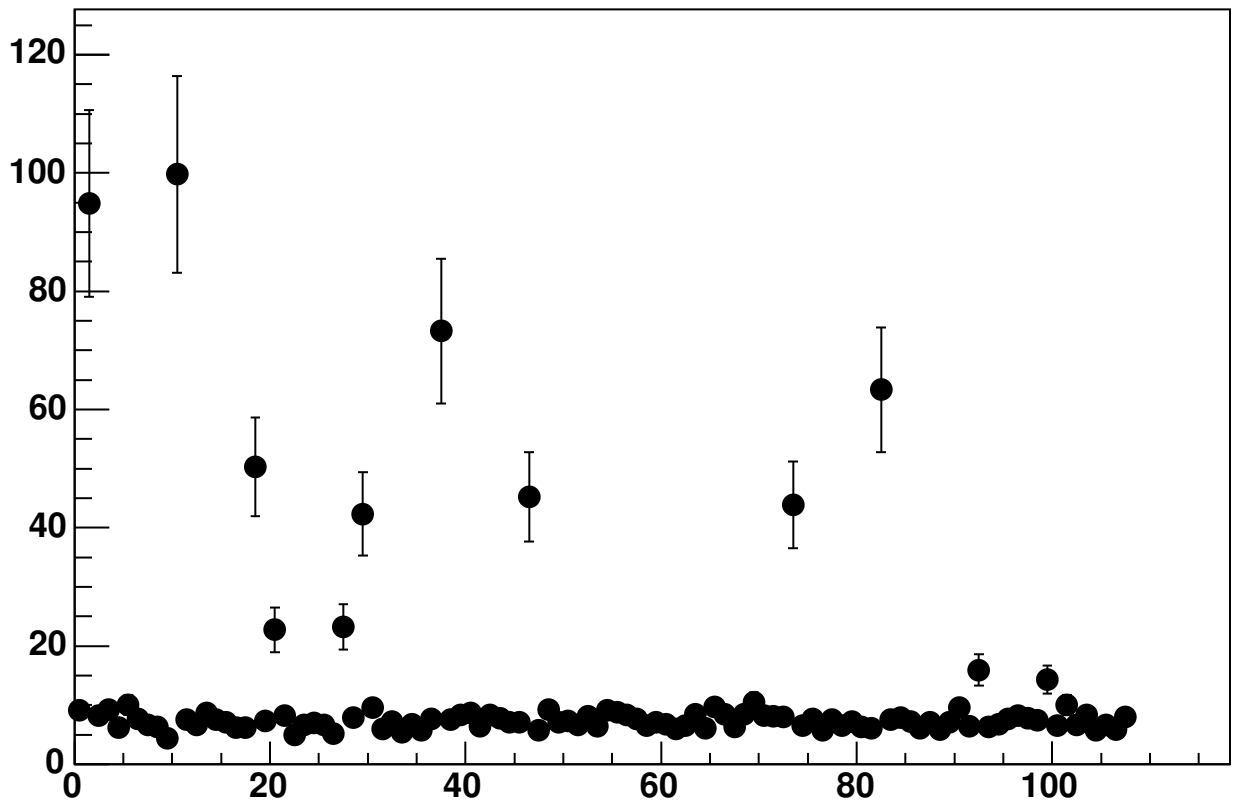
Enable 3, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



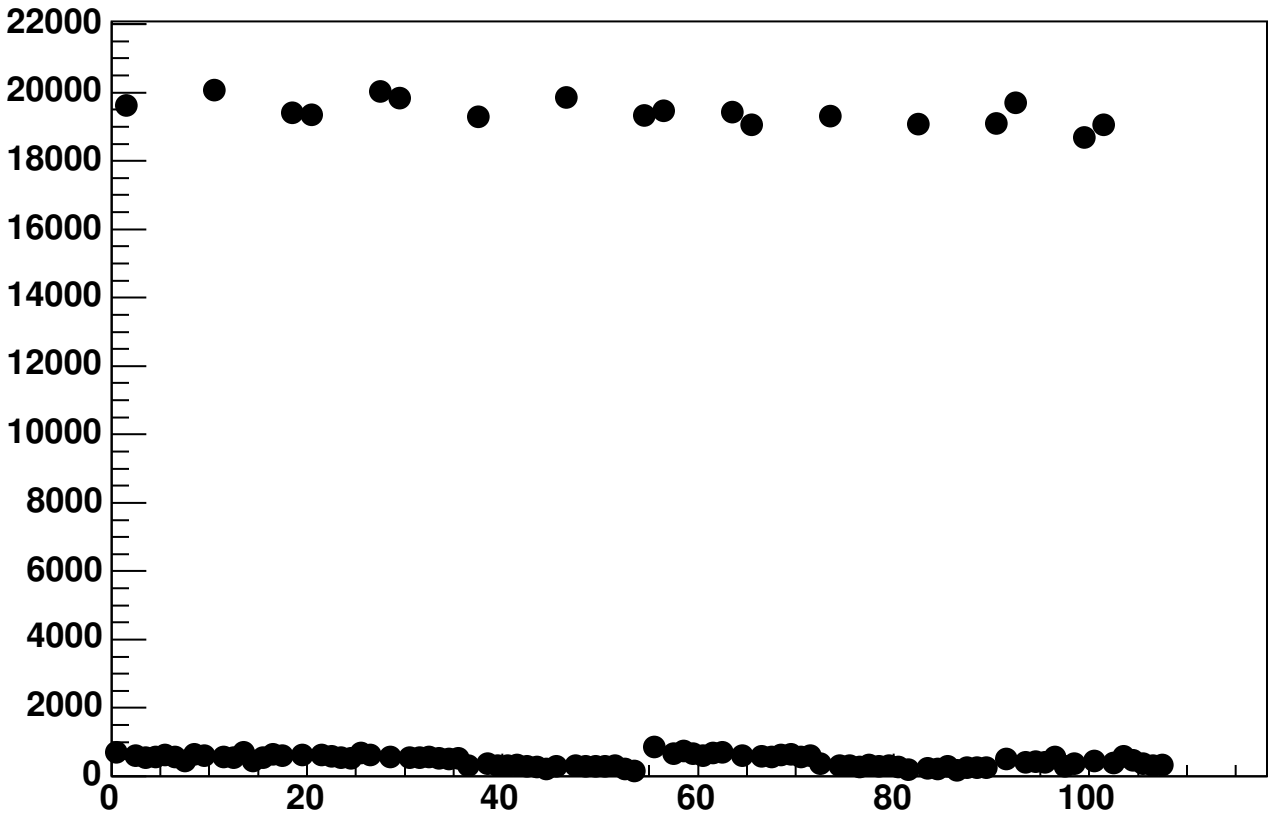
Enable 3, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



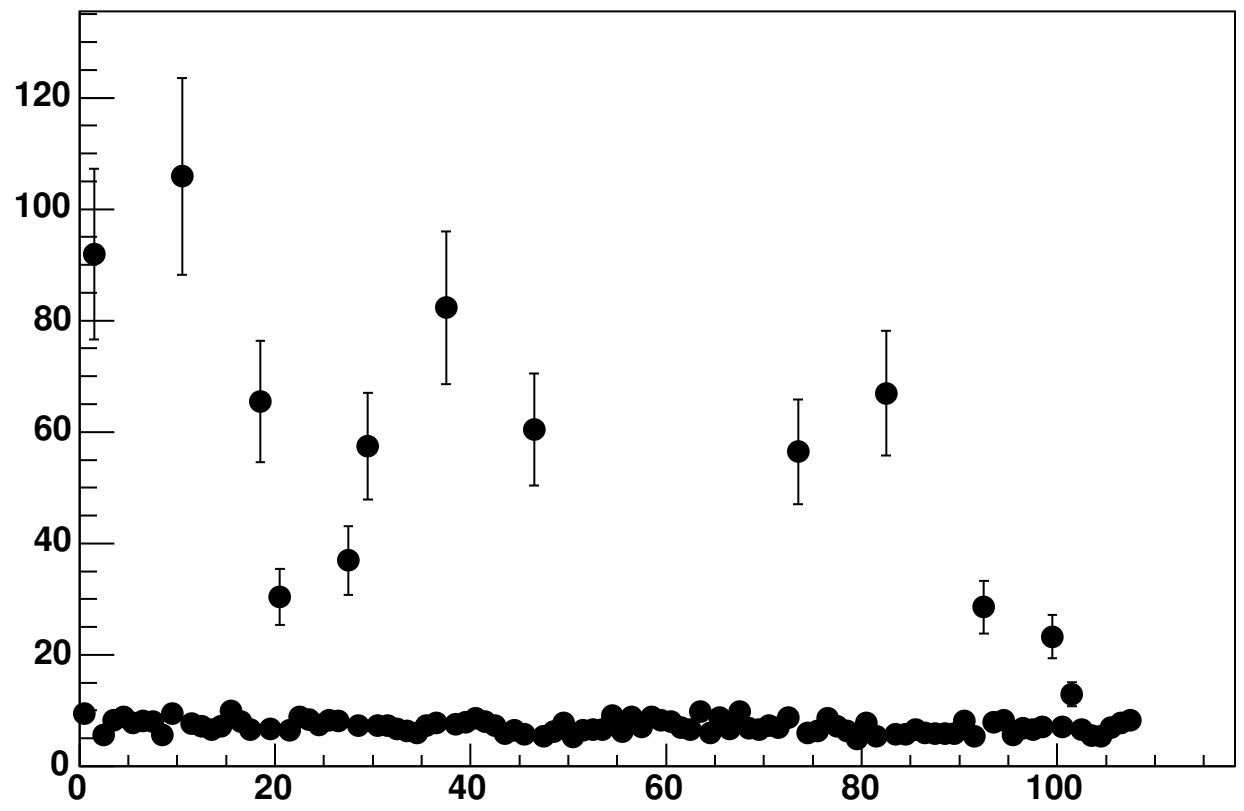
Enable 3, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



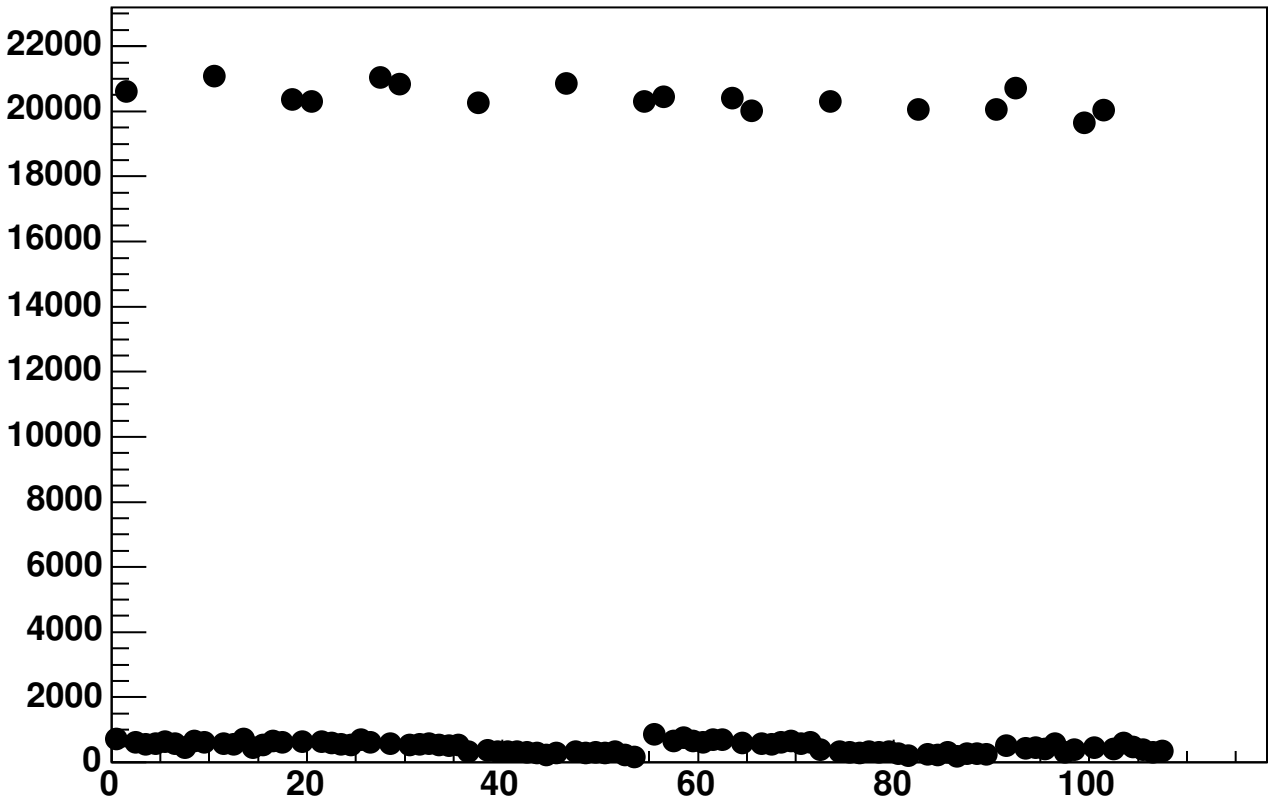
Enable 3, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



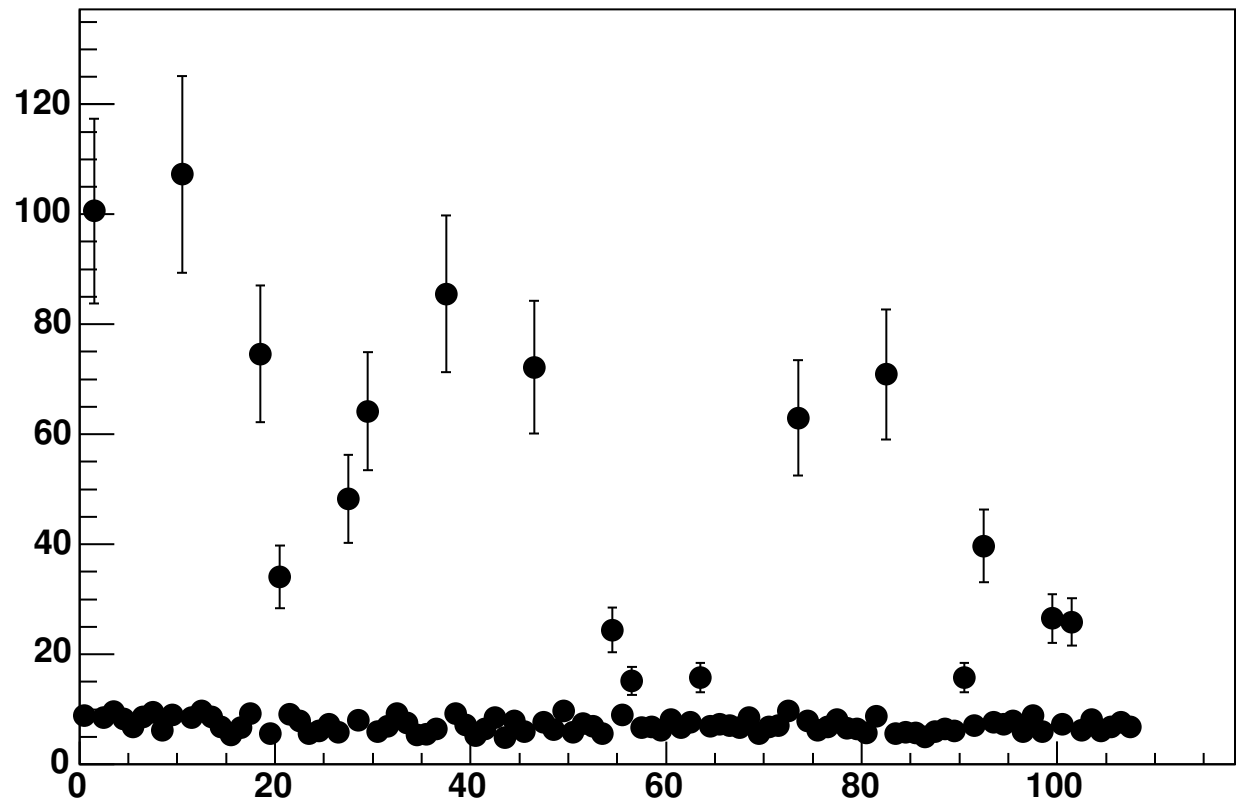
Enable 3, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



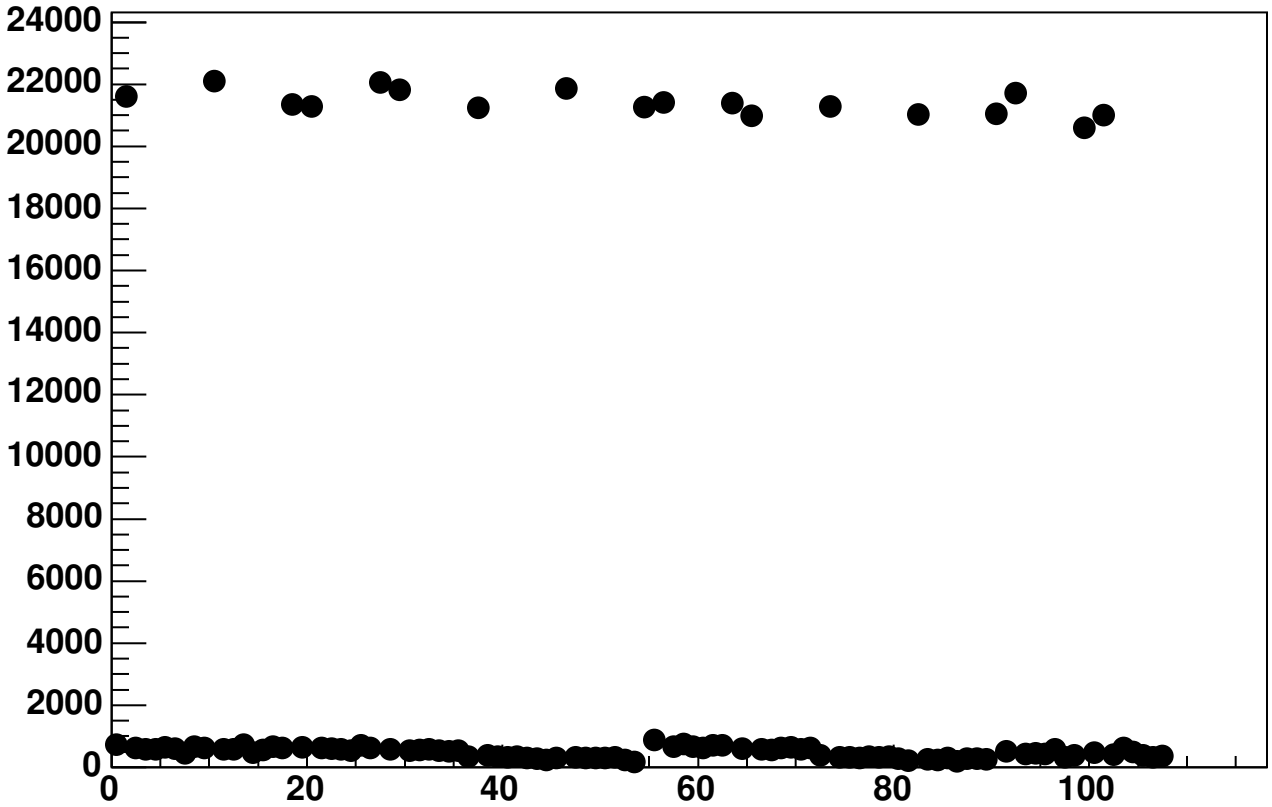
Enable 3, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



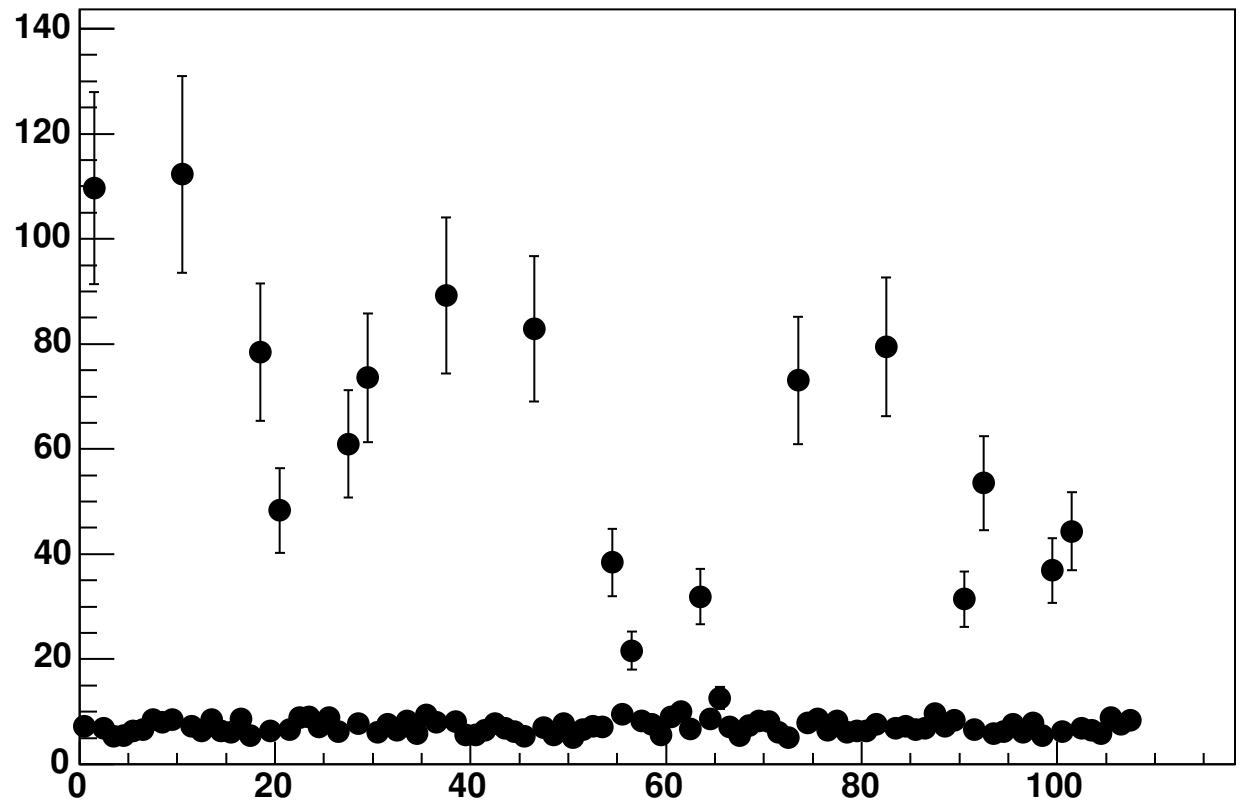
Enable 3, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



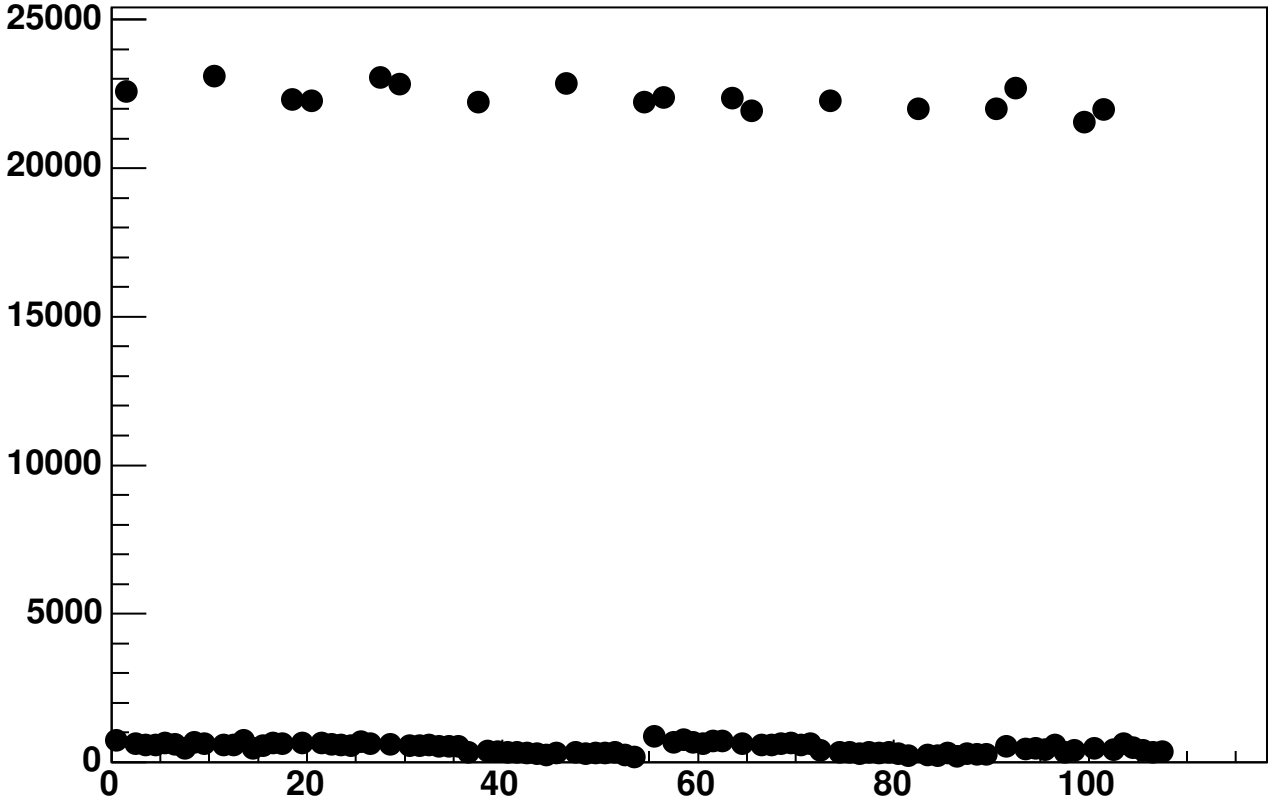
Enable 3, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



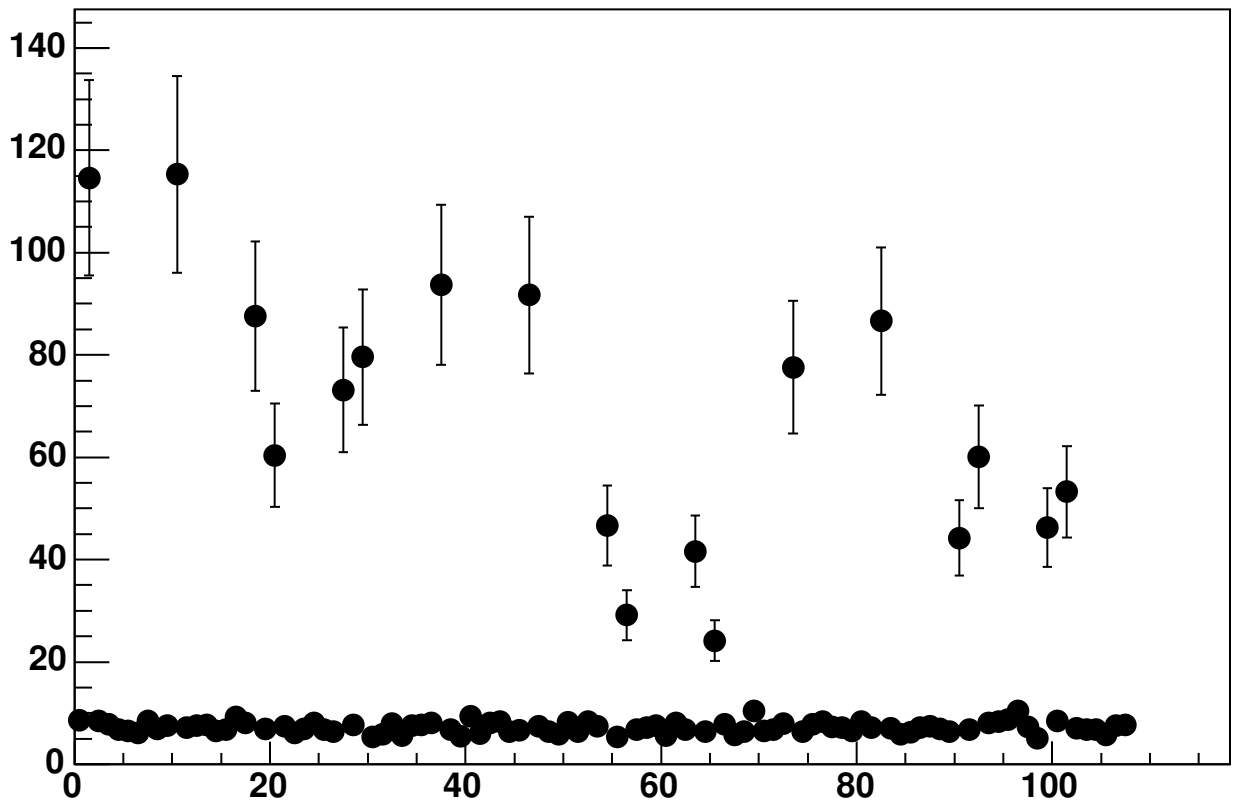
Enable 3, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



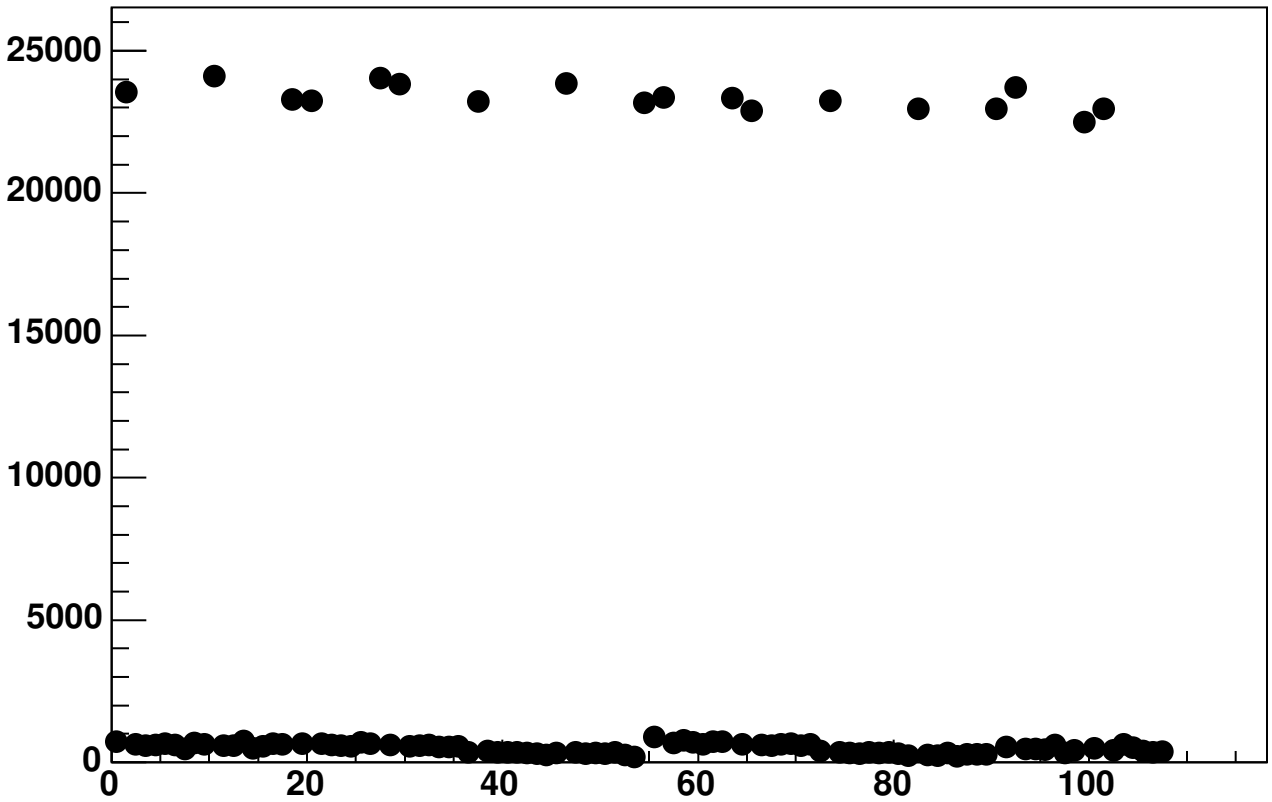
Enable 3, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



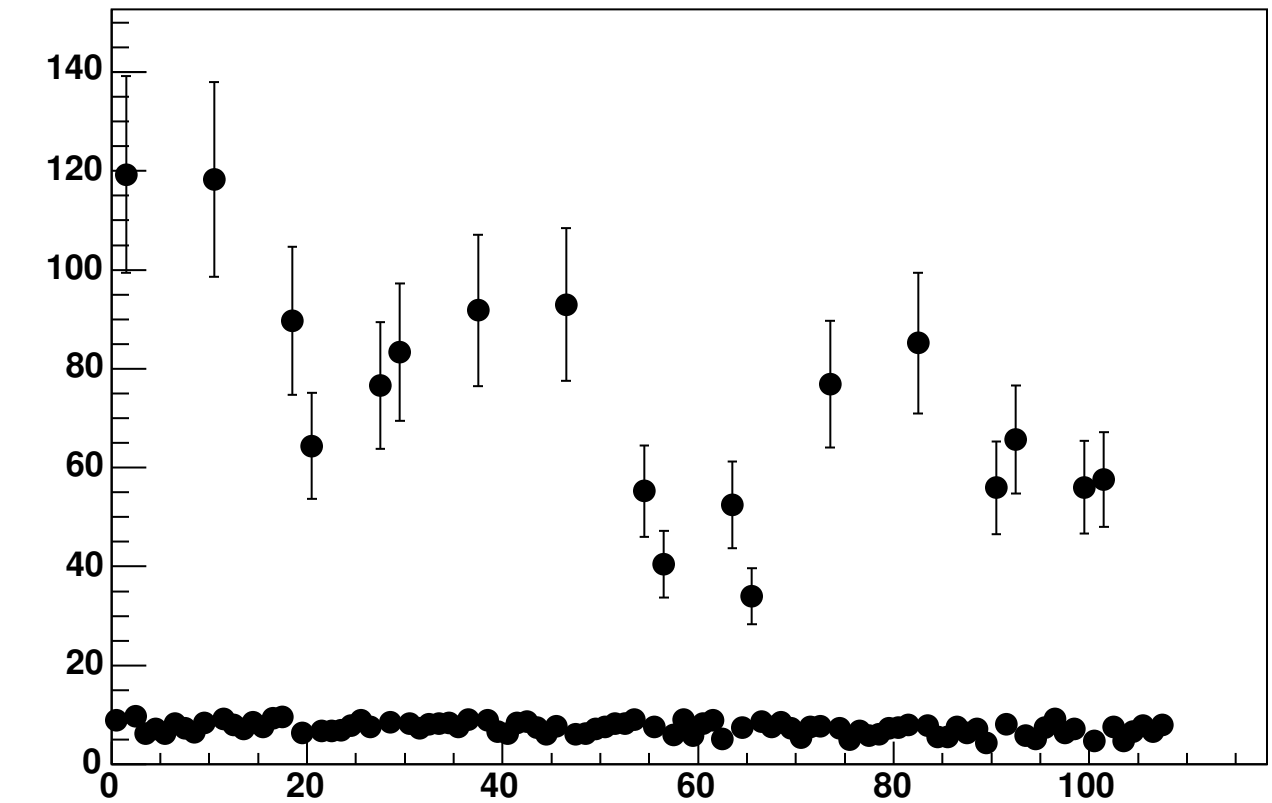
Enable 3, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



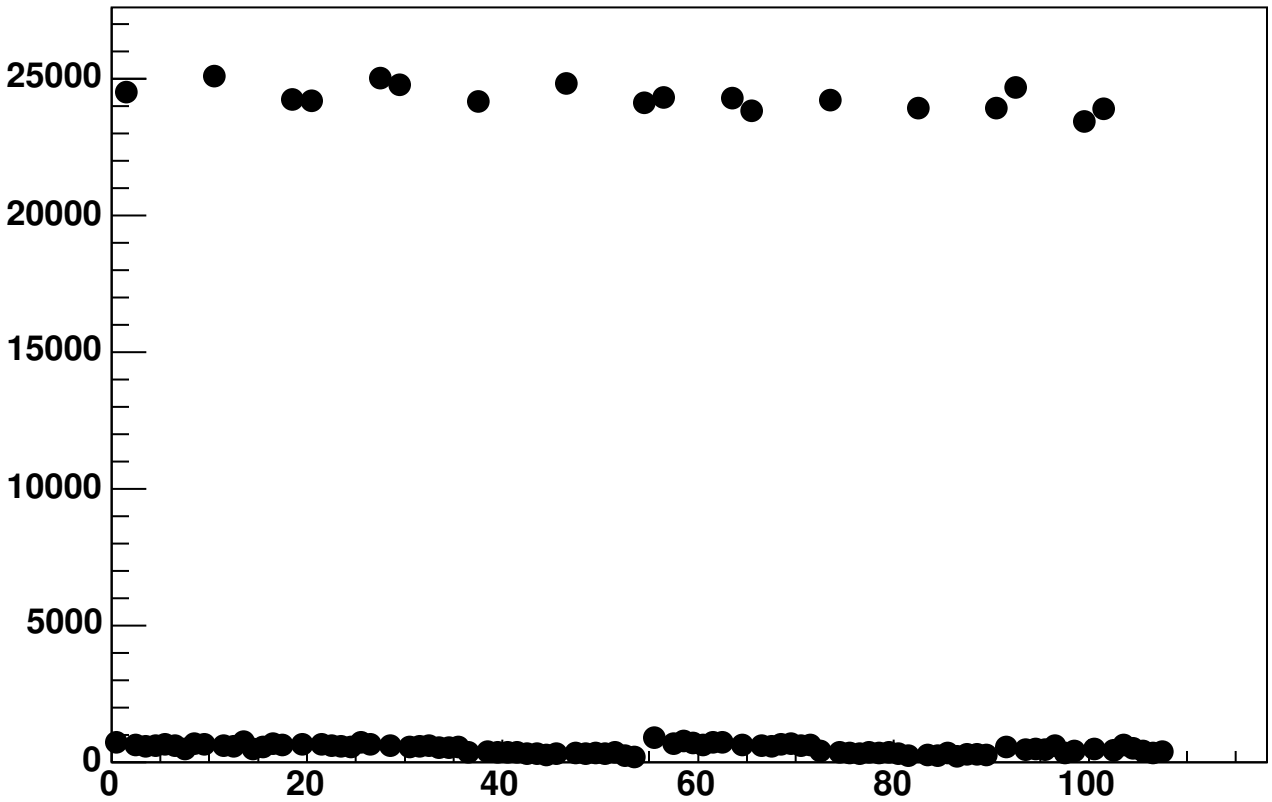
Enable 3, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



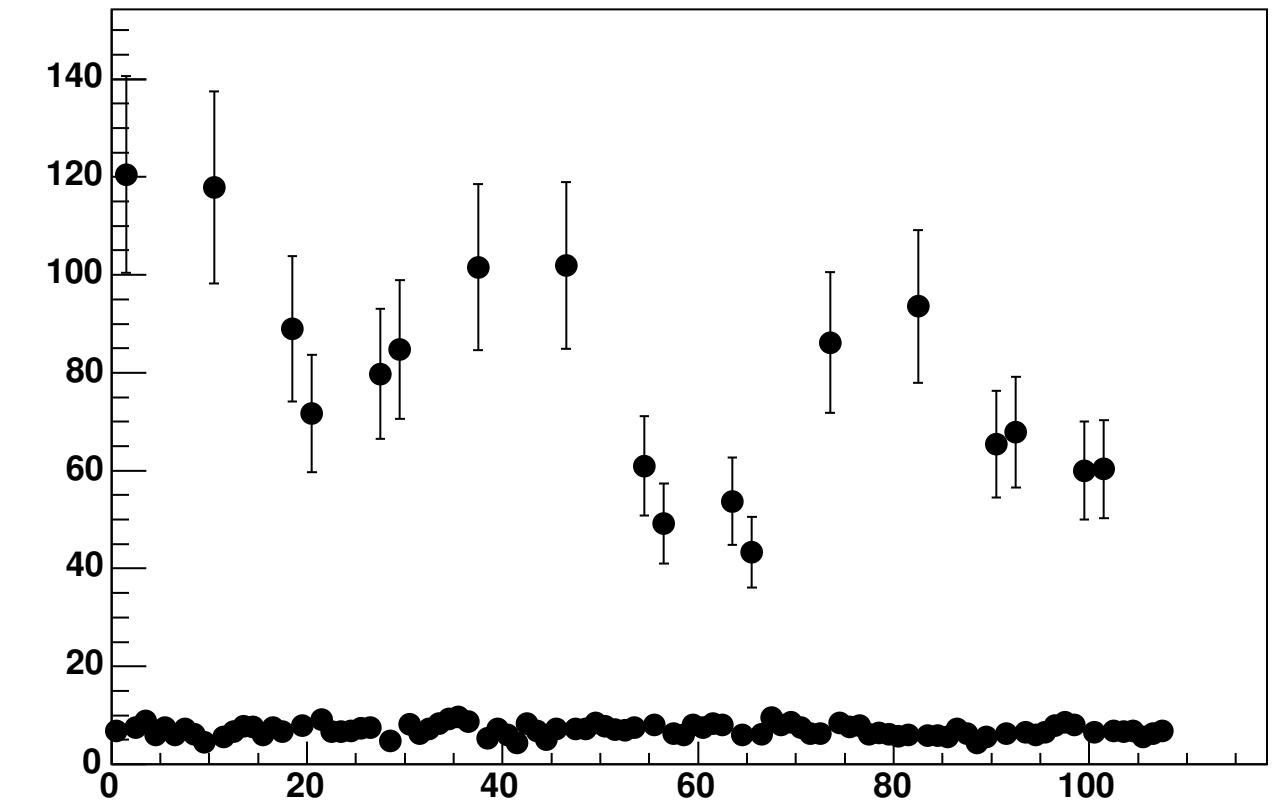
Enable 3, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



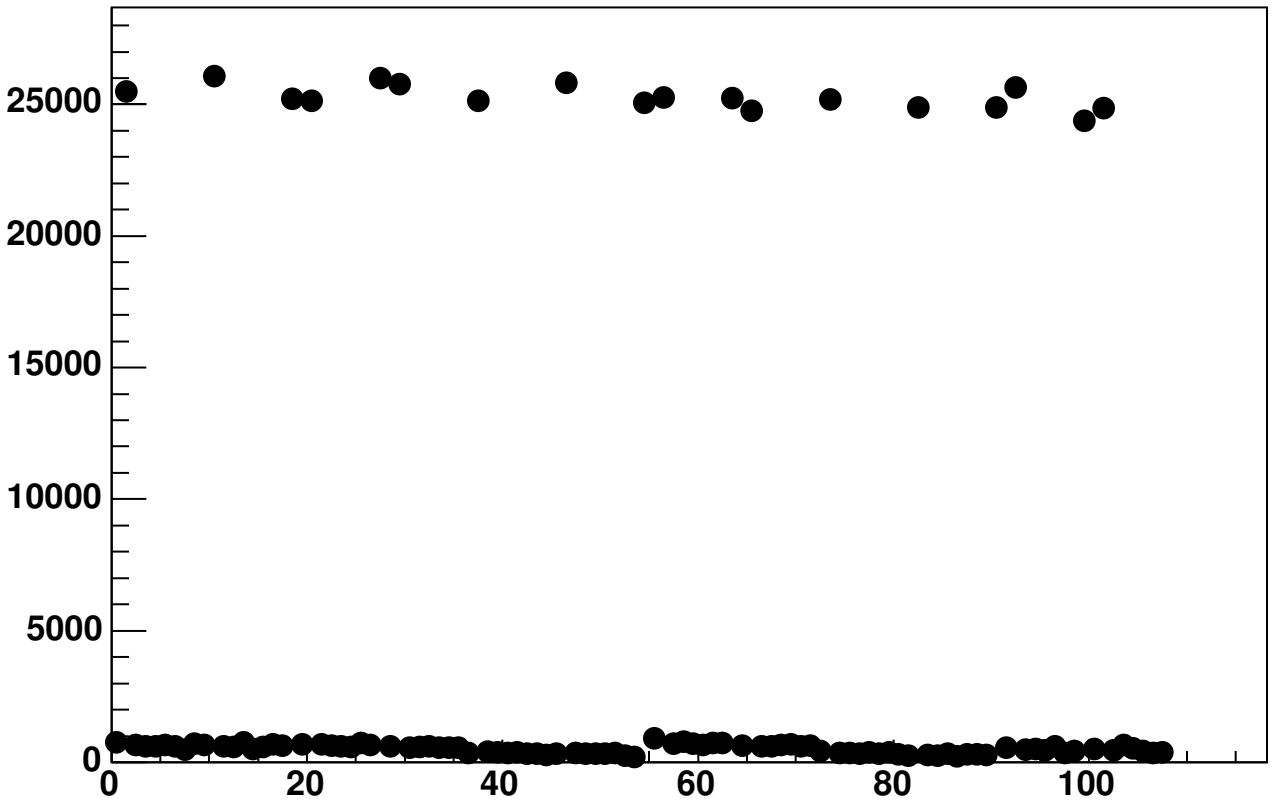
Enable 3, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



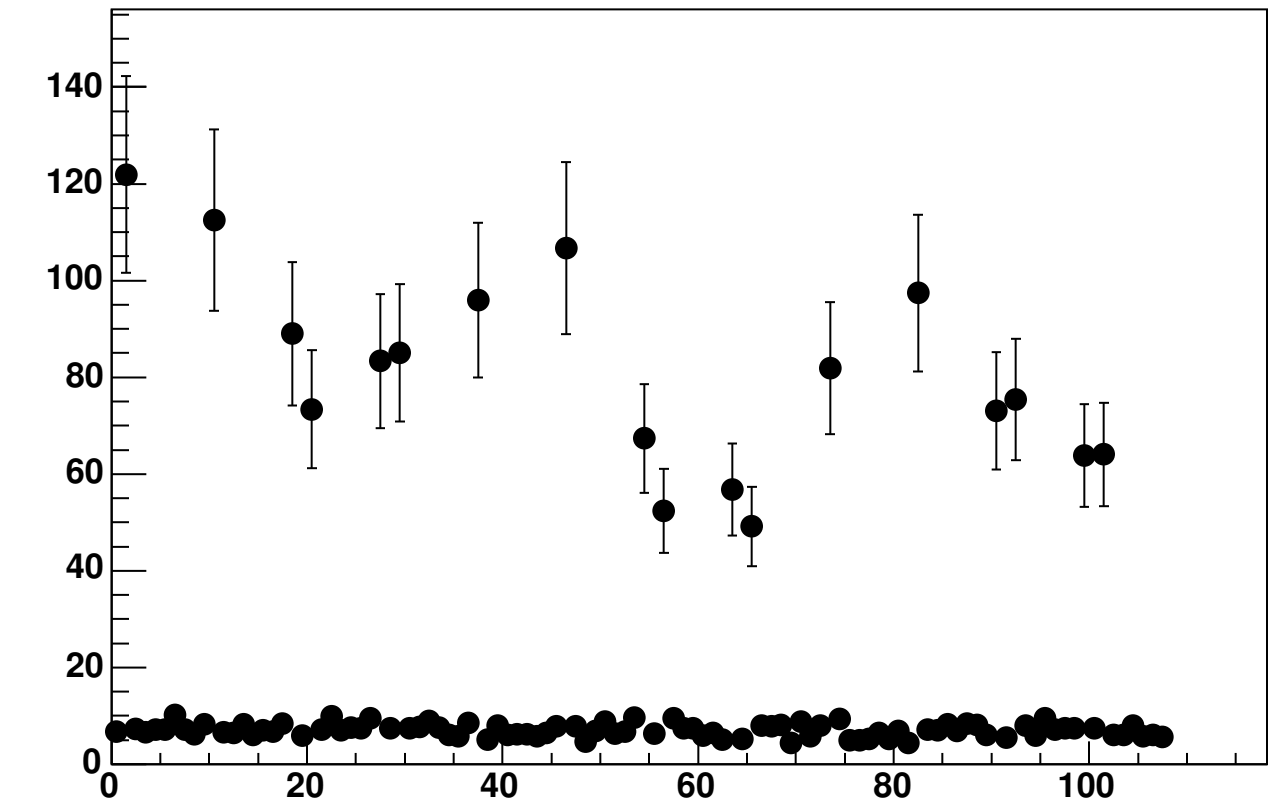
Enable 3, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



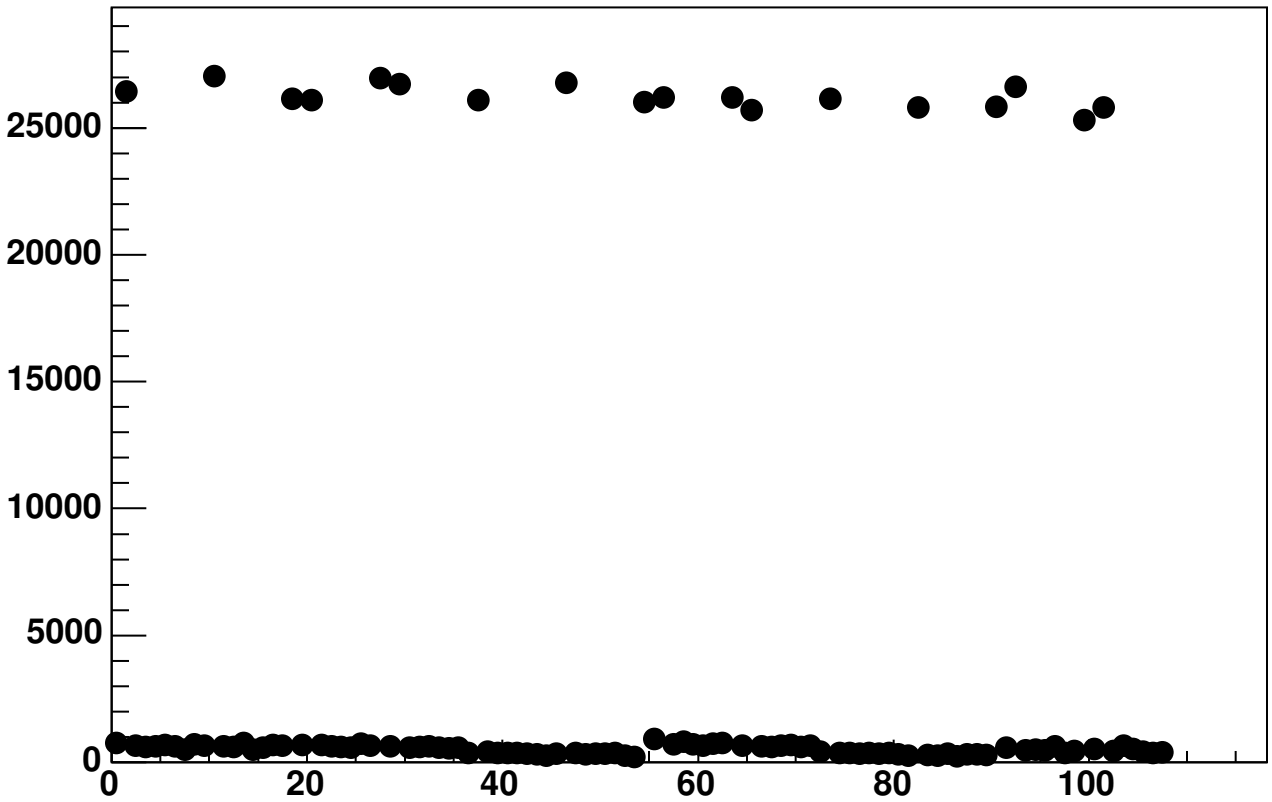
Enable 3, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



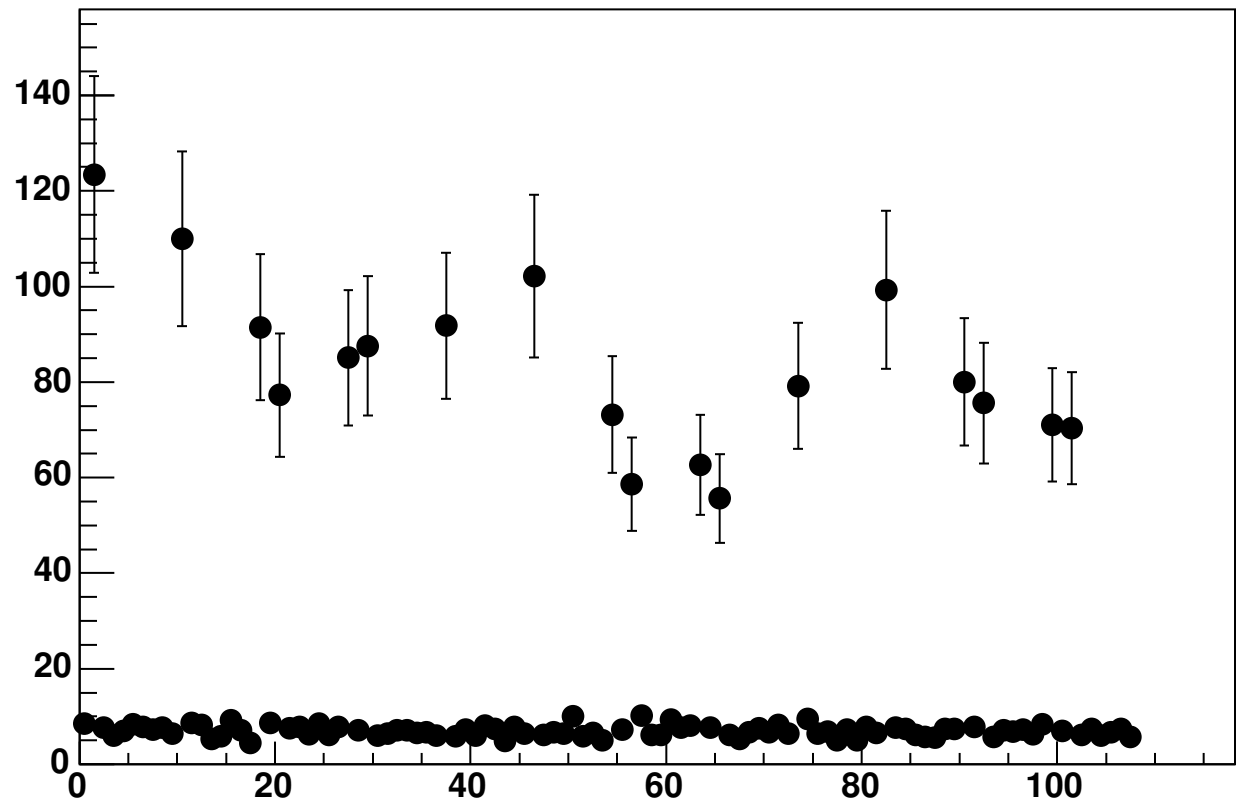
Enable 3, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



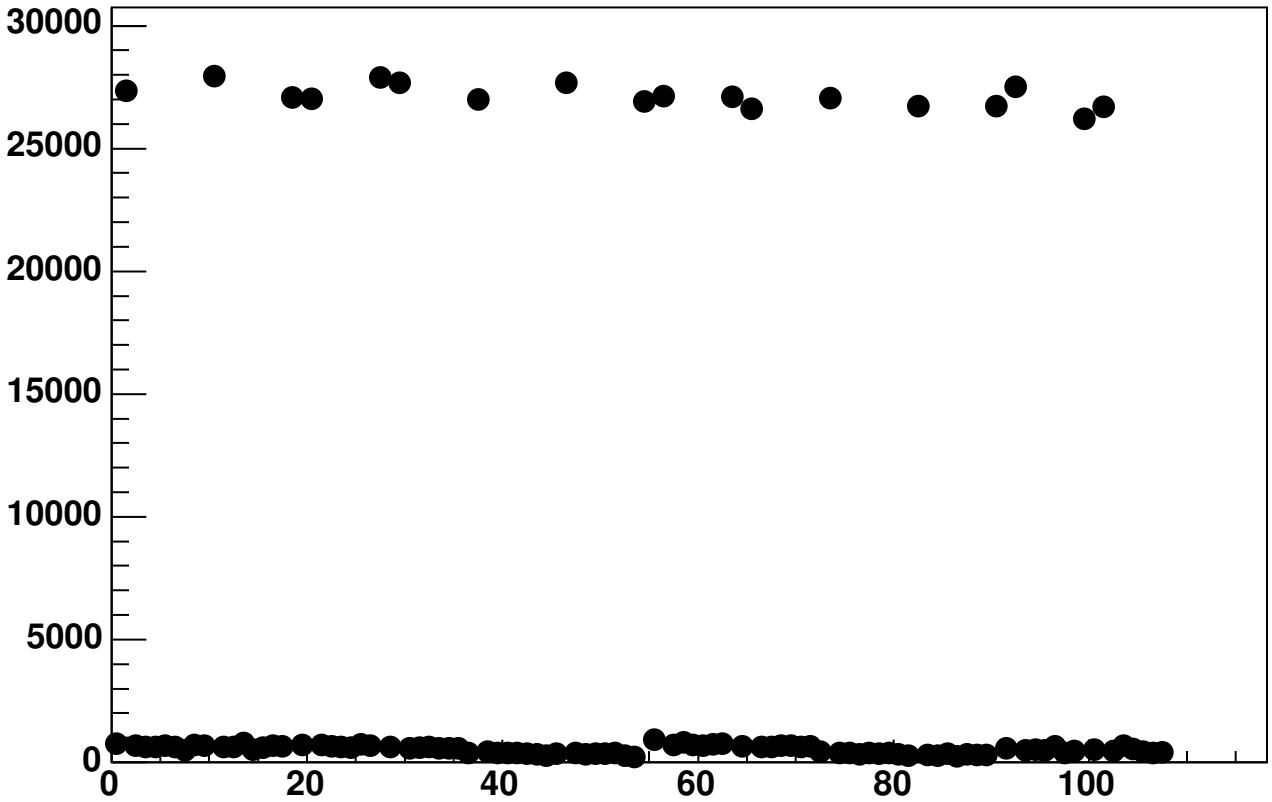
Enable 3, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



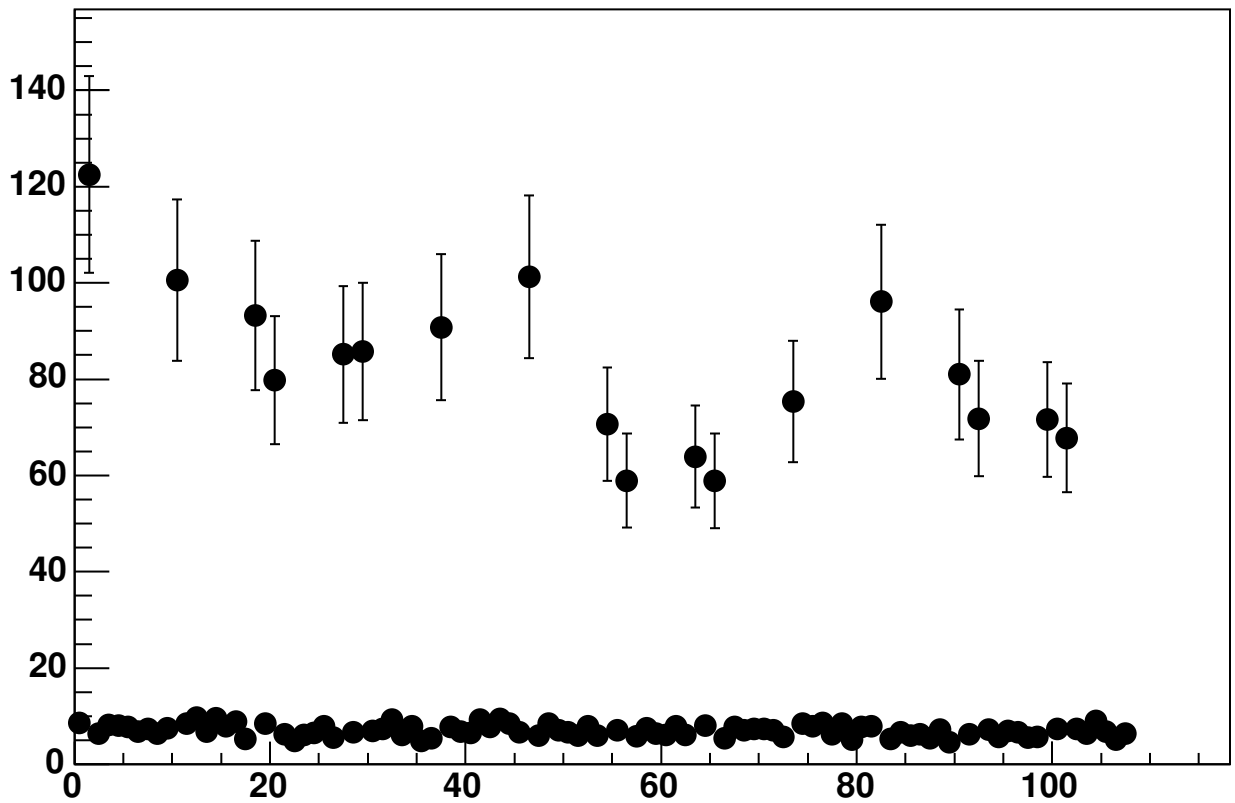
Enable 3, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



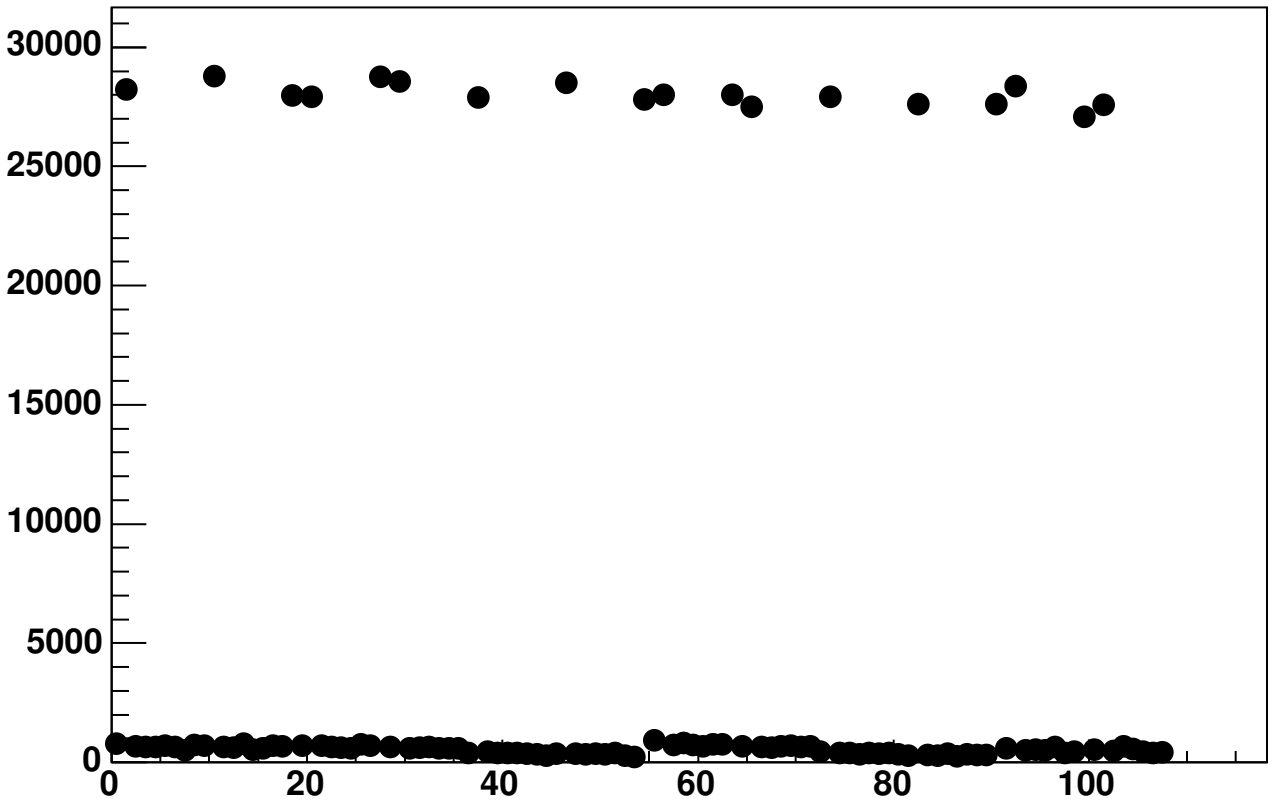
Enable 3, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



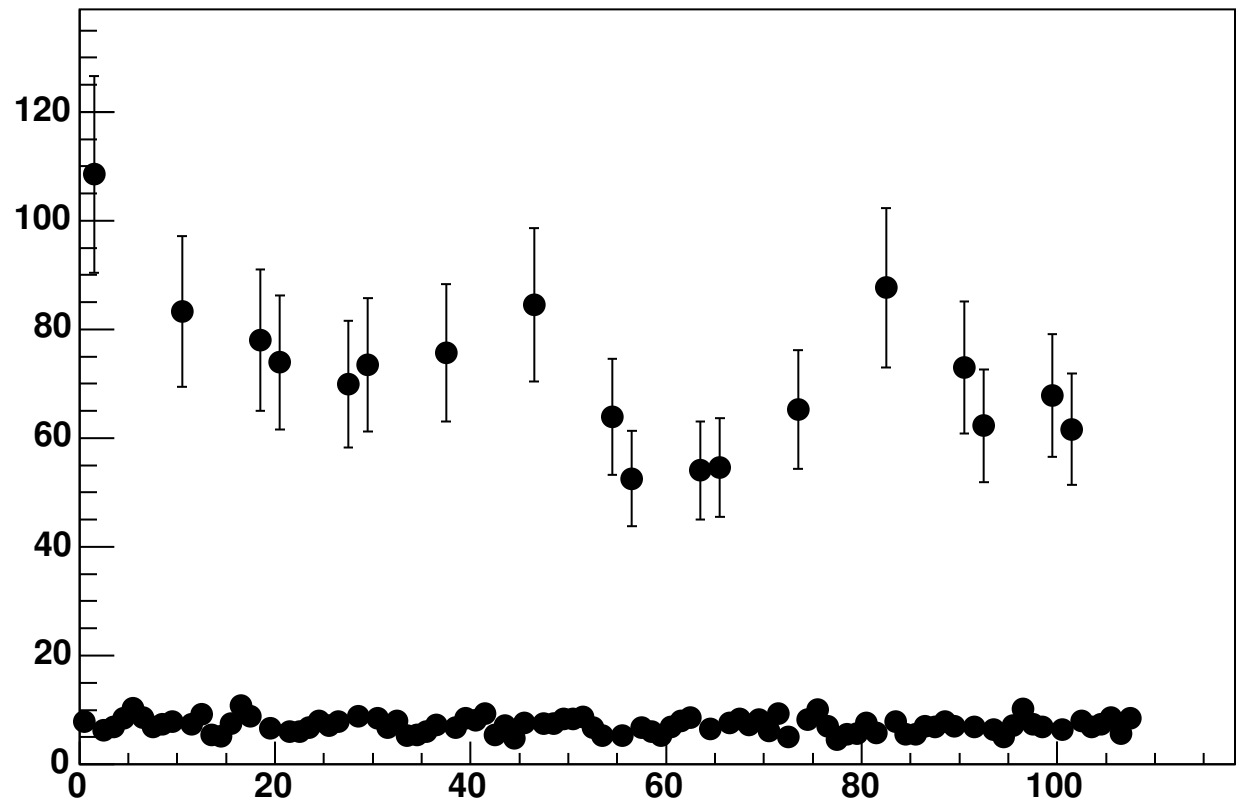
Enable 3, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



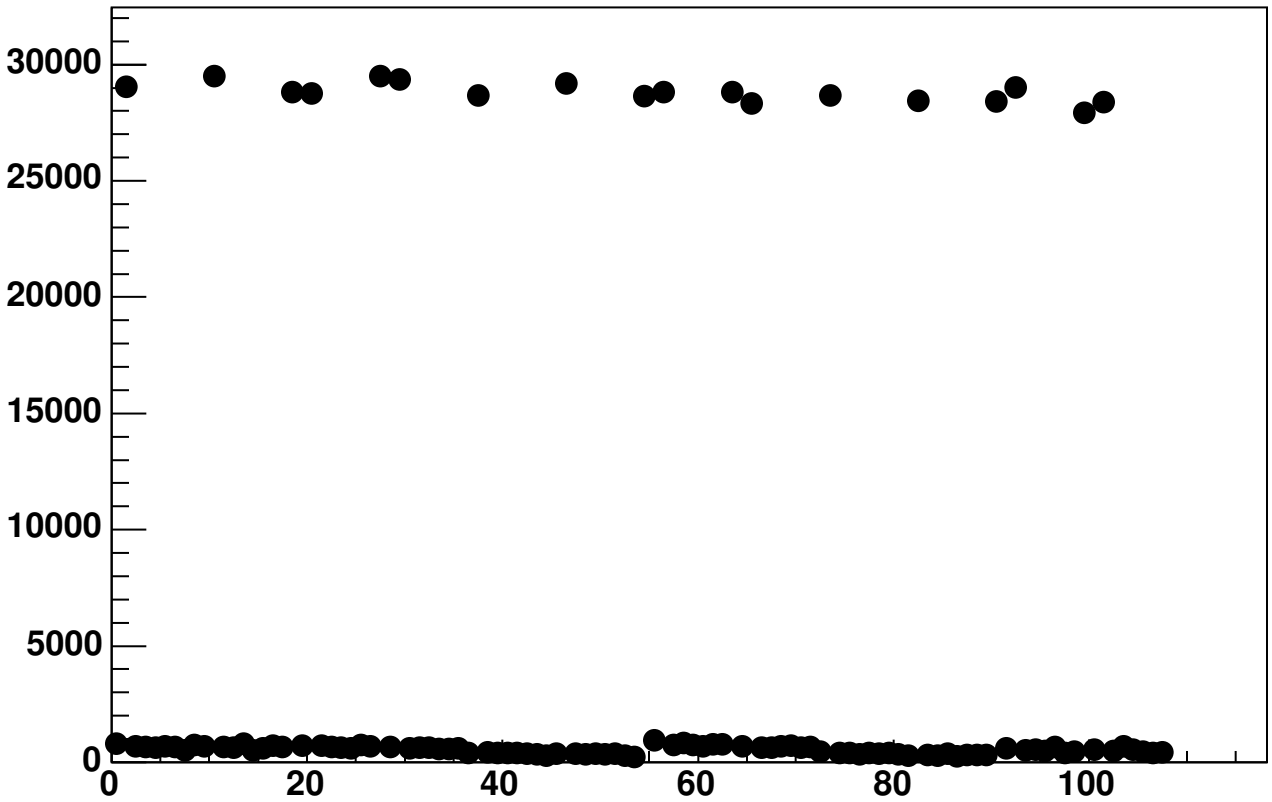
Enable 3, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



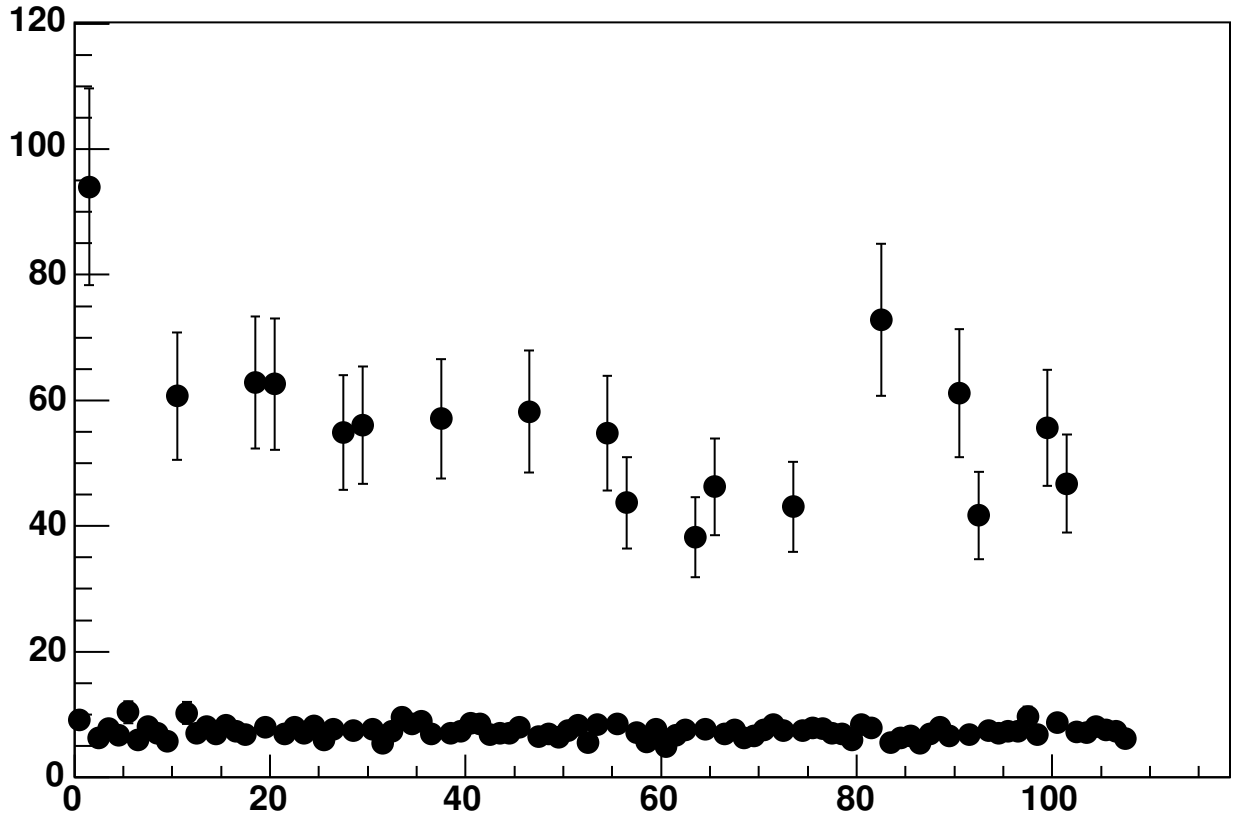
Enable 3, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



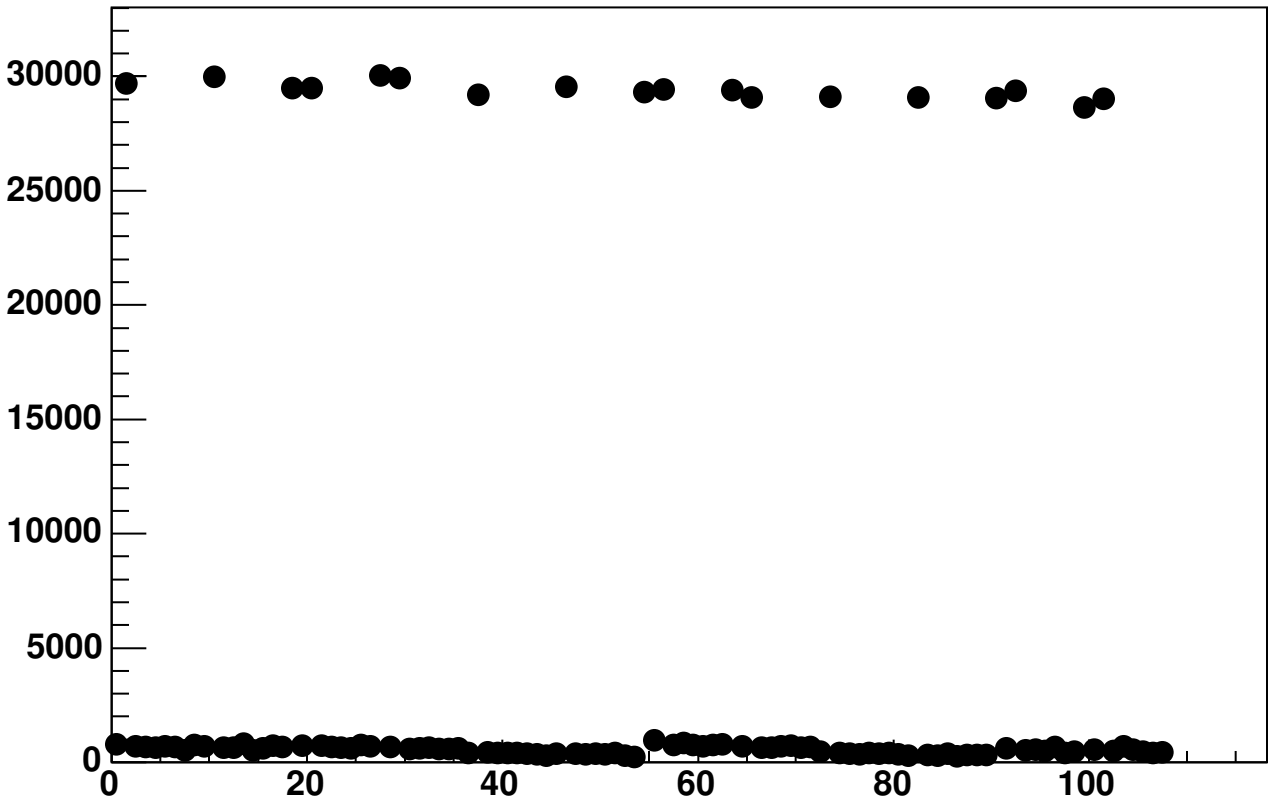
Enable 3, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



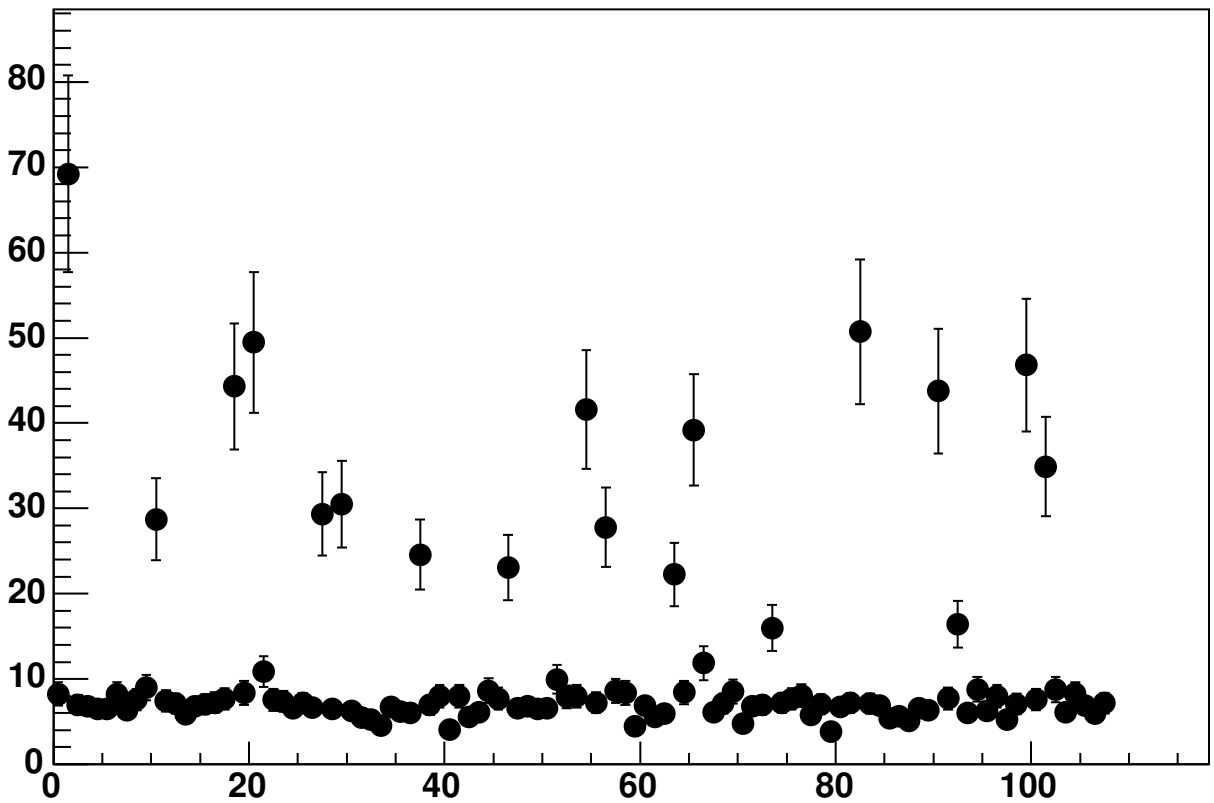
Enable 3, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



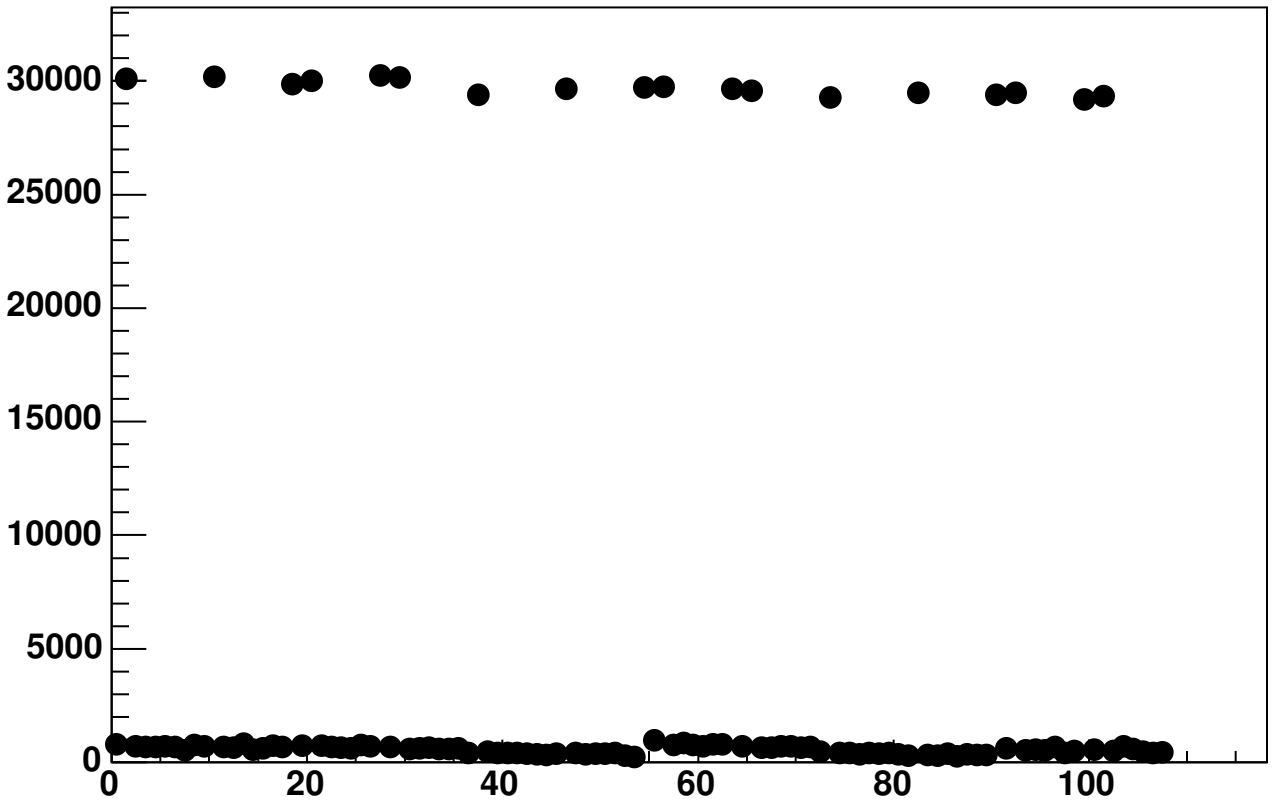
Enable 3, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



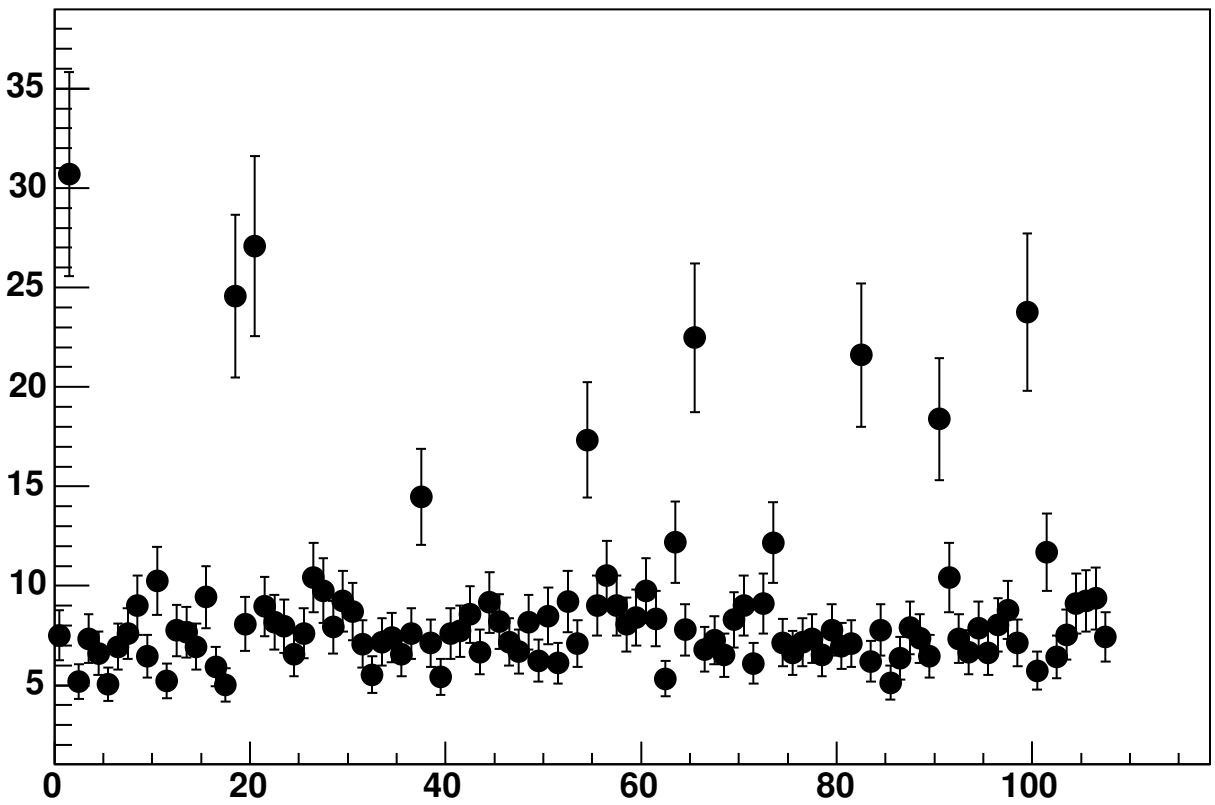
Enable 3, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



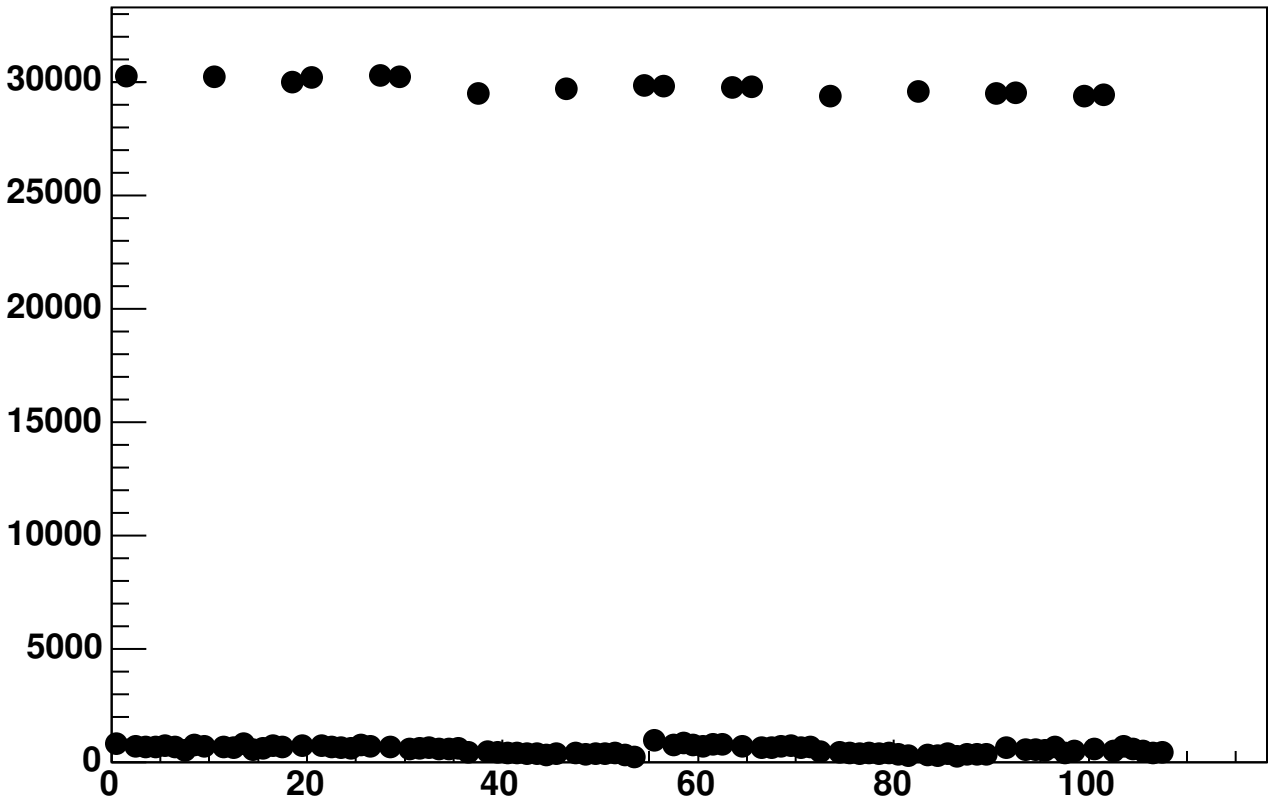
Enable 3, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



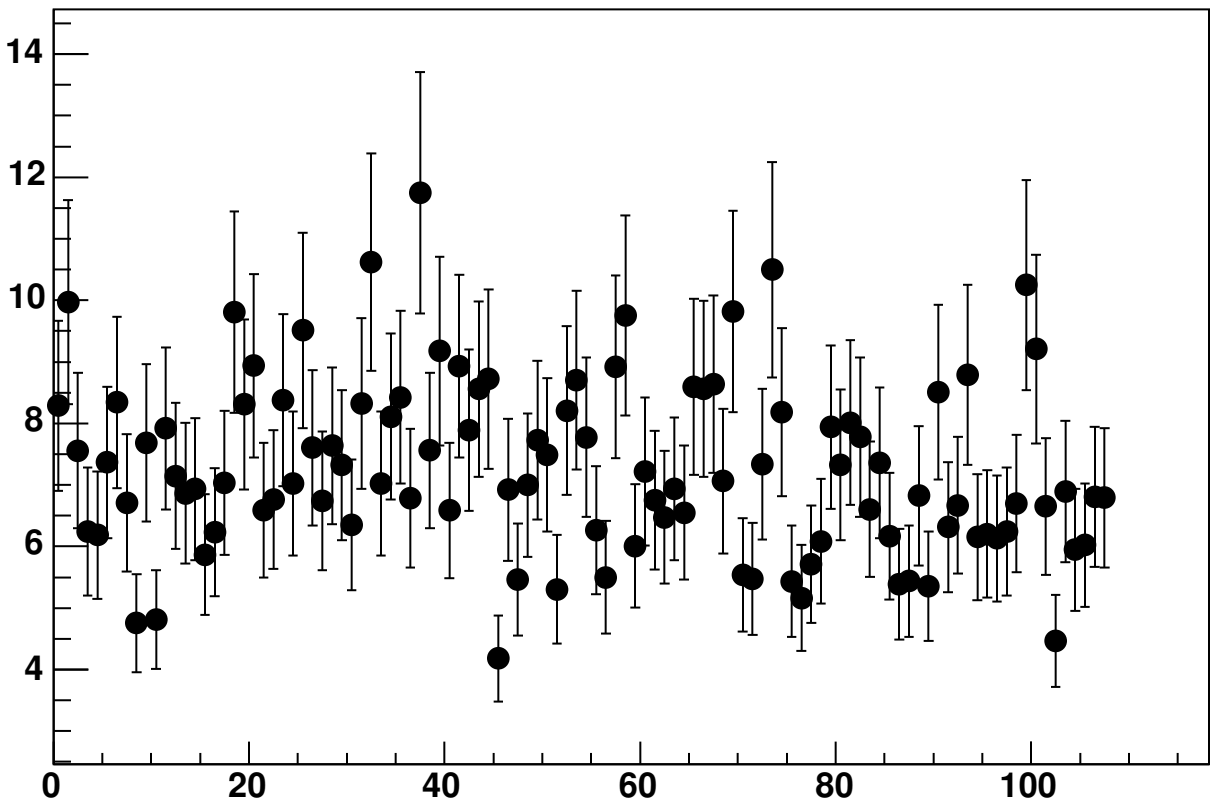
Enable 3, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



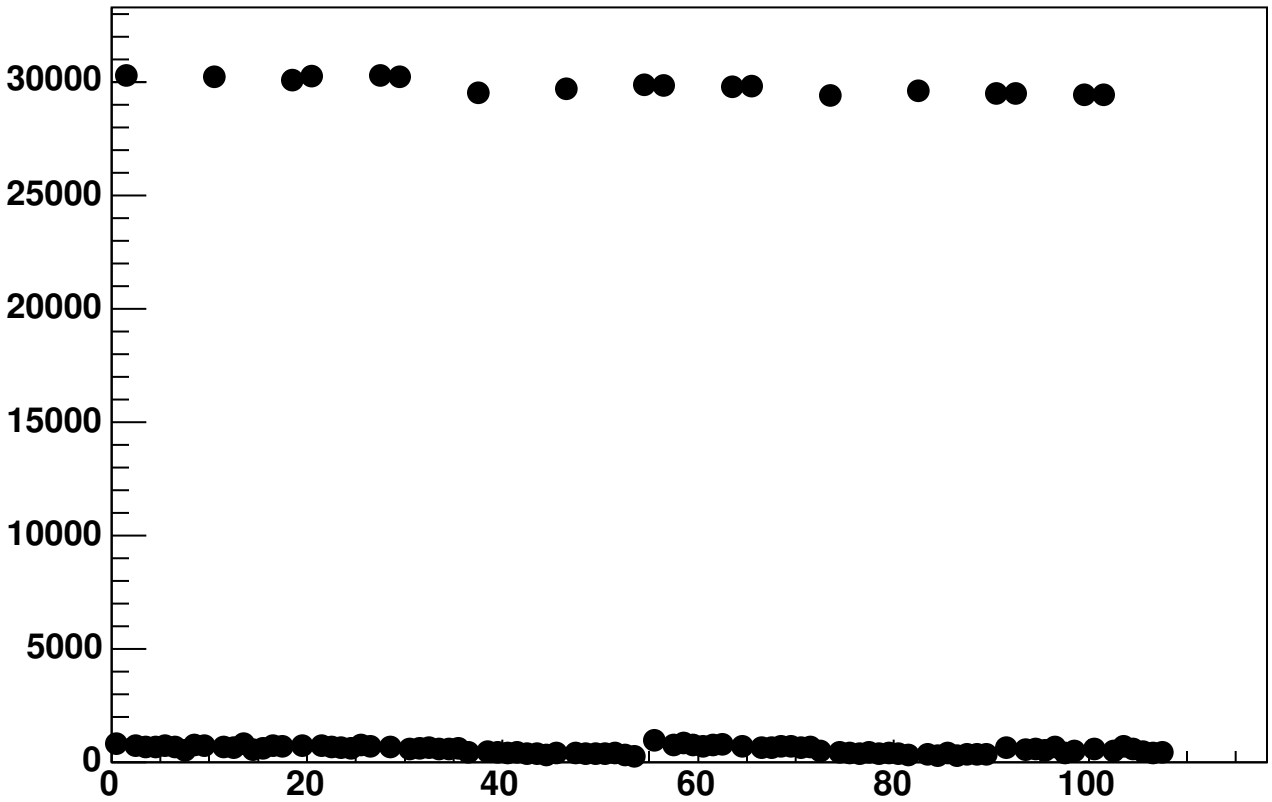
Enable 3, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



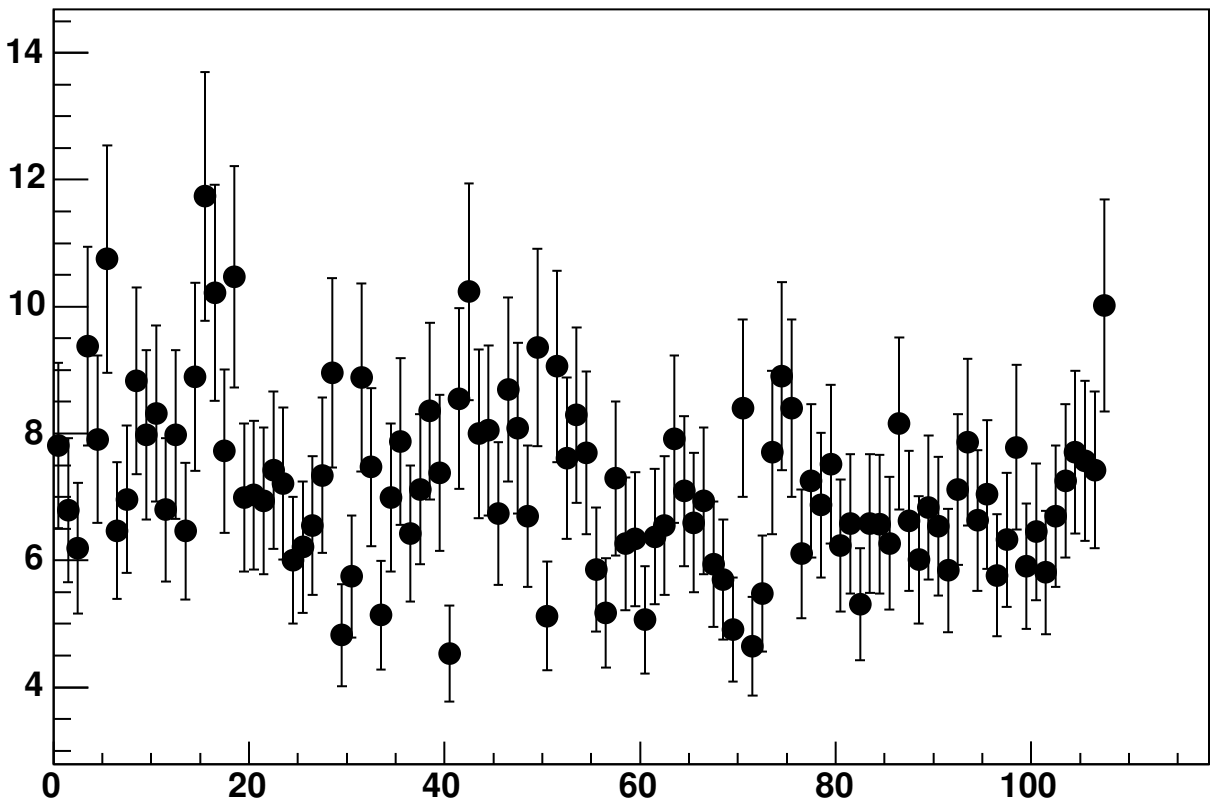
Enable 3, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



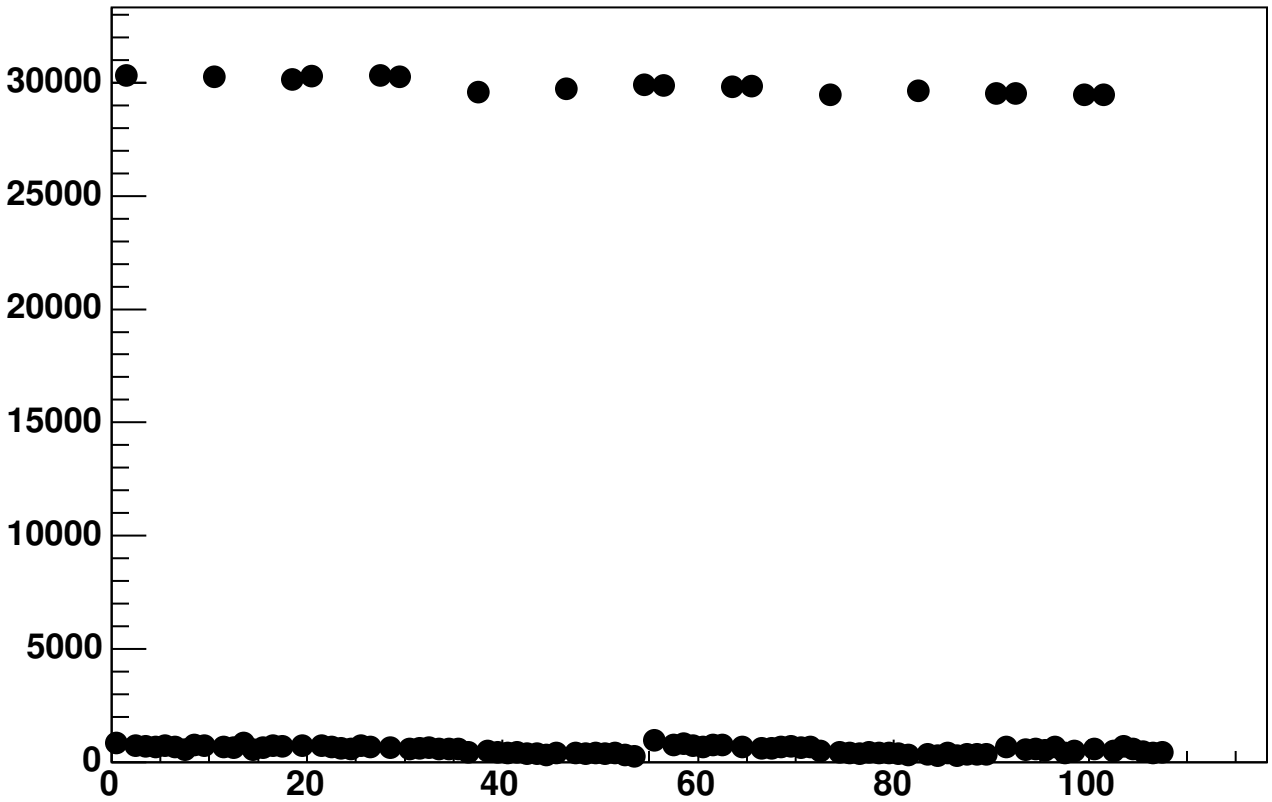
Enable 3, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



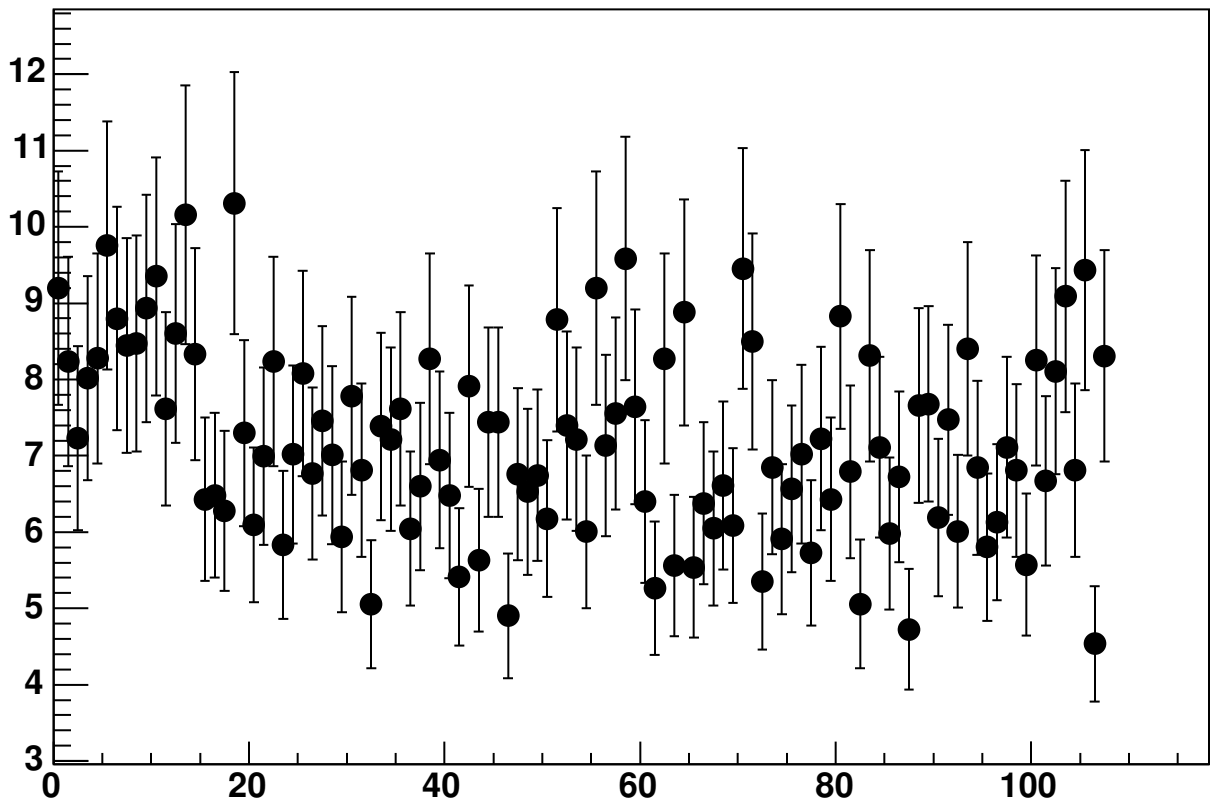
Enable 3, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



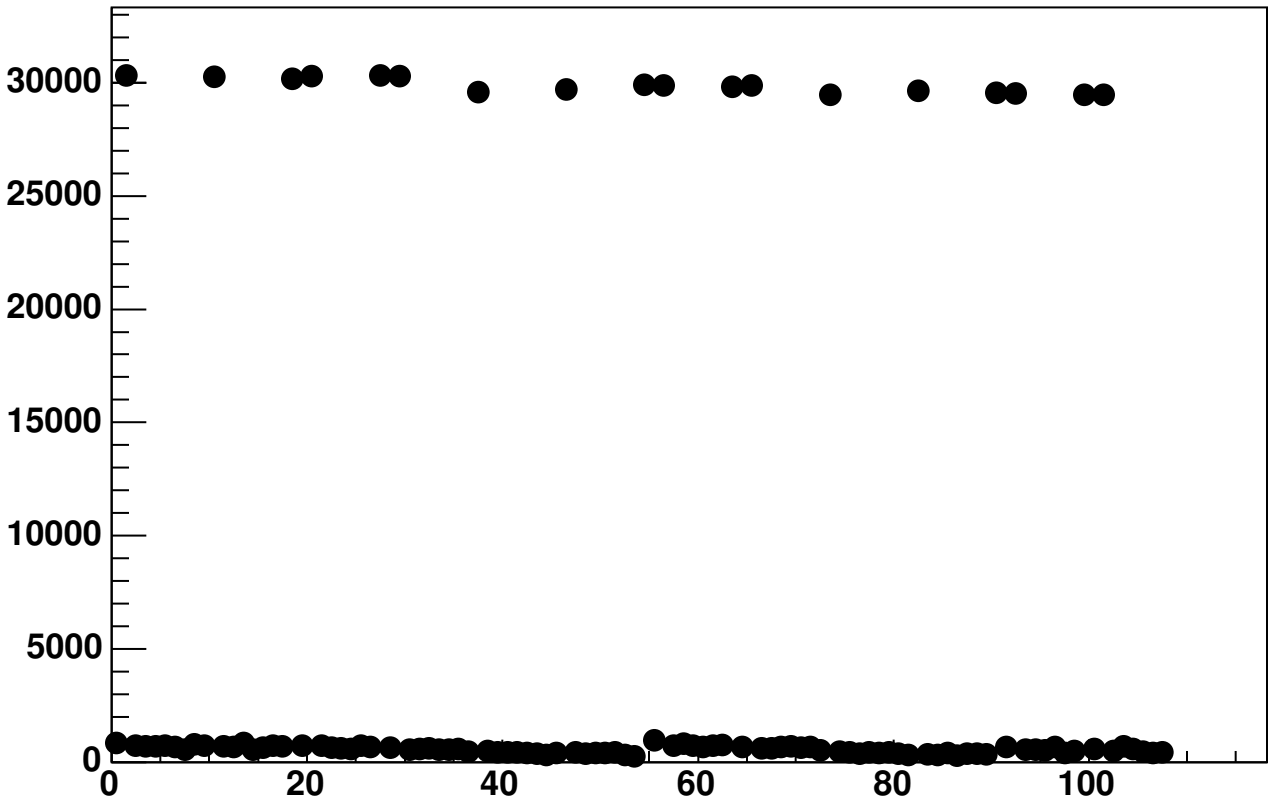
Enable 3, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



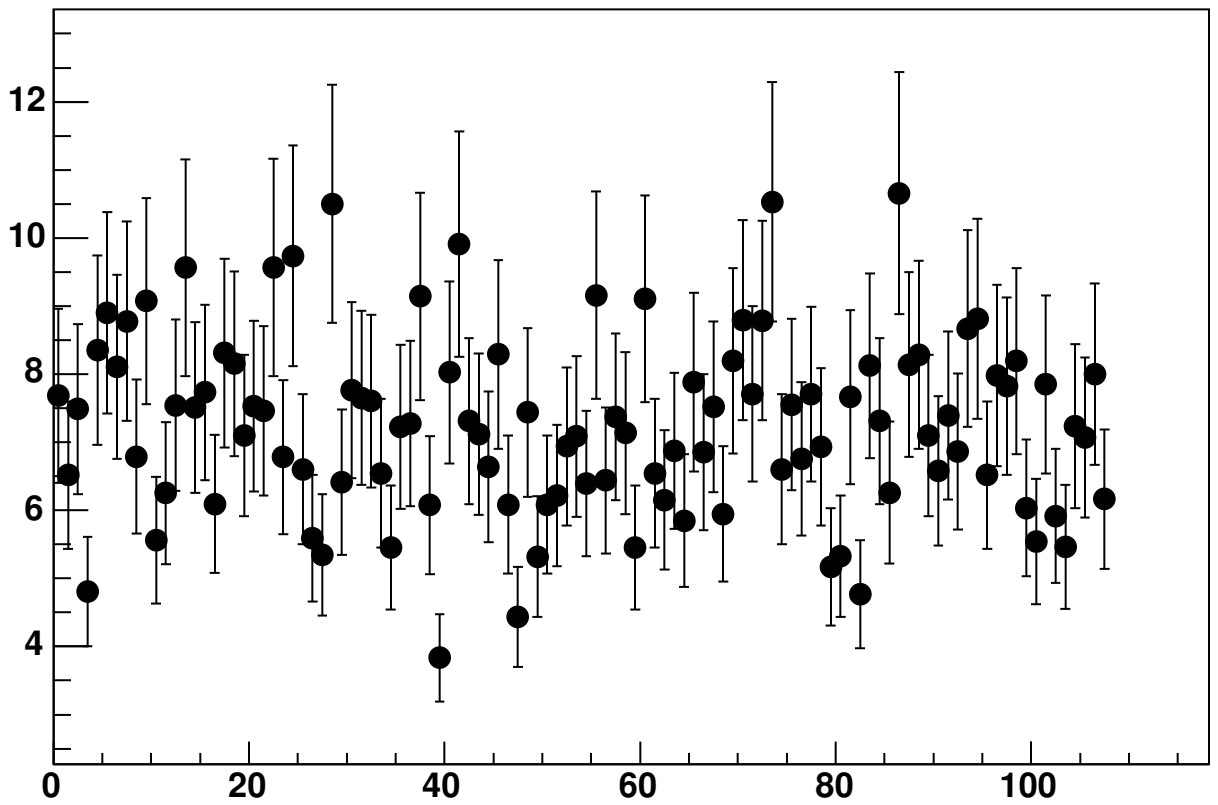
Enable 3, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



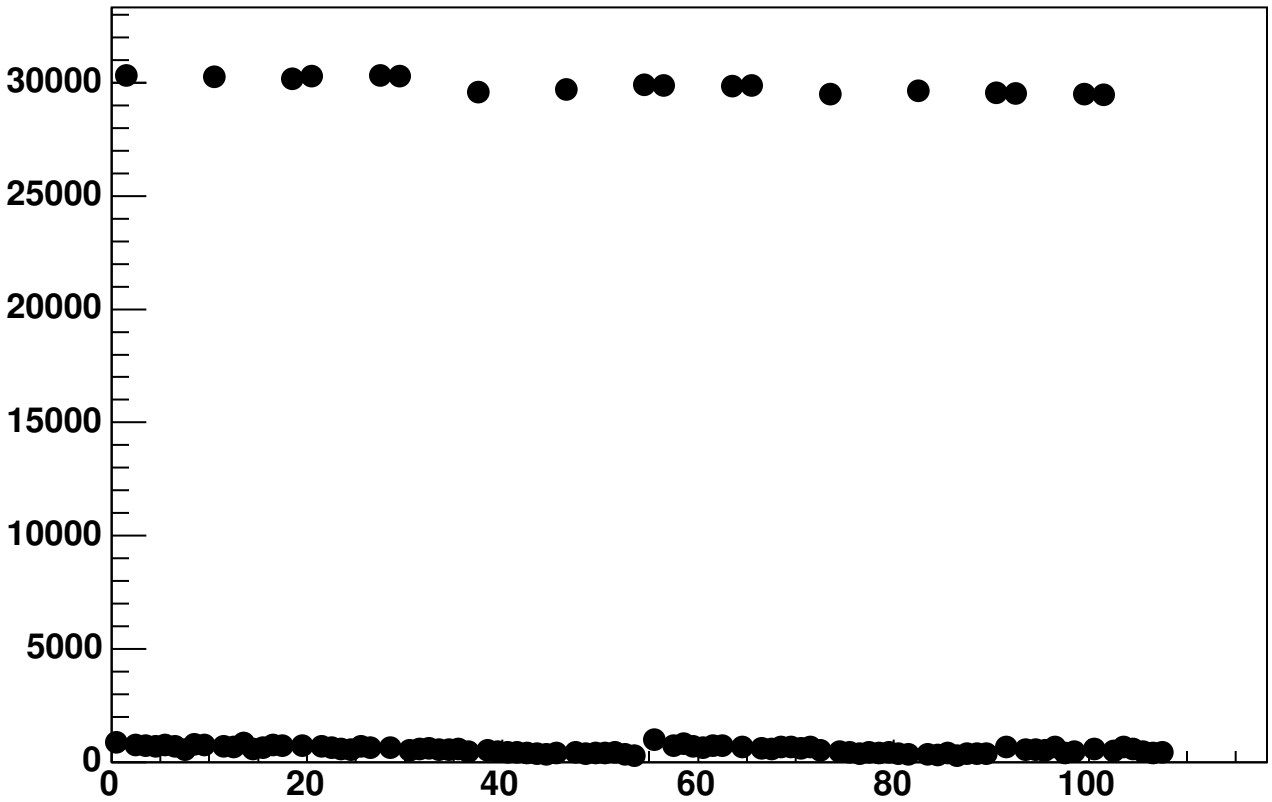
Enable 3, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



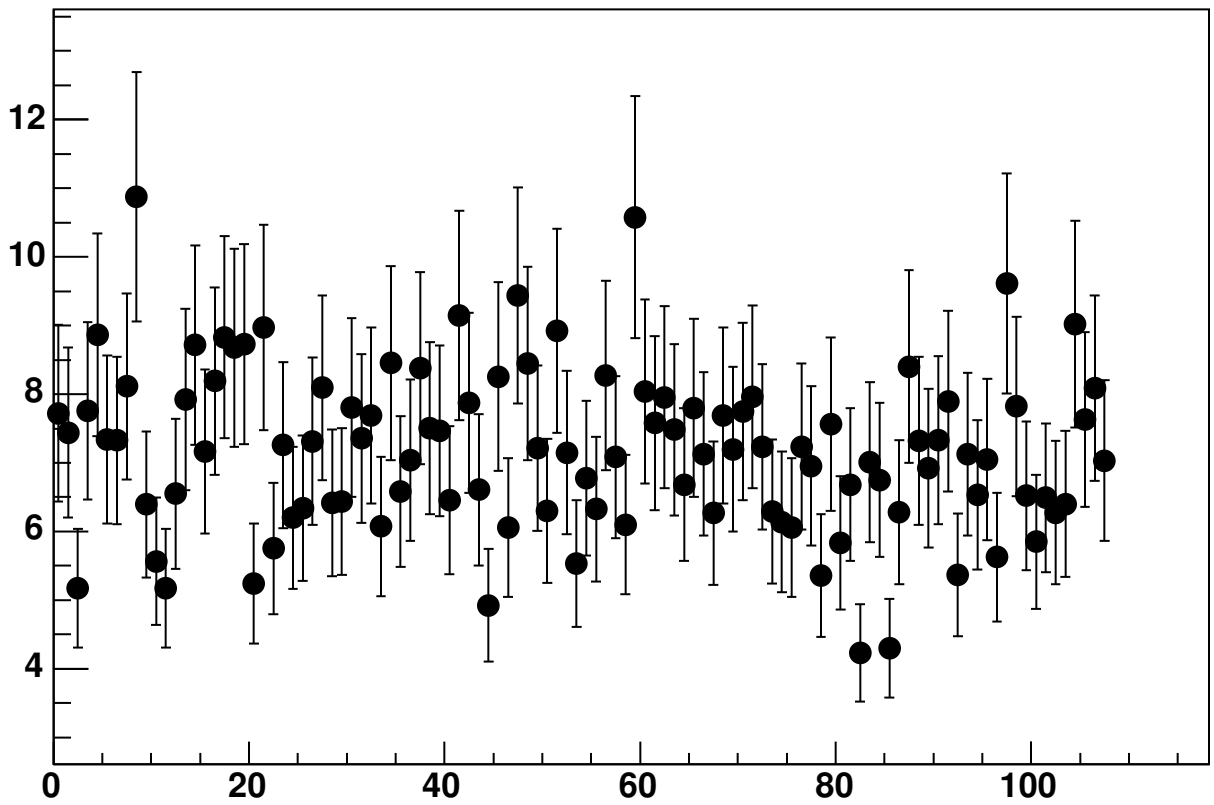
Enable 3, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



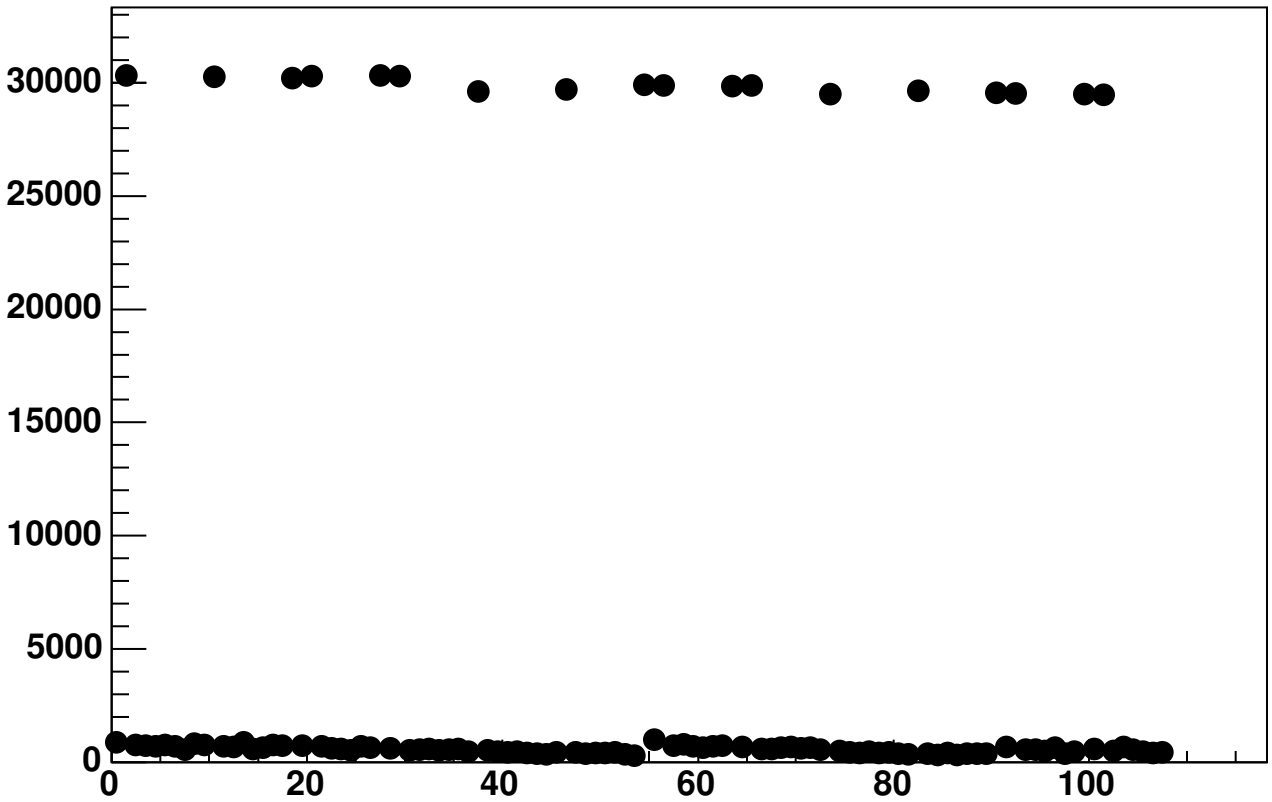
Enable 3, Hold=35, DAC=14400, ADC Mean vs 18*Chip+Chan



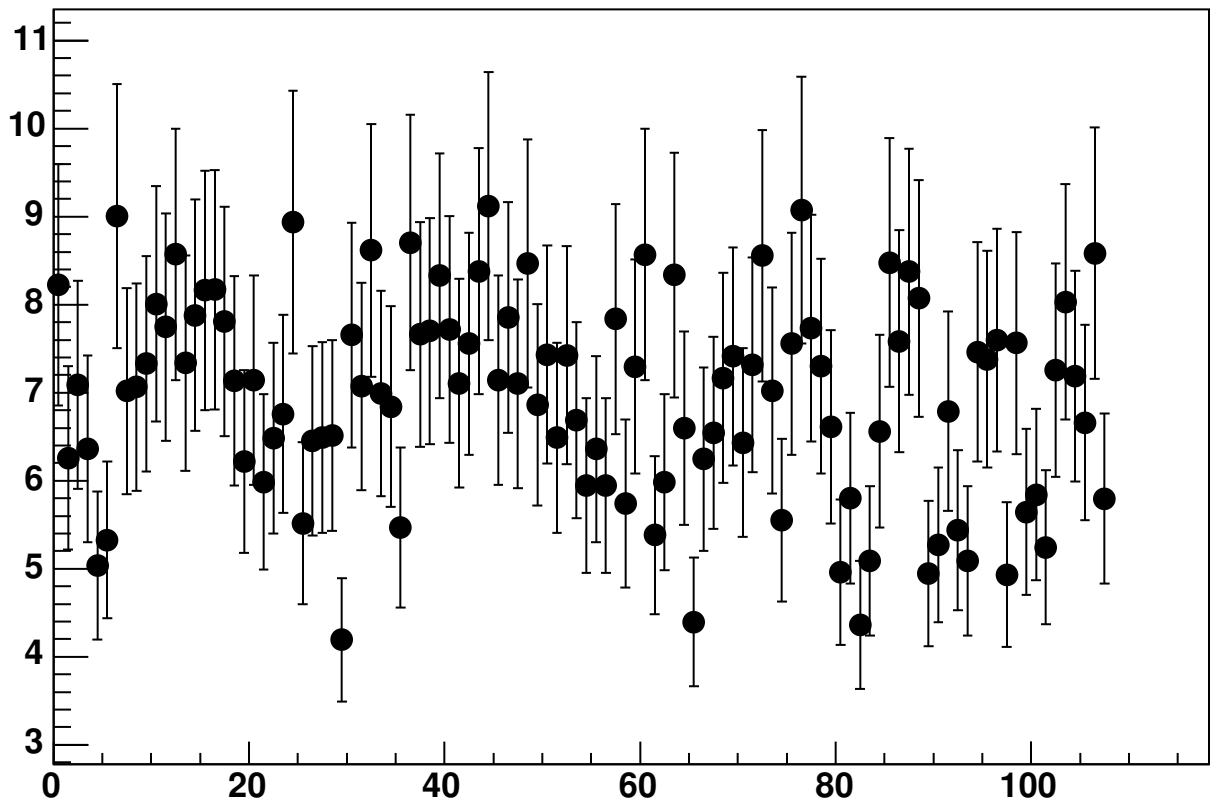
Enable 3, Hold=35, DAC=14400, ADC Noise vs 18*Chip+Chan



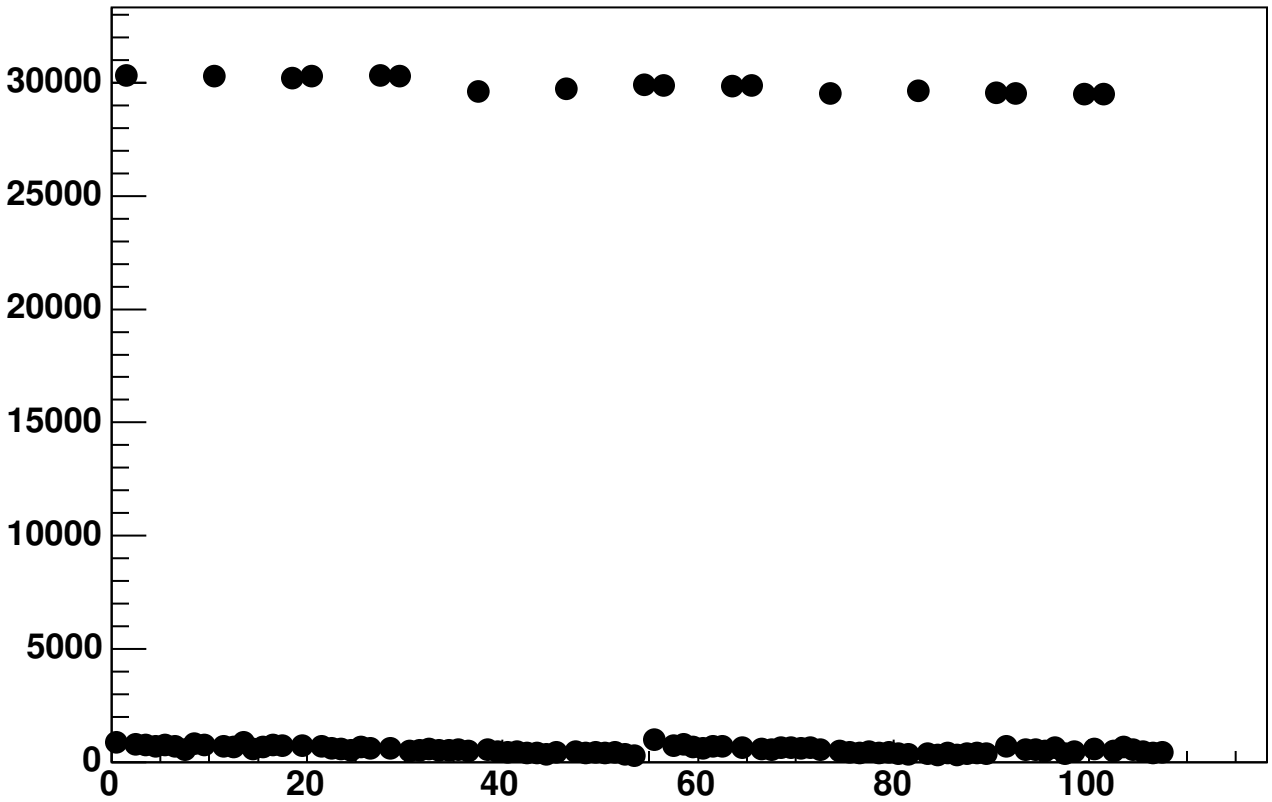
Enable 3, Hold=35, DAC=14800, ADC Mean vs 18*Chip+Chan



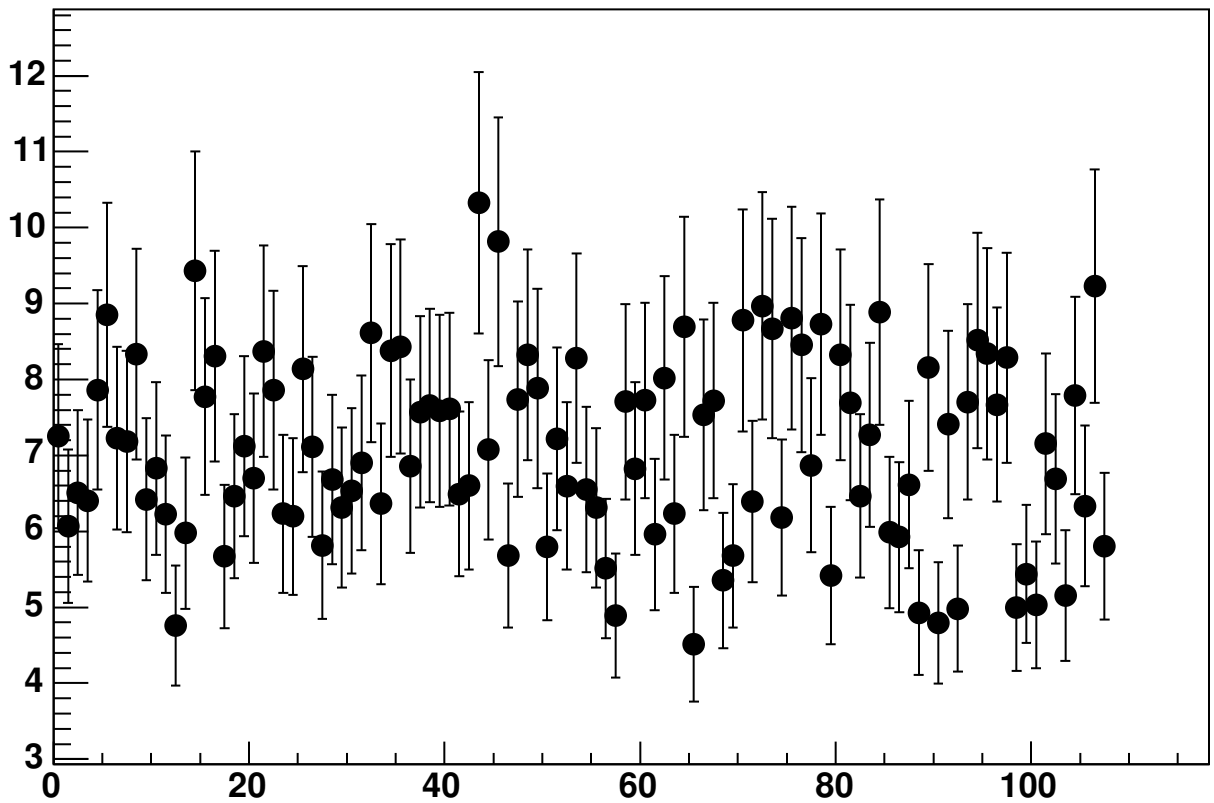
Enable 3, Hold=35, DAC=14800, ADC Noise vs 18*Chip+Chan



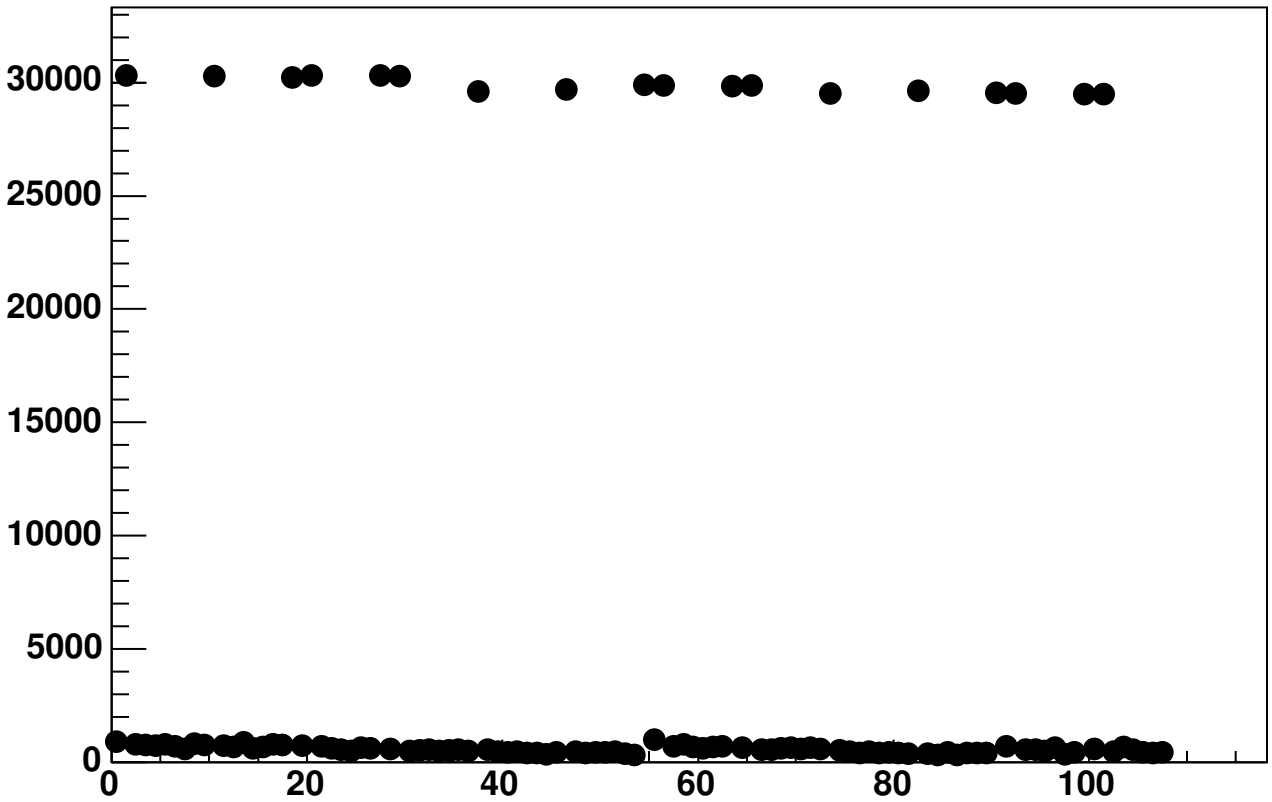
Enable 3, Hold=35, DAC=15200, ADC Mean vs 18*Chip+Chan



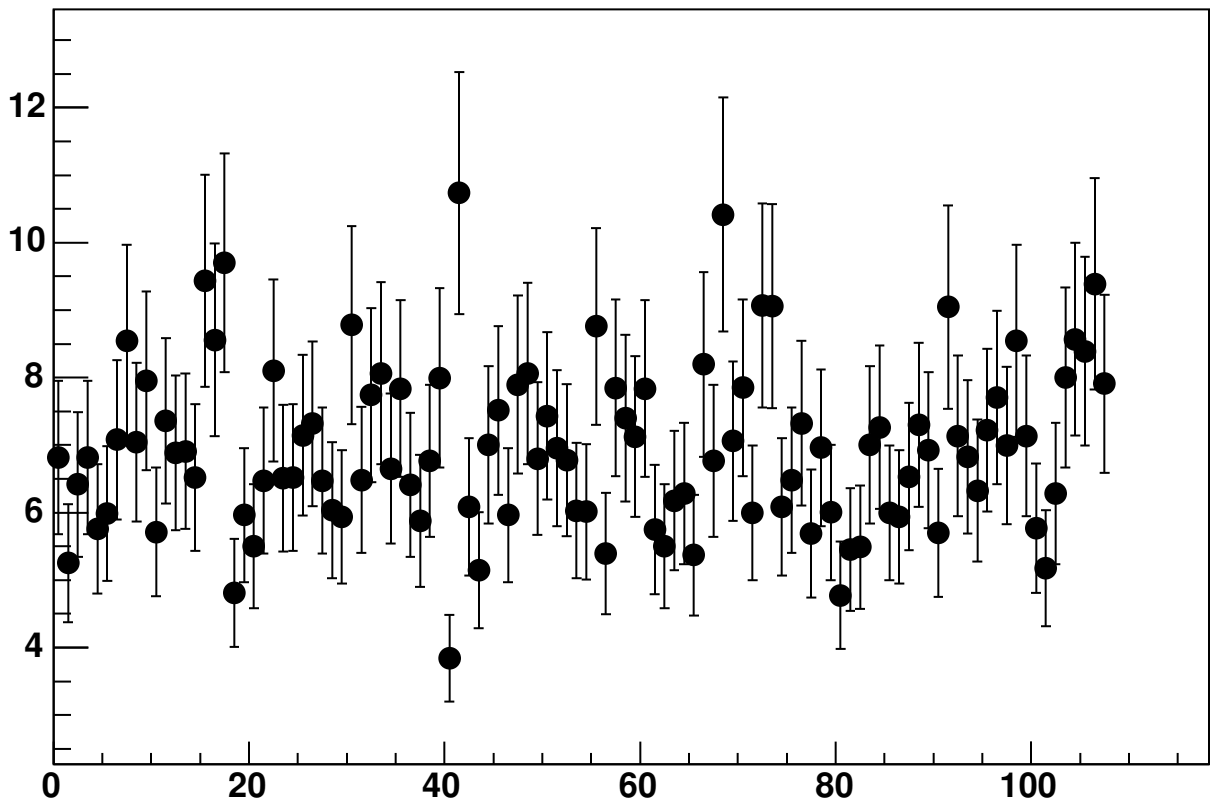
Enable 3, Hold=35, DAC=15200, ADC Noise vs 18*Chip+Chan



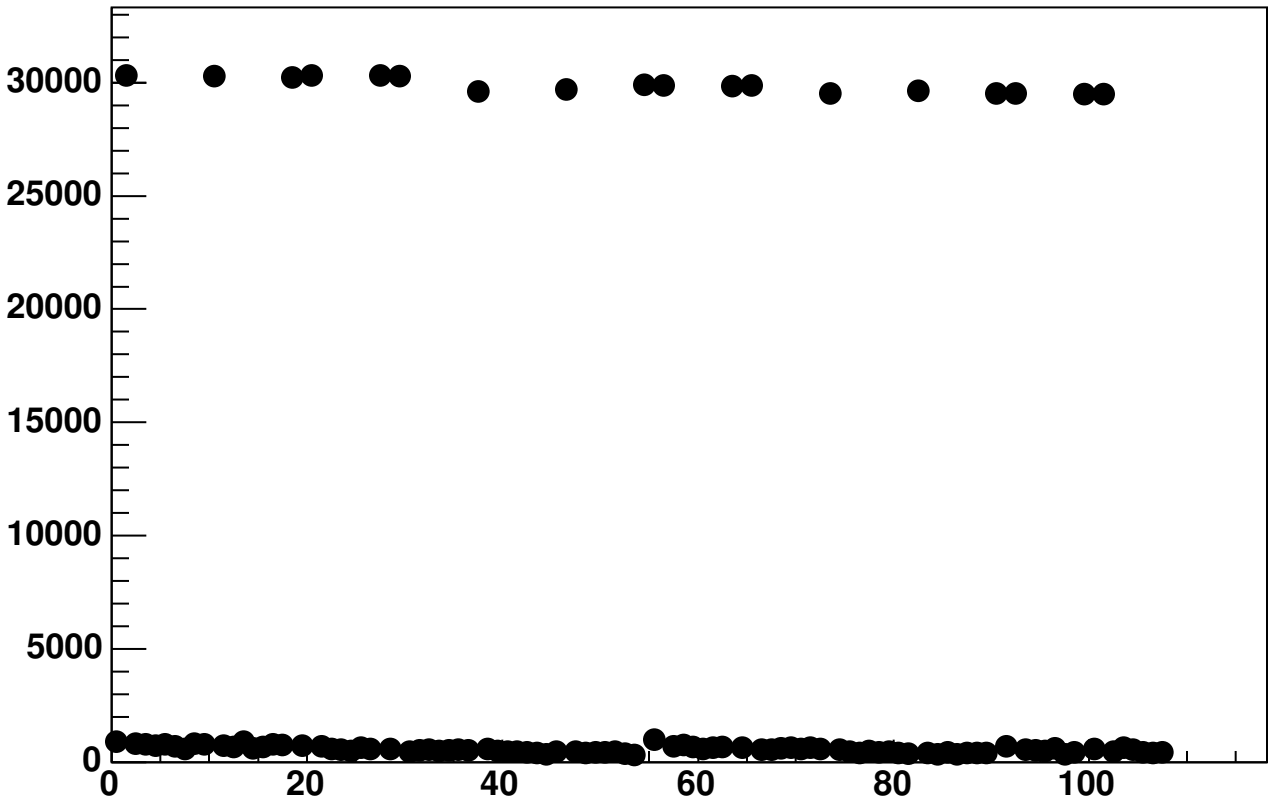
Enable 3, Hold=35, DAC=15600, ADC Mean vs 18*Chip+Chan



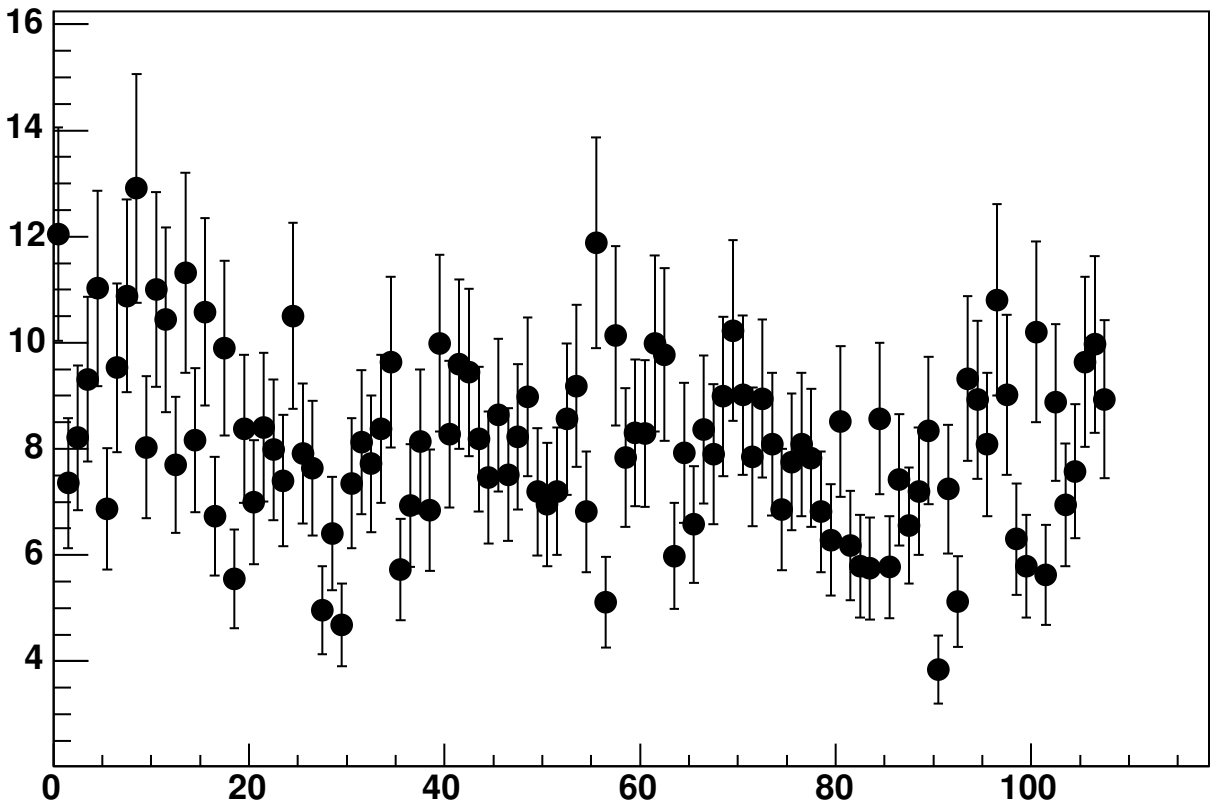
Enable 3, Hold=35, DAC=15600, ADC Noise vs 18*Chip+Chan



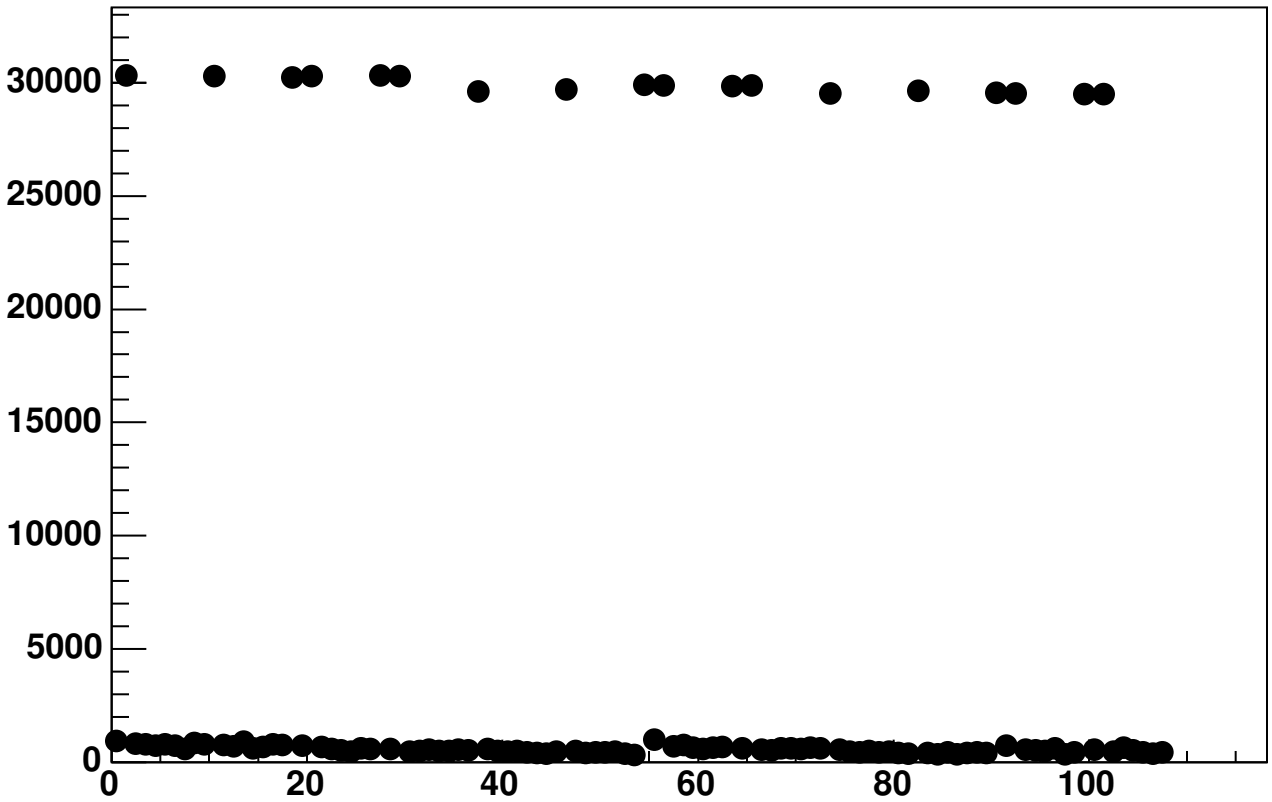
Enable 3, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



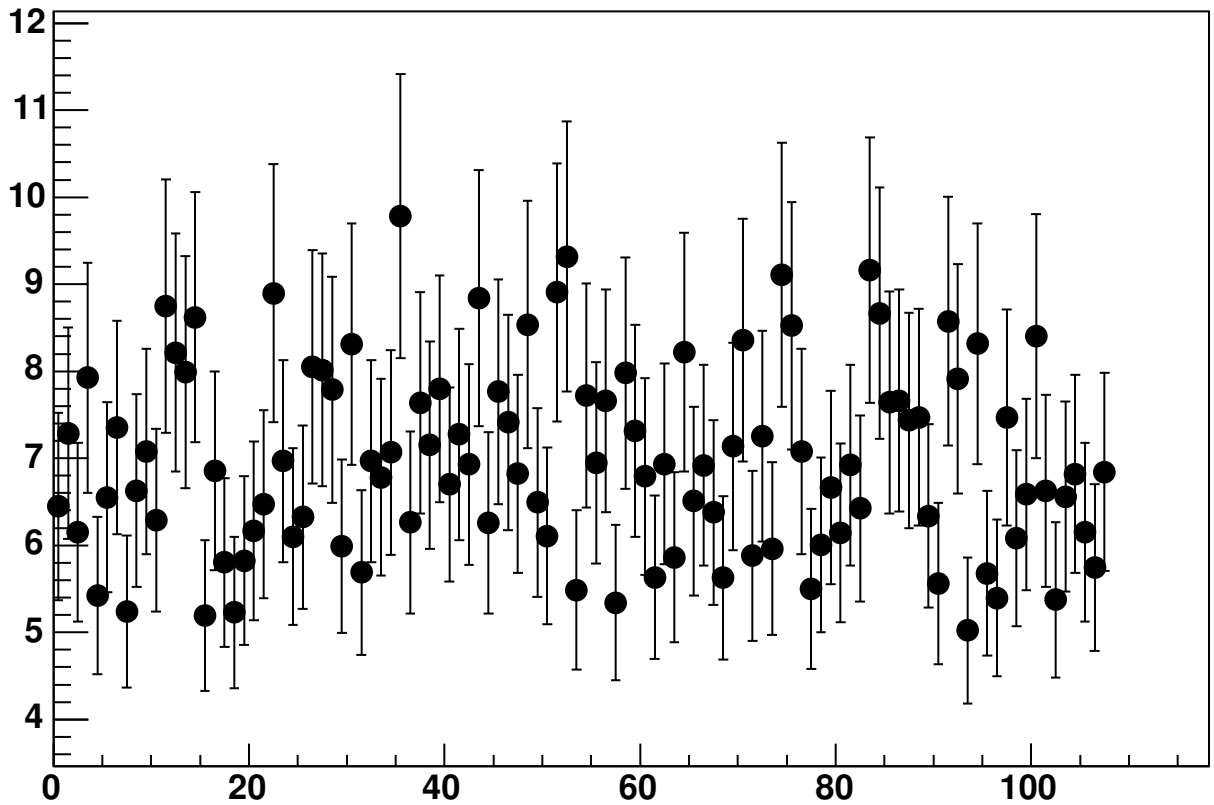
Enable 3, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



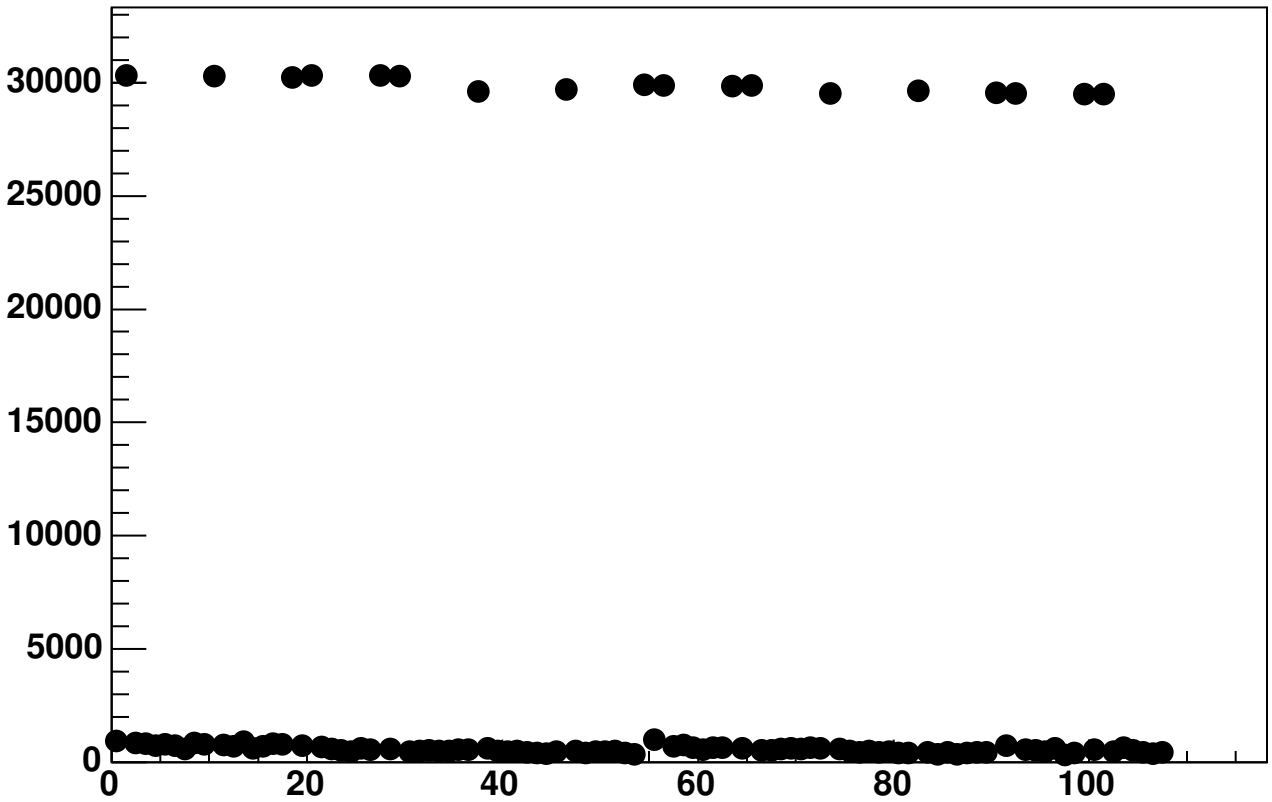
Enable 3, Hold=35, DAC=16400, ADC Mean vs 18*Chip+Chan



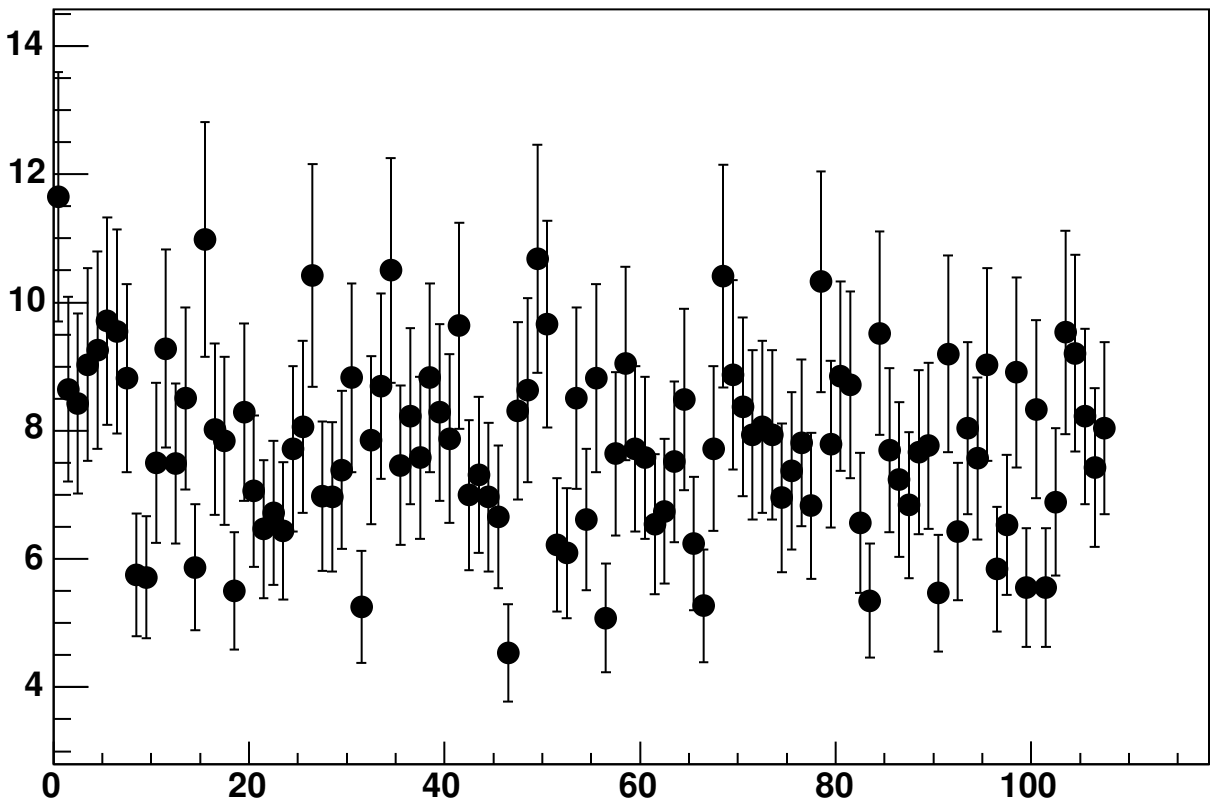
Enable 3, Hold=35, DAC=16400, ADC Noise vs 18*Chip+Chan



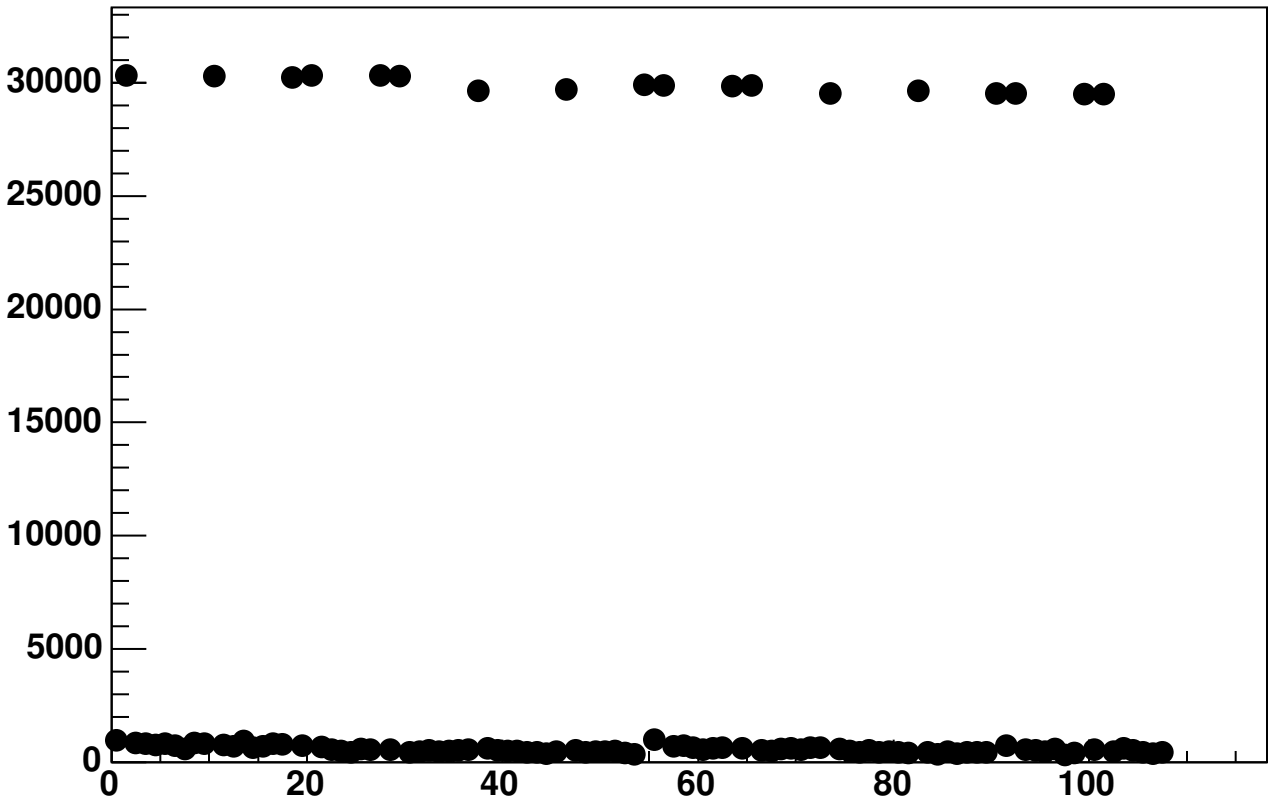
Enable 3, Hold=35, DAC=16800, ADC Mean vs 18*Chip+Chan



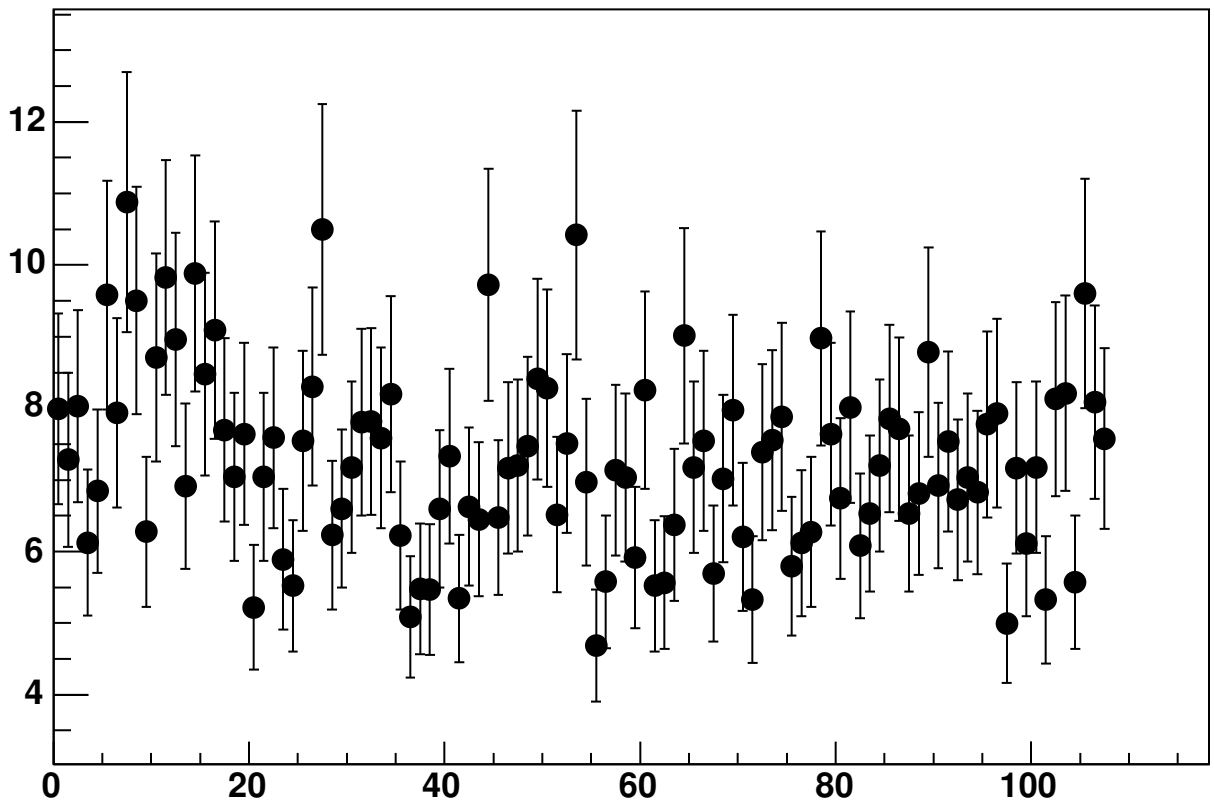
Enable 3, Hold=35, DAC=16800, ADC Noise vs 18*Chip+Chan



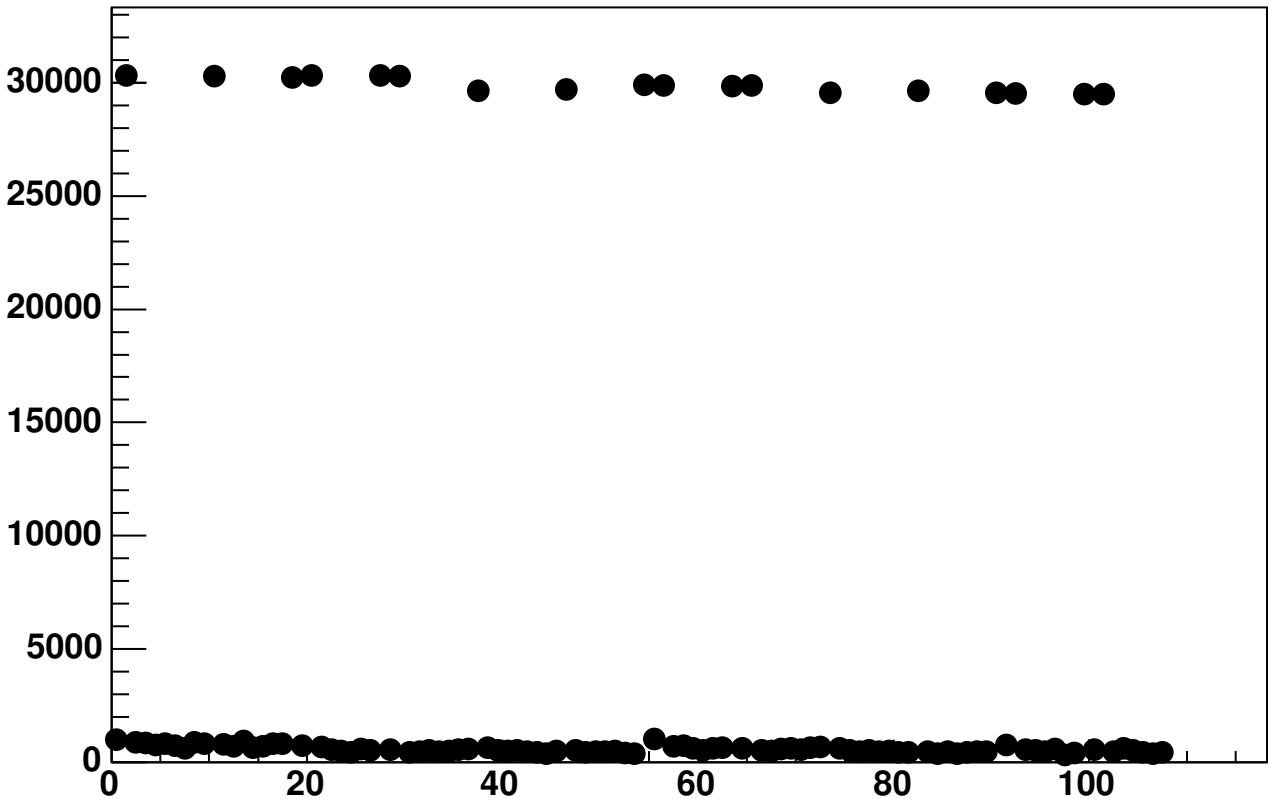
Enable 3, Hold=35, DAC=17200, ADC Mean vs 18*Chip+Chan



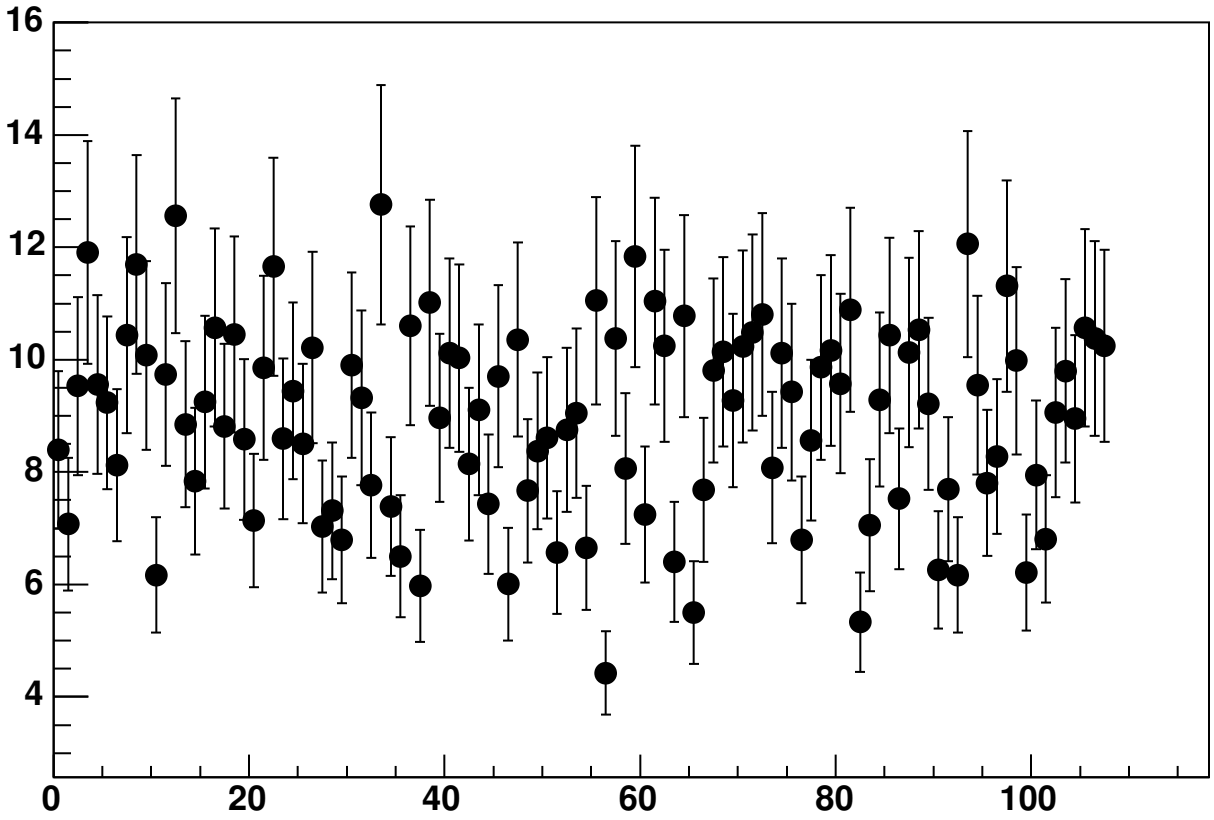
Enable 3, Hold=35, DAC=17200, ADC Noise vs 18*Chip+Chan



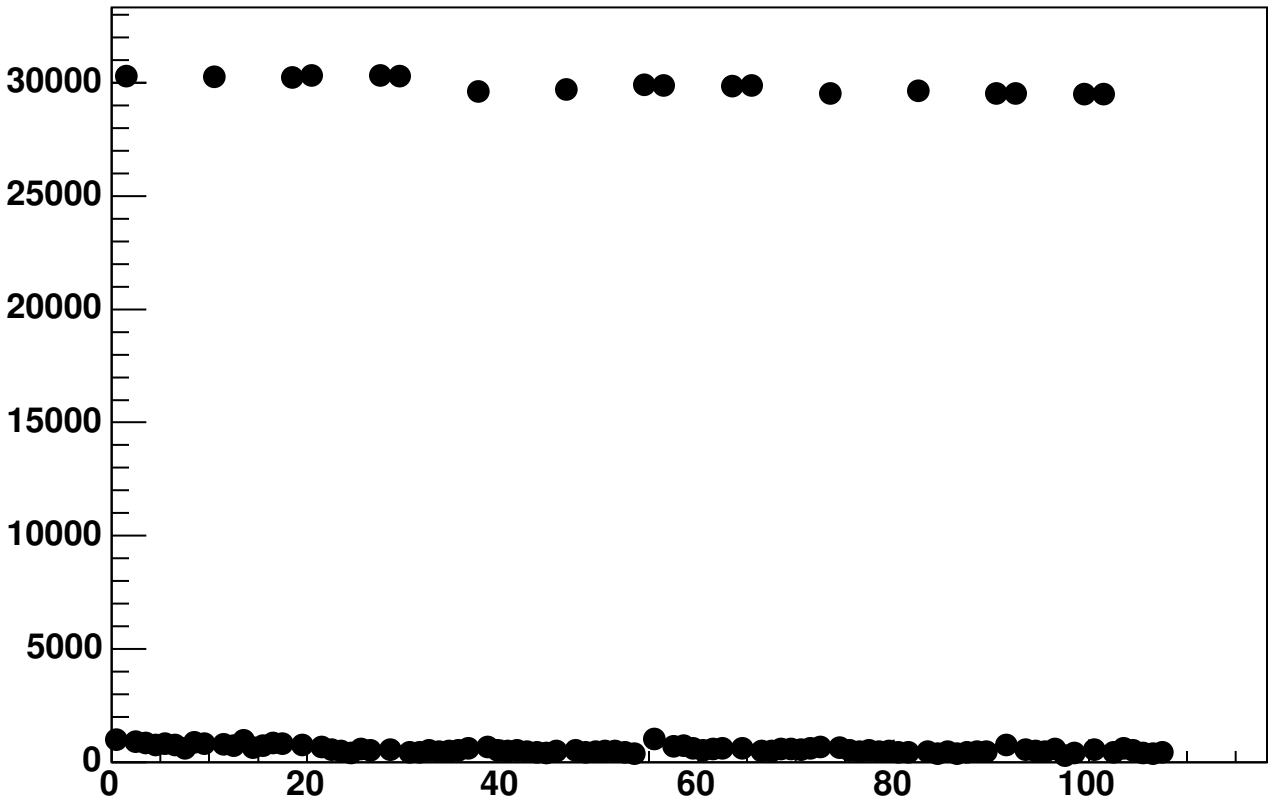
Enable 3, Hold=35, DAC=17600, ADC Mean vs 18*Chip+Chan



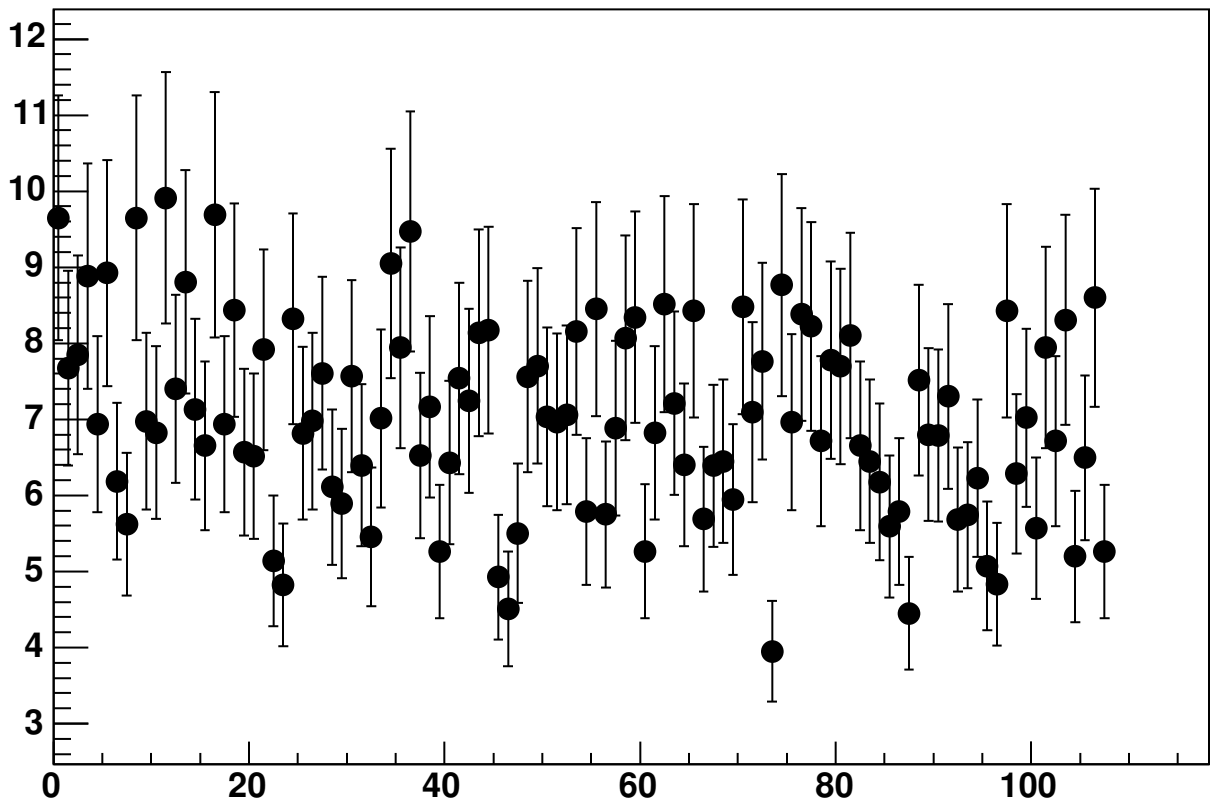
Enable 3, Hold=35, DAC=17600, ADC Noise vs 18*Chip+Chan



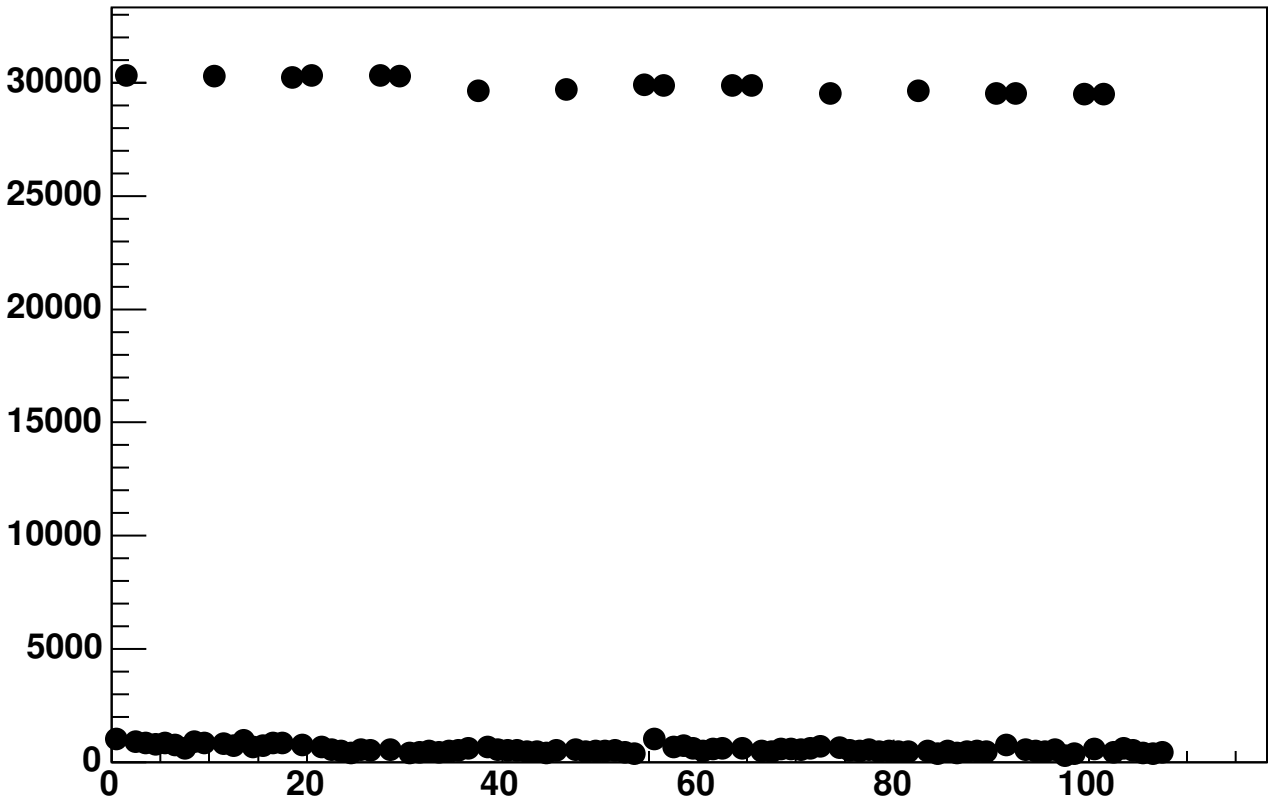
Enable 3, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



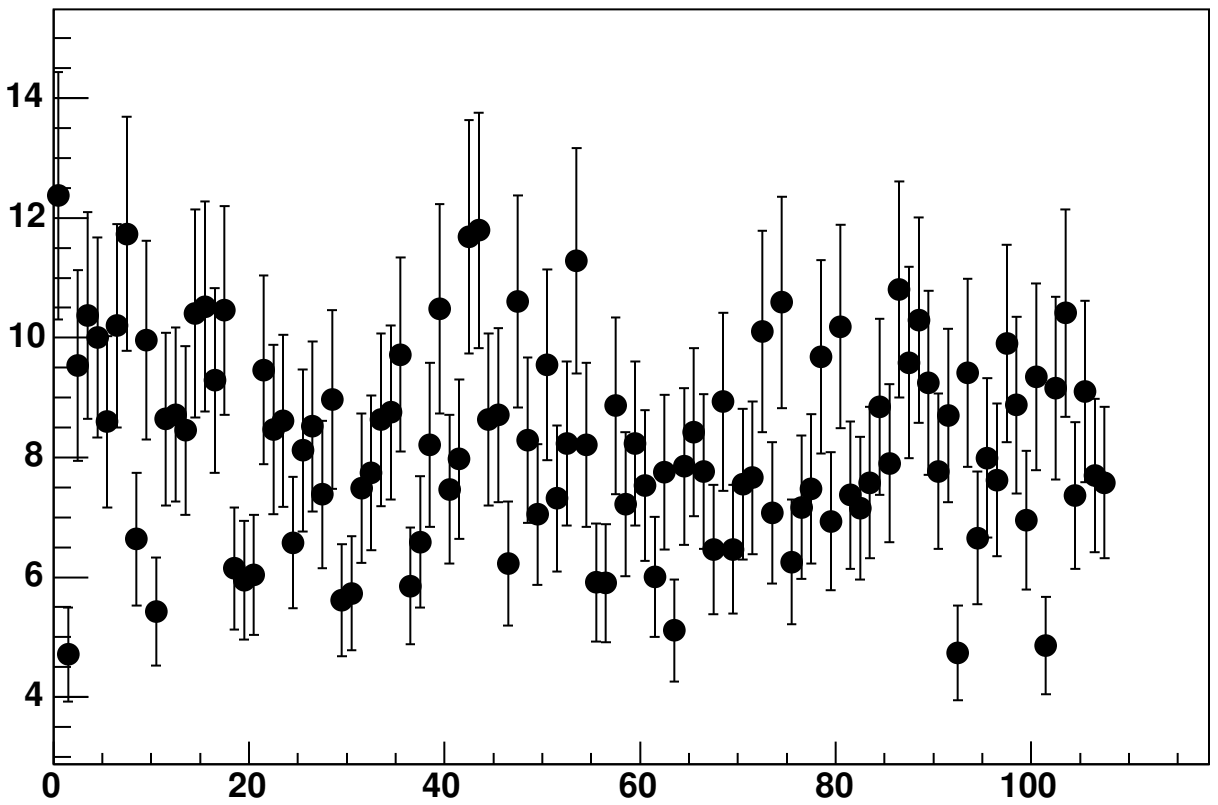
Enable 3, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



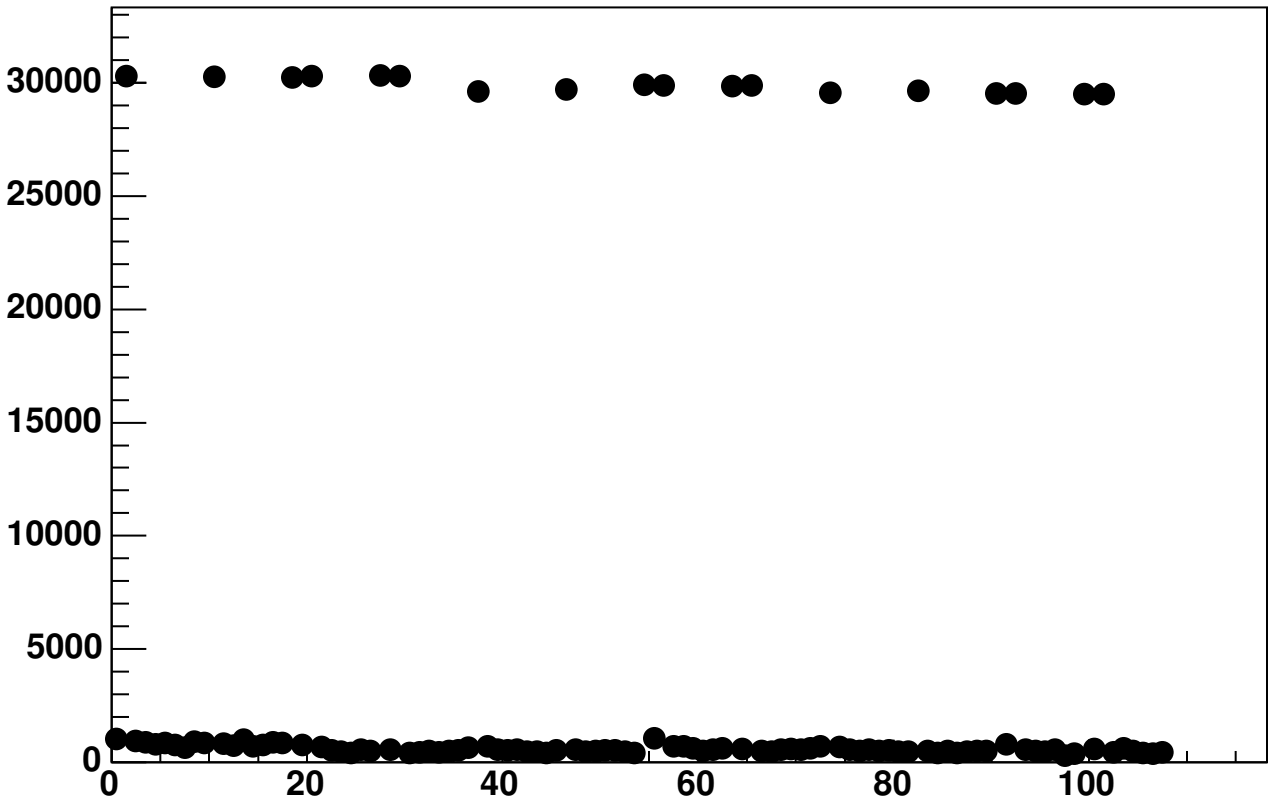
Enable 3, Hold=35, DAC=18400, ADC Mean vs 18*Chip+Chan



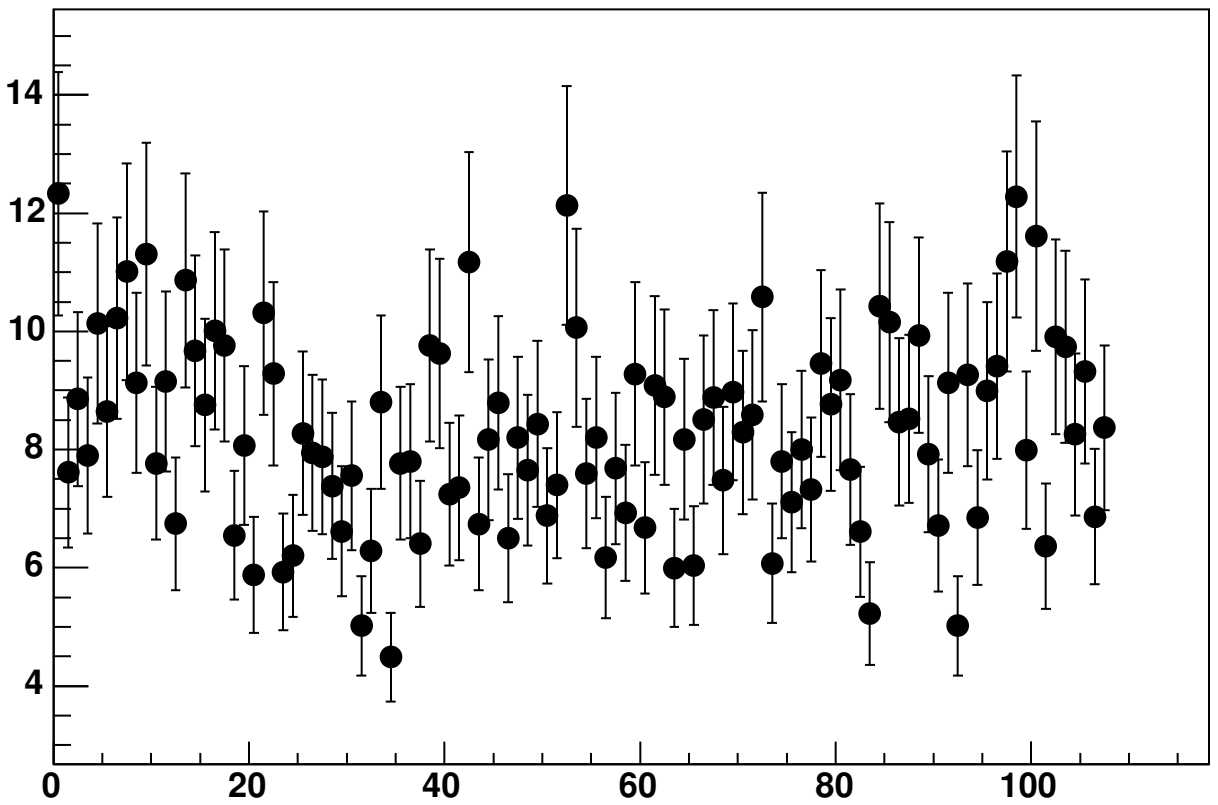
Enable 3, Hold=35, DAC=18400, ADC Noise vs 18*Chip+Chan



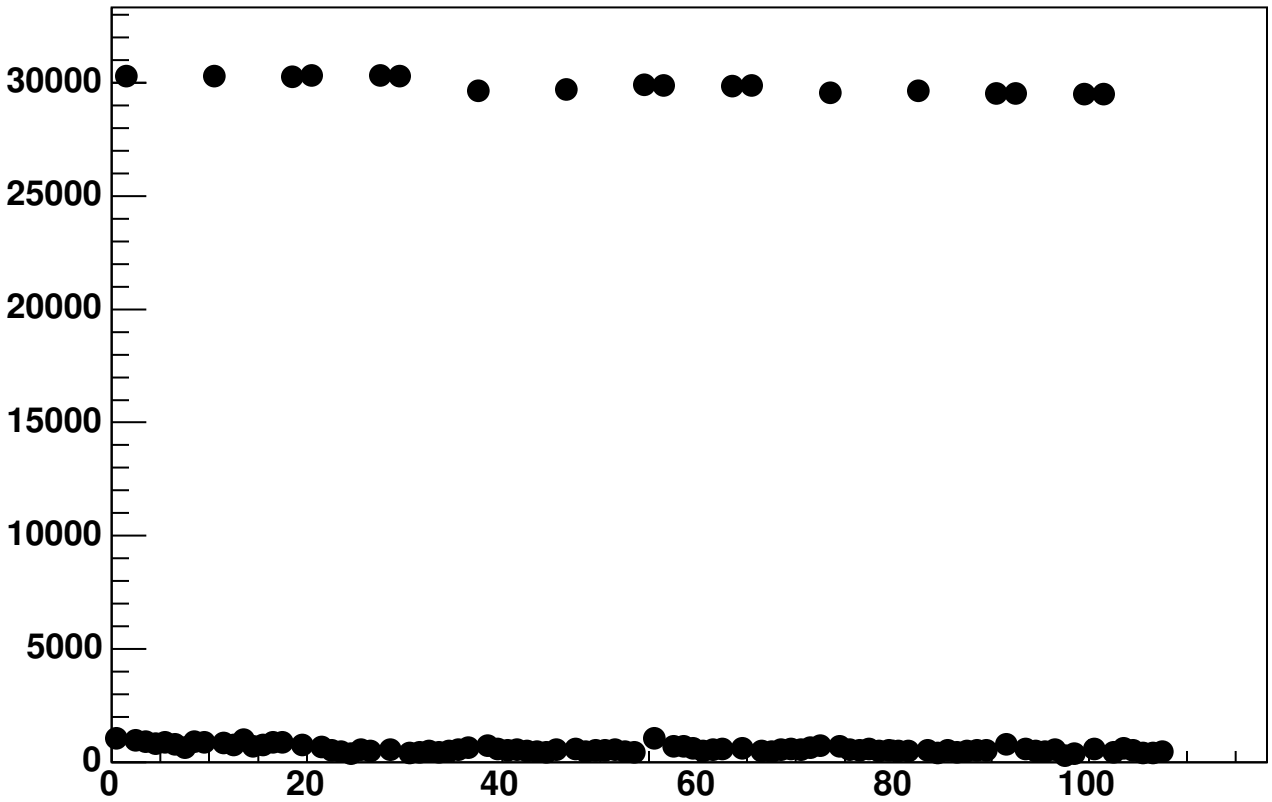
Enable 3, Hold=35, DAC=18800, ADC Mean vs 18*Chip+Chan



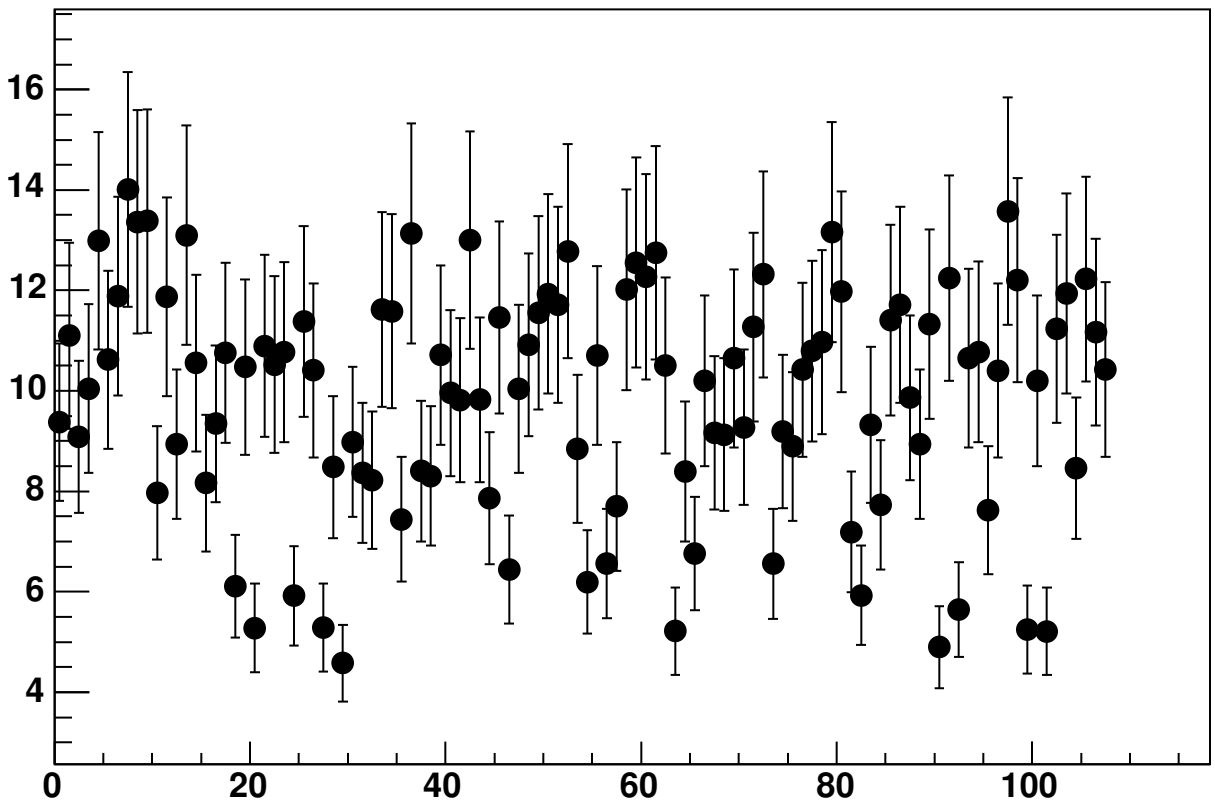
Enable 3, Hold=35, DAC=18800, ADC Noise vs 18*Chip+Chan



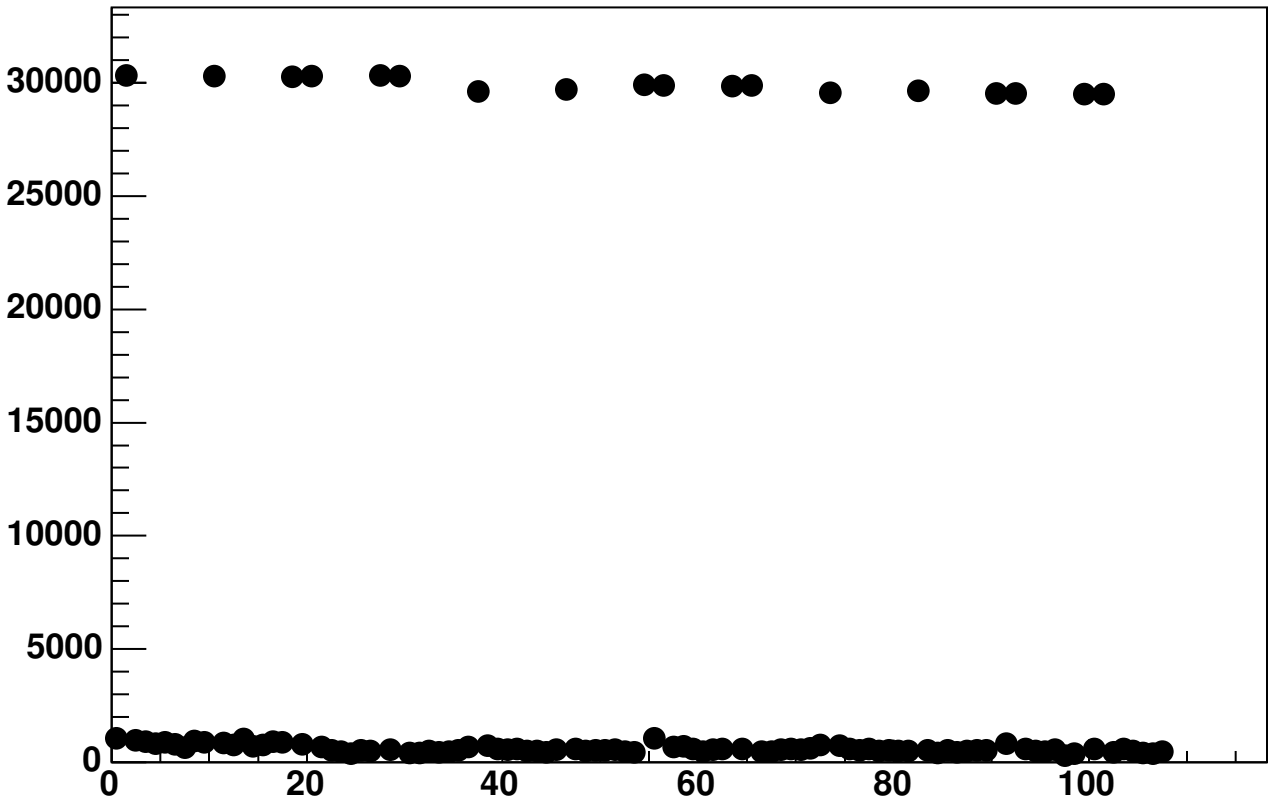
Enable 3, Hold=35, DAC=19200, ADC Mean vs 18*Chip+Chan



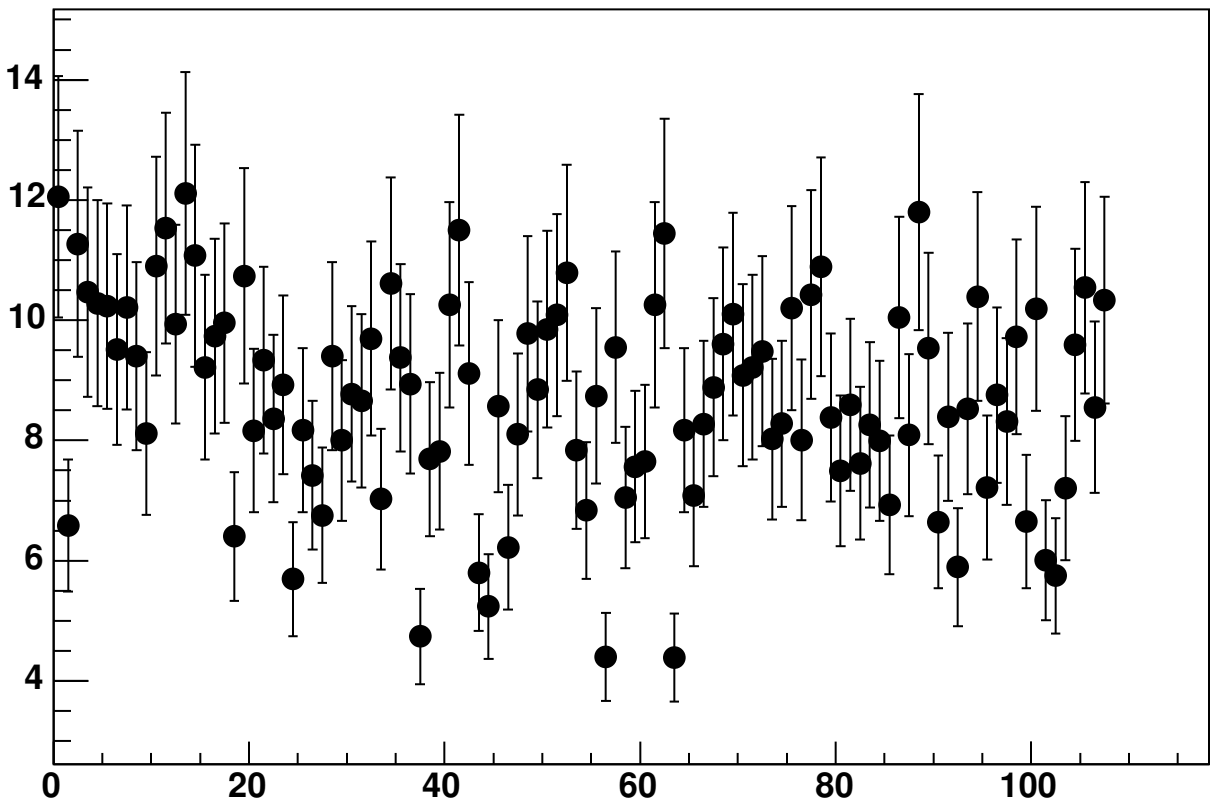
Enable 3, Hold=35, DAC=19200, ADC Noise vs 18*Chip+Chan



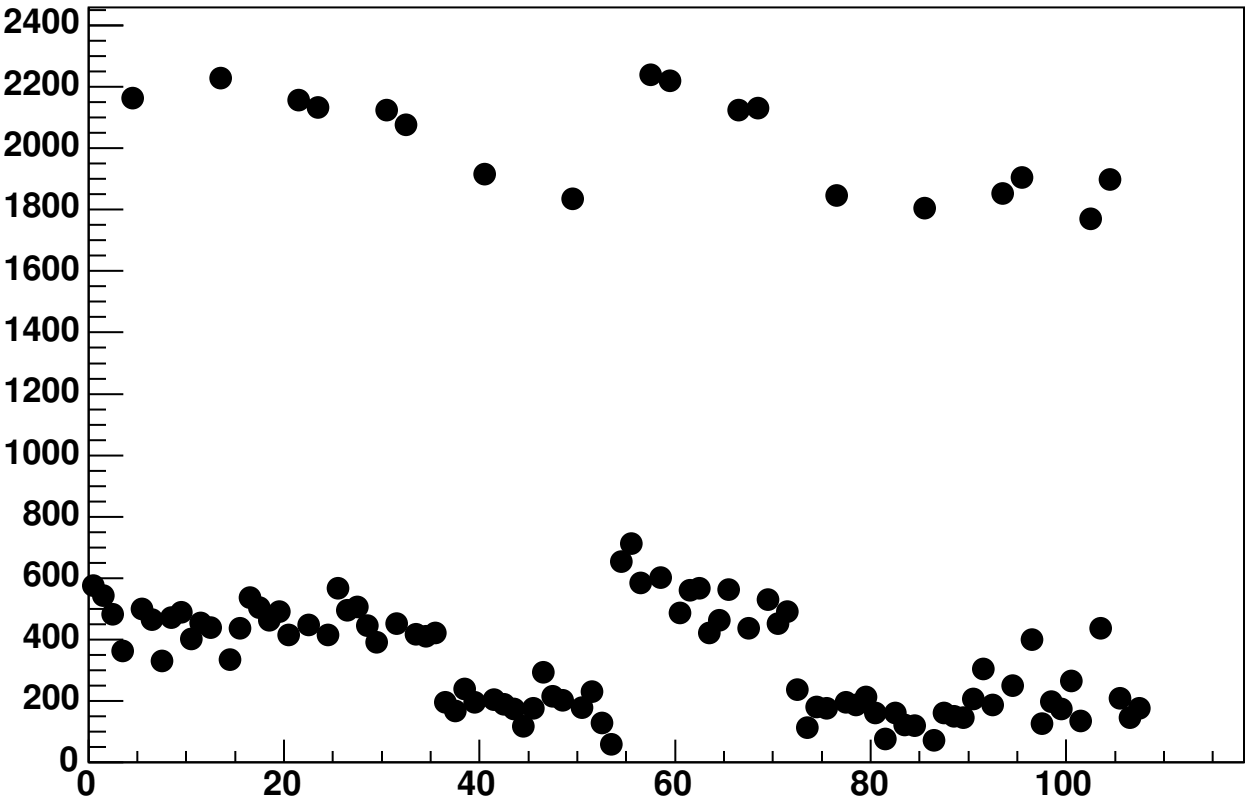
Enable 3, Hold=35, DAC=19600, ADC Mean vs 18*Chip+Chan



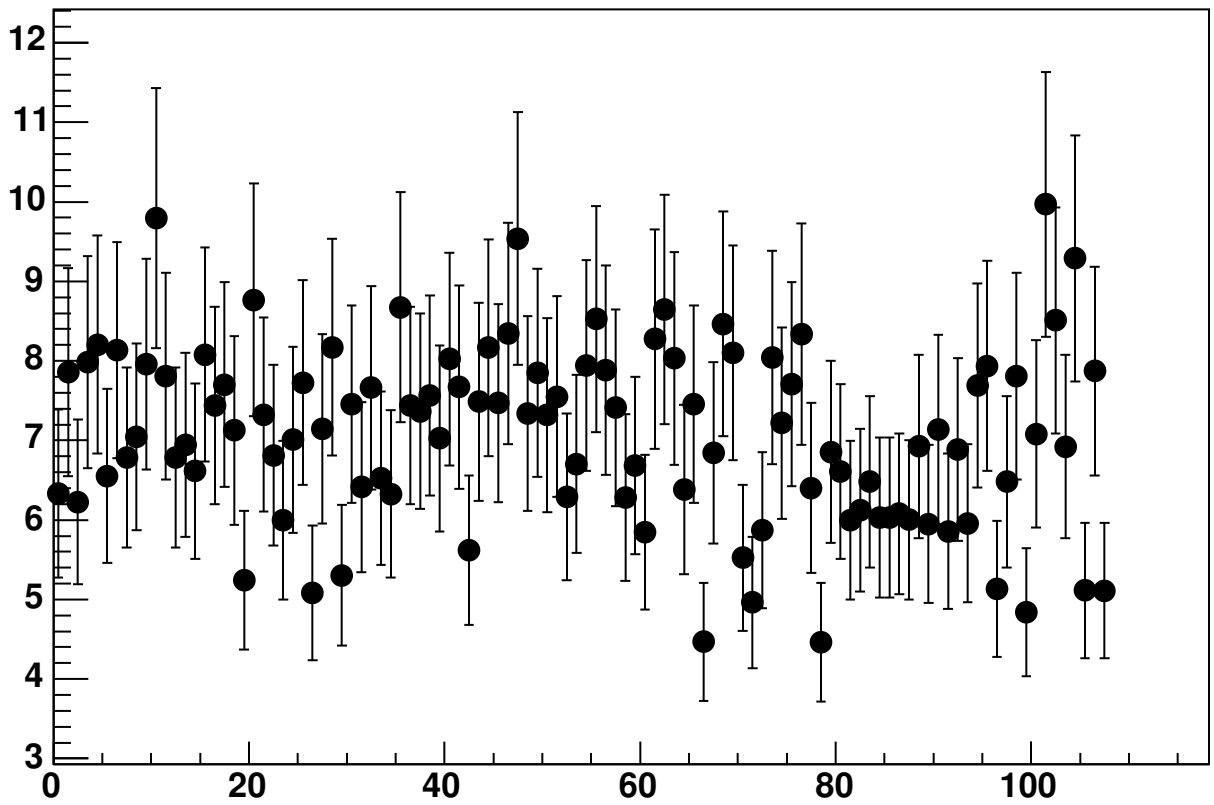
Enable 3, Hold=35, DAC=19600, ADC Noise vs 18*Chip+Chan



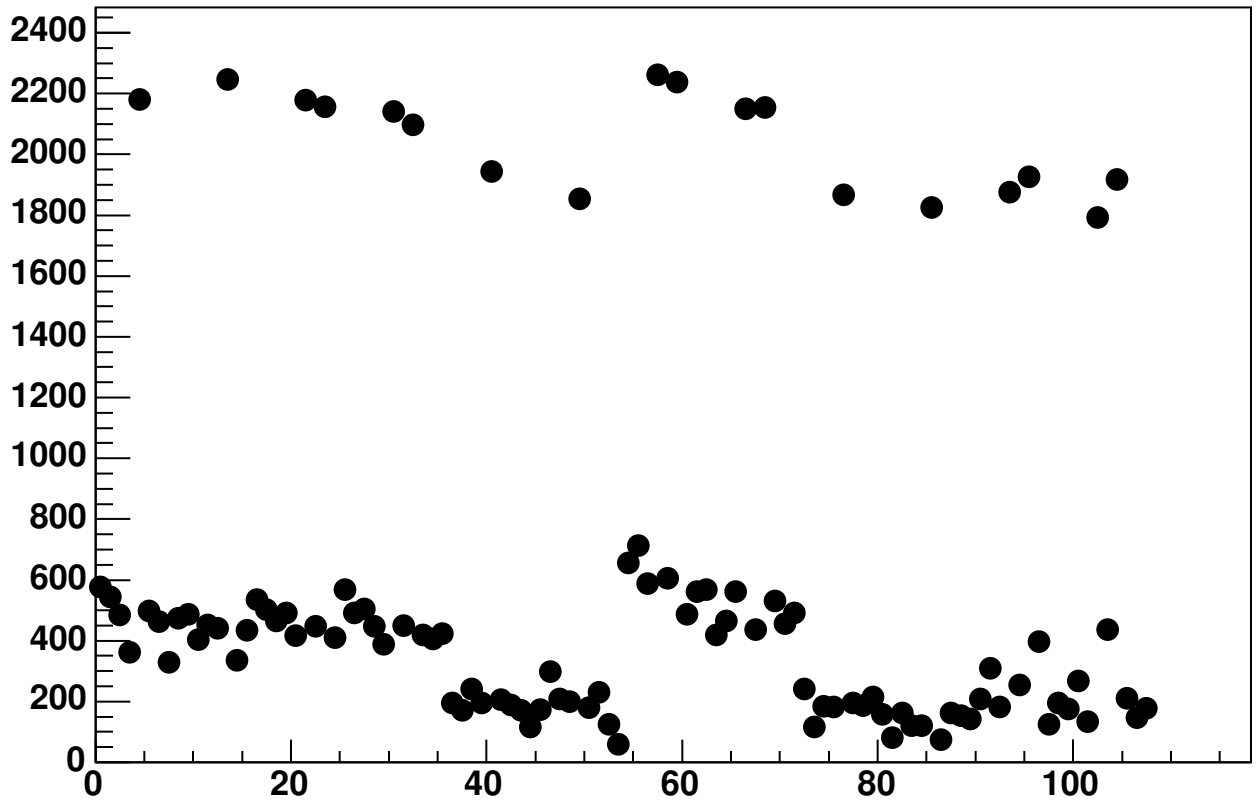
Enable 4, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



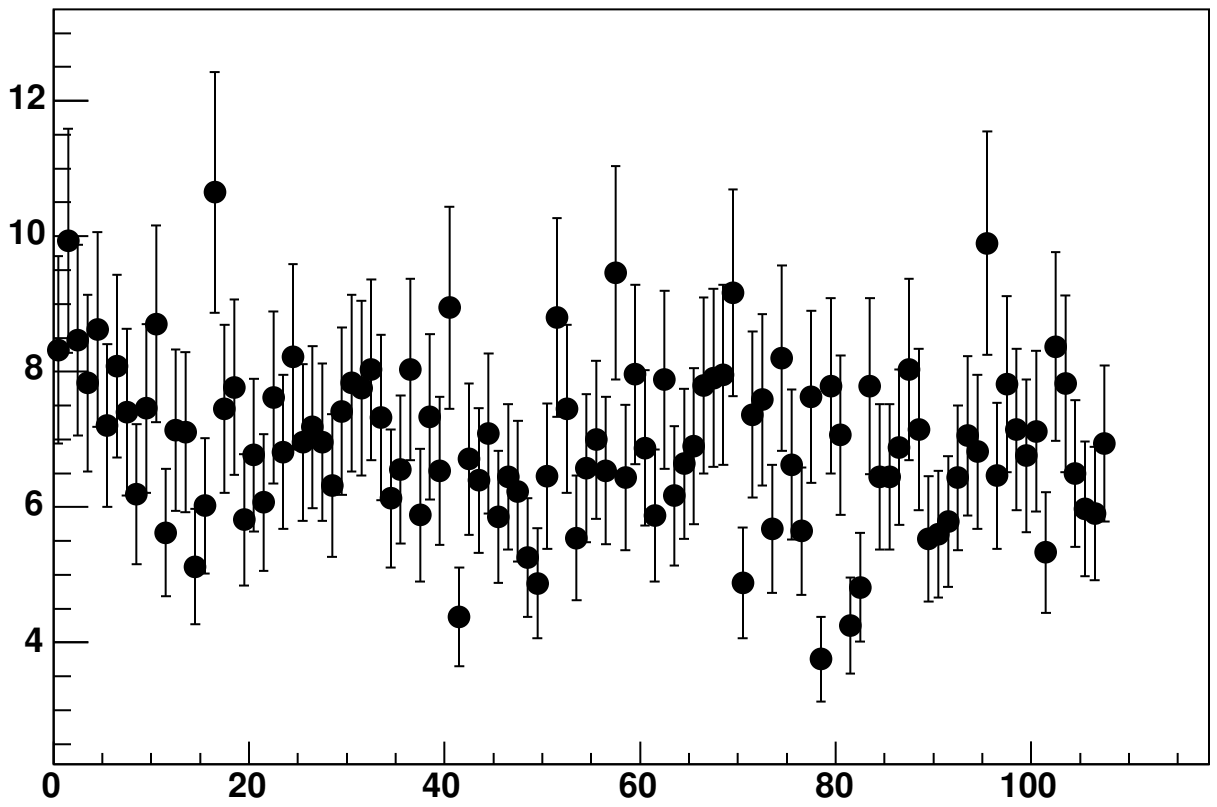
Enable 4, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



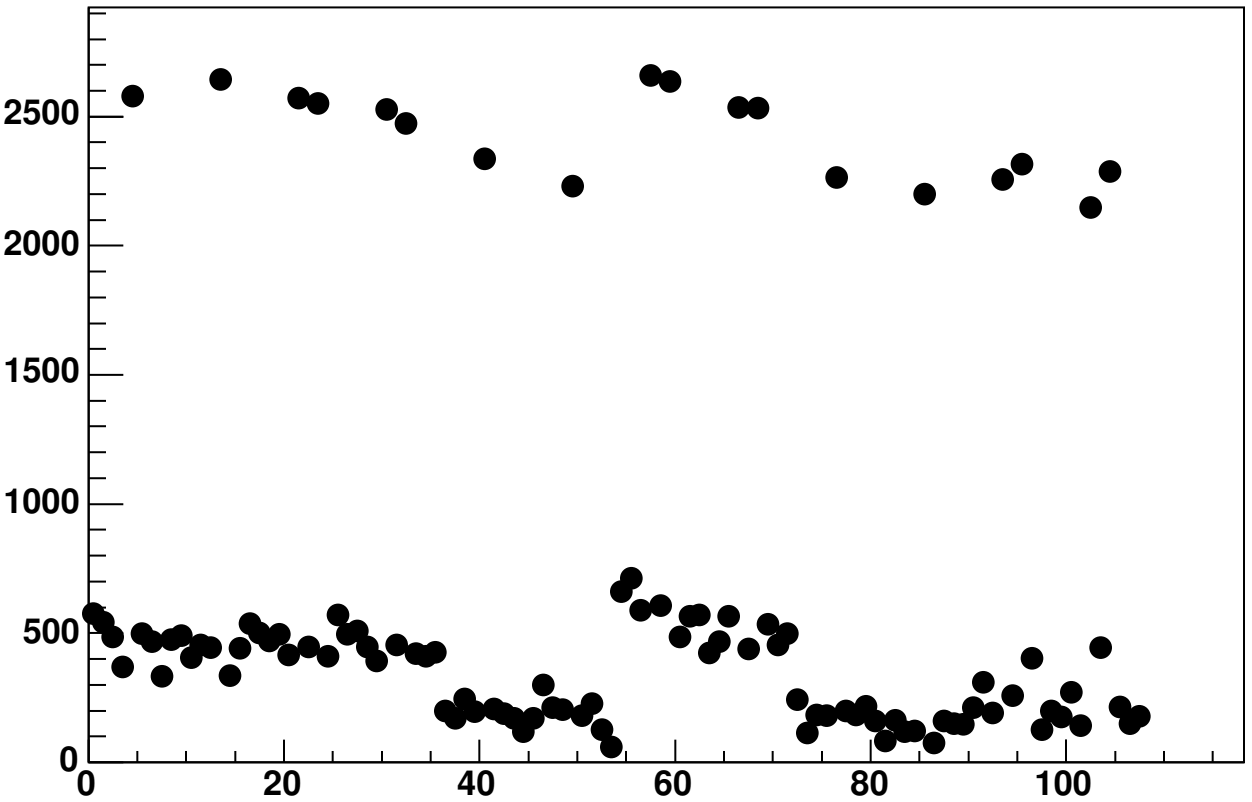
Enable 4, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



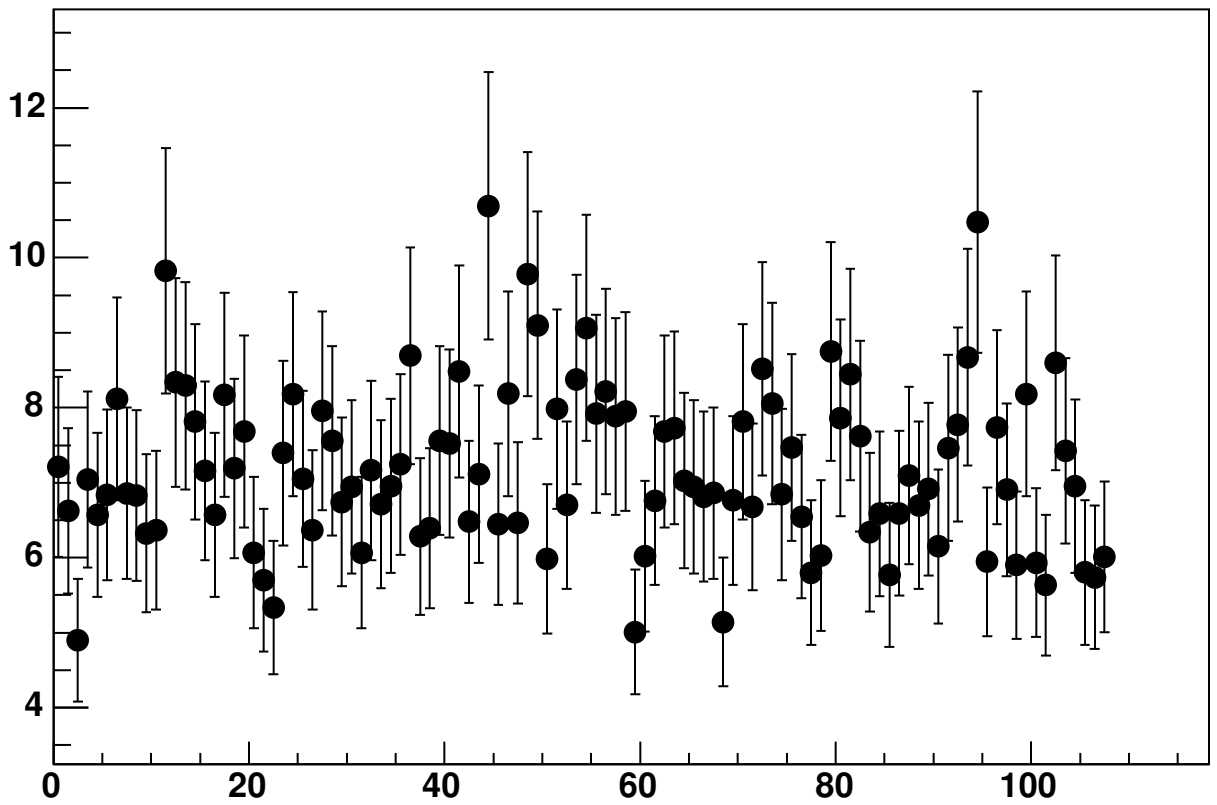
Enable 4, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



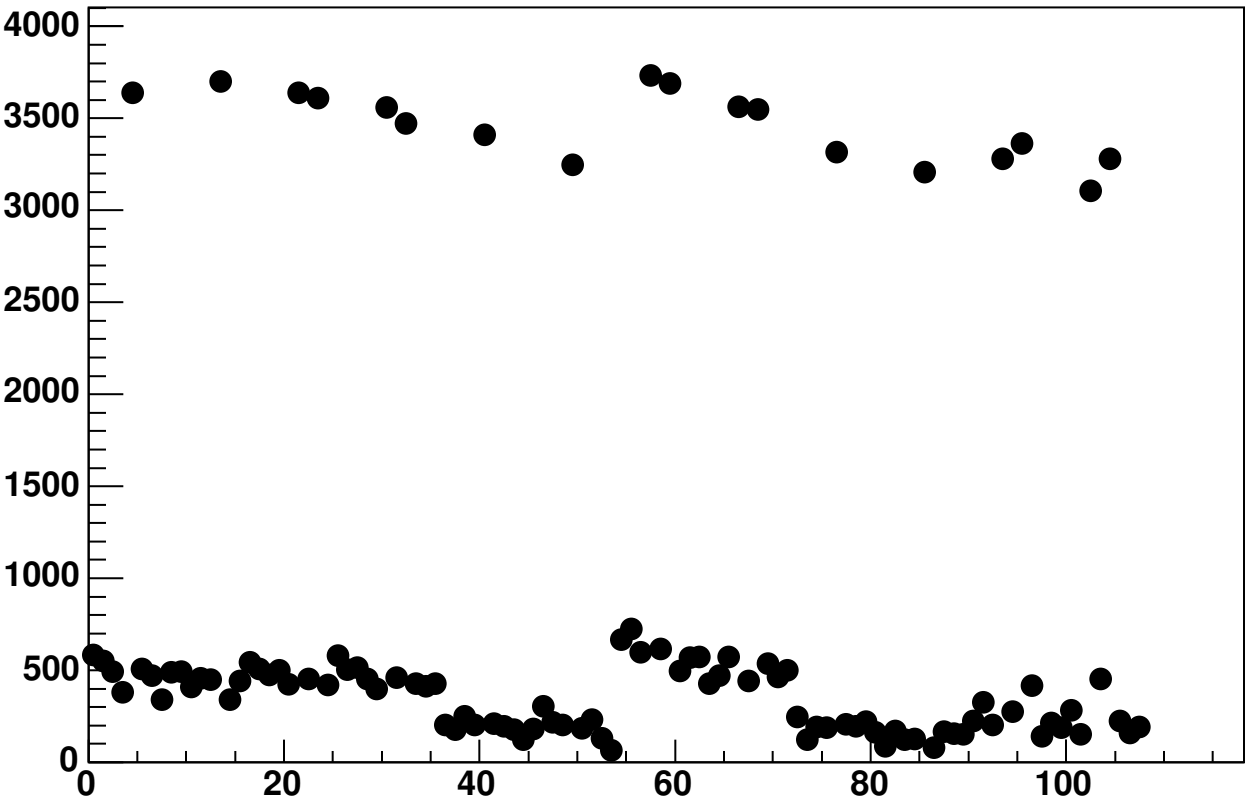
Enable 4, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



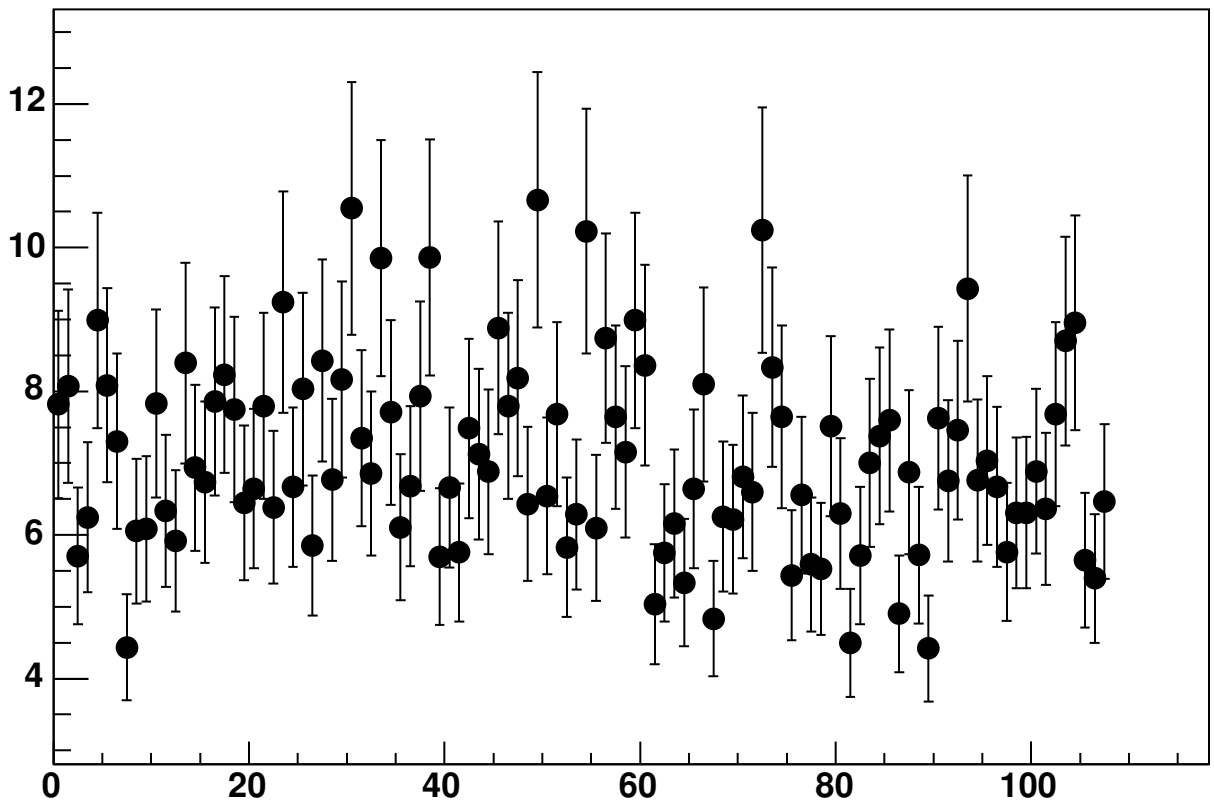
Enable 4, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



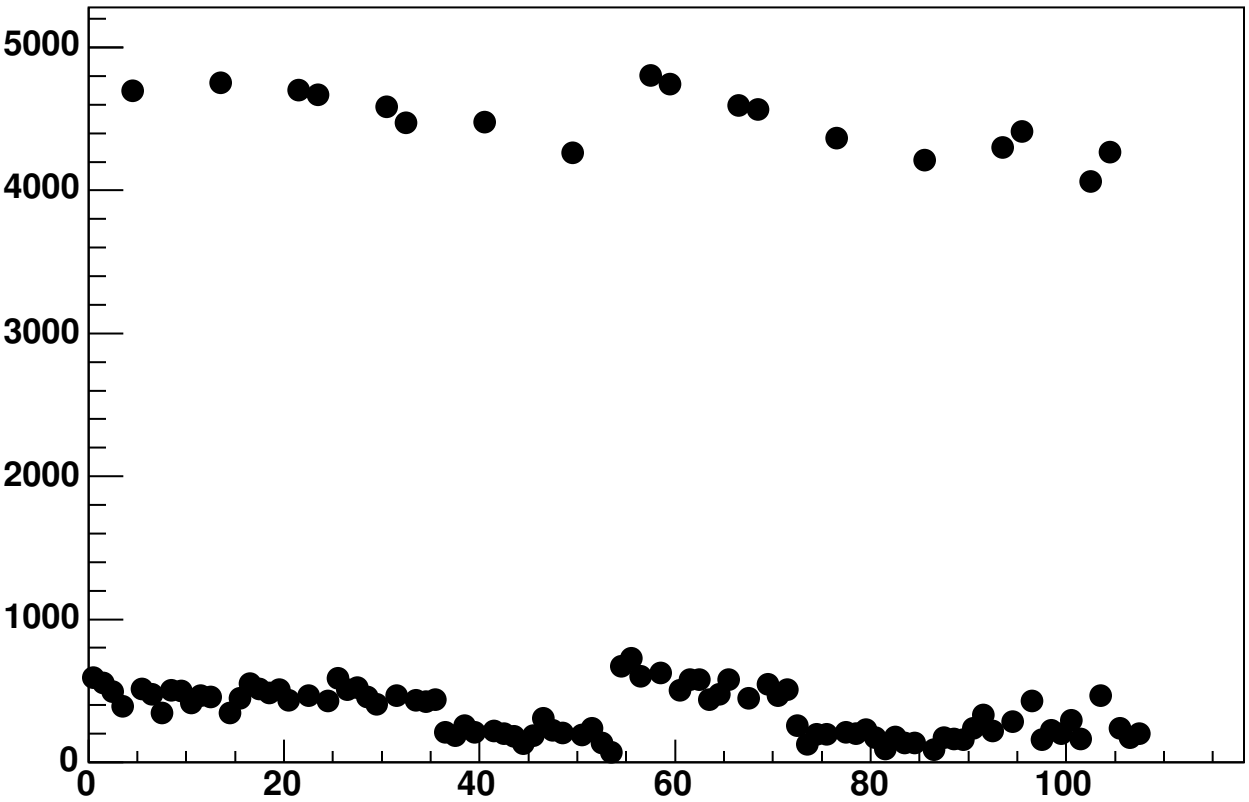
Enable 4, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



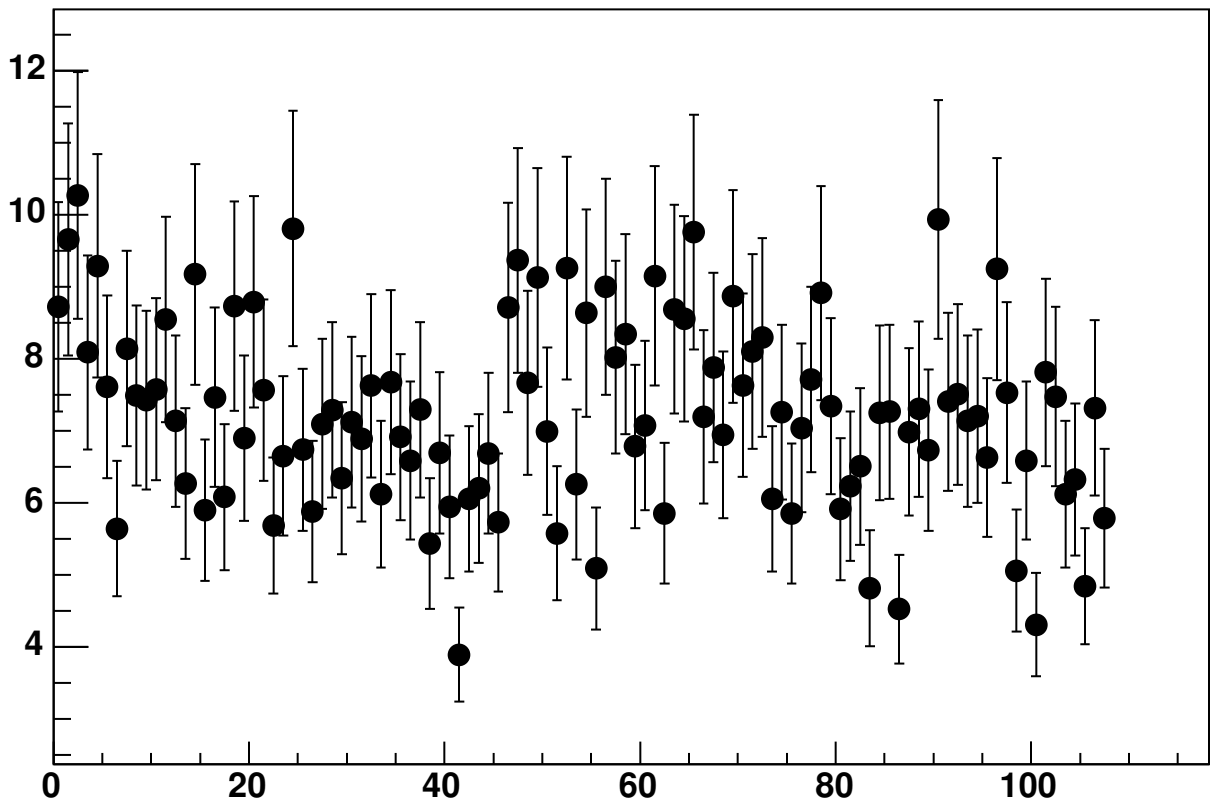
Enable 4, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



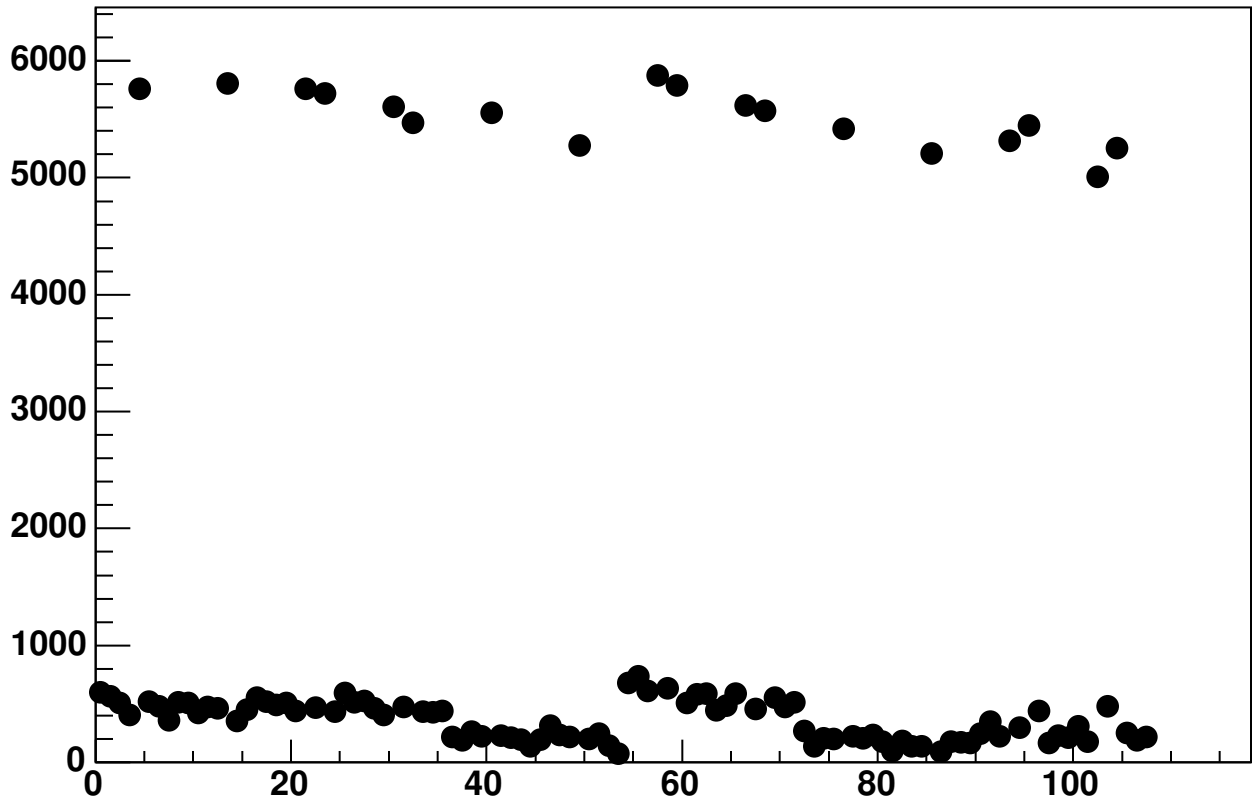
Enable 4, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



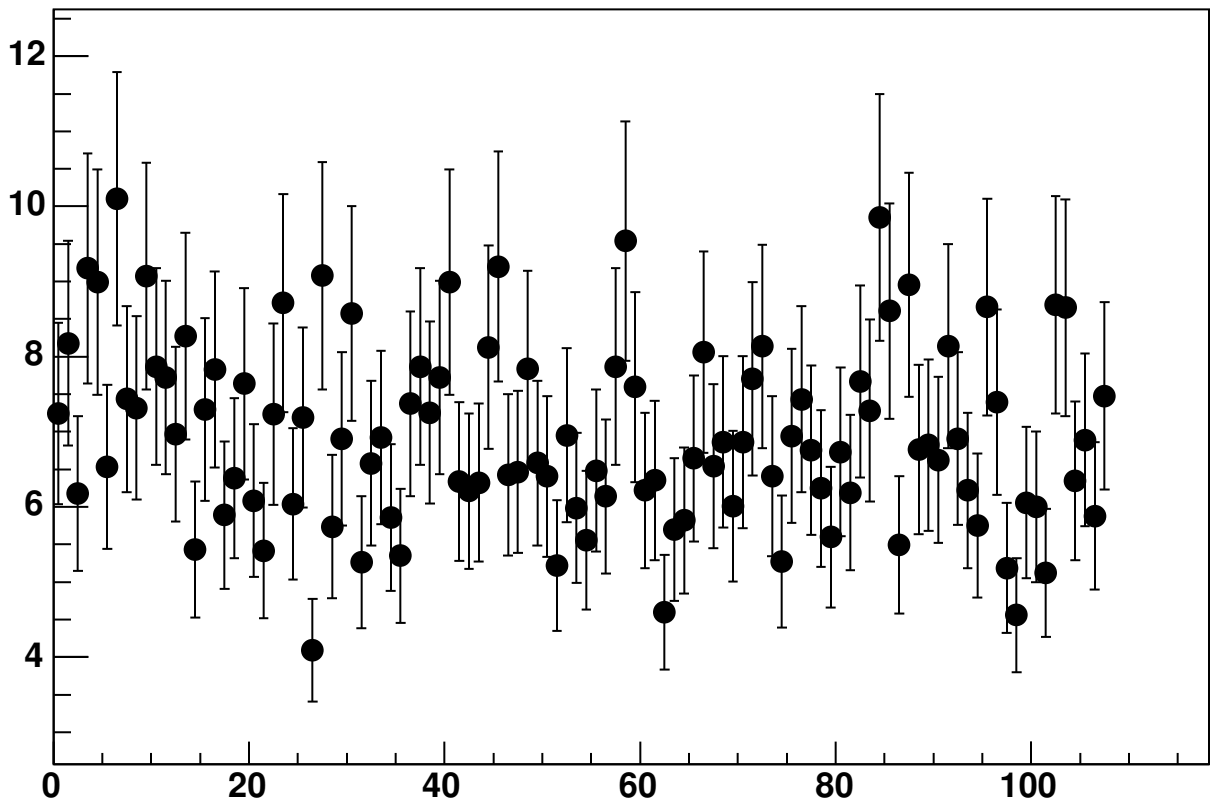
Enable 4, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



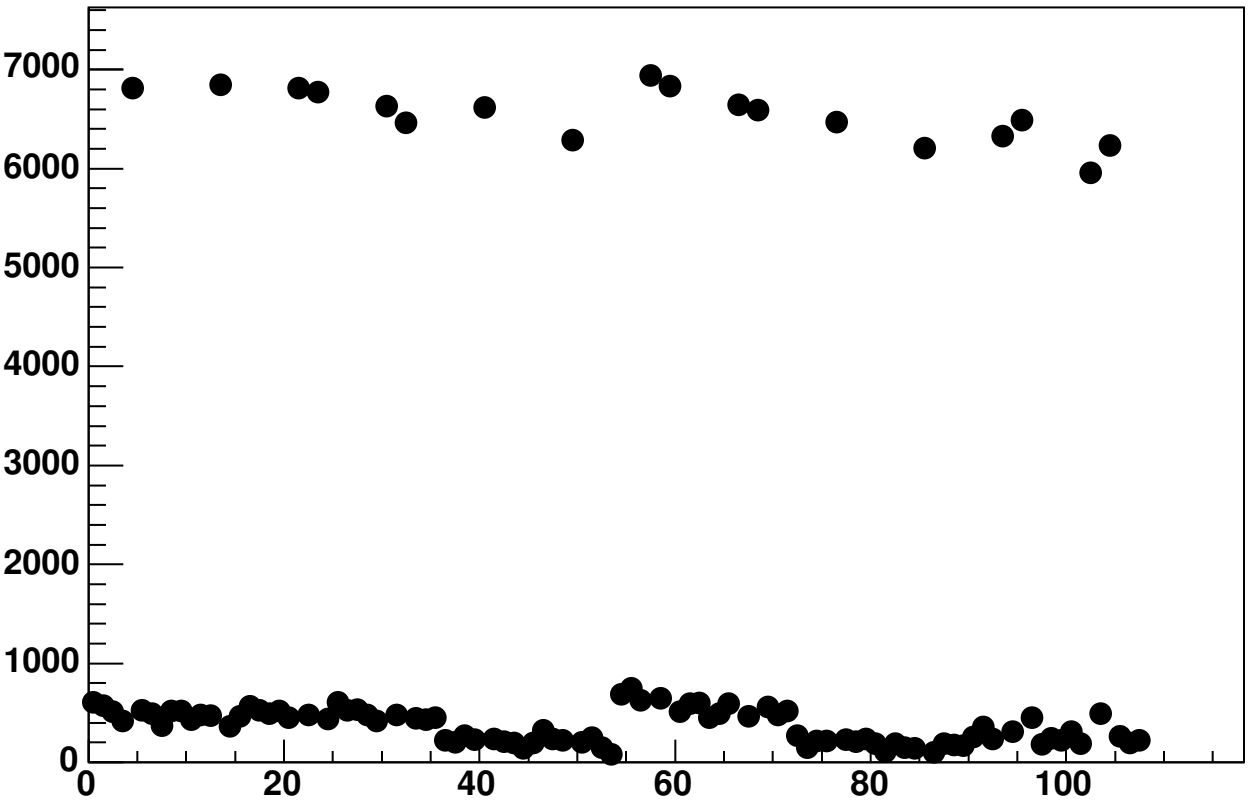
Enable 4, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



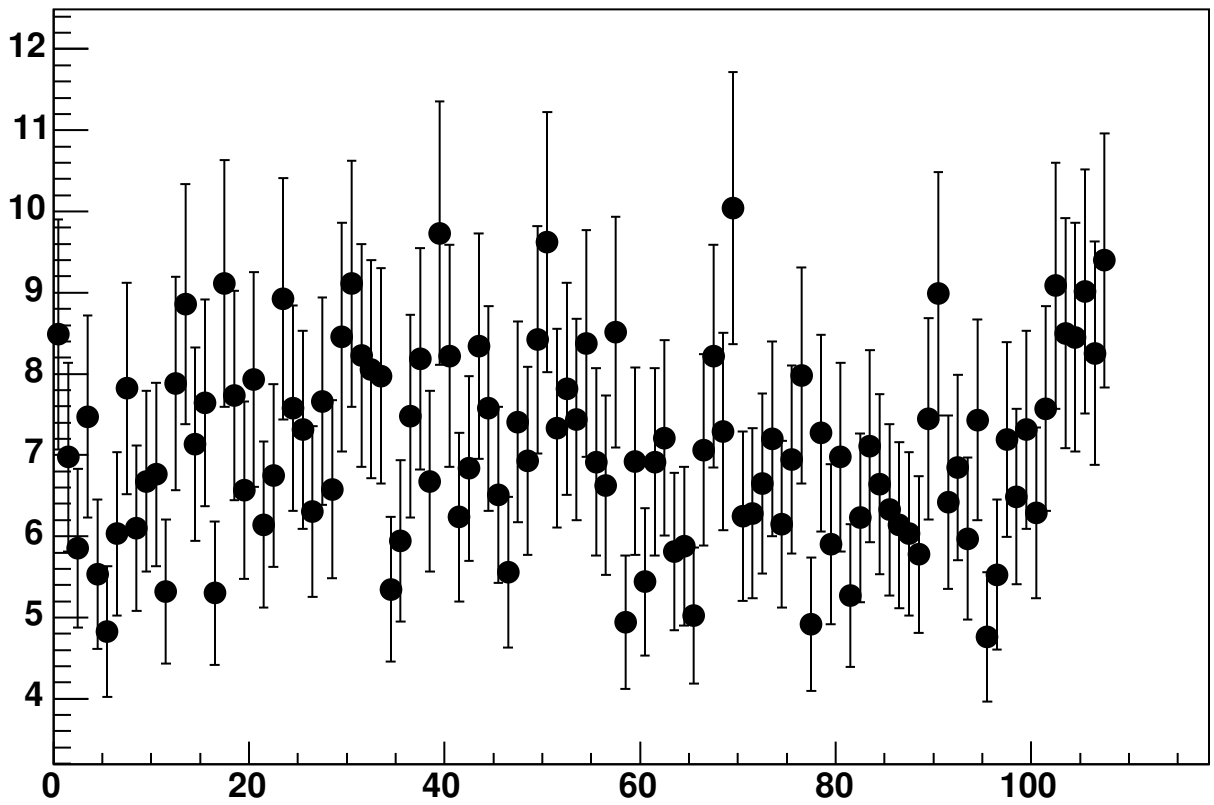
Enable 4, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



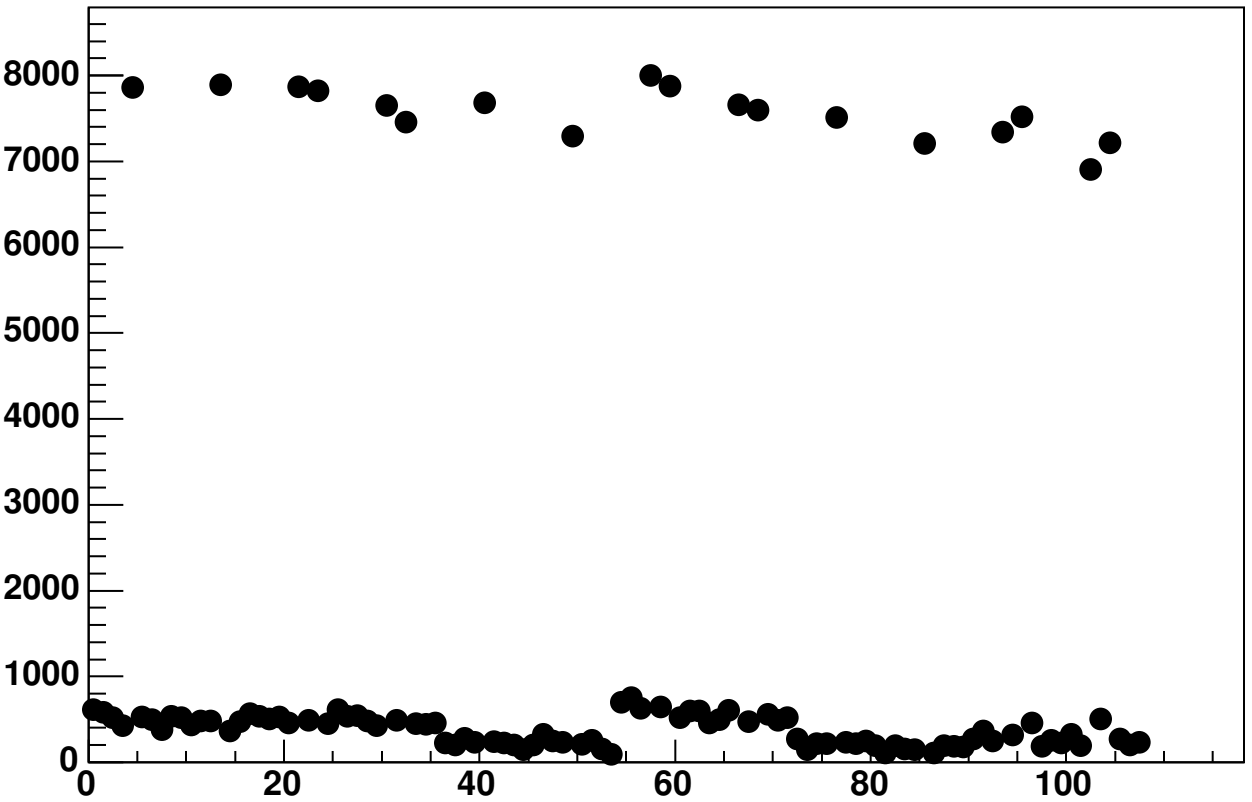
Enable 4, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



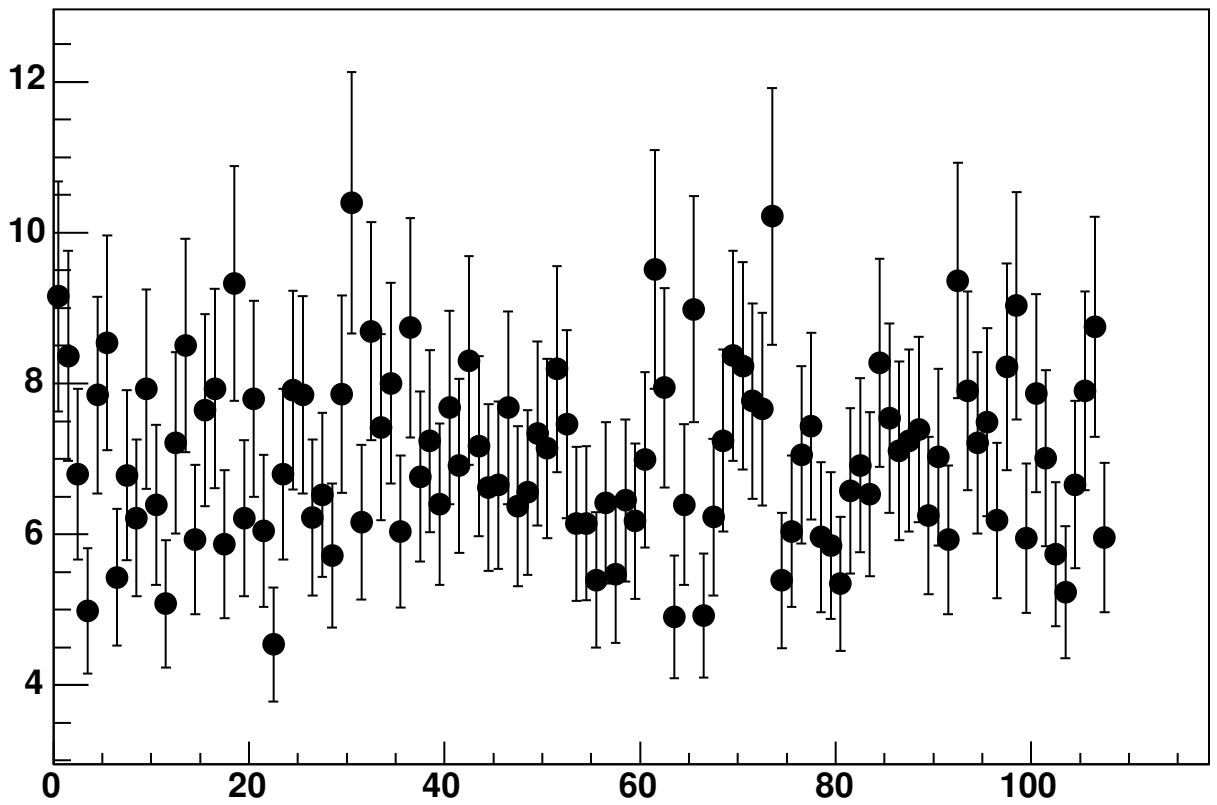
Enable 4, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



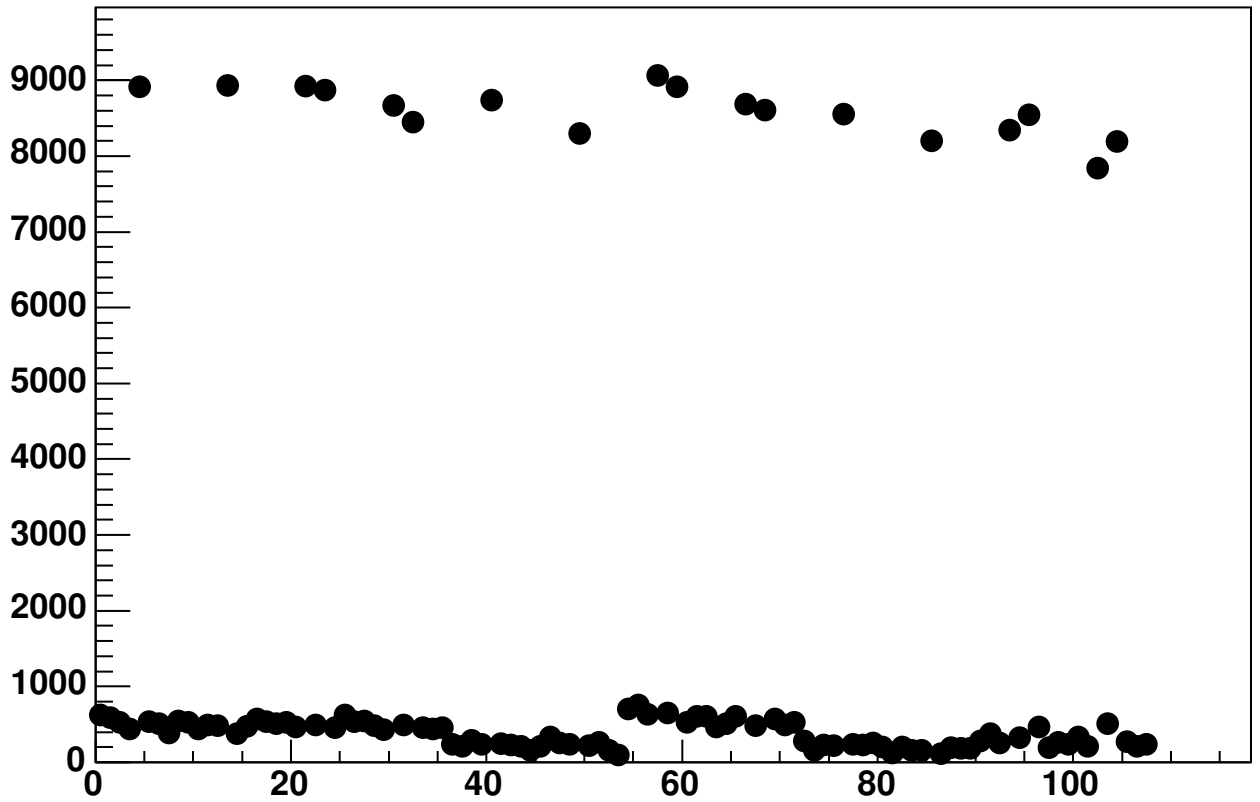
Enable 4, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



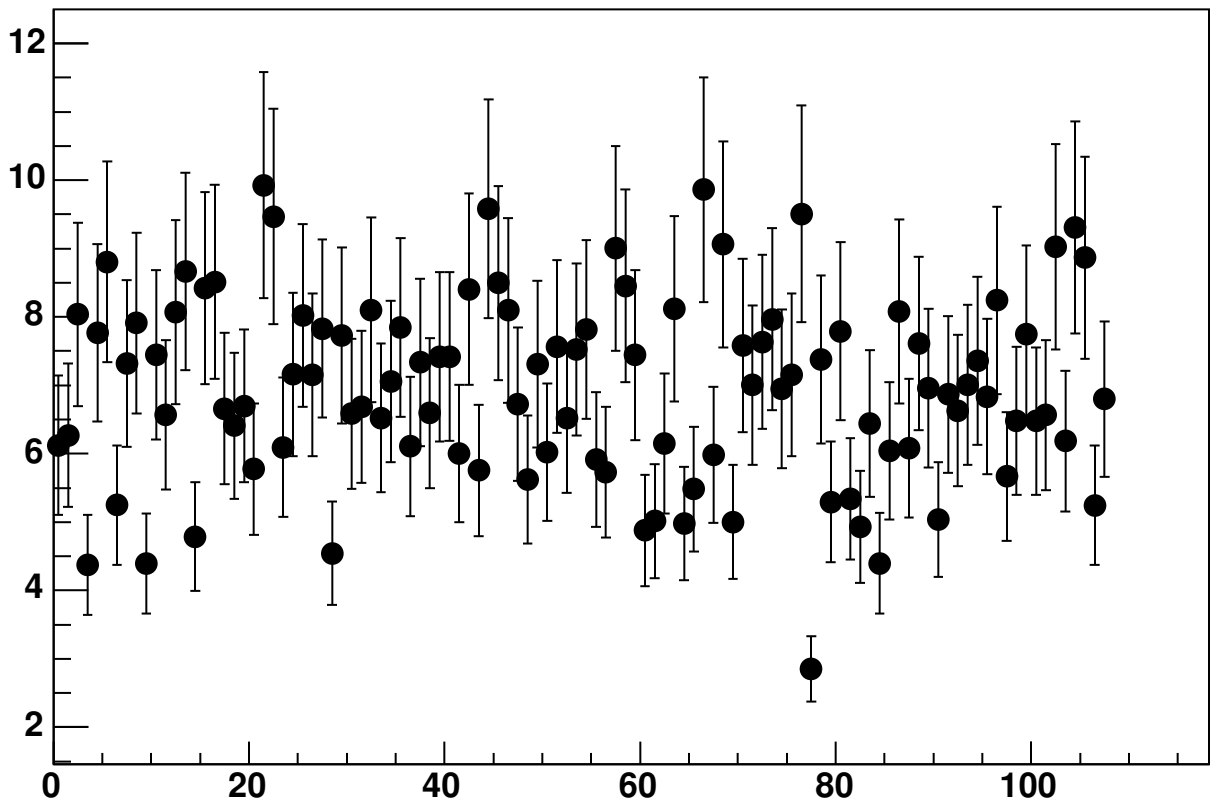
Enable 4, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



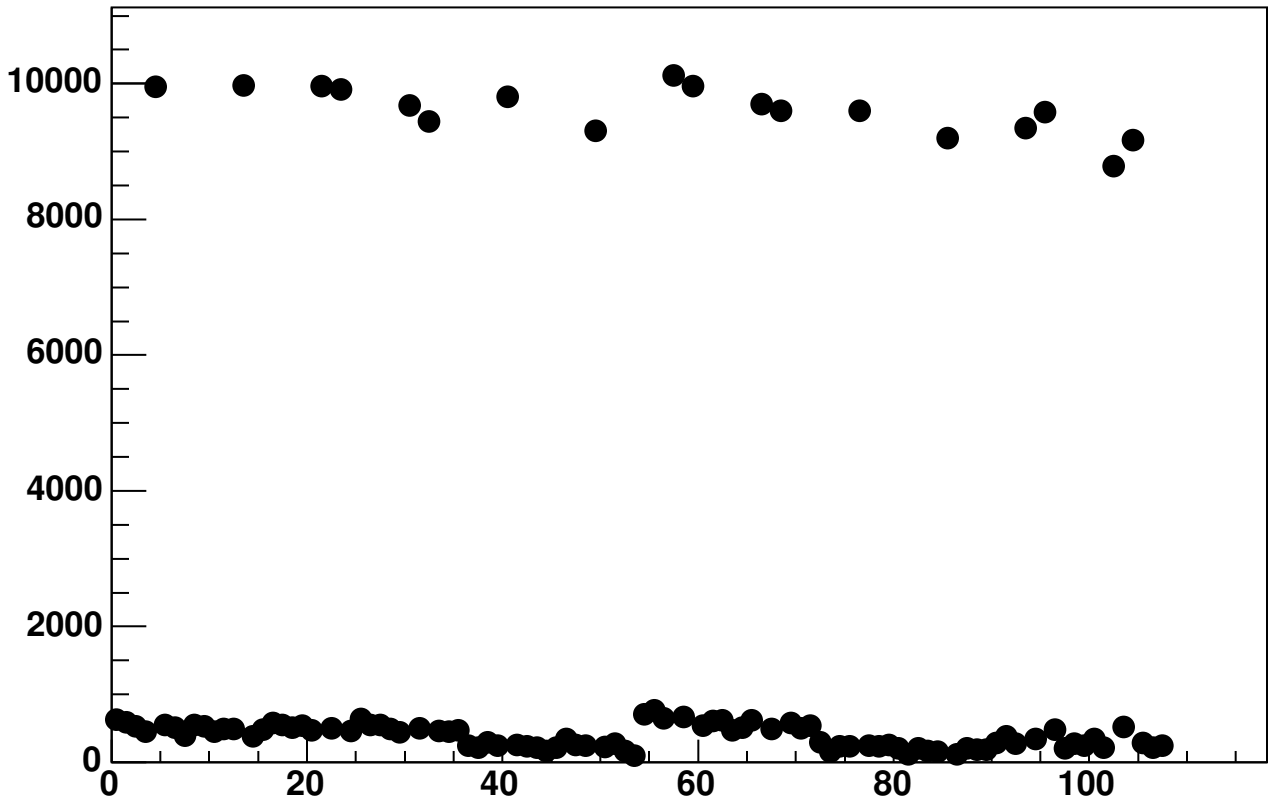
Enable 4, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



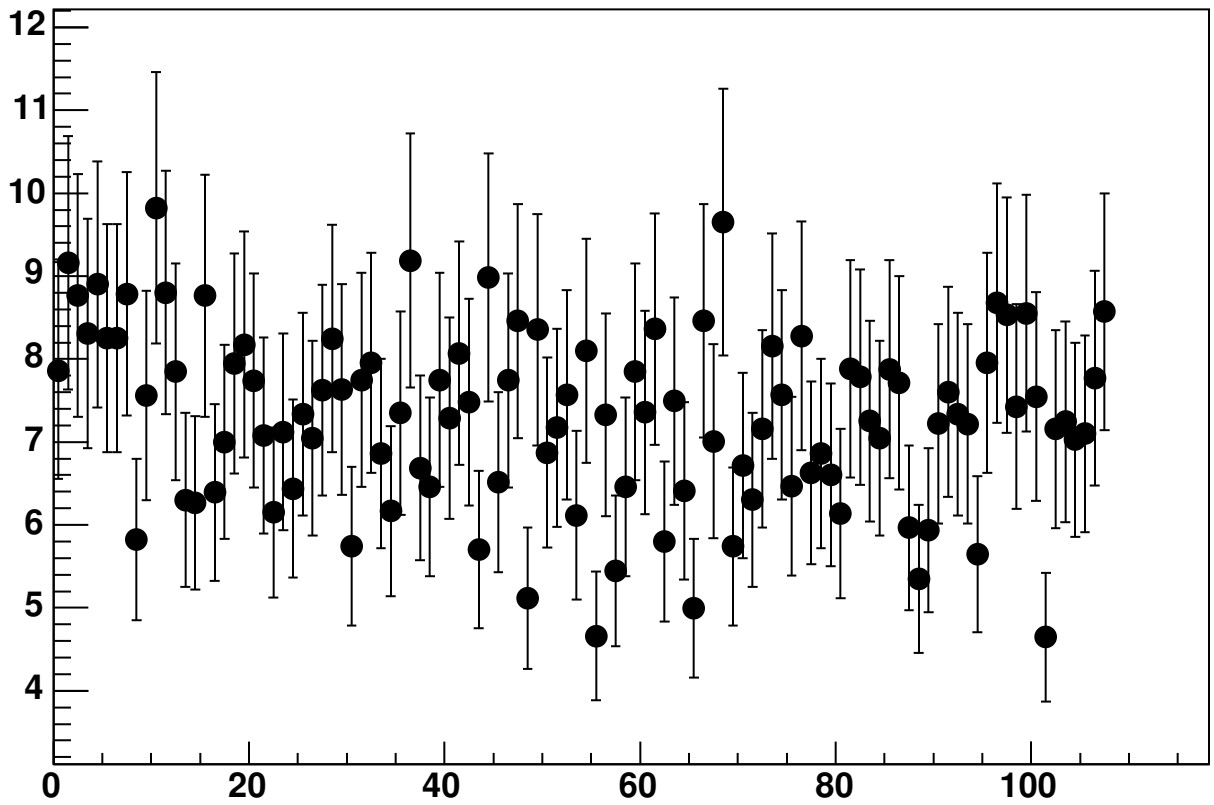
Enable 4, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



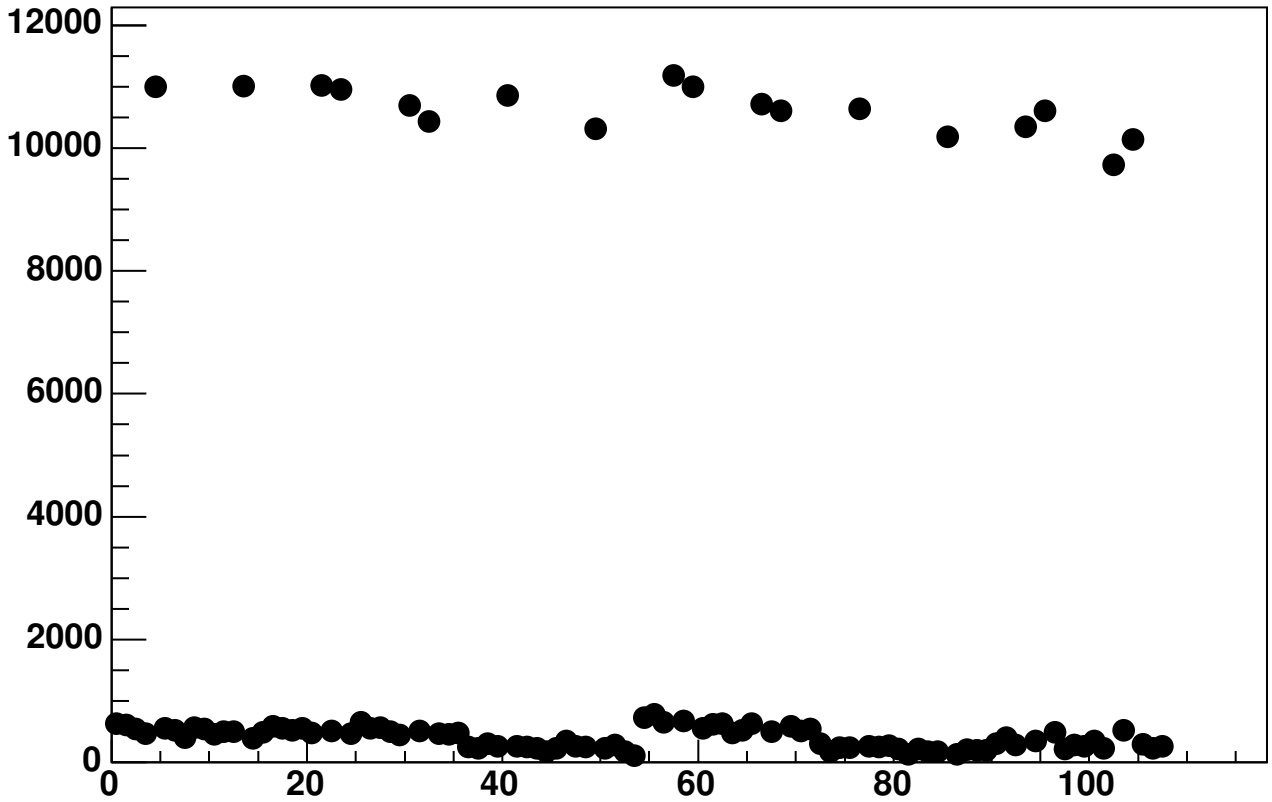
Enable 4, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



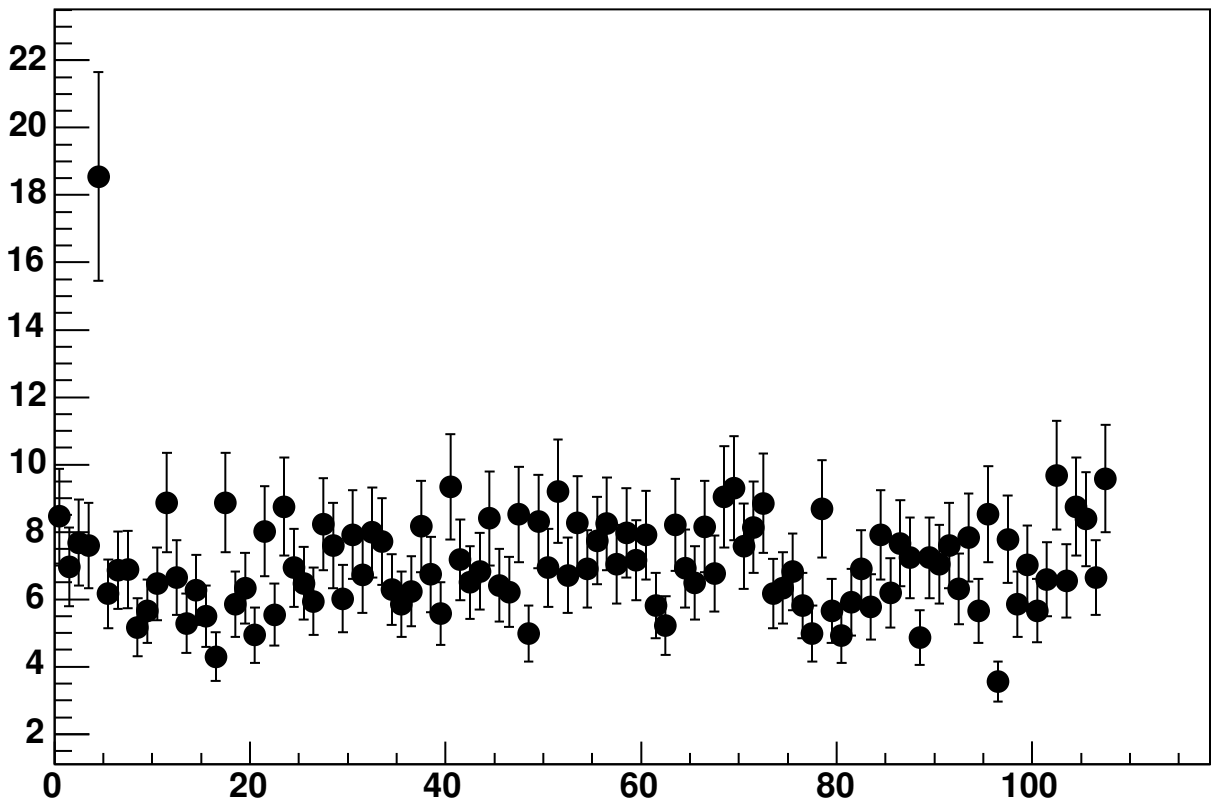
Enable 4, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



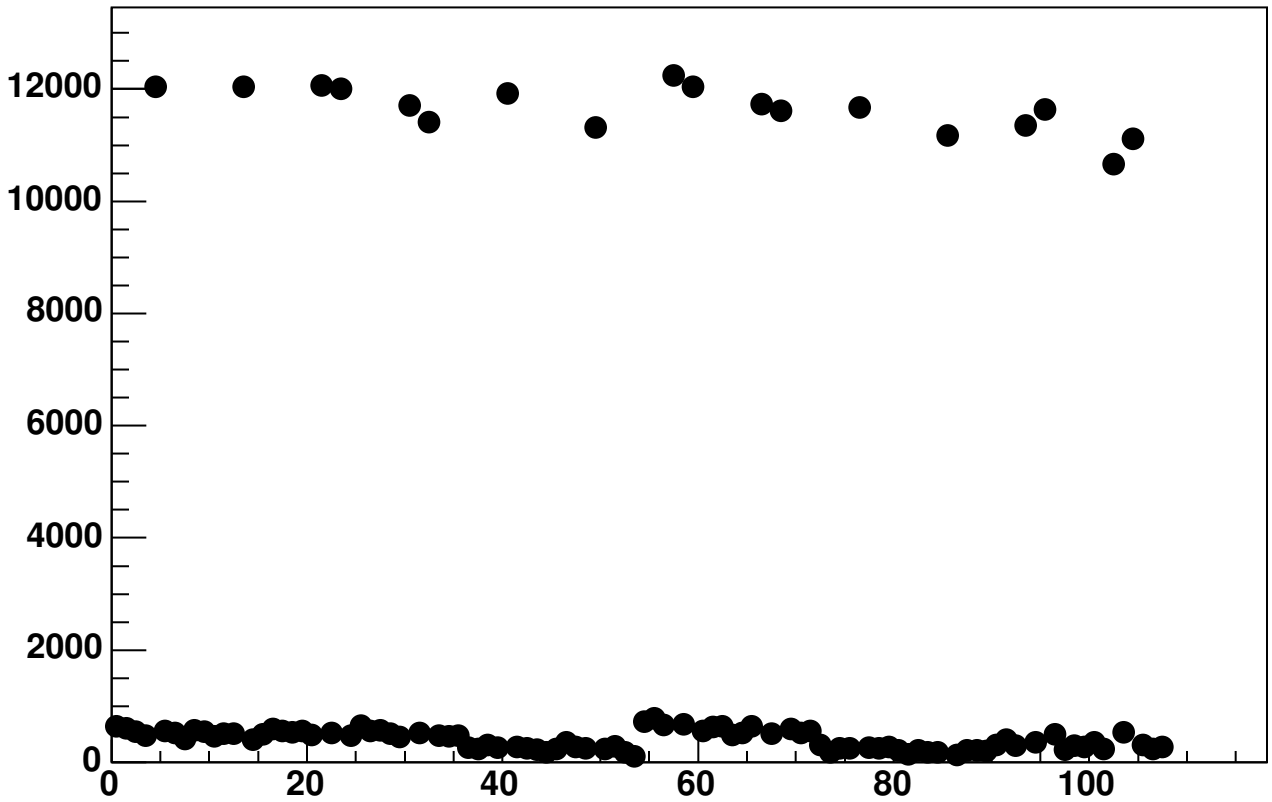
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



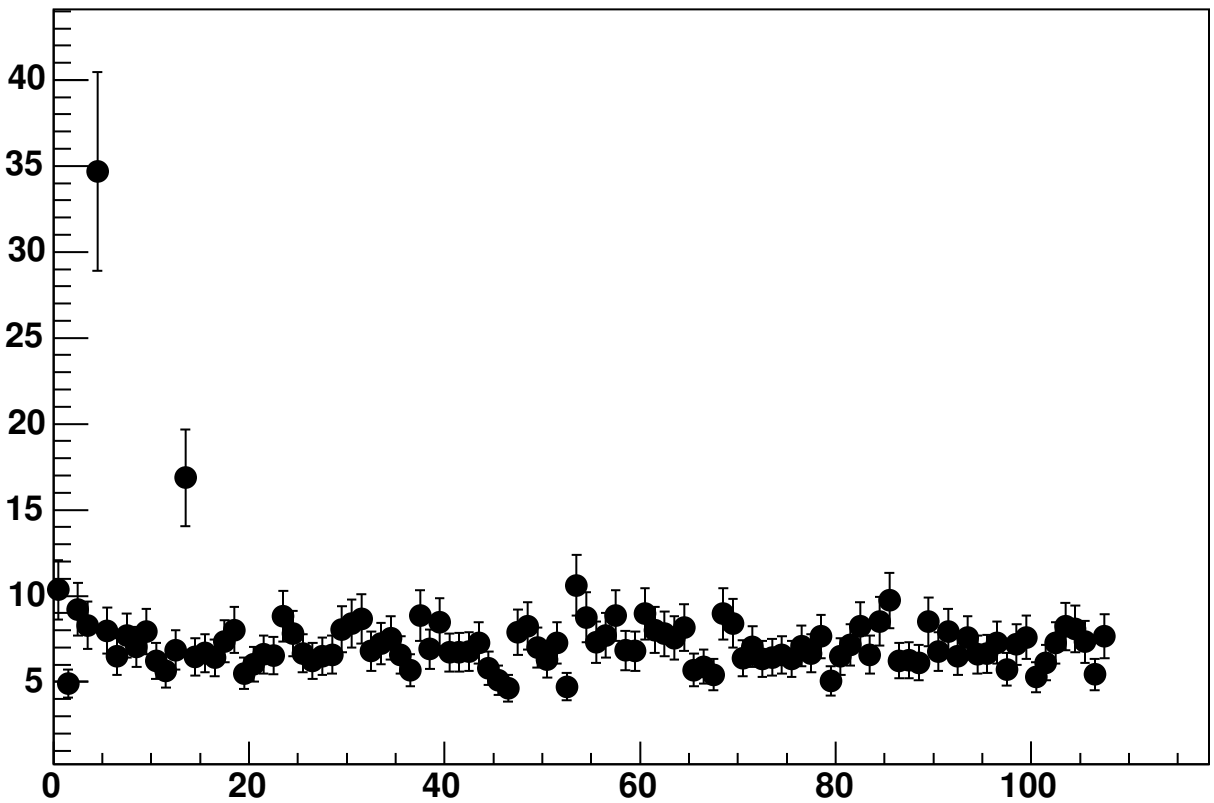
Enable 4, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



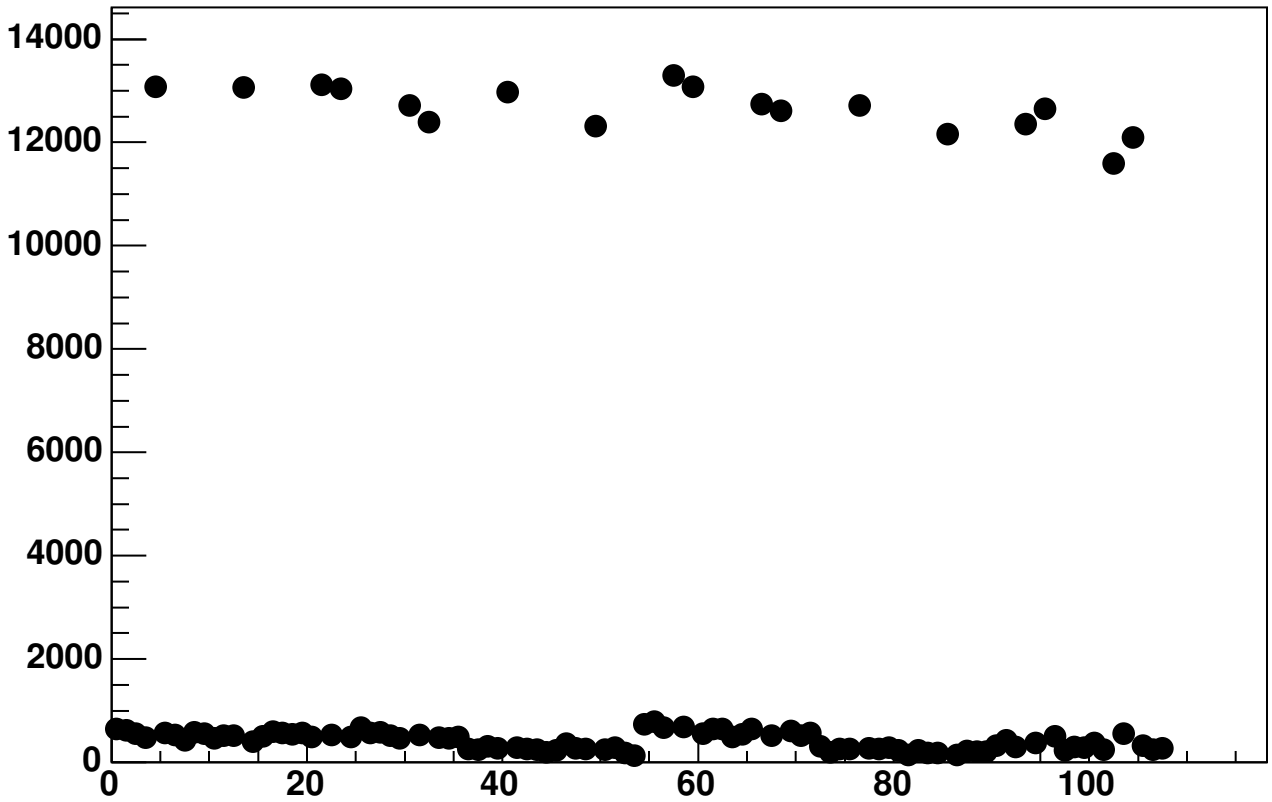
Enable 4, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



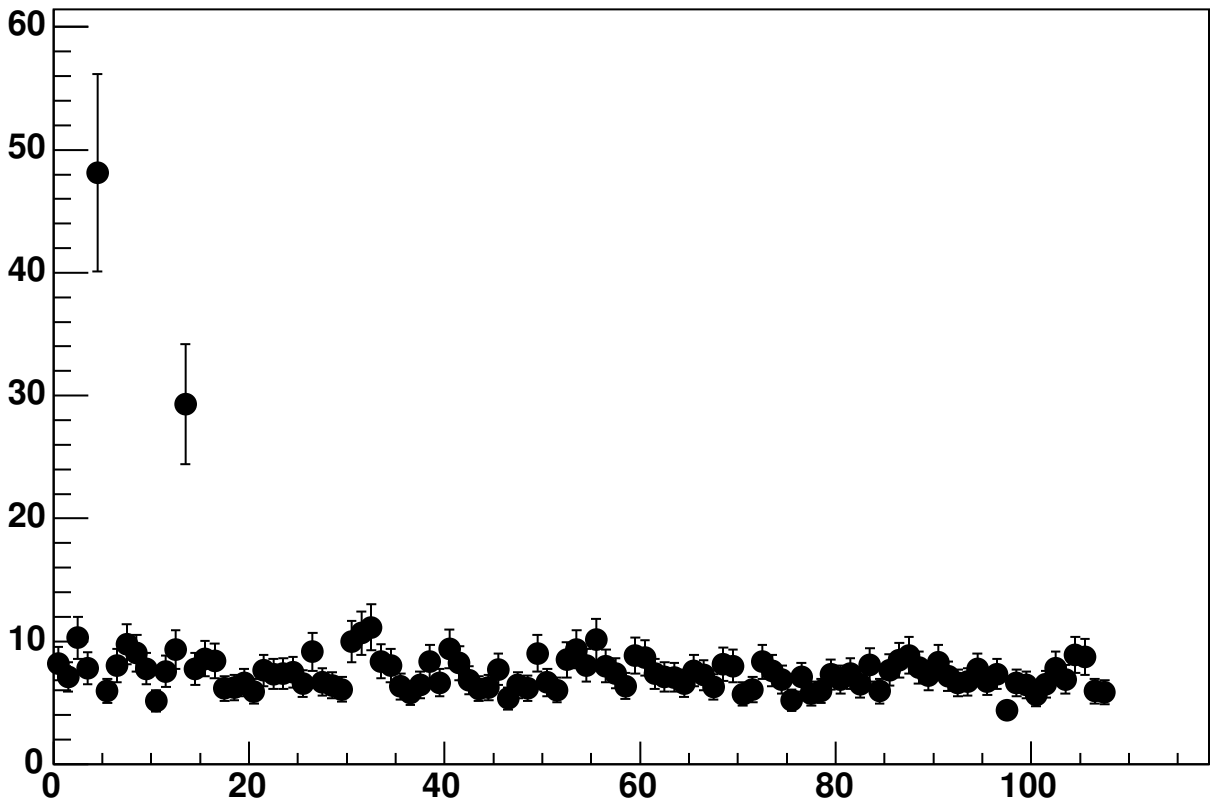
Enable 4, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



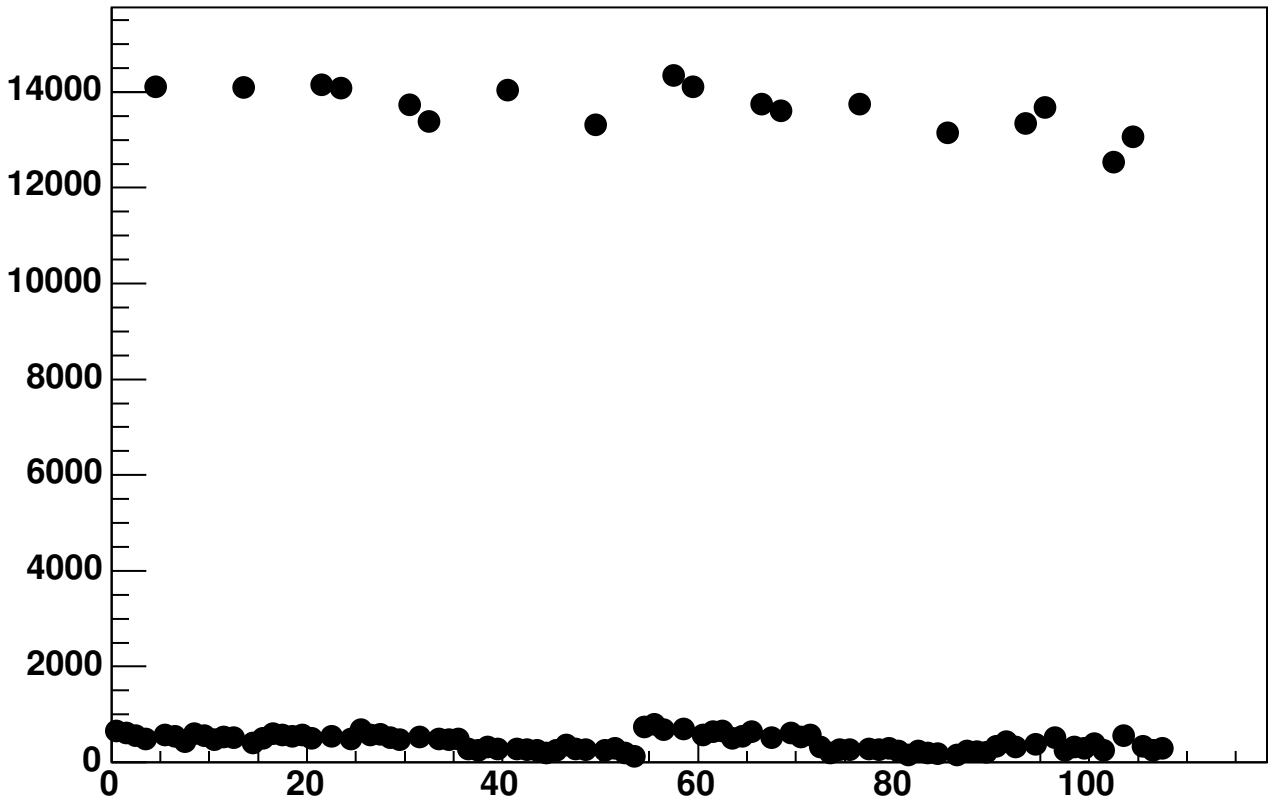
Enable 4, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



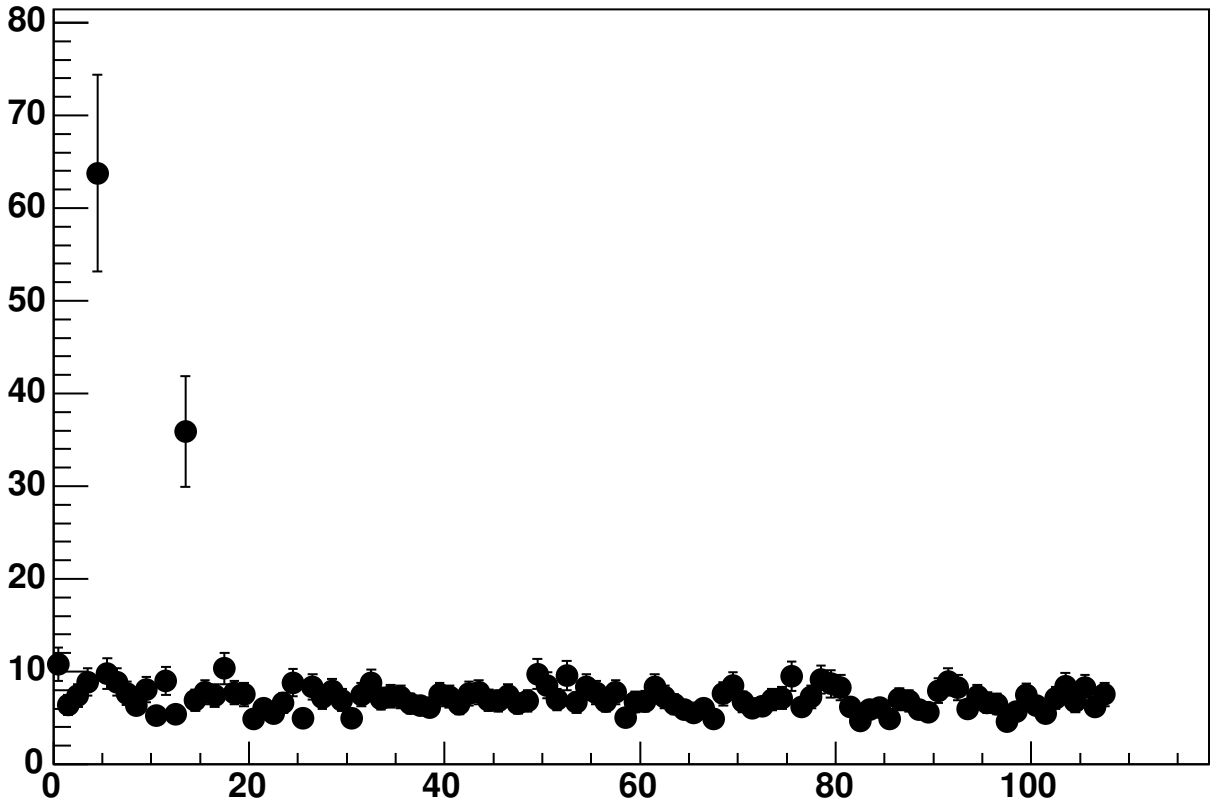
Enable 4, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



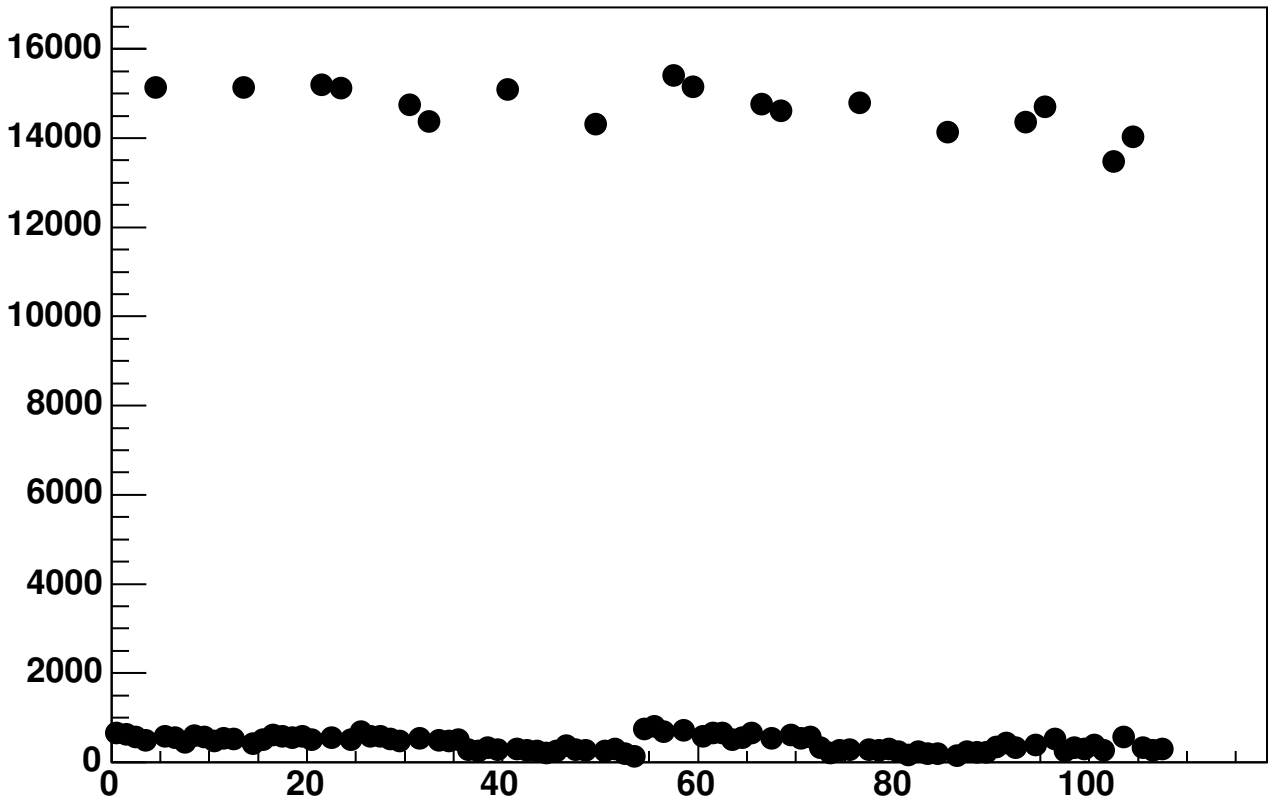
Enable 4, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



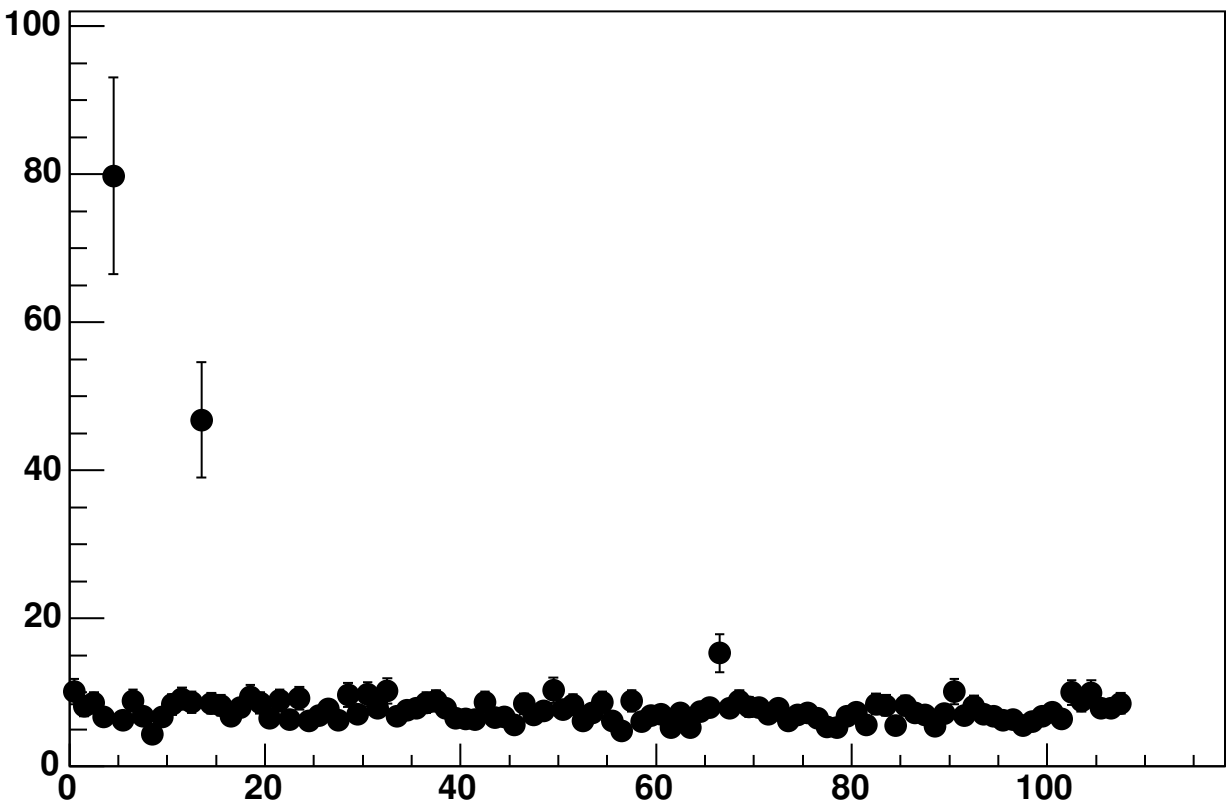
Enable 4, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



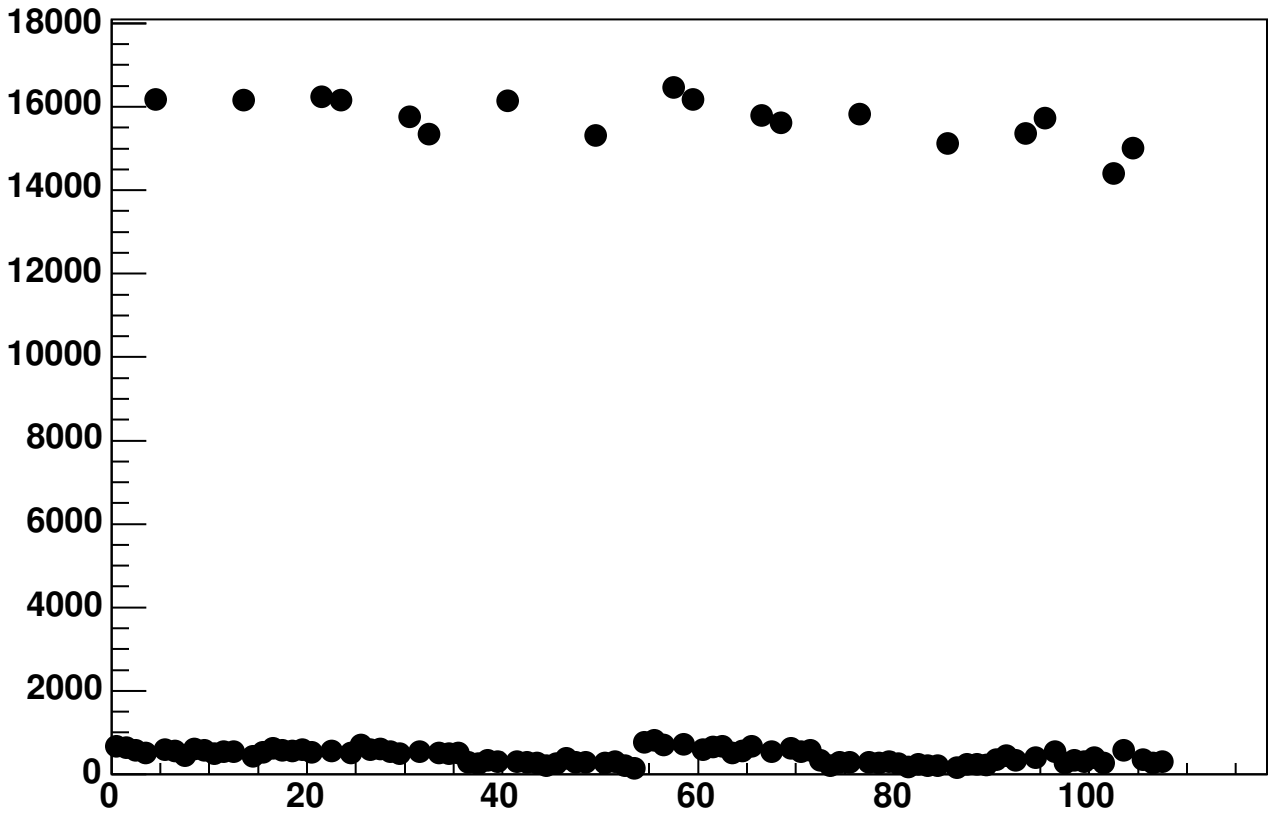
Enable 4, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



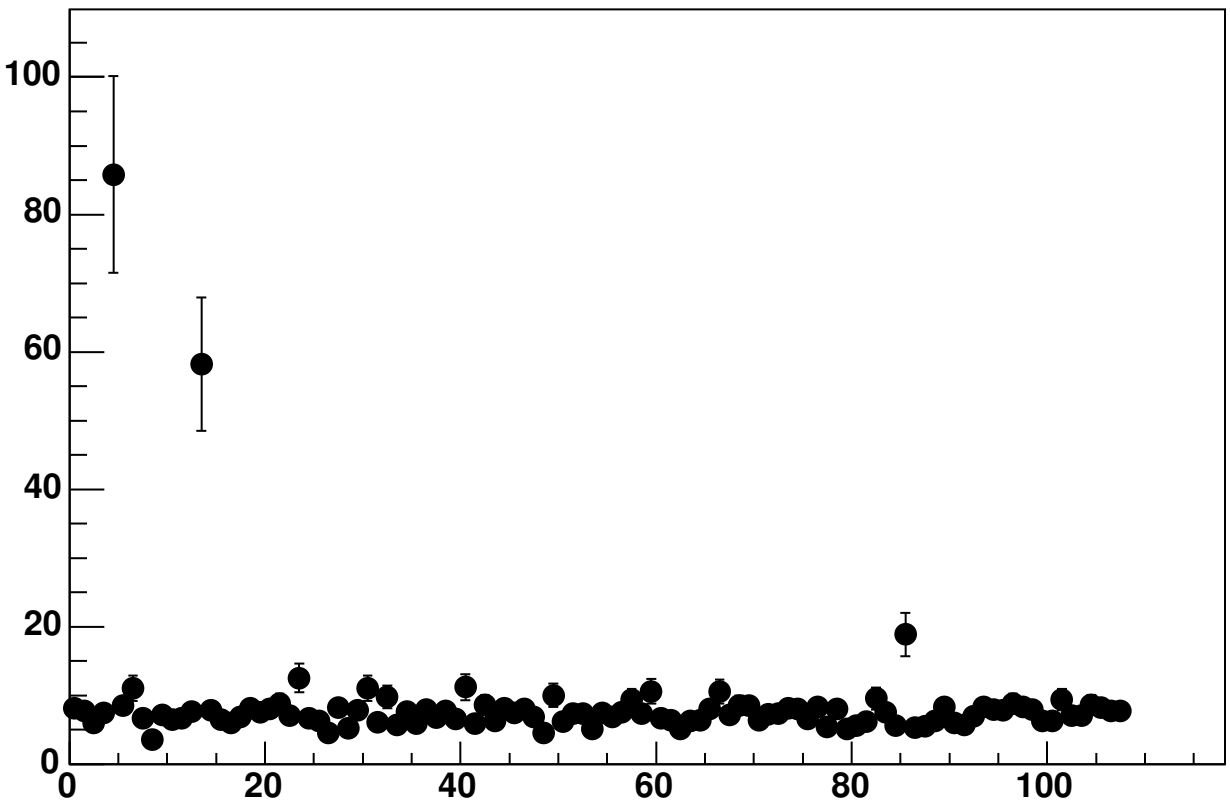
Enable 4, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



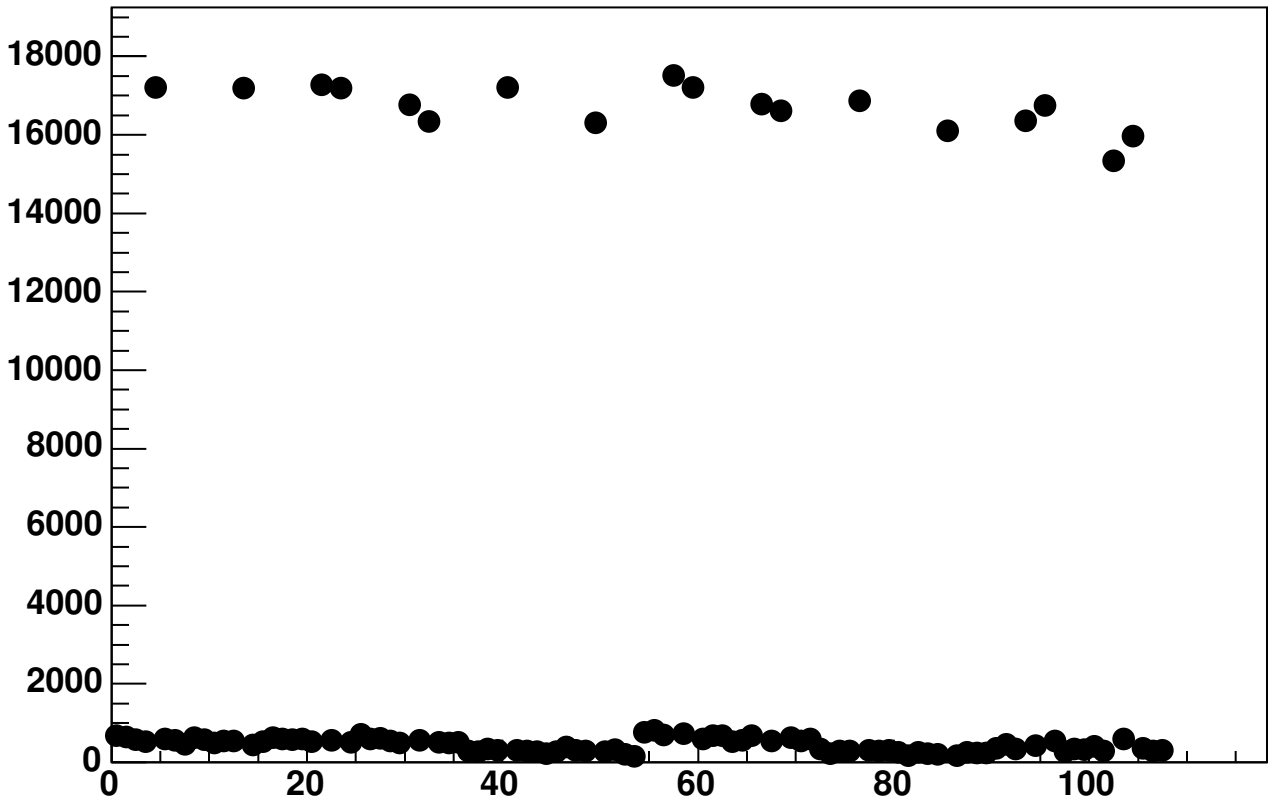
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



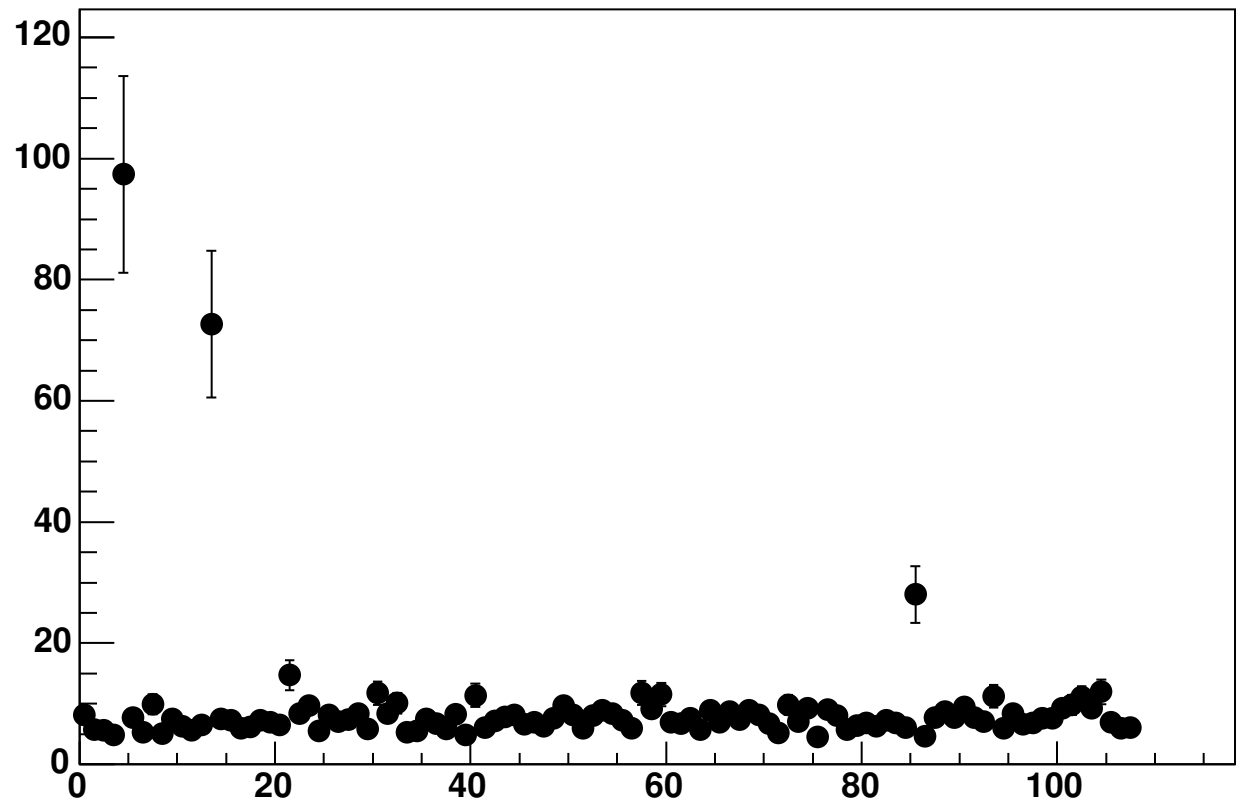
Enable 4, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



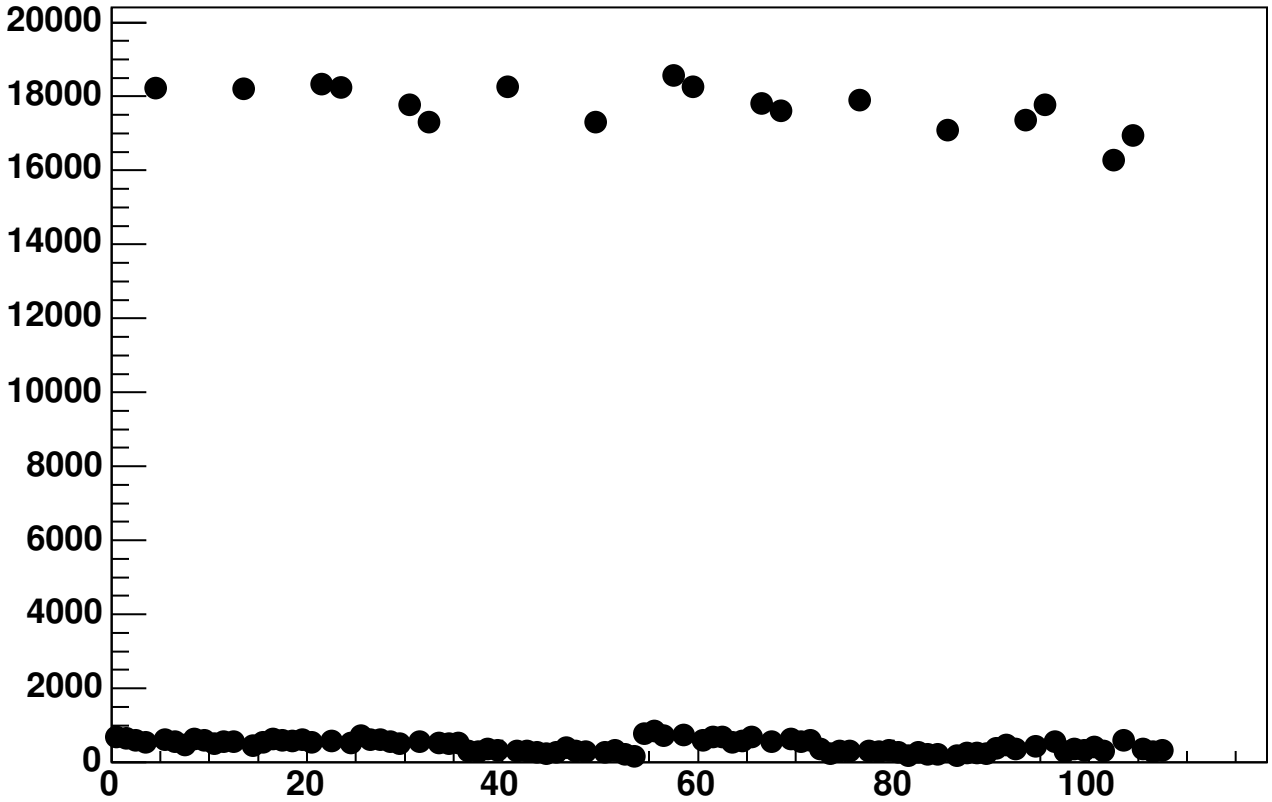
Enable 4, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



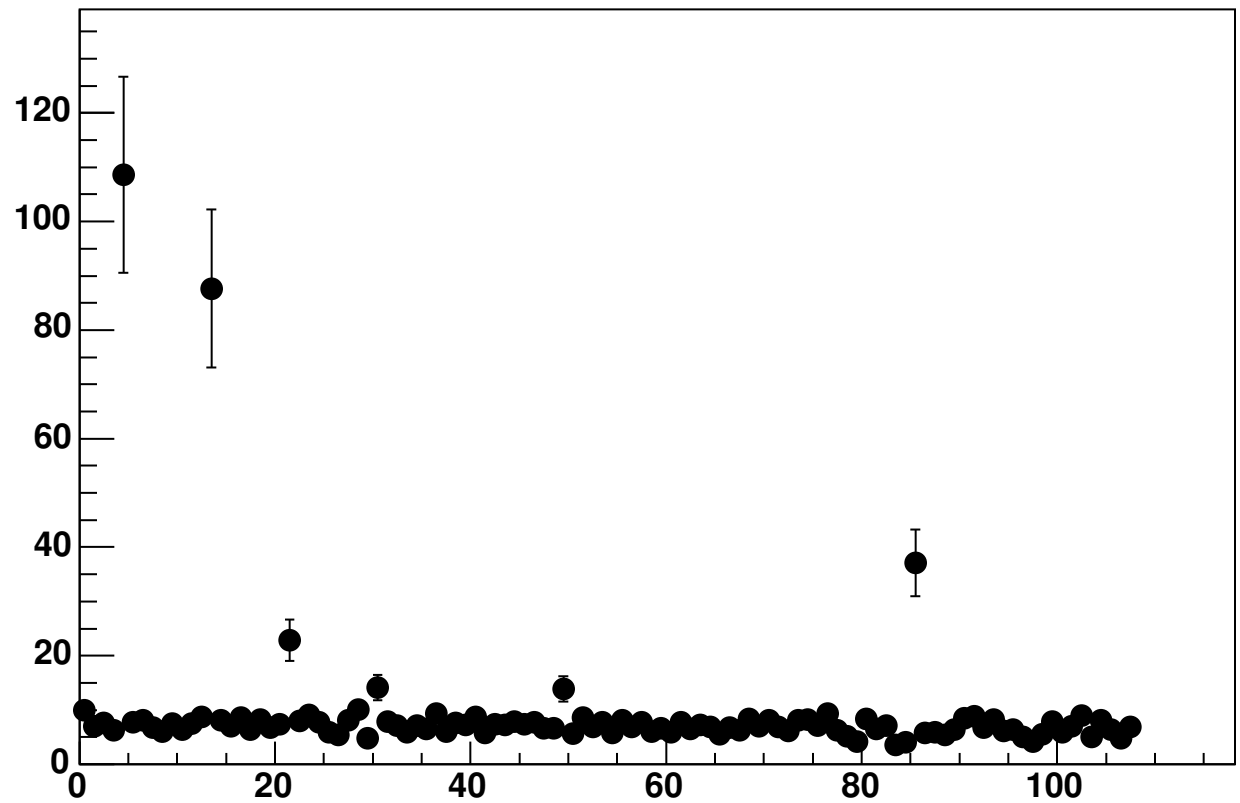
Enable 4, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



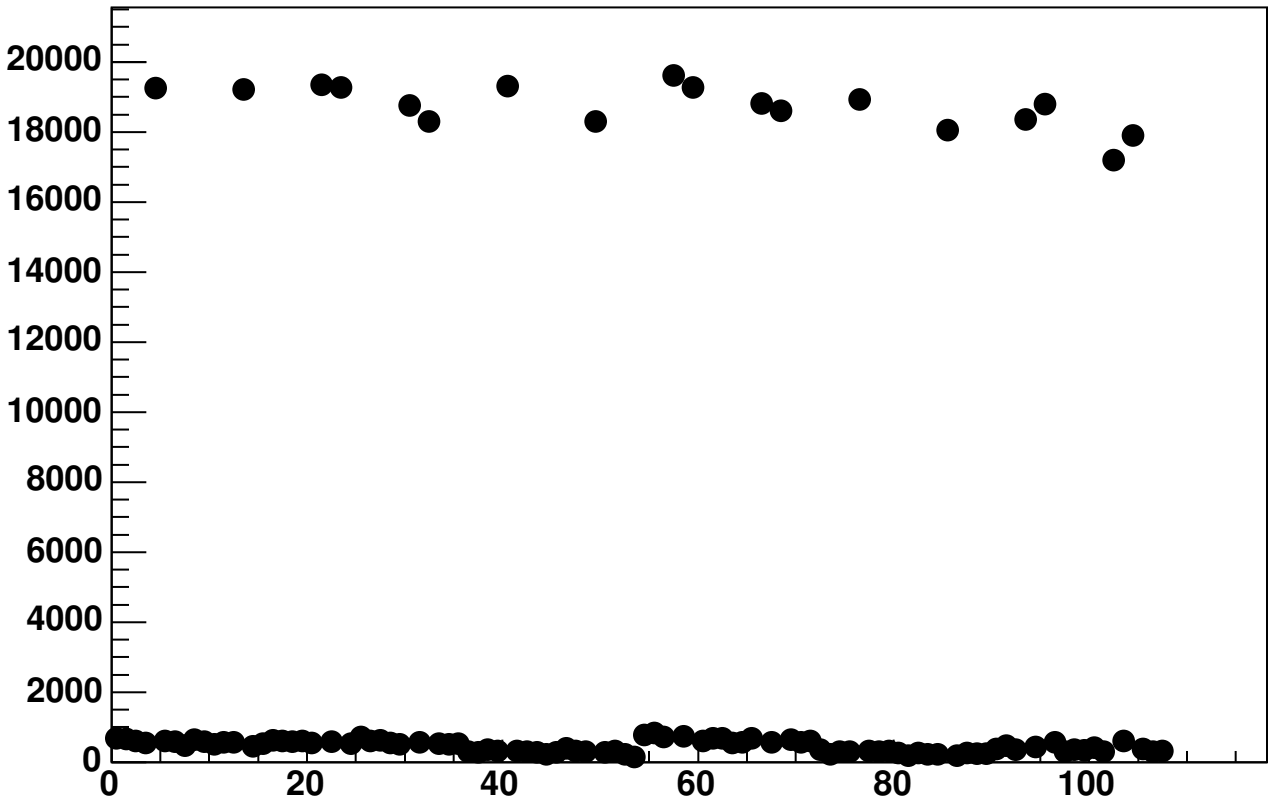
Enable 4, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



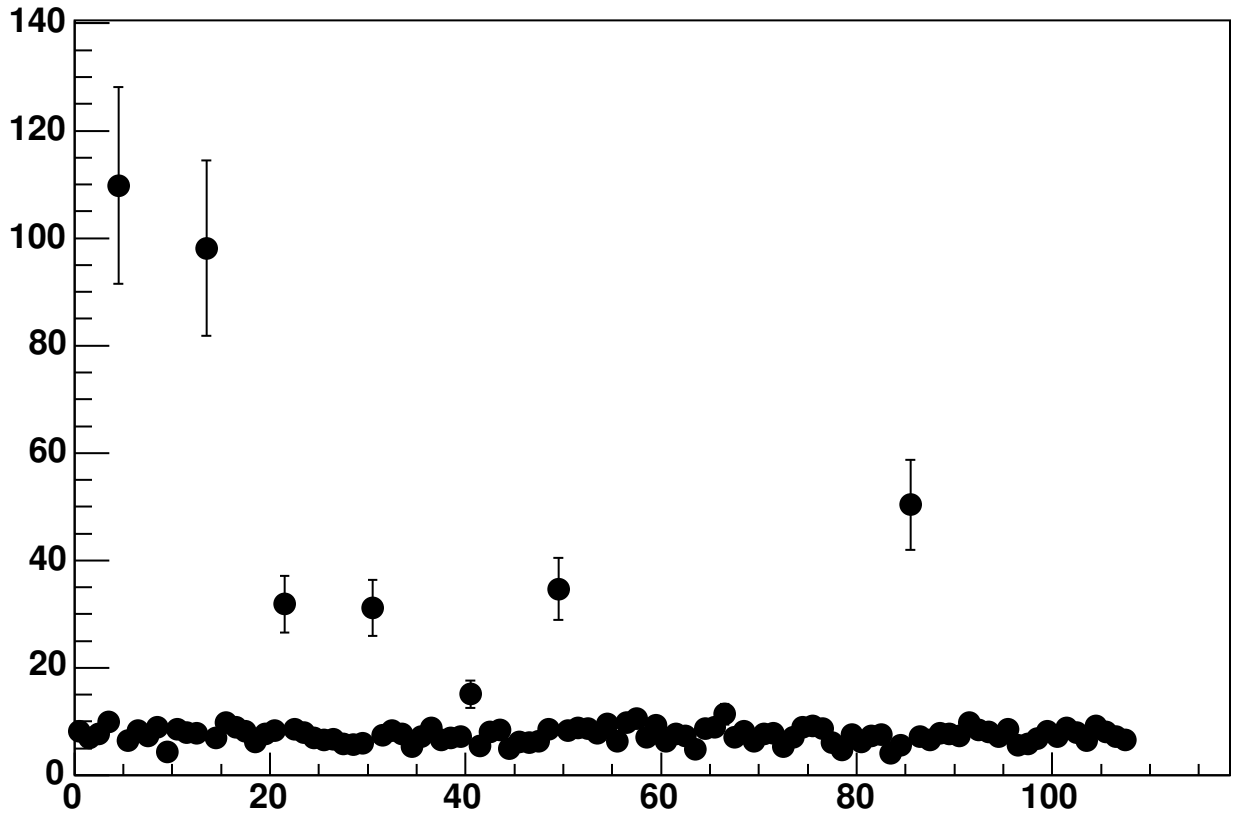
Enable 4, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



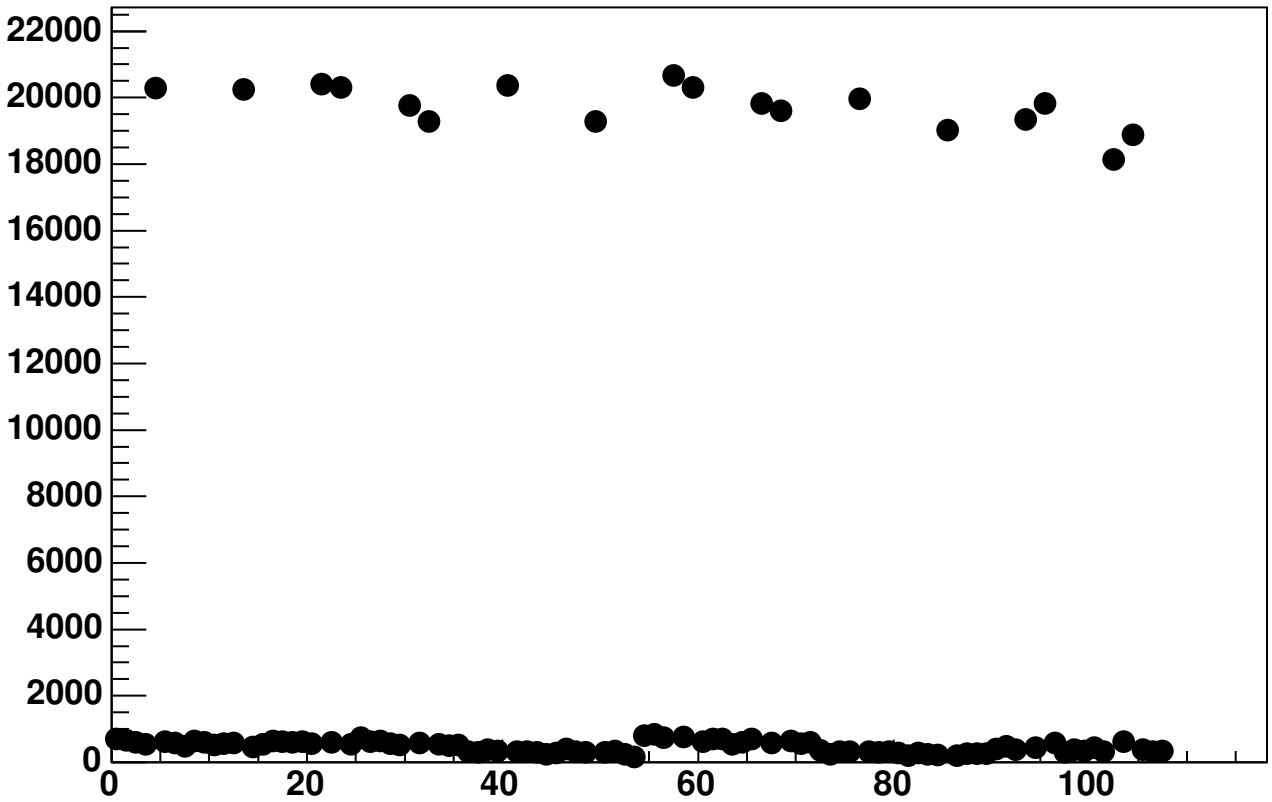
Enable 4, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



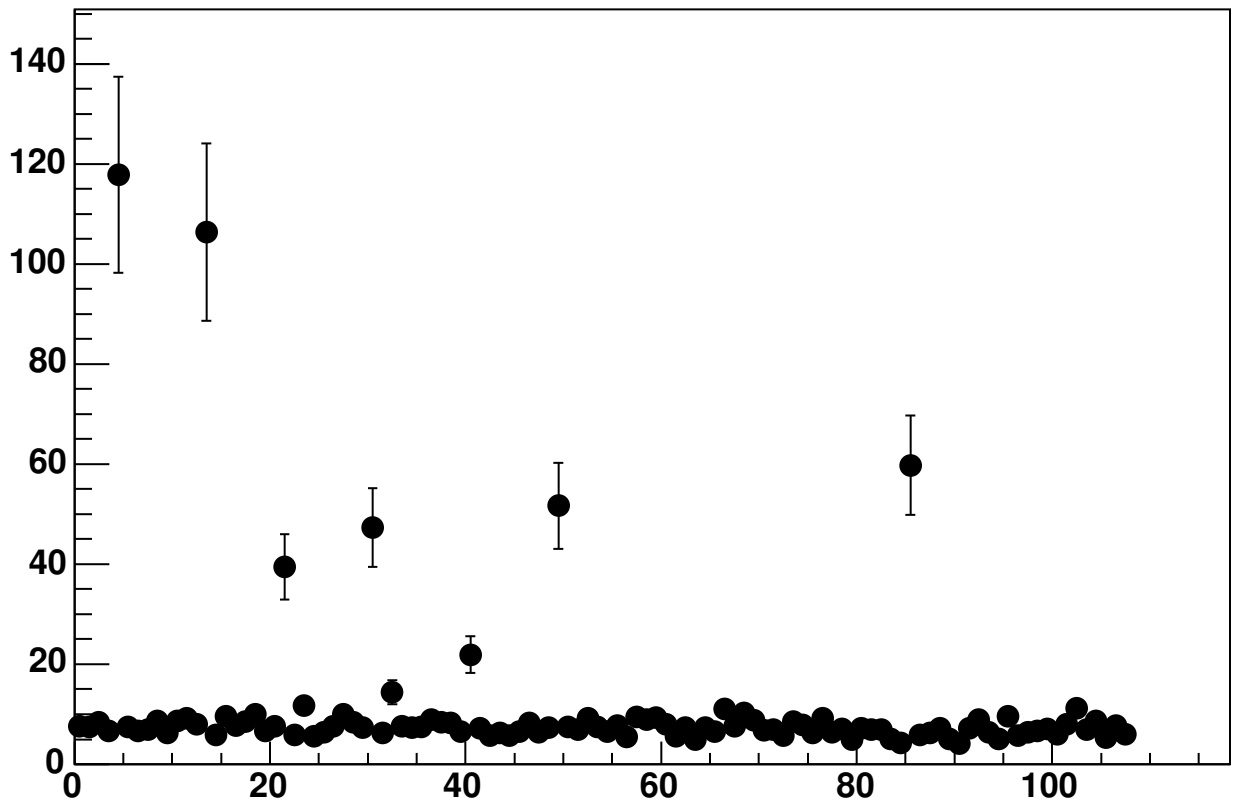
Enable 4, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



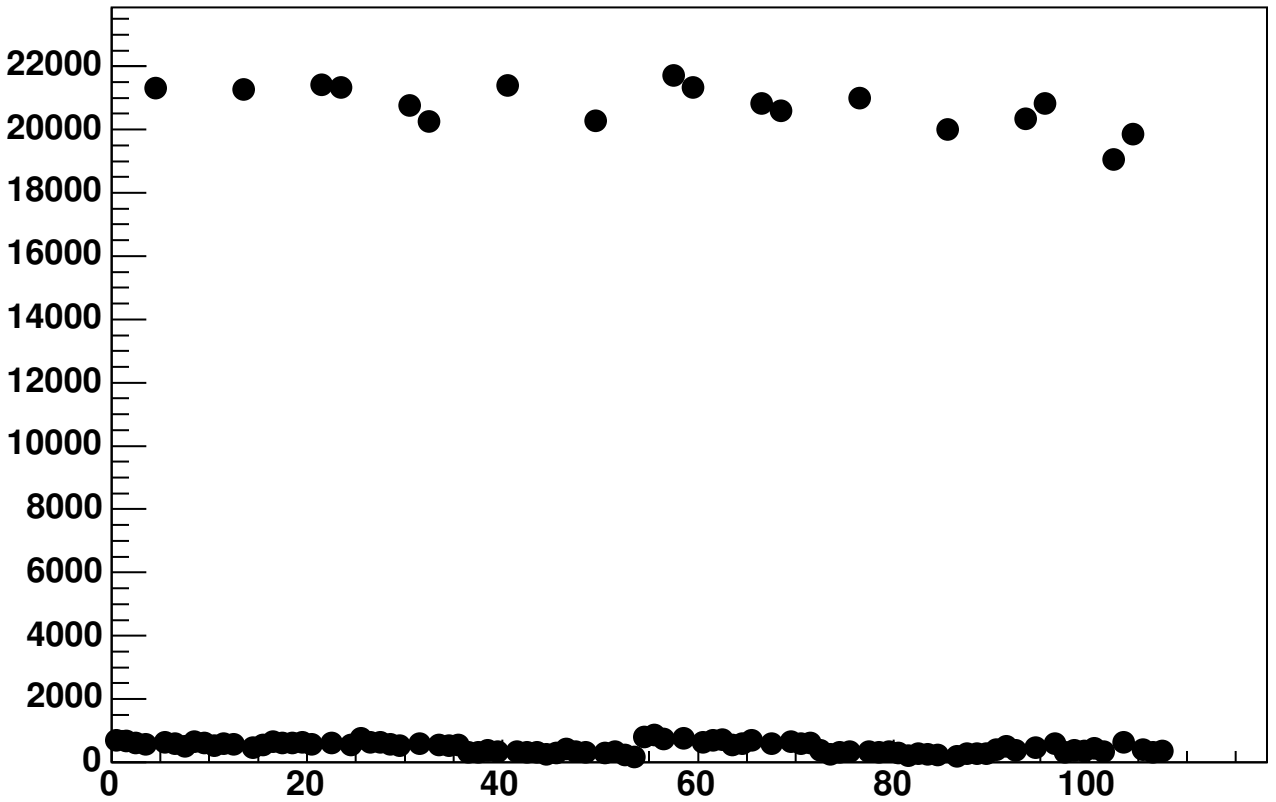
Enable 4, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



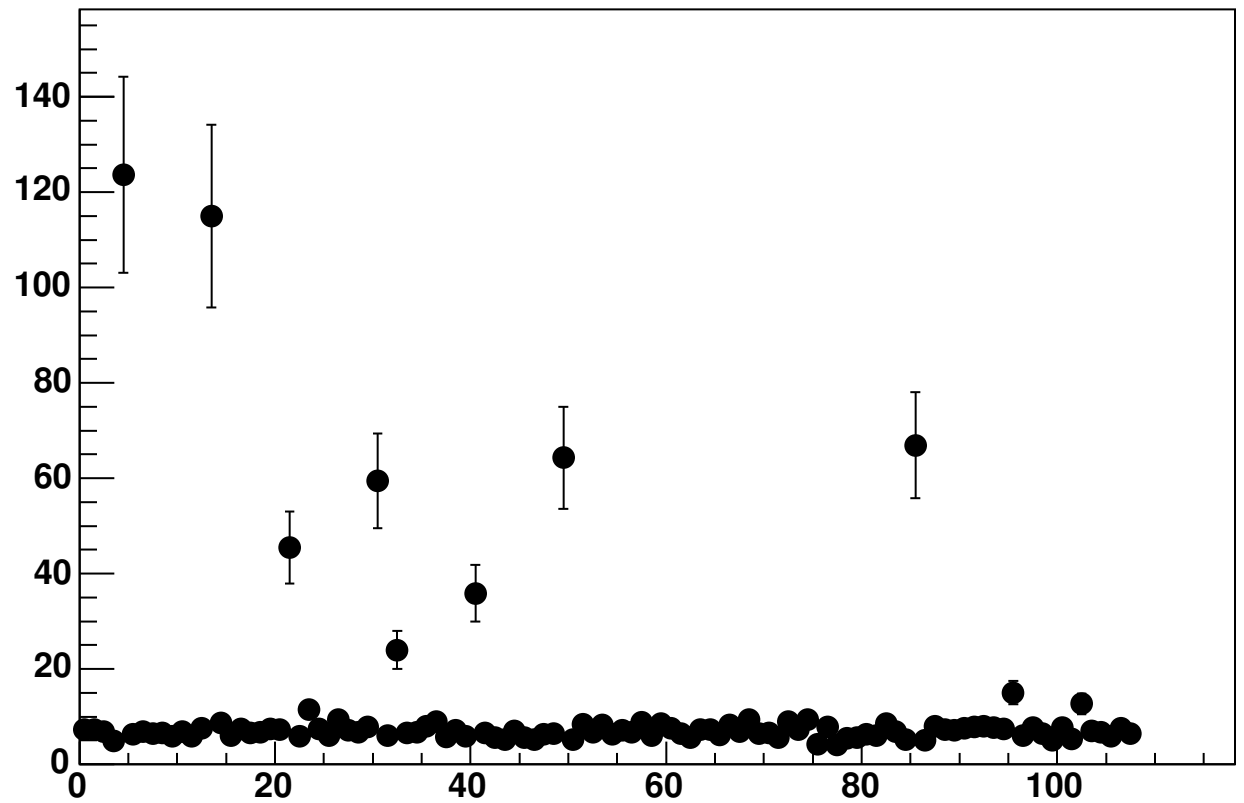
Enable 4, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan

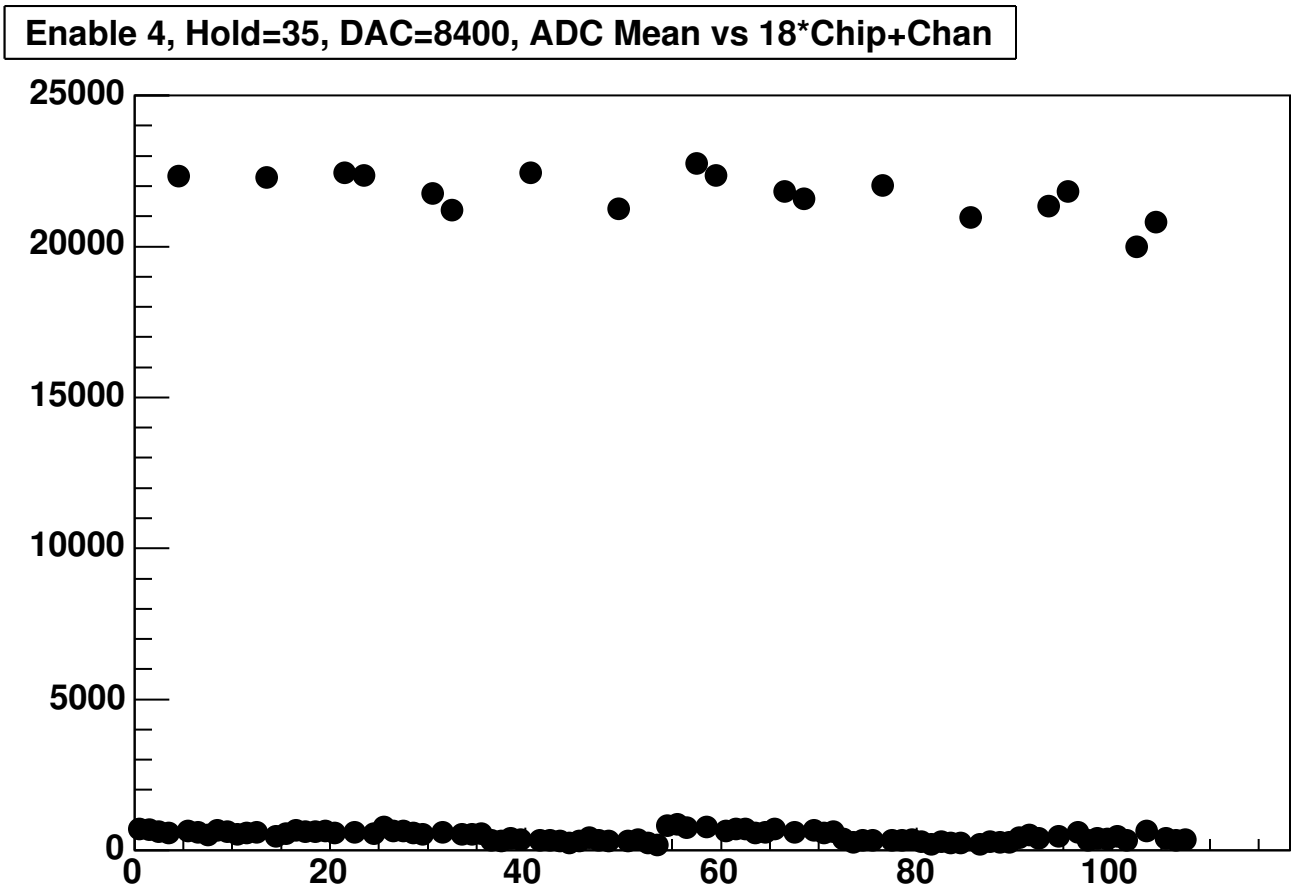


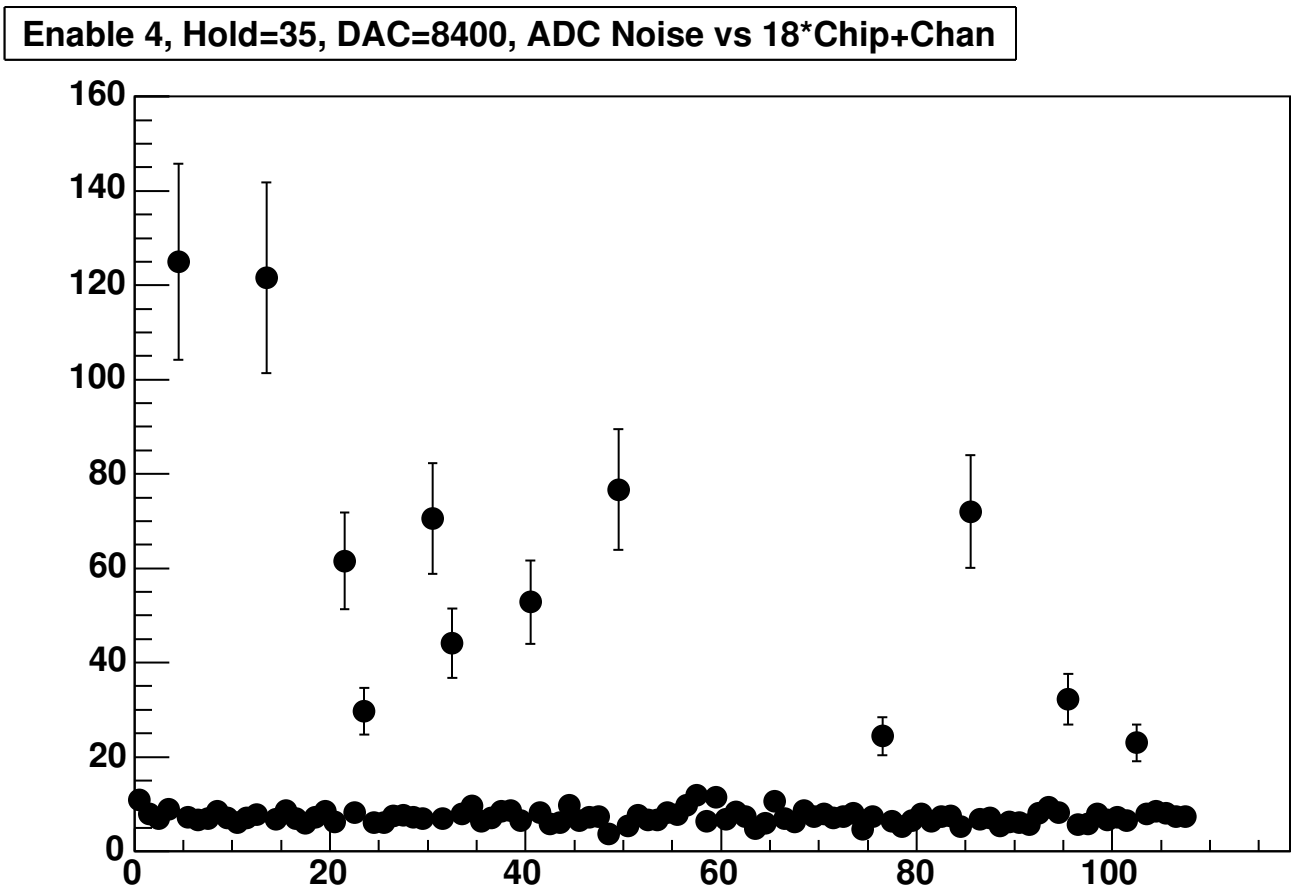
Enable 4, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



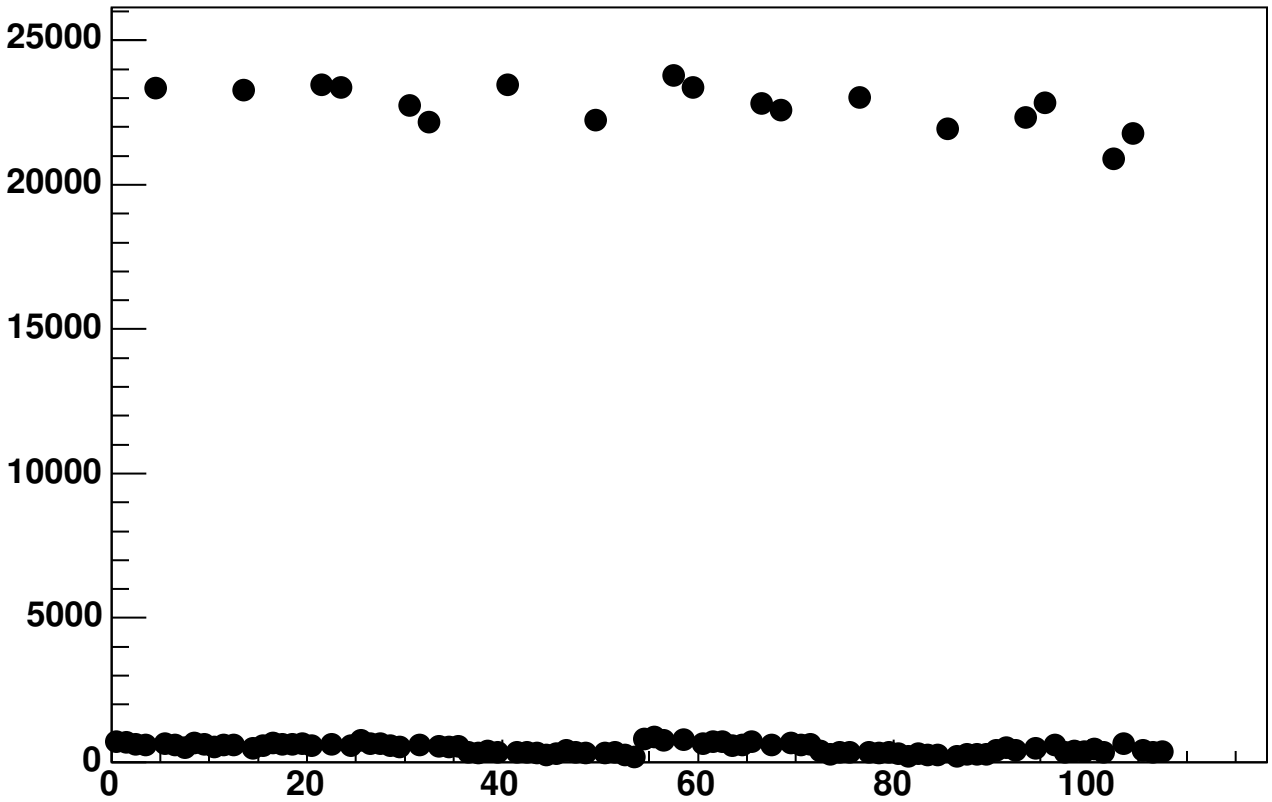
Enable 4, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



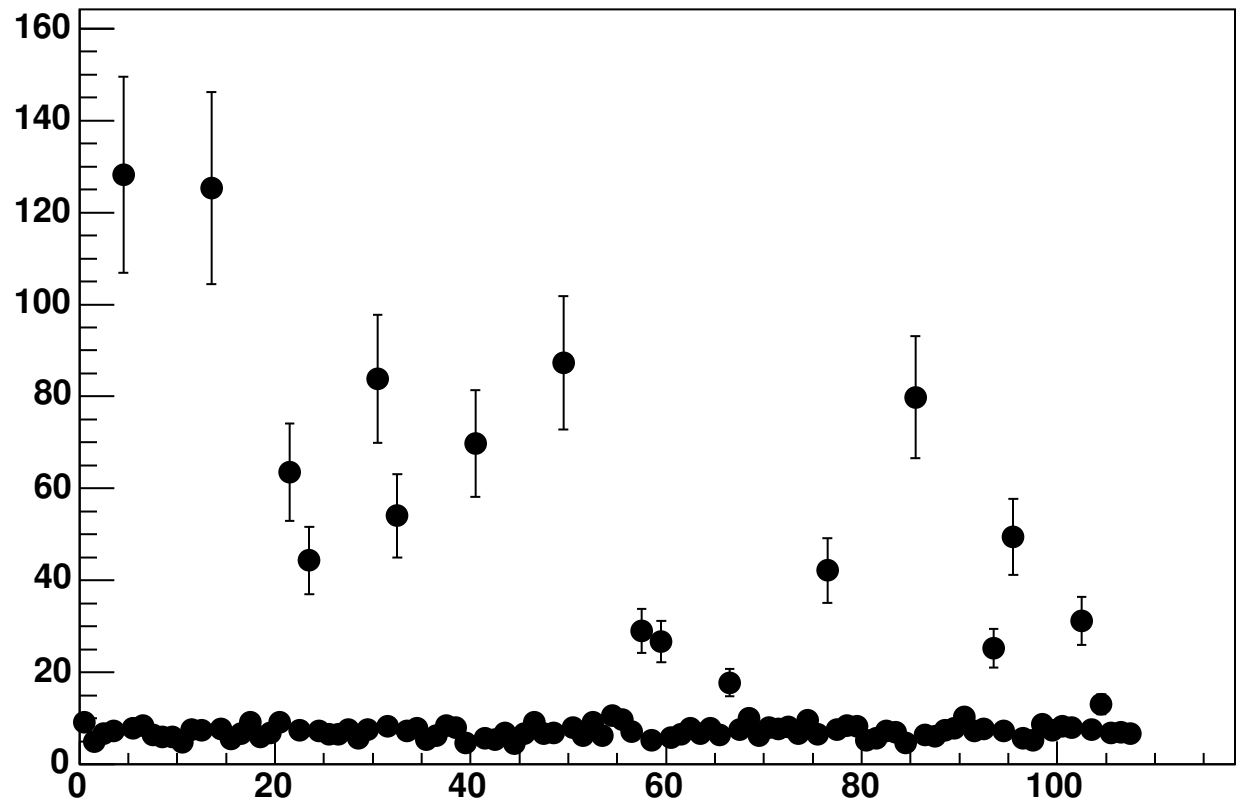

Enable 4, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan
25000
20000
15000
10000
5000
0
0
20
40
60
80
100


Enable 4, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan
160
140
120
100
80
60
40
20
0
0
20
40
60
80
100

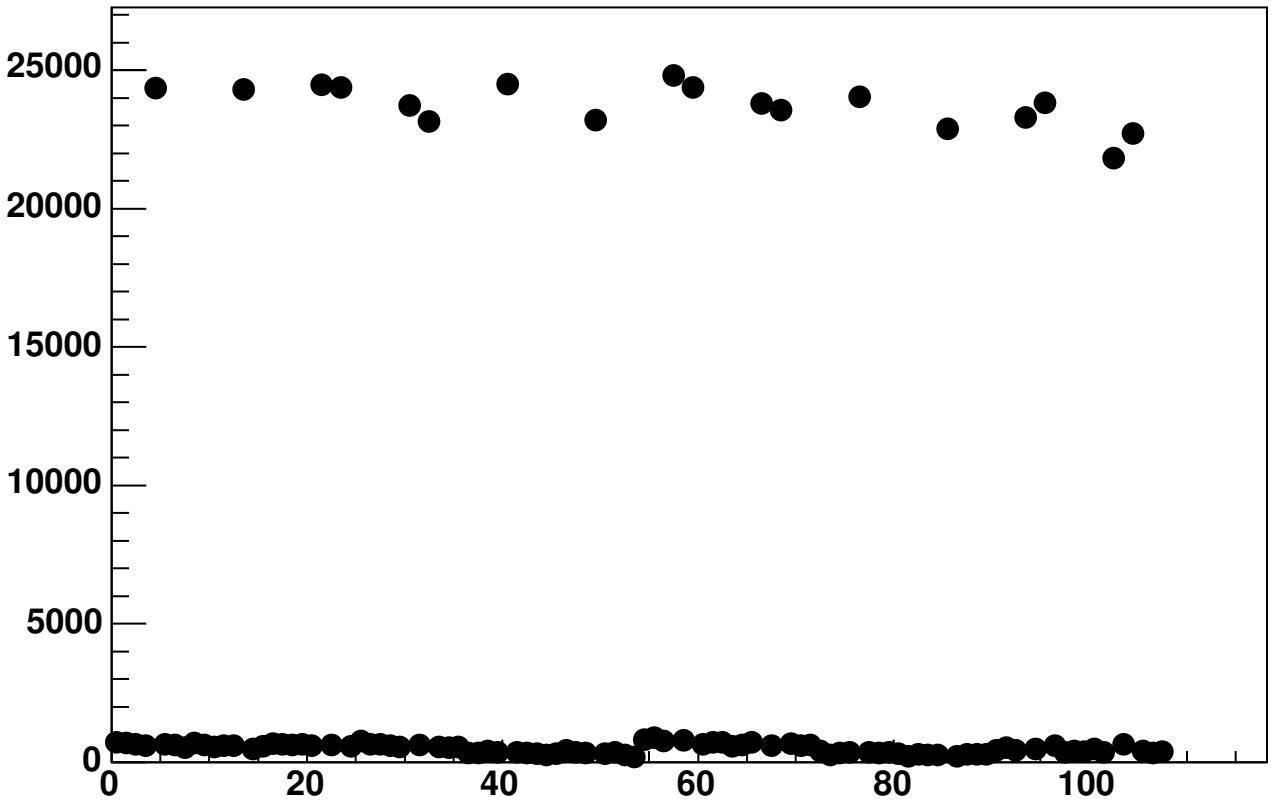
Enable 4, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



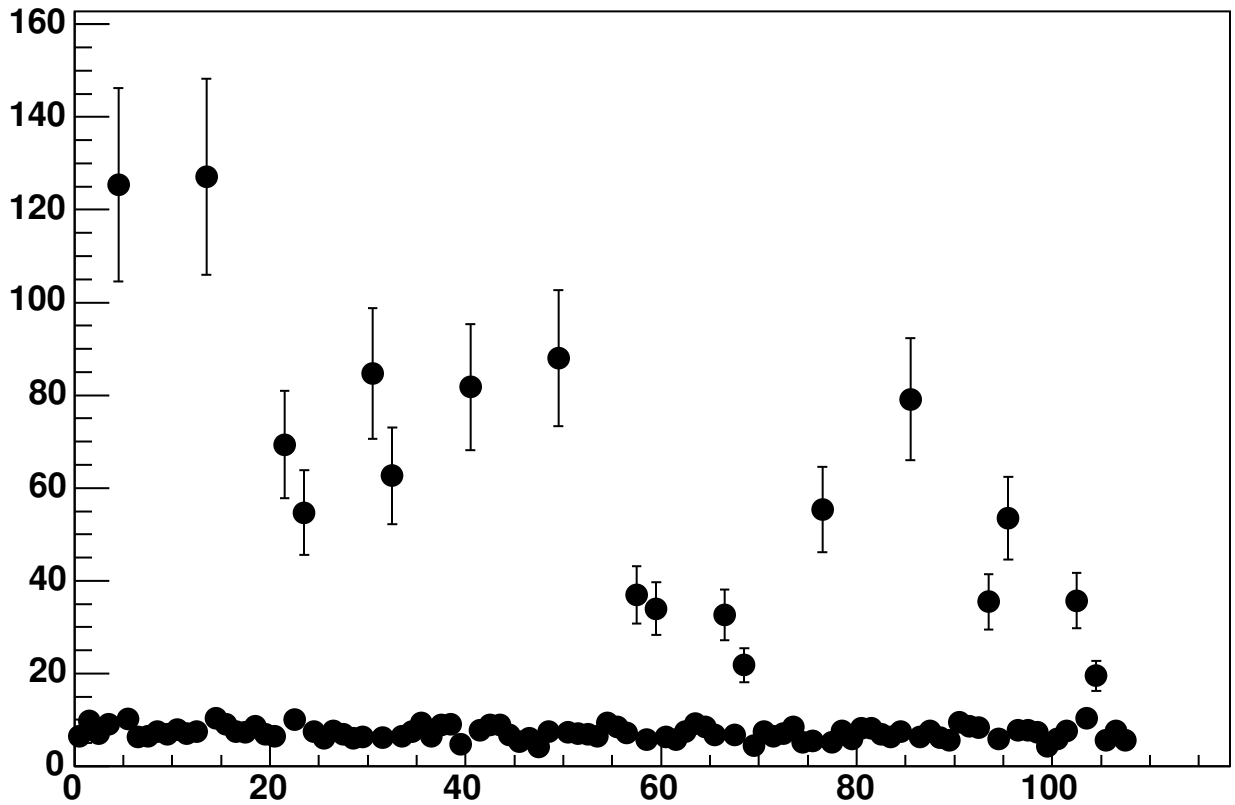
Enable 4, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



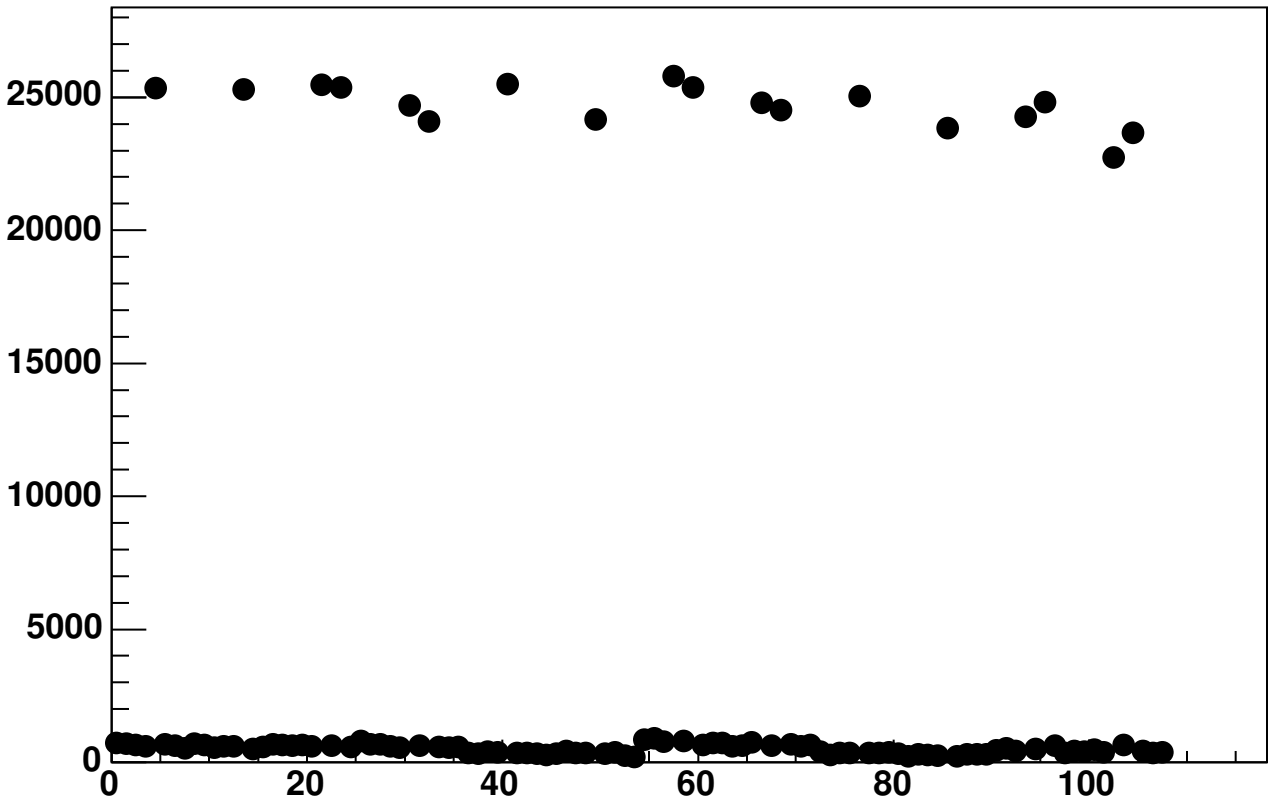
Enable 4, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



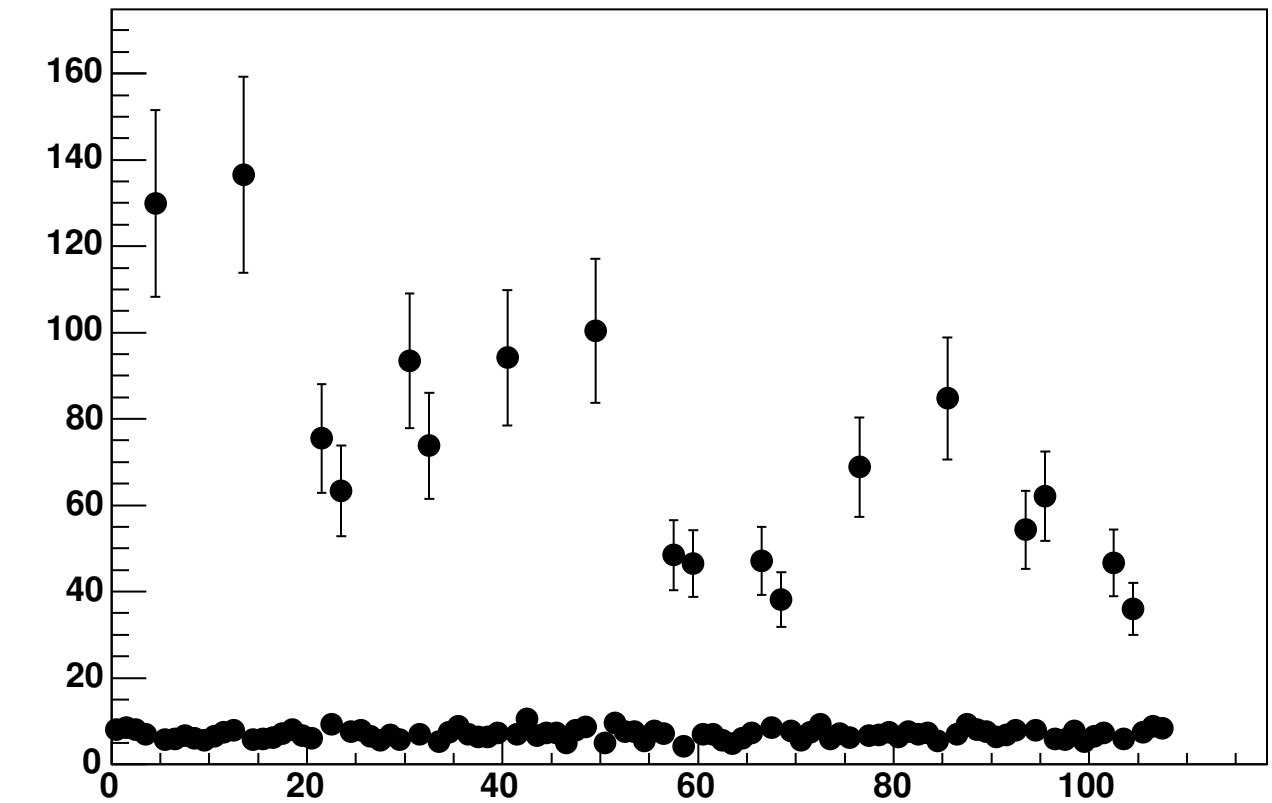
Enable 4, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



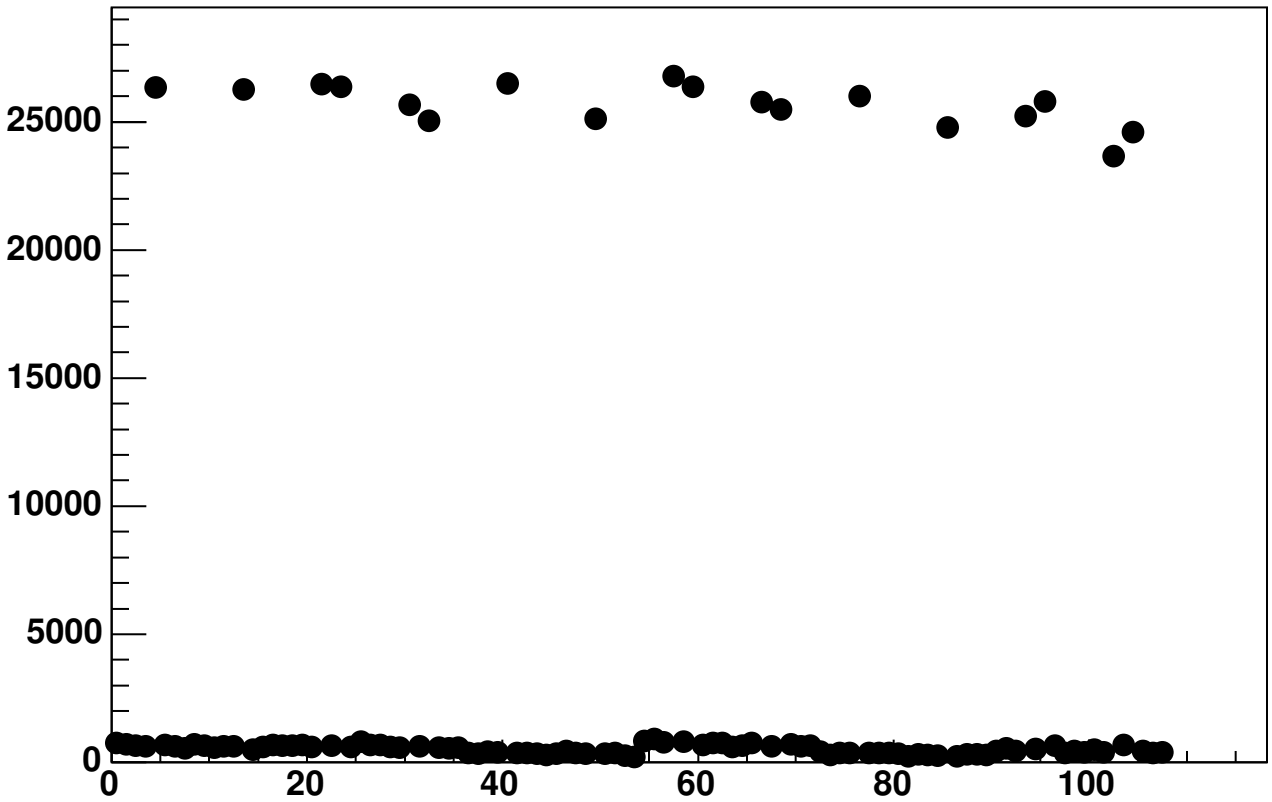
Enable 4, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



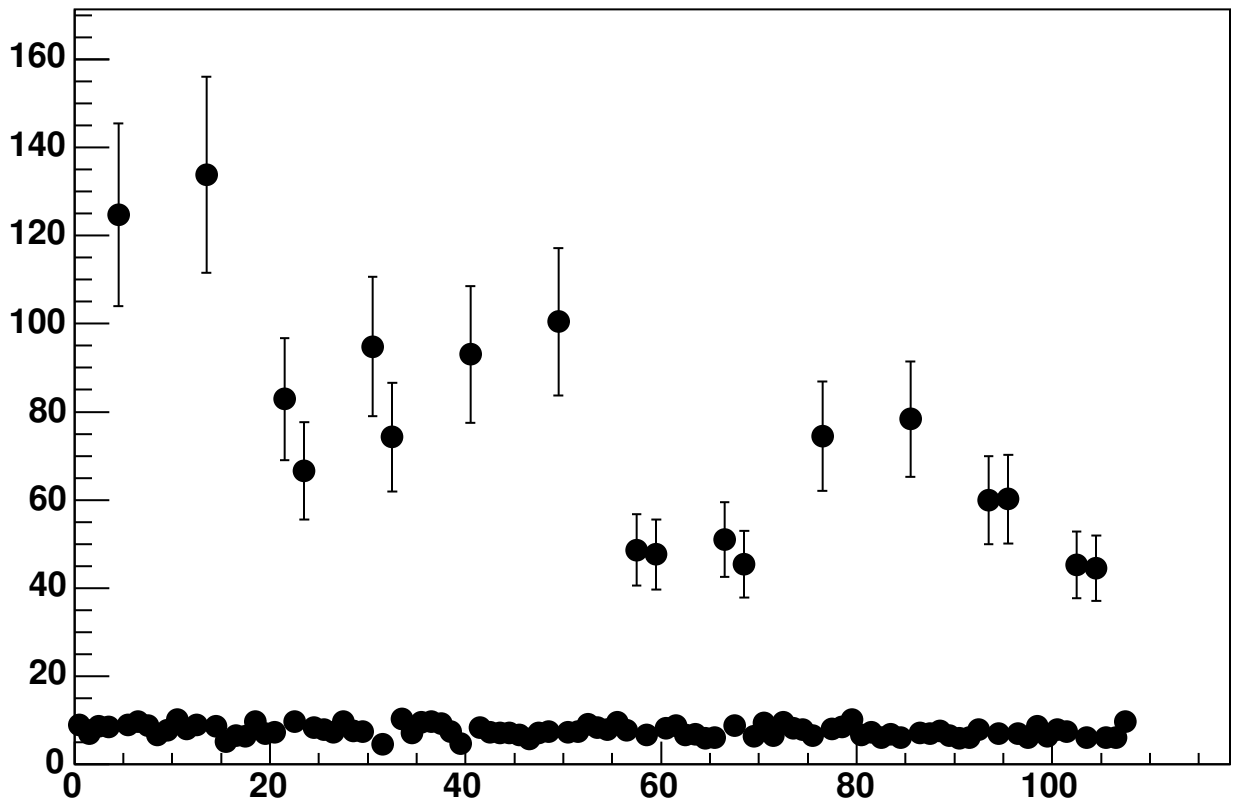
Enable 4, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



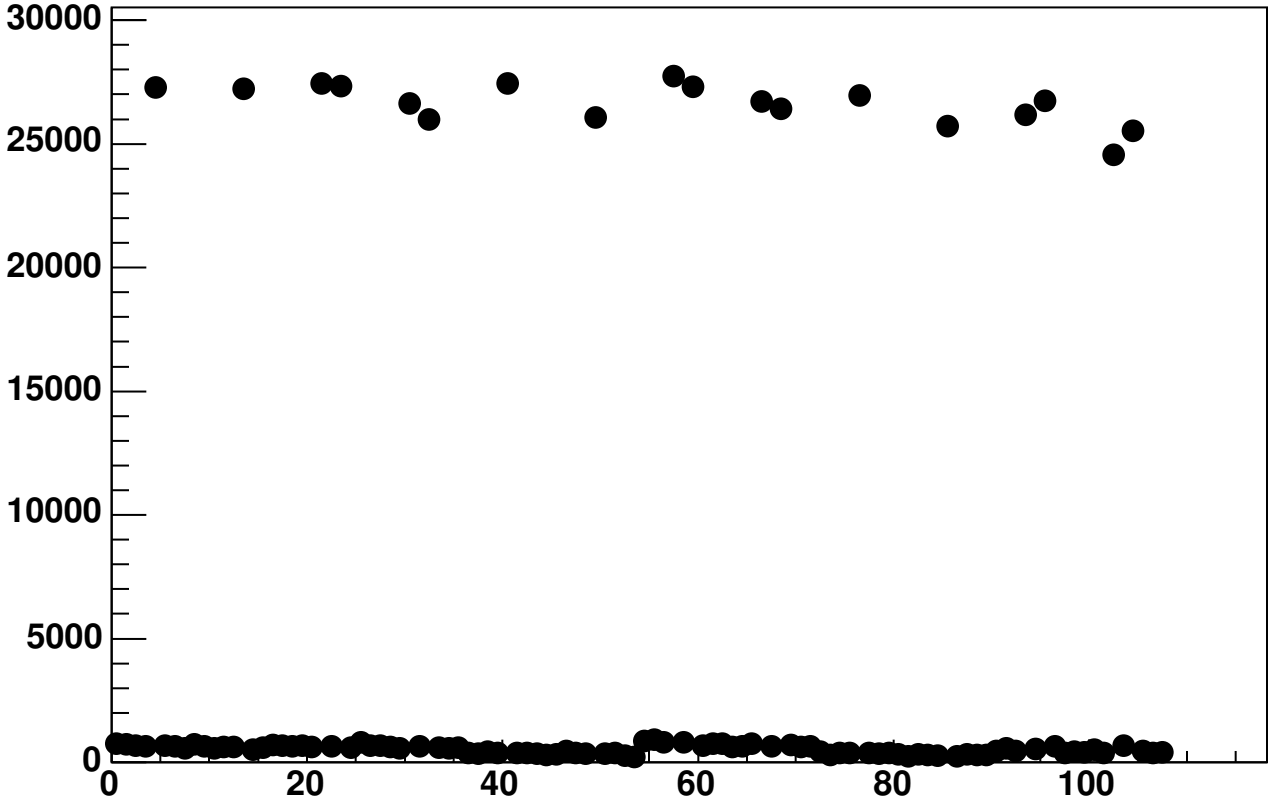
Enable 4, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



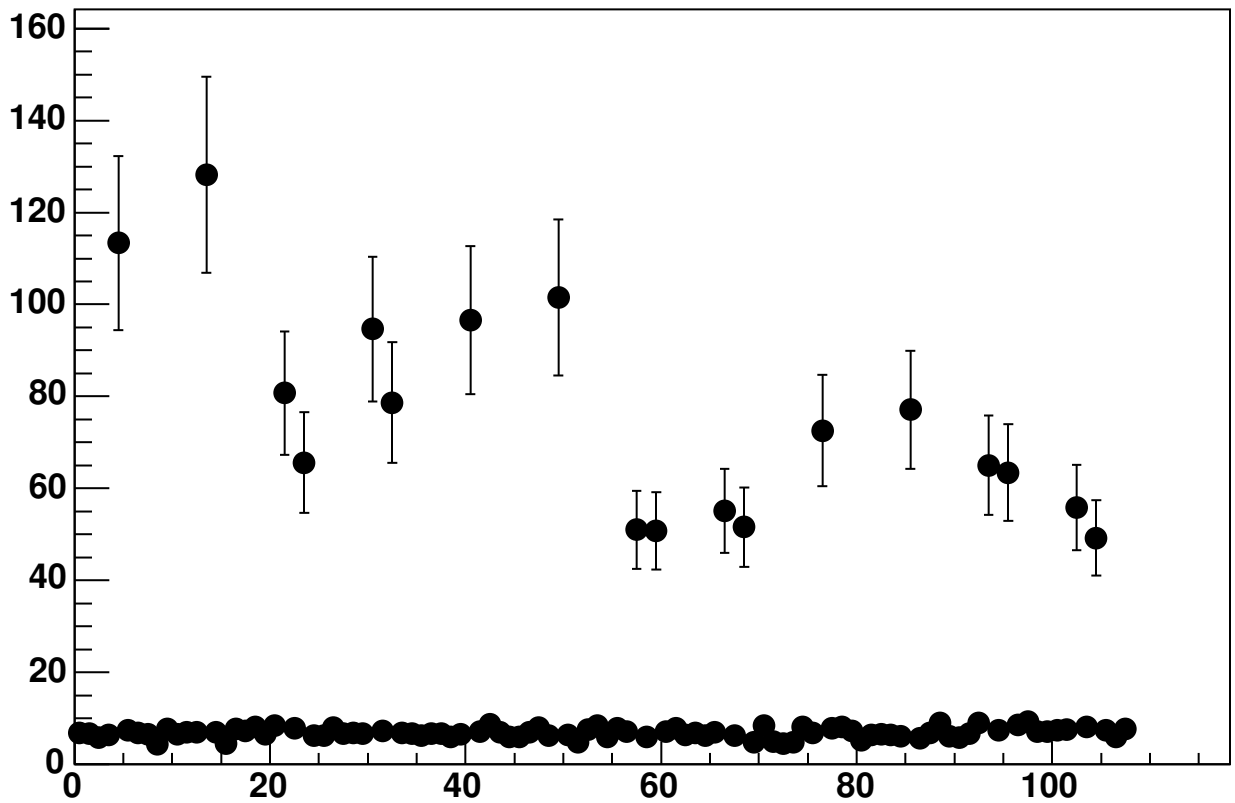
Enable 4, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



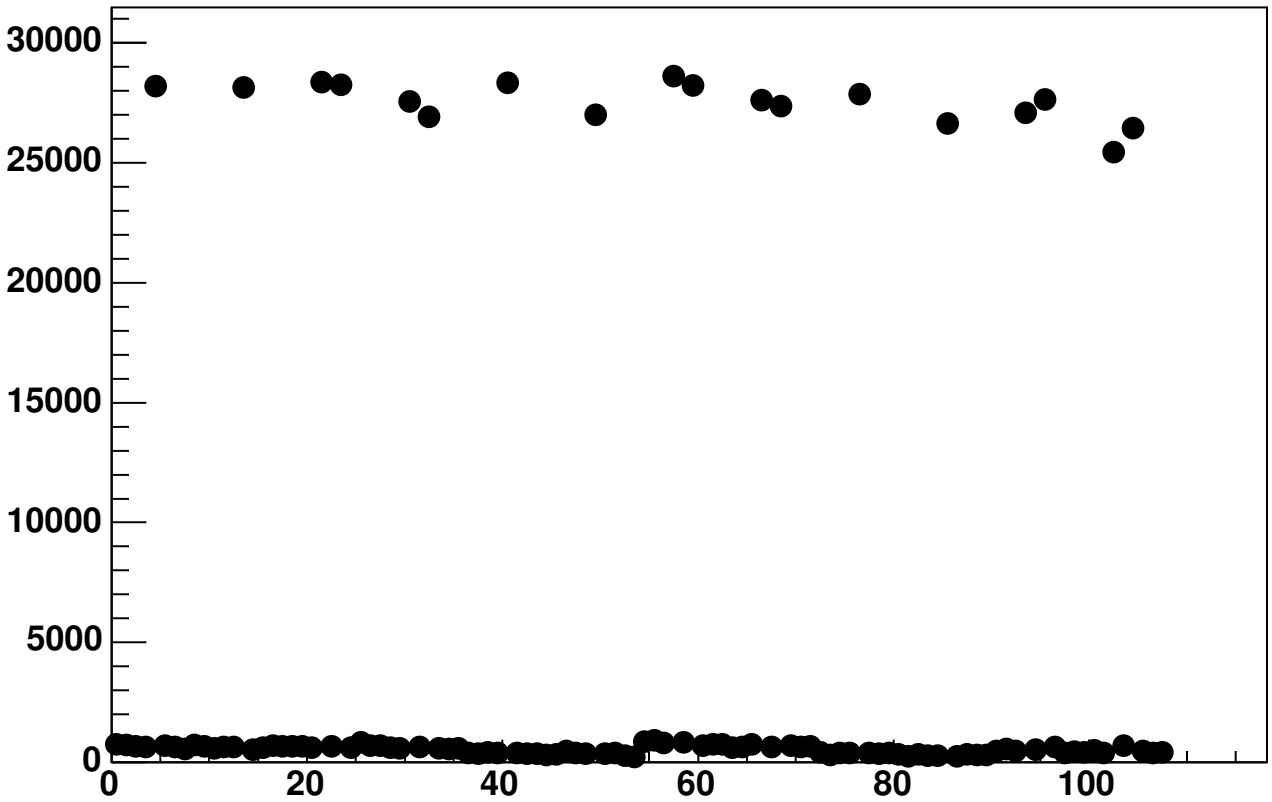
Enable 4, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



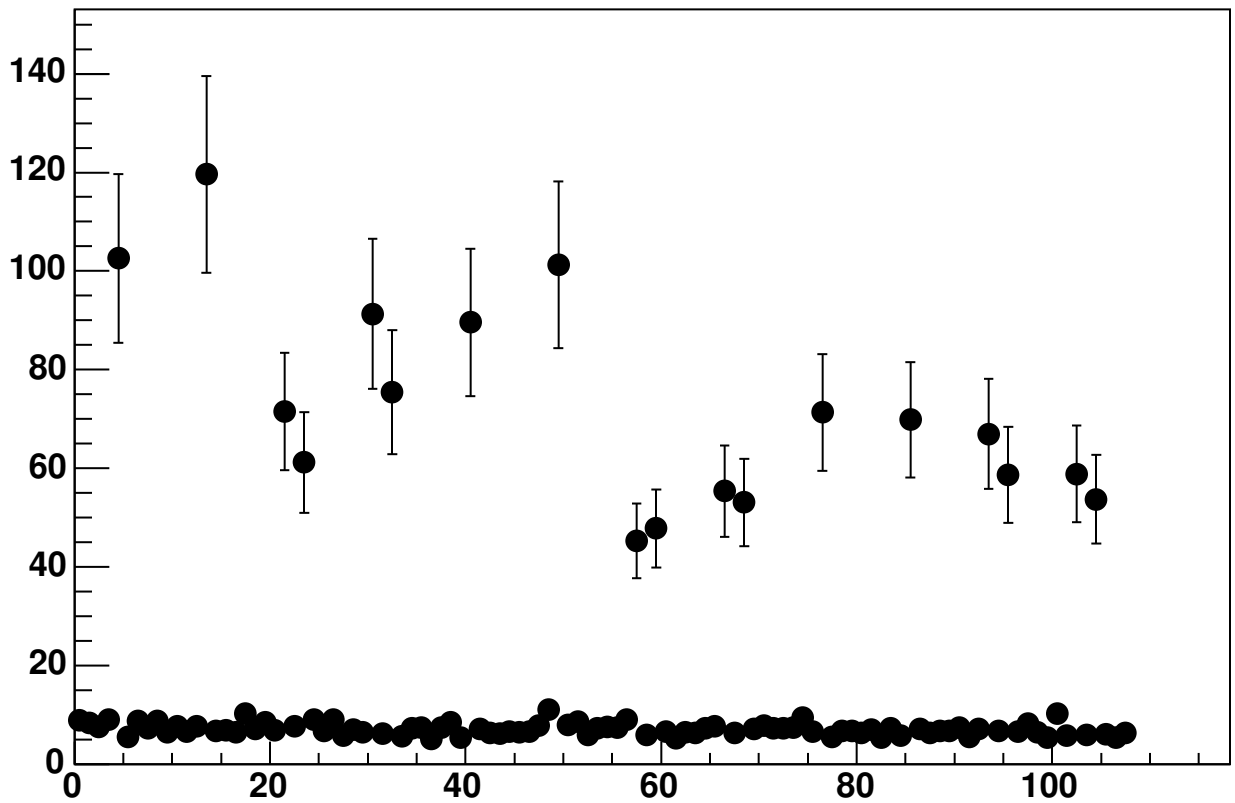
Enable 4, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



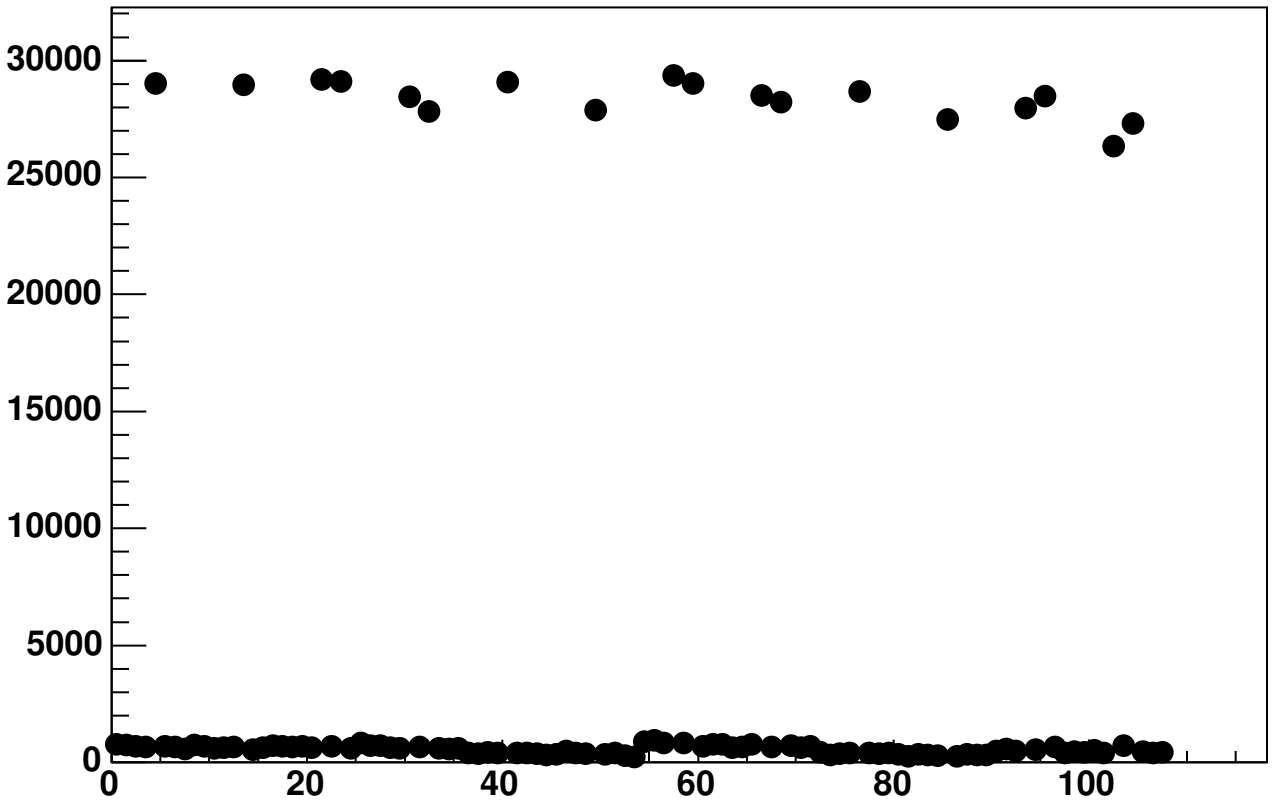
Enable 4, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



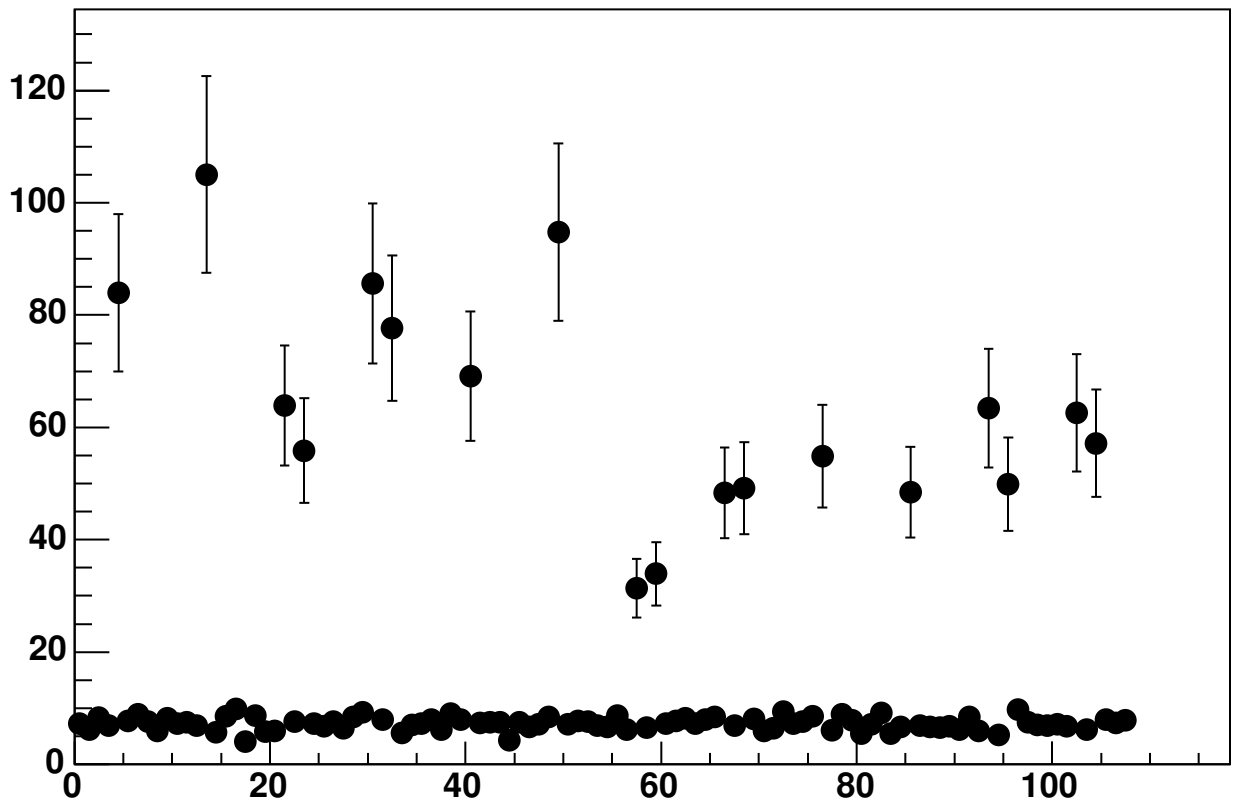
Enable 4, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



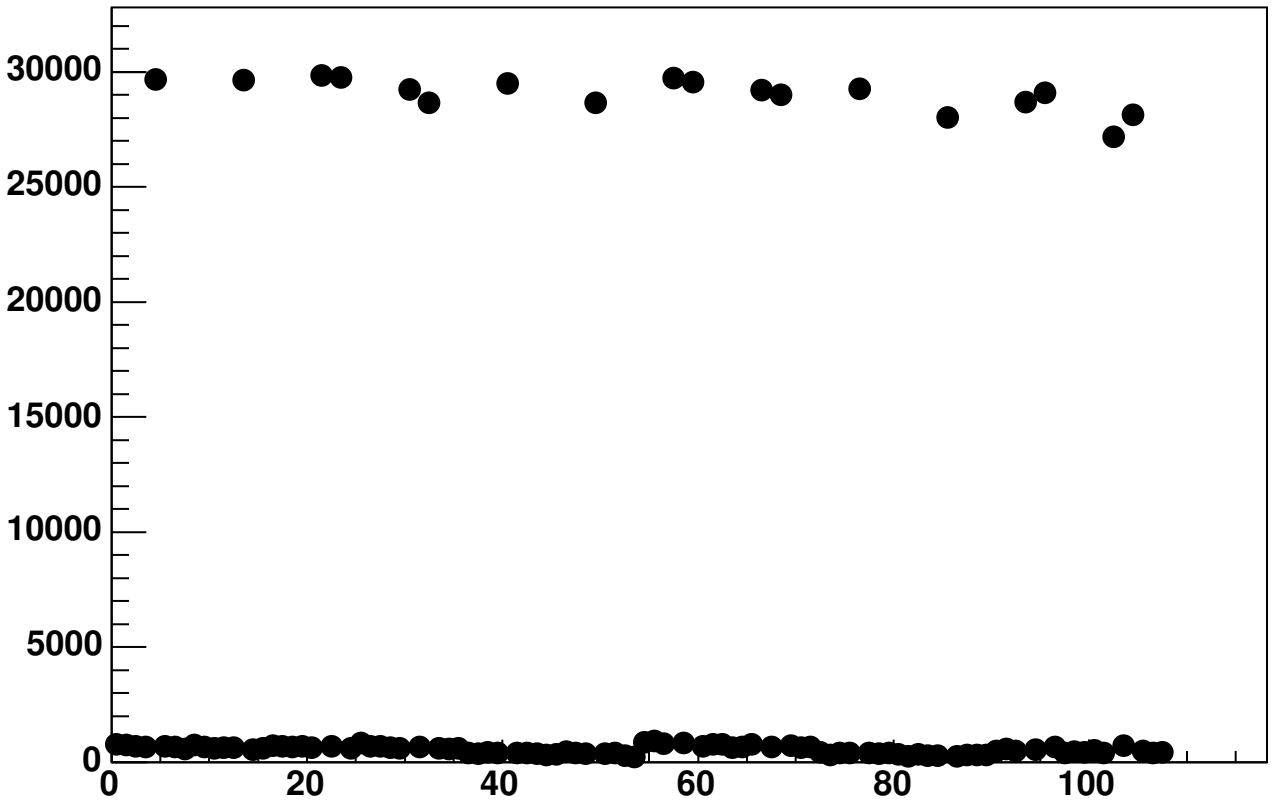
Enable 4, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



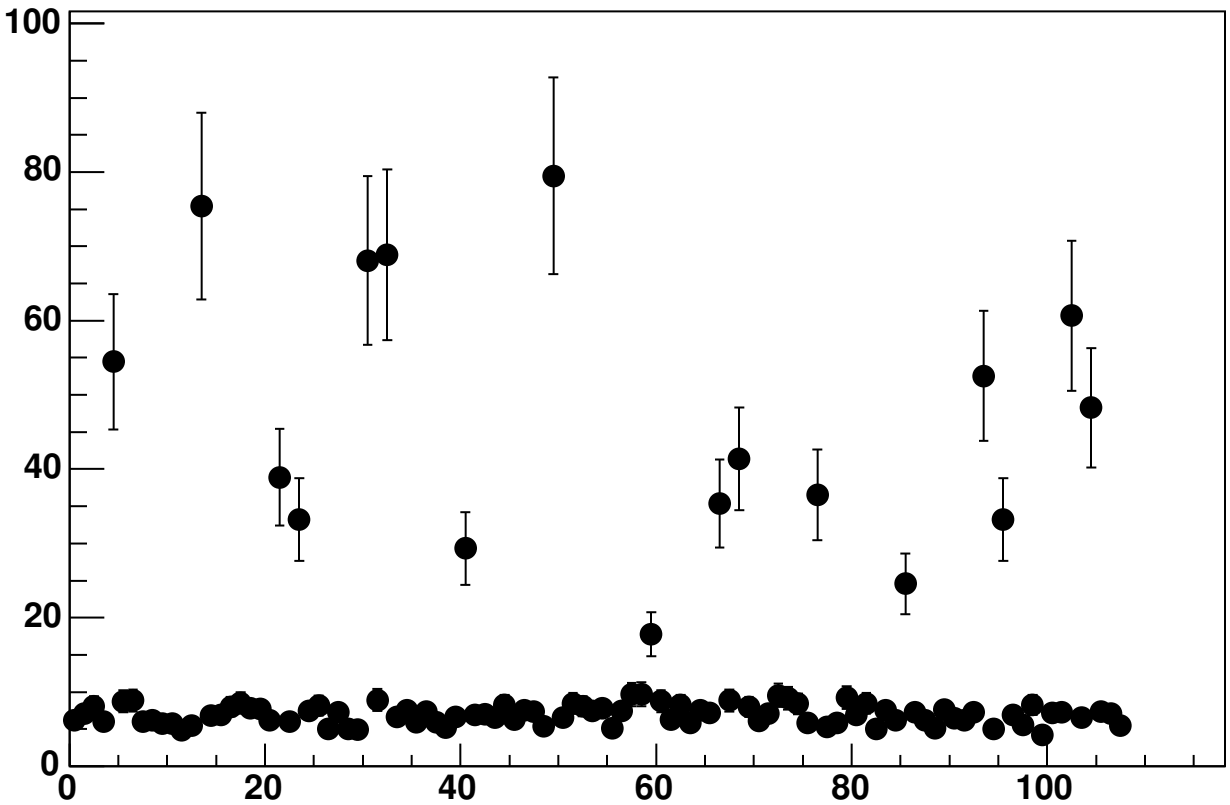
Enable 4, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



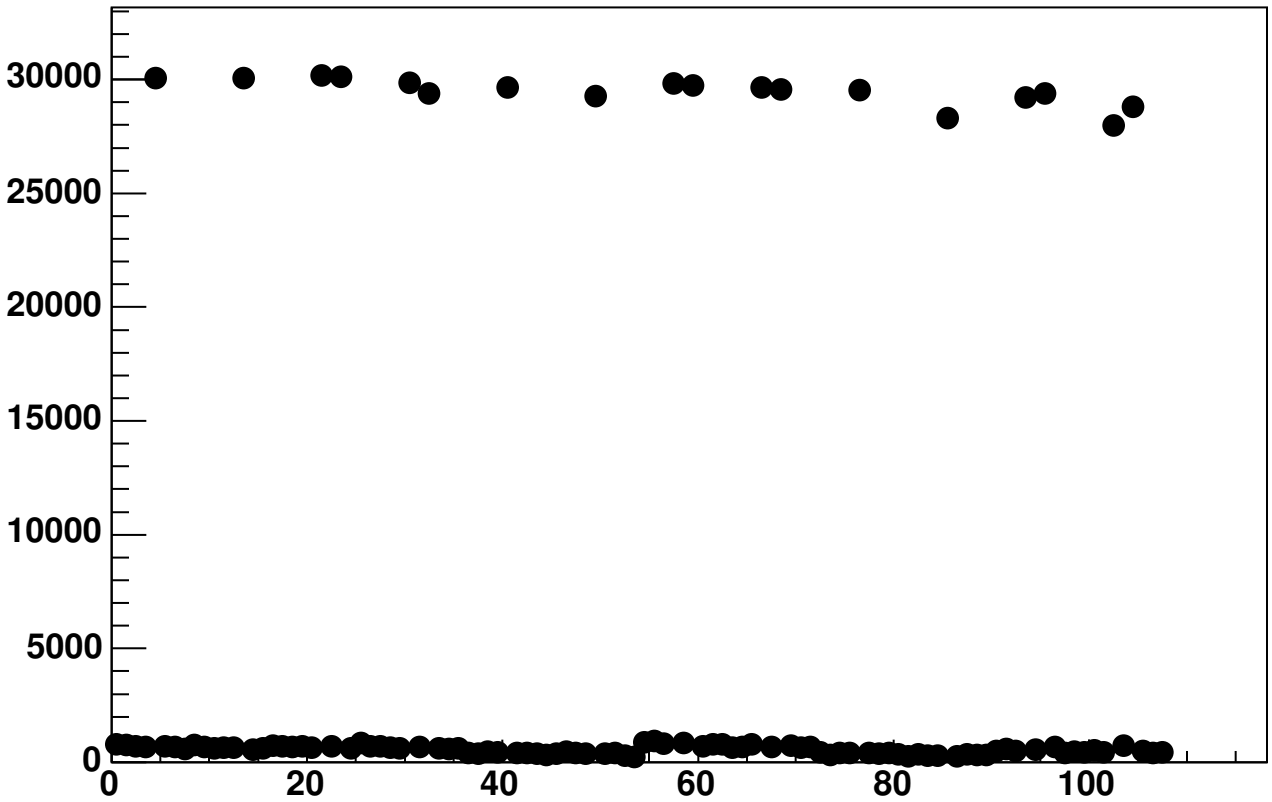
Enable 4, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



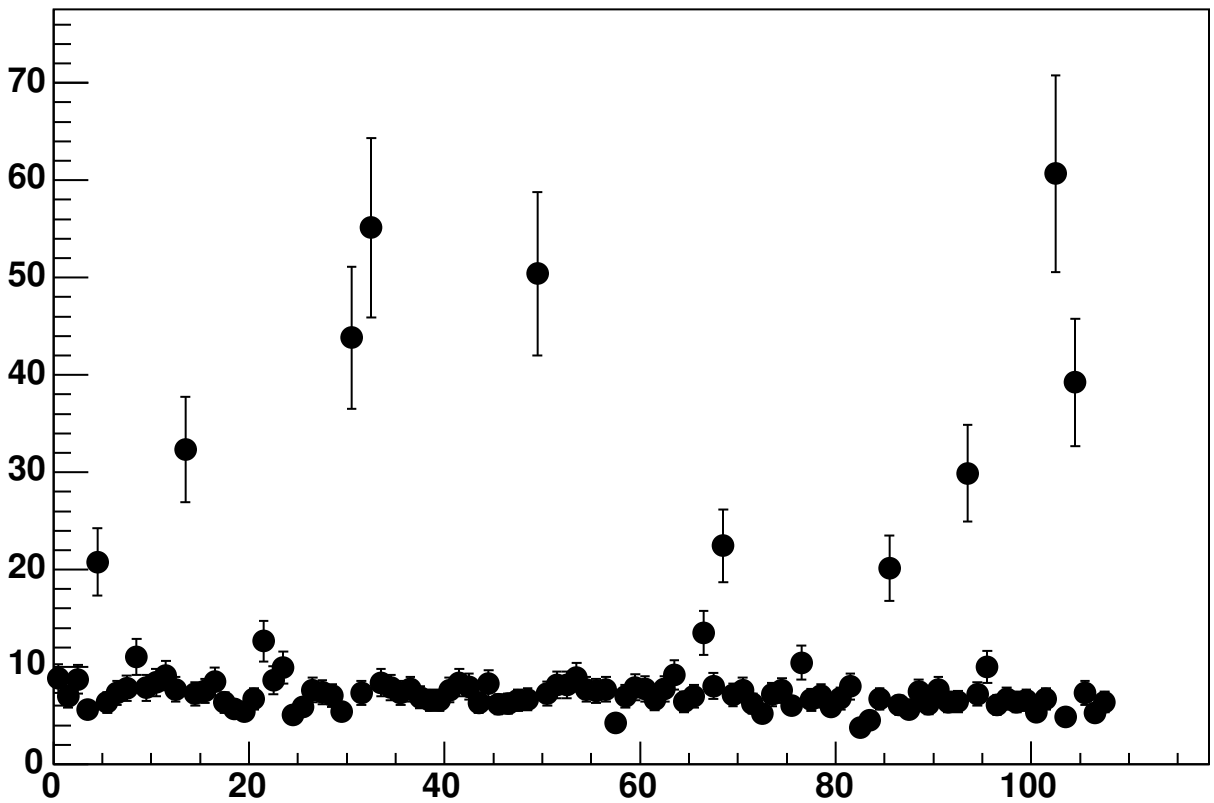
Enable 4, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



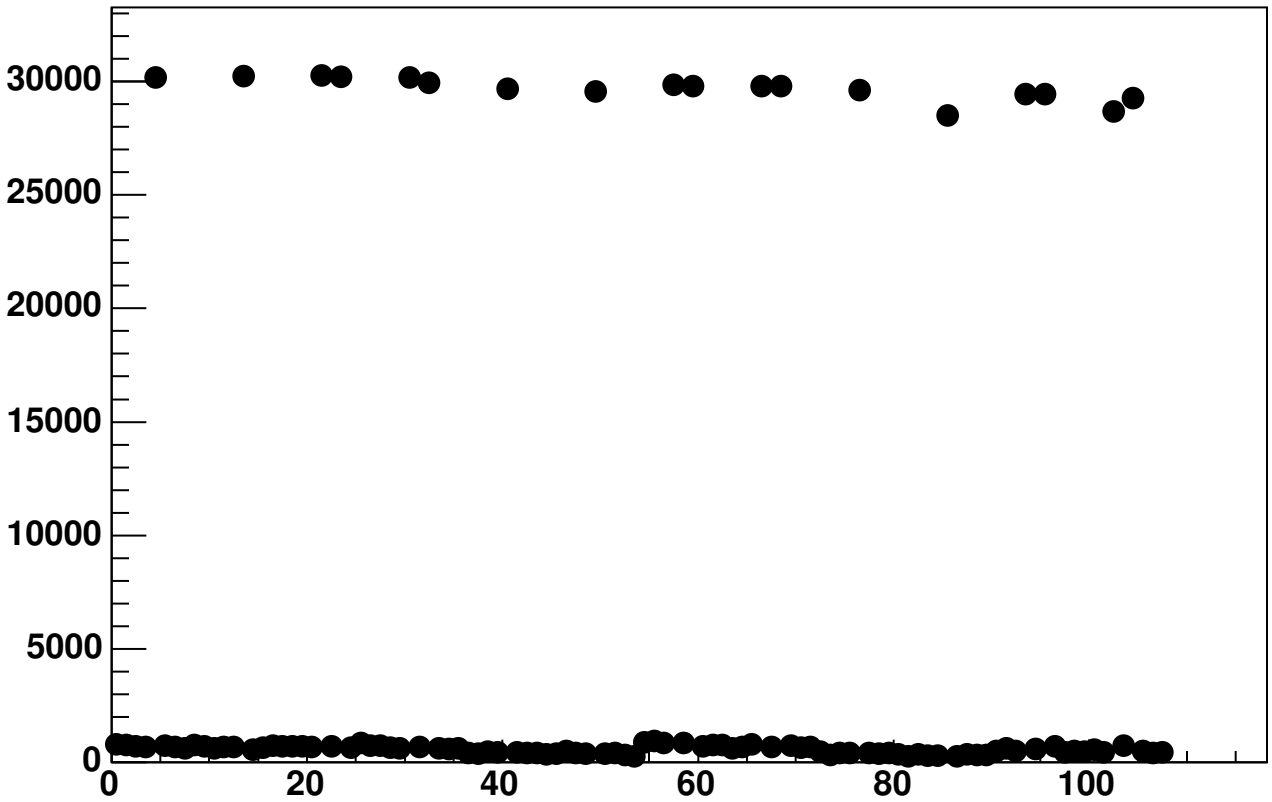
Enable 4, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



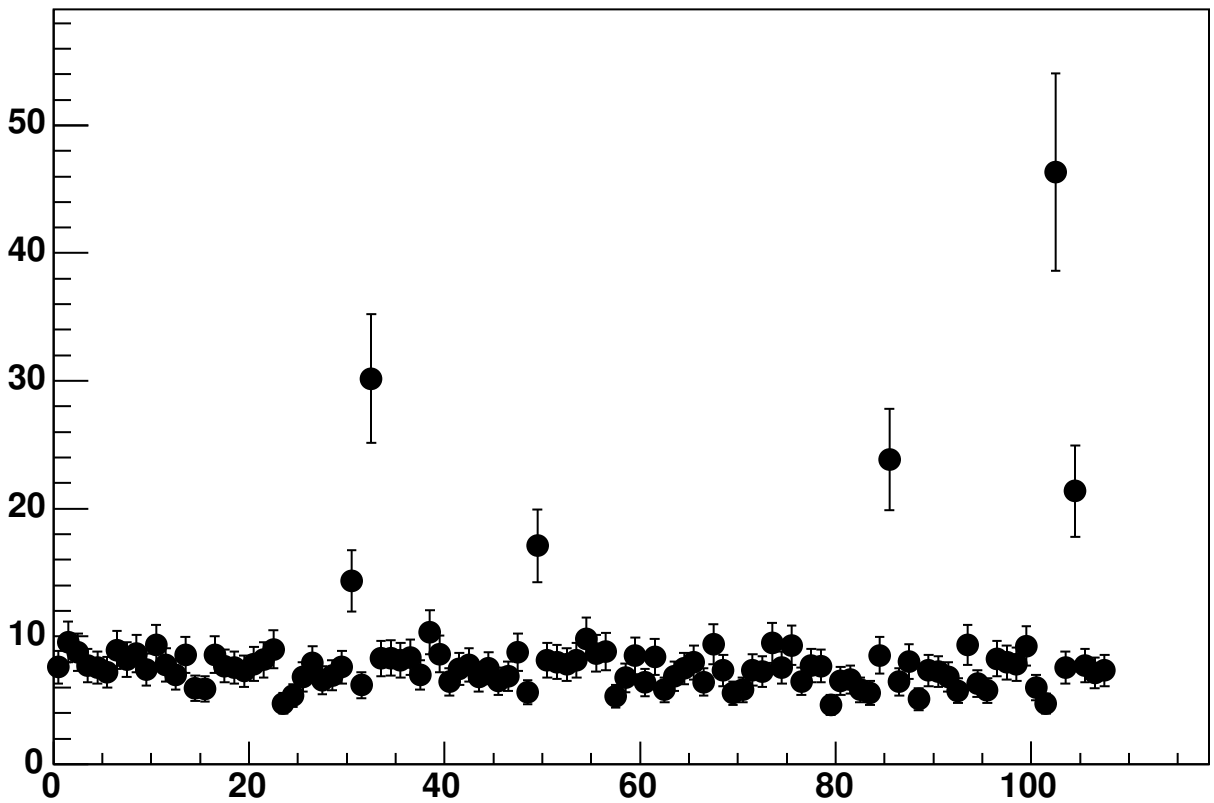
Enable 4, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



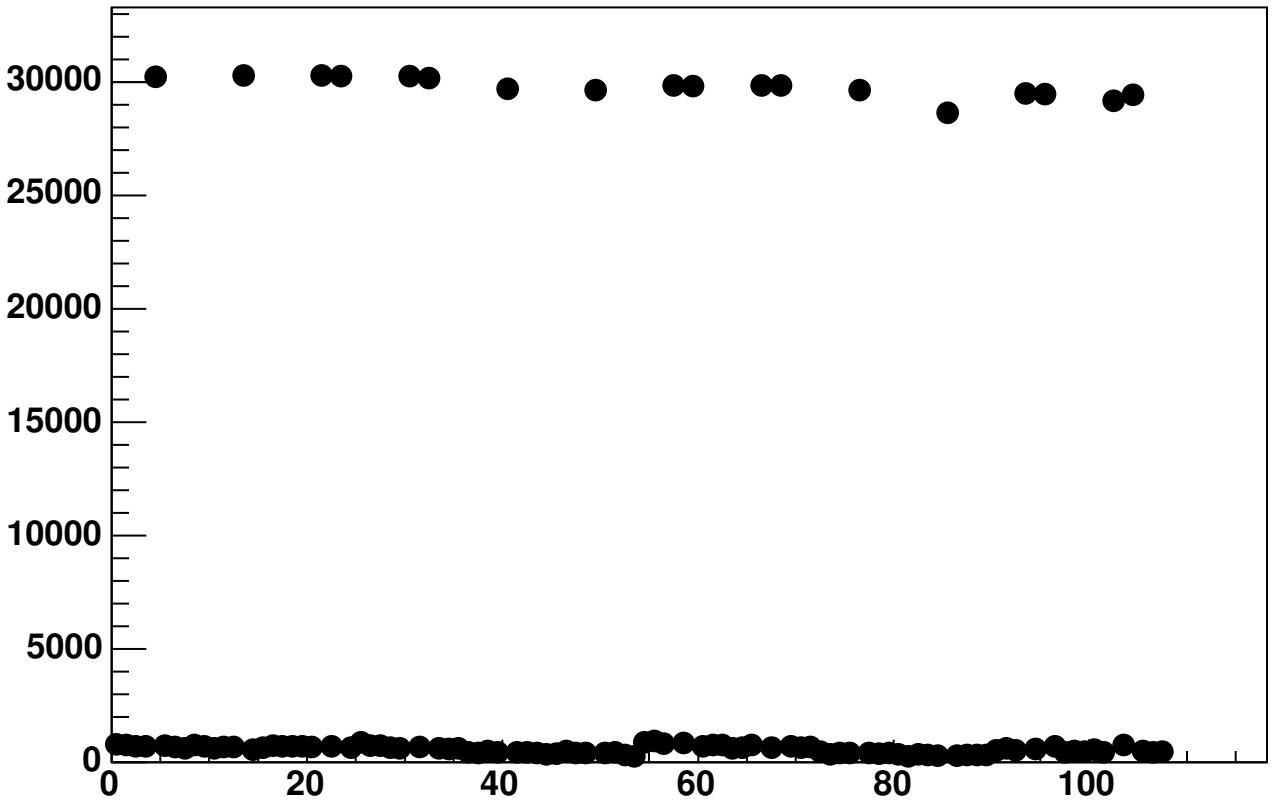
Enable 4, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



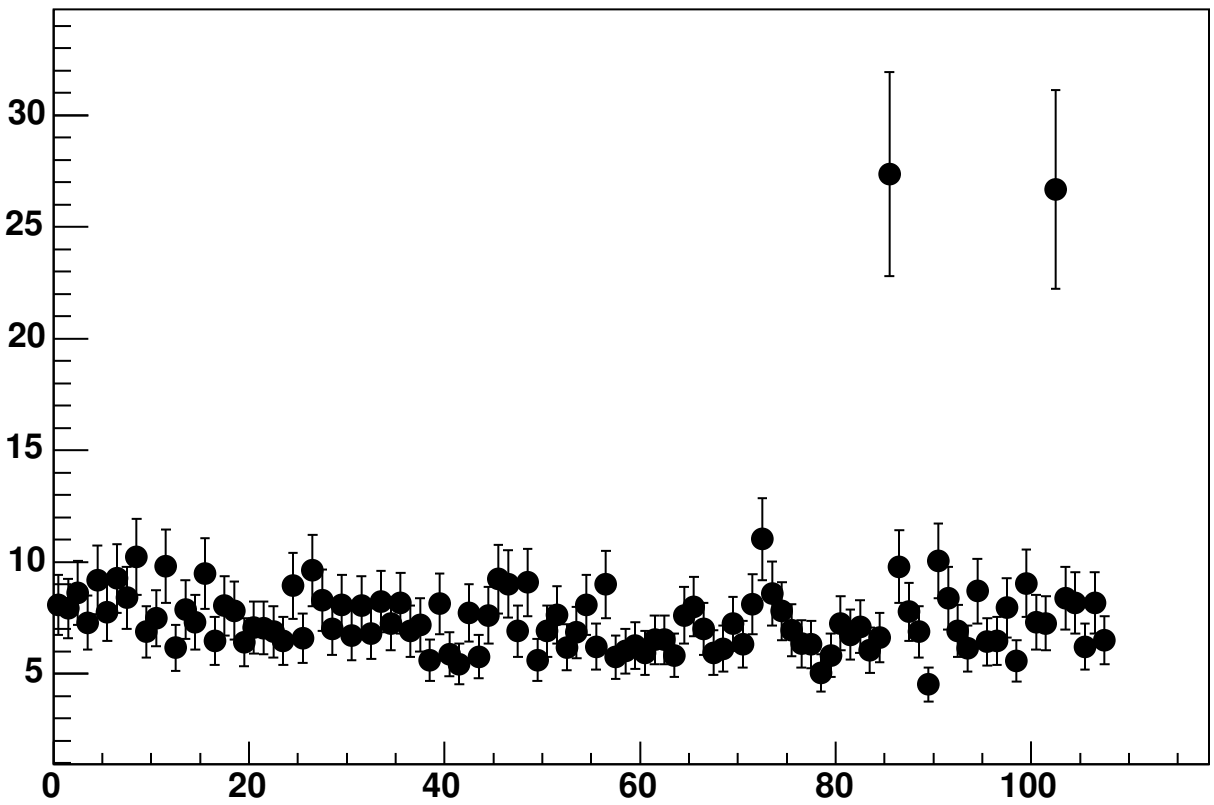
Enable 4, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



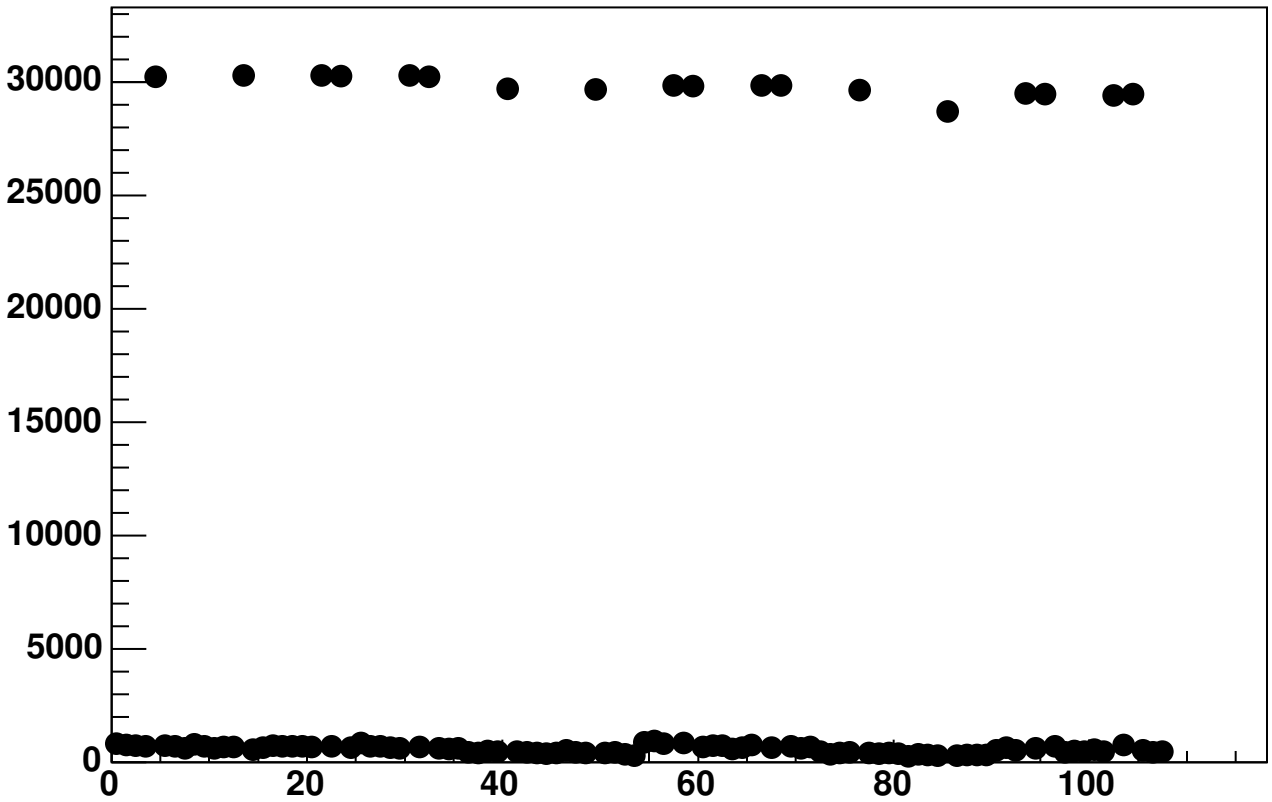
Enable 4, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



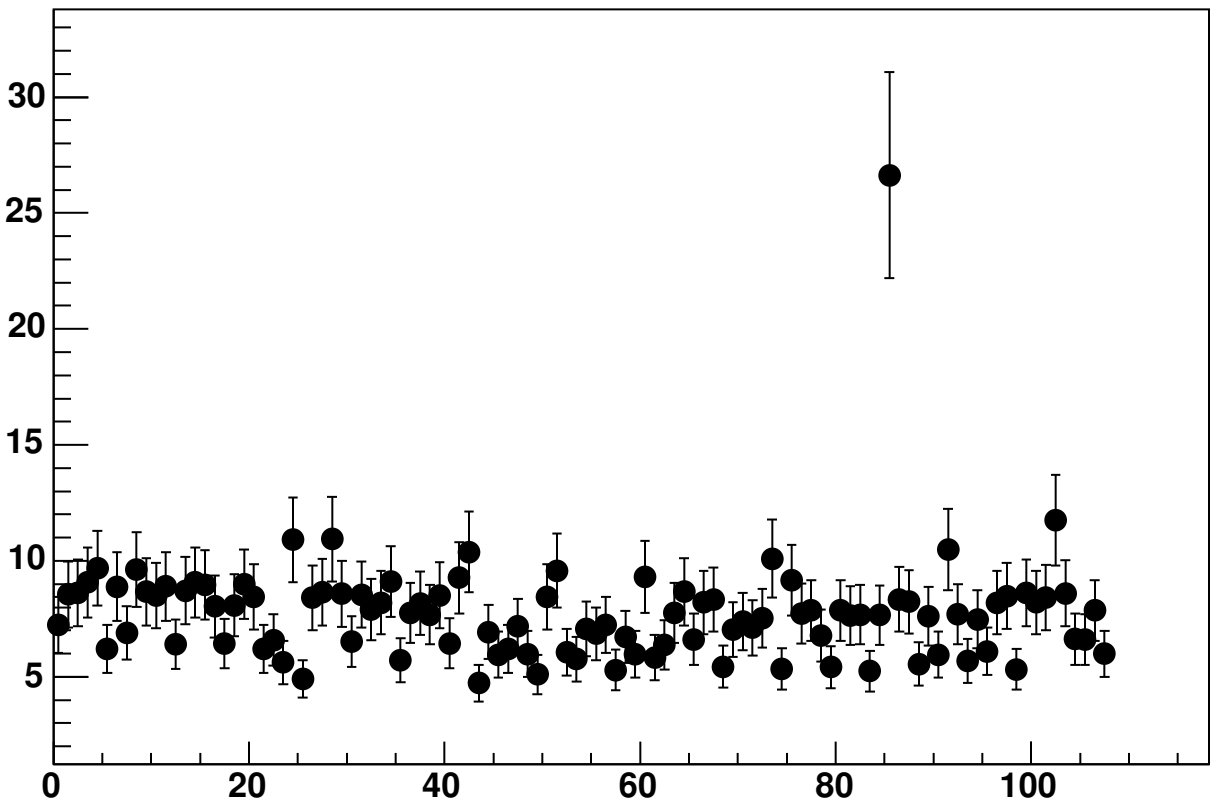
Enable 4, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



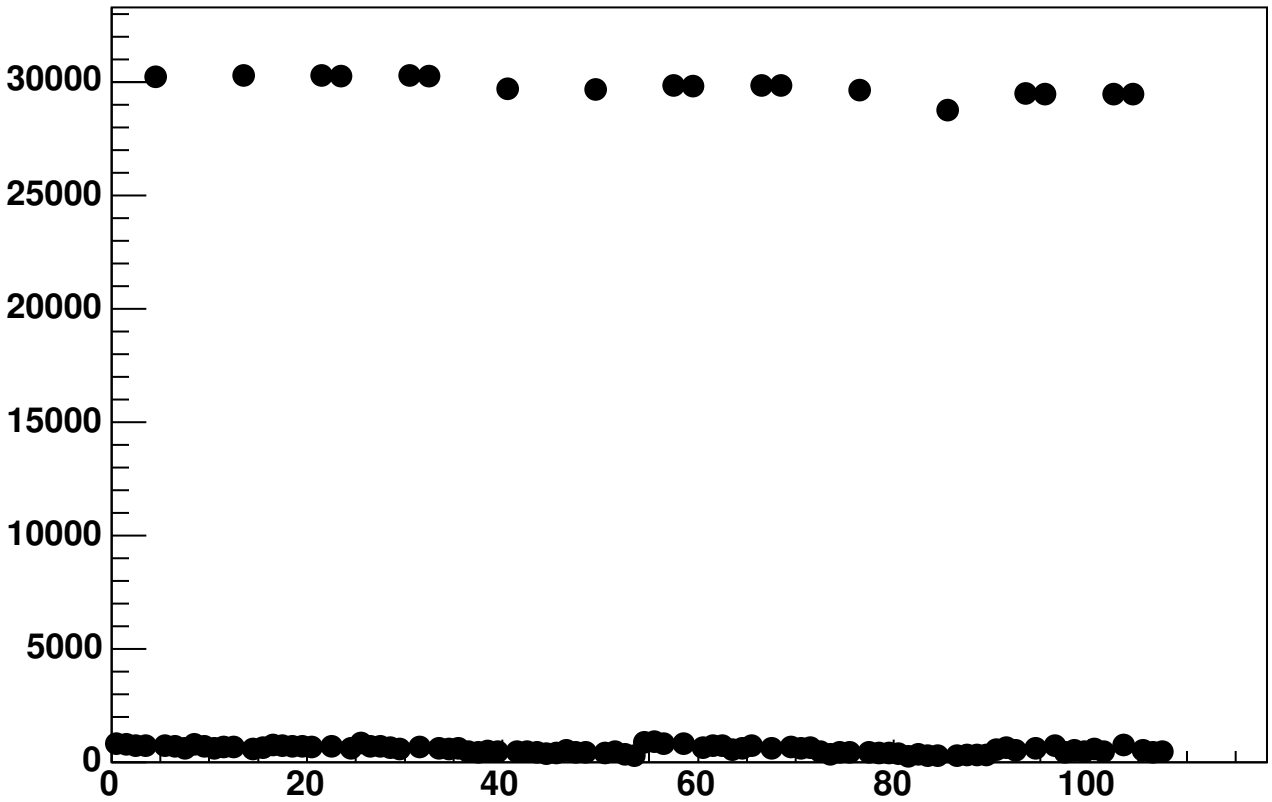
Enable 4, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



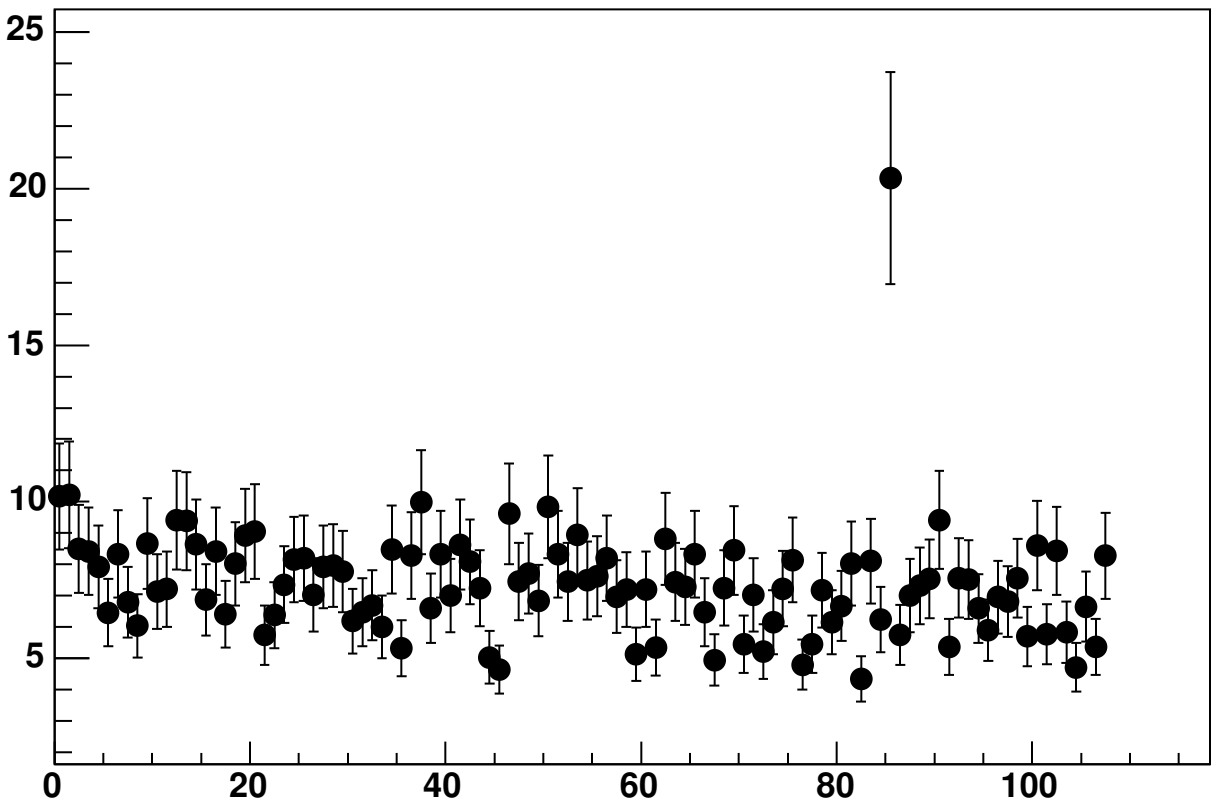
Enable 4, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



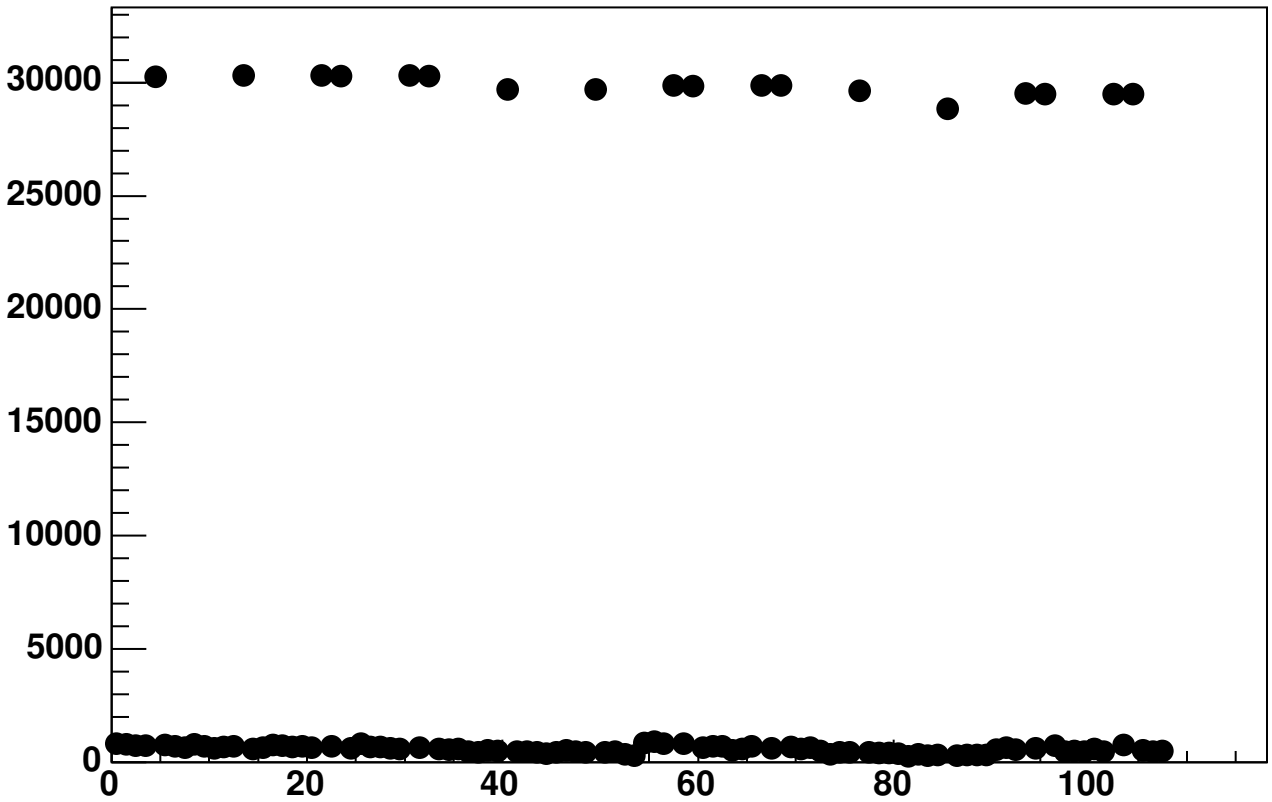
Enable 4, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



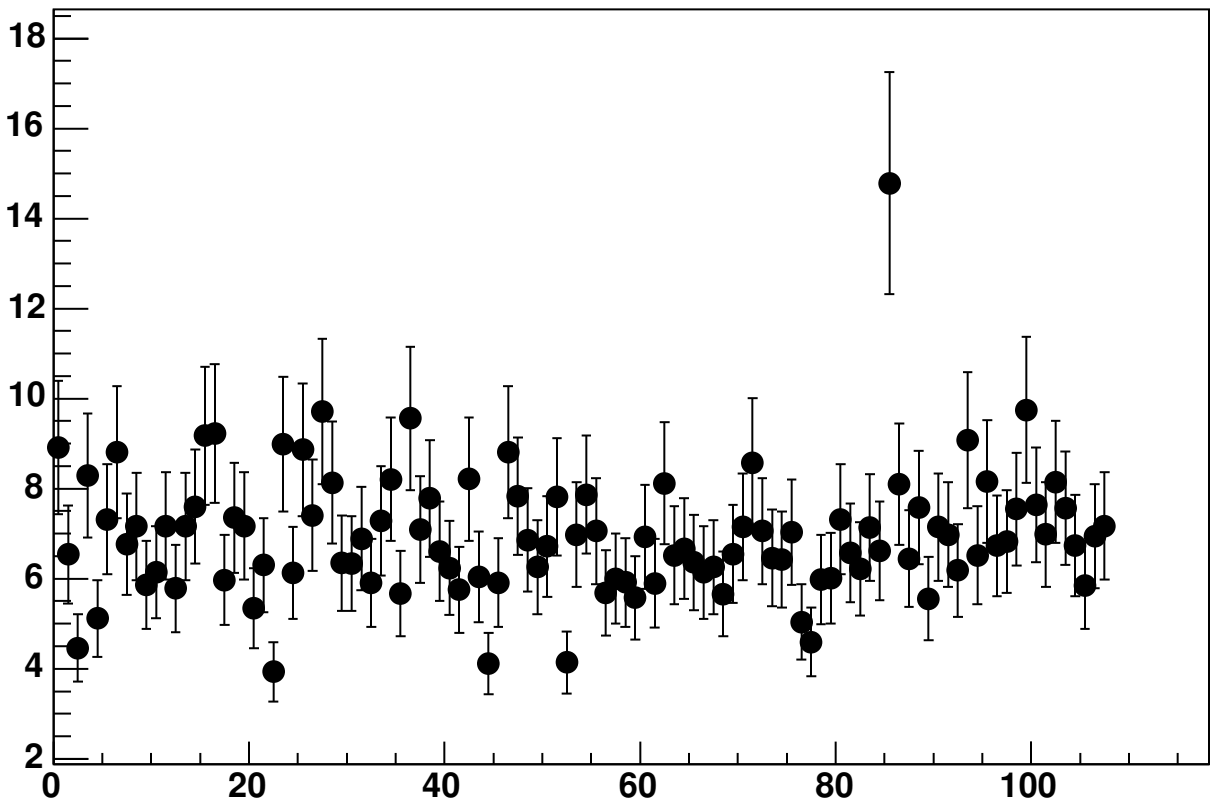
Enable 4, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



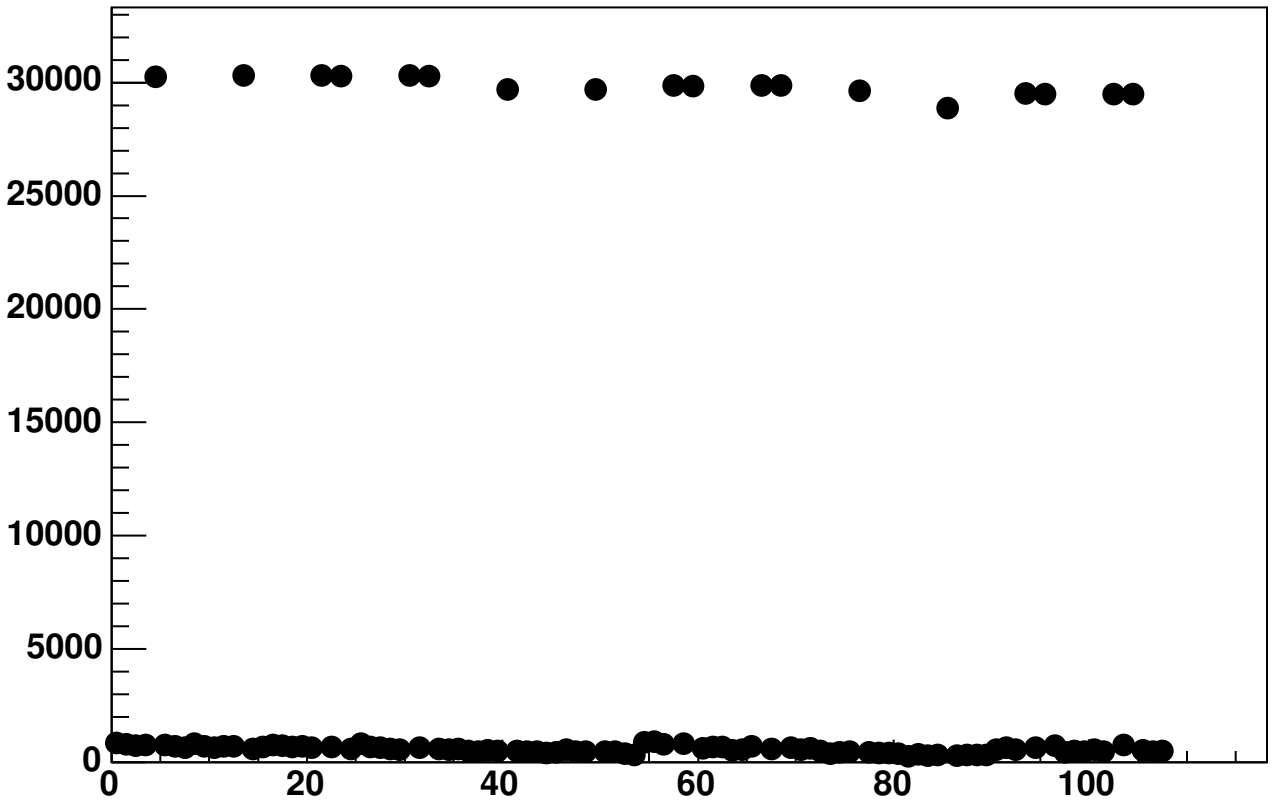
Enable 4, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



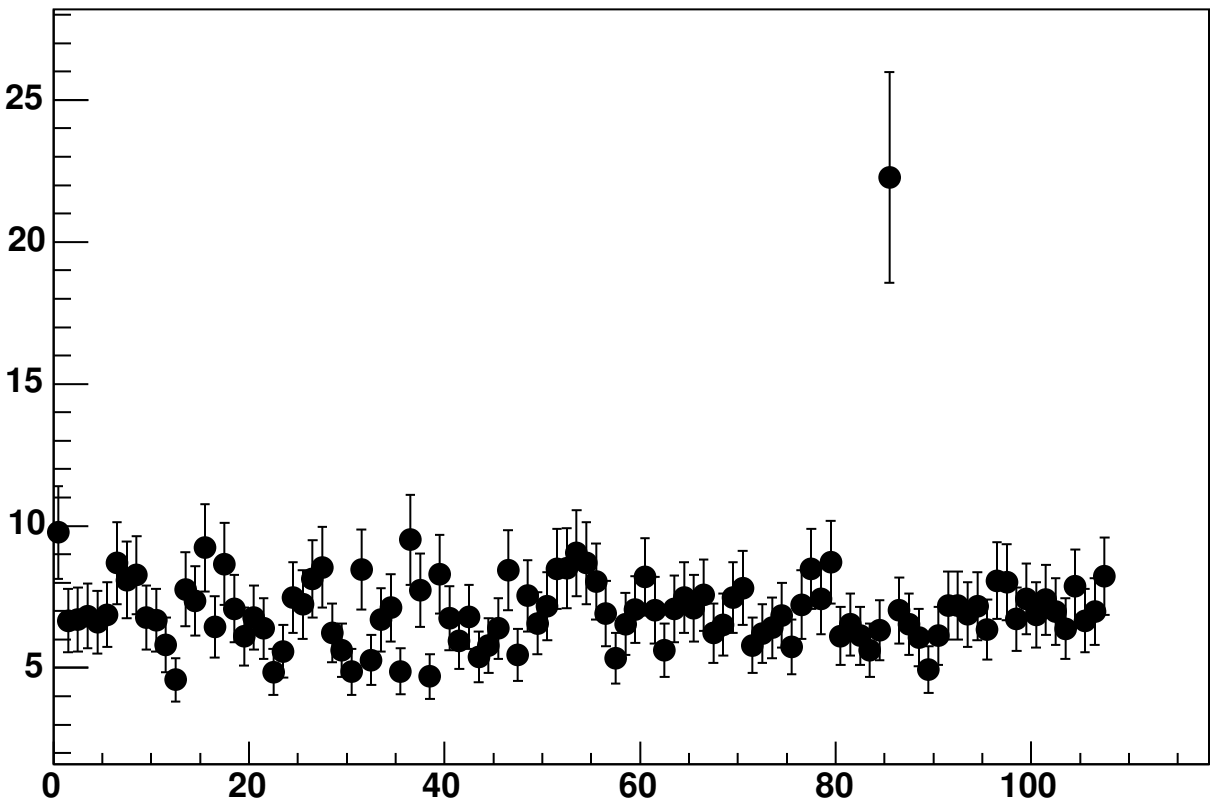
Enable 4, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



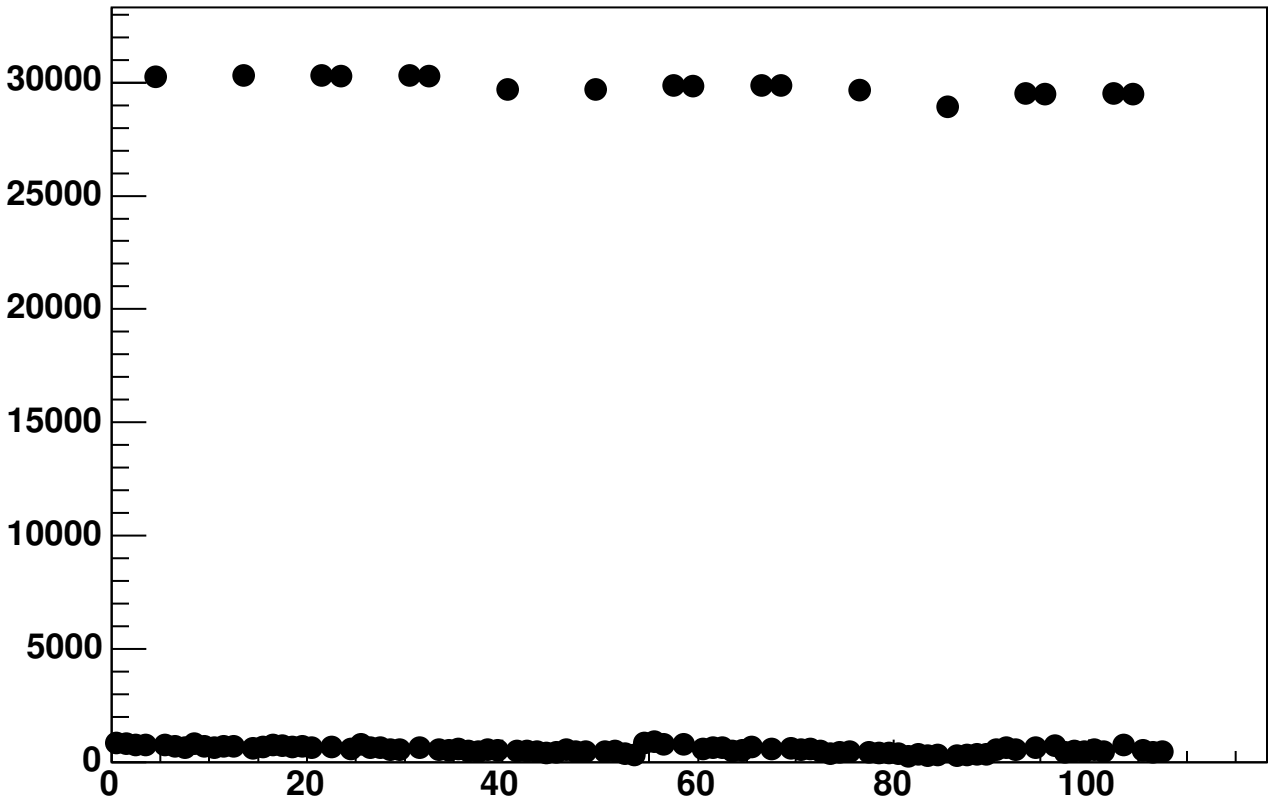
Enable 4, Hold=35, DAC=14400, ADC Mean vs 18*Chip+Chan



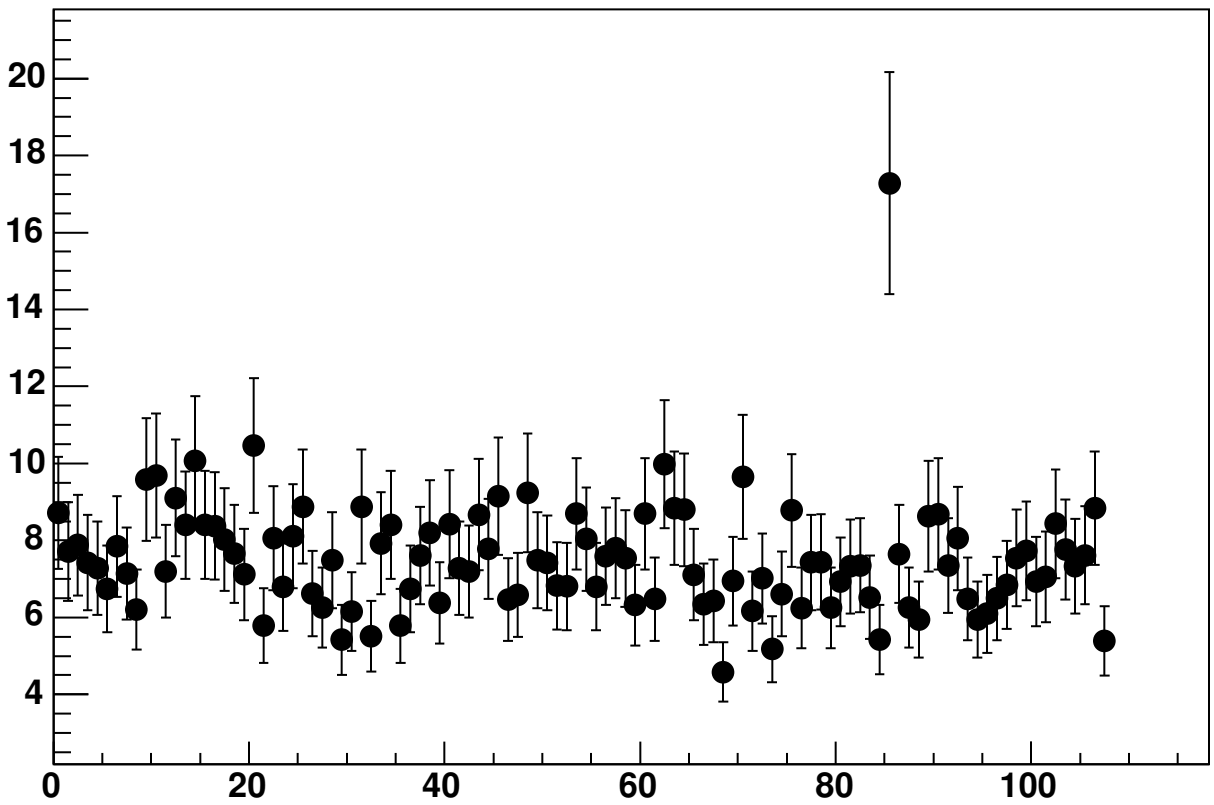
Enable 4, Hold=35, DAC=14400, ADC Noise vs 18*Chip+Chan



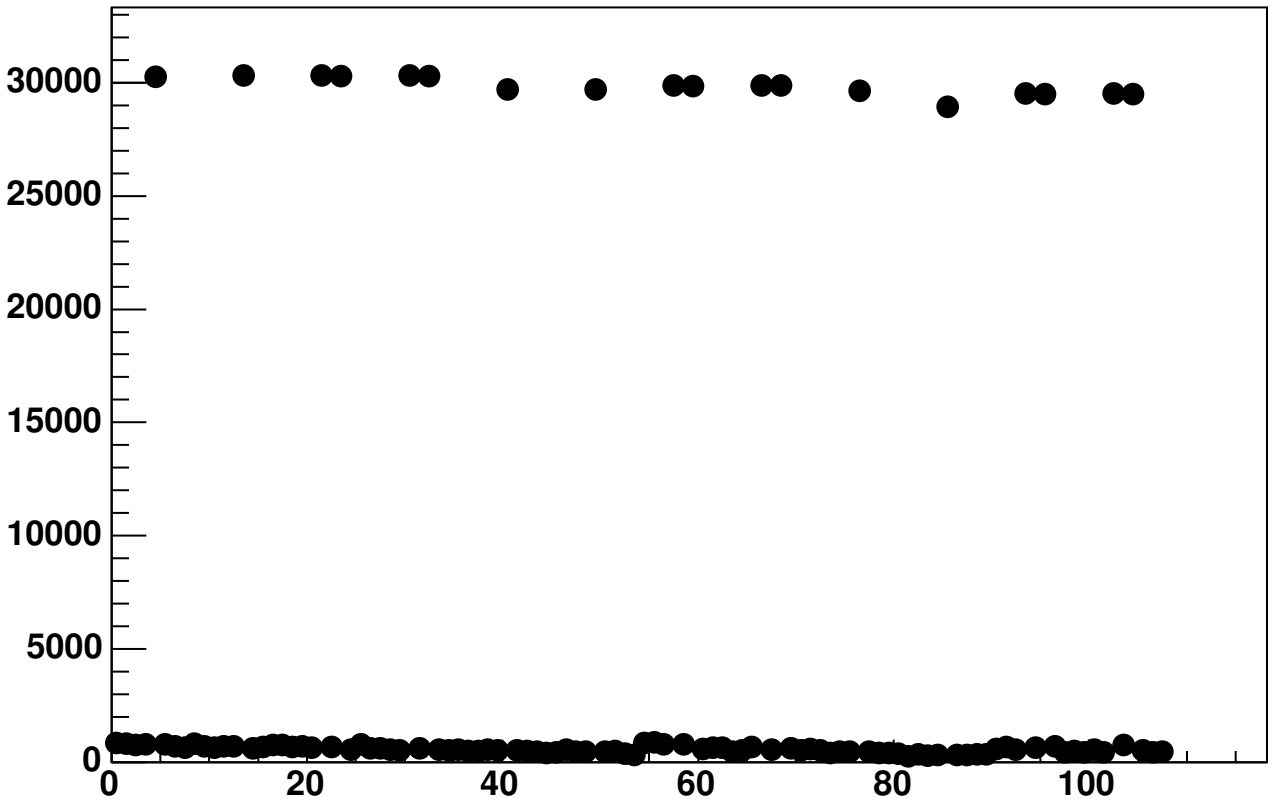
Enable 4, Hold=35, DAC=14800, ADC Mean vs 18*Chip+Chan



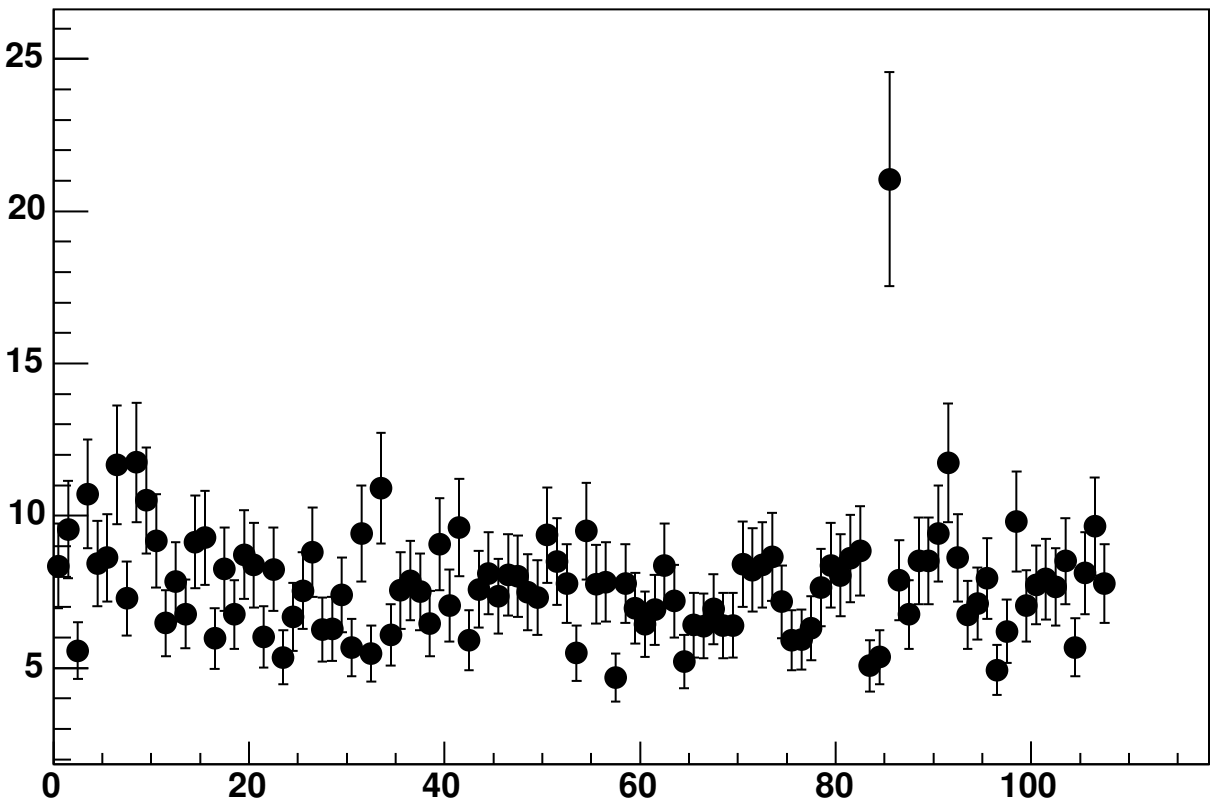
Enable 4, Hold=35, DAC=14800, ADC Noise vs 18*Chip+Chan



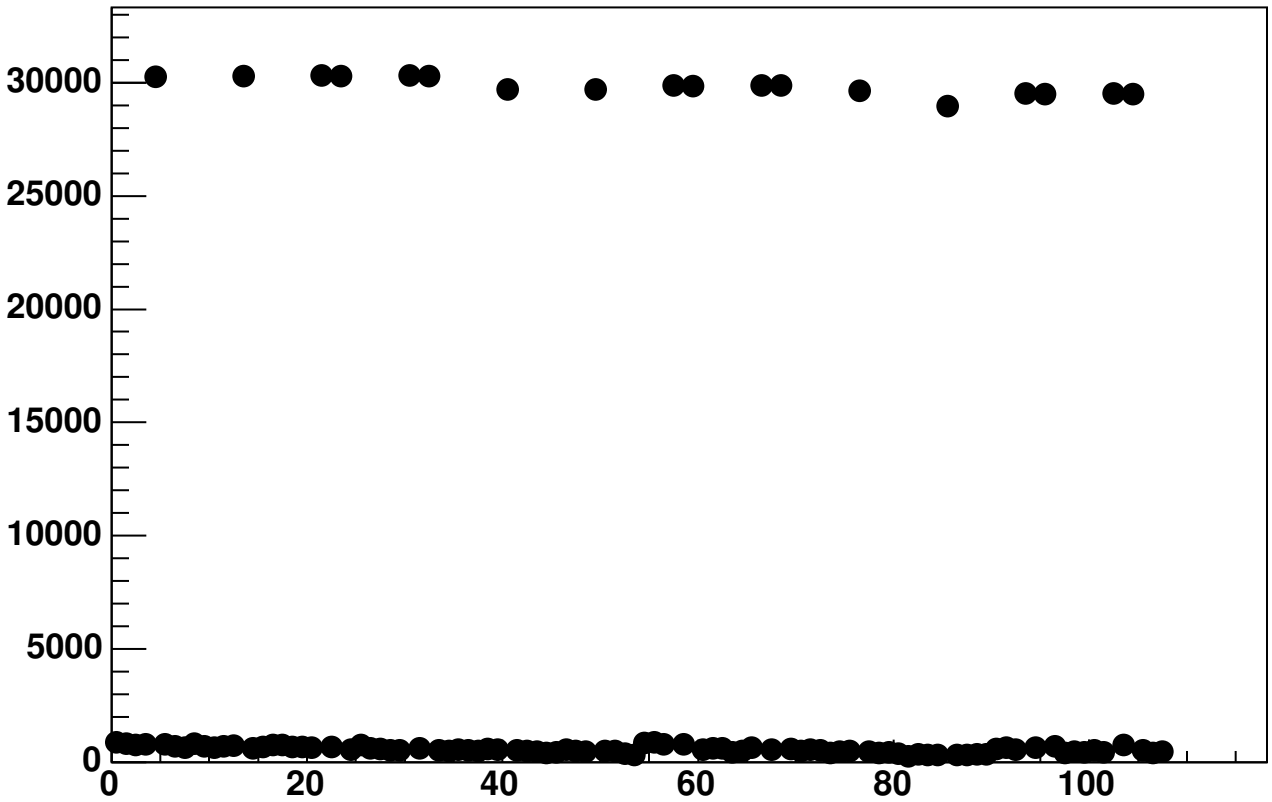
Enable 4, Hold=35, DAC=15200, ADC Mean vs 18*Chip+Chan



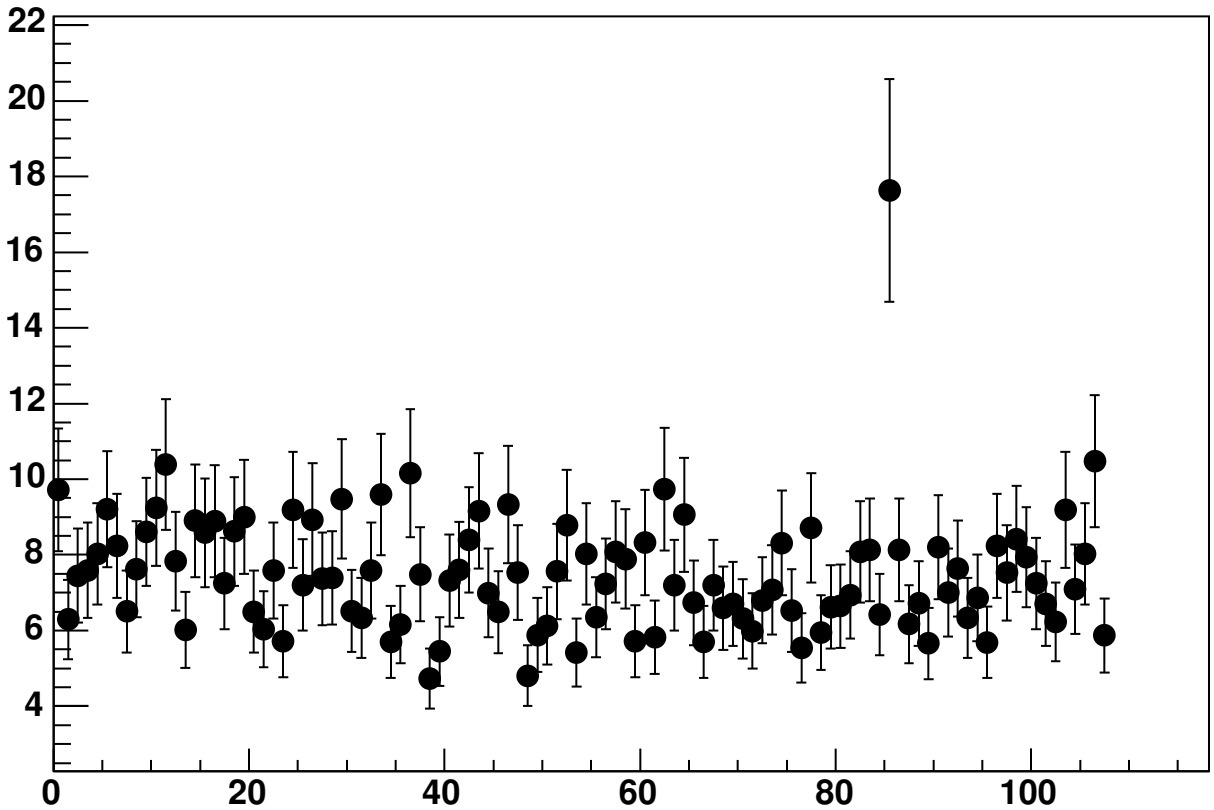
Enable 4, Hold=35, DAC=15200, ADC Noise vs 18*Chip+Chan



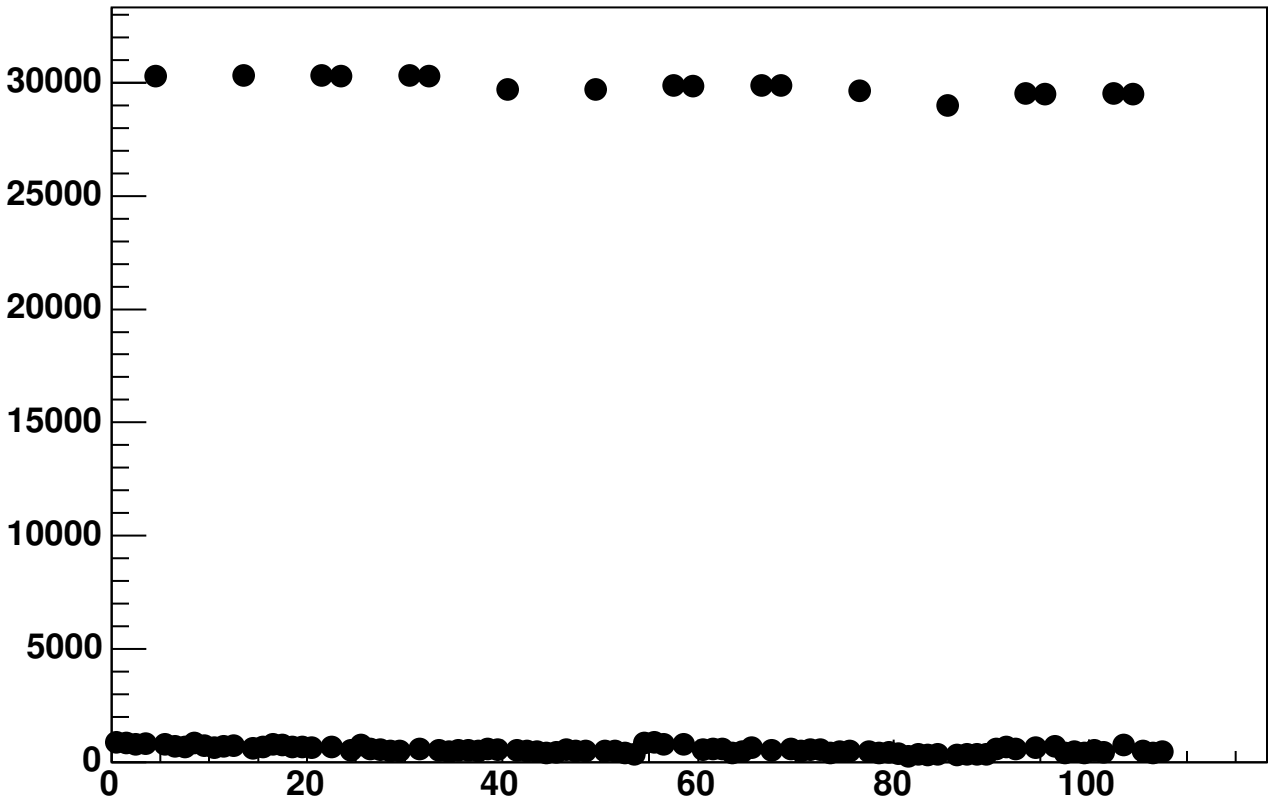
Enable 4, Hold=35, DAC=15600, ADC Mean vs 18*Chip+Chan



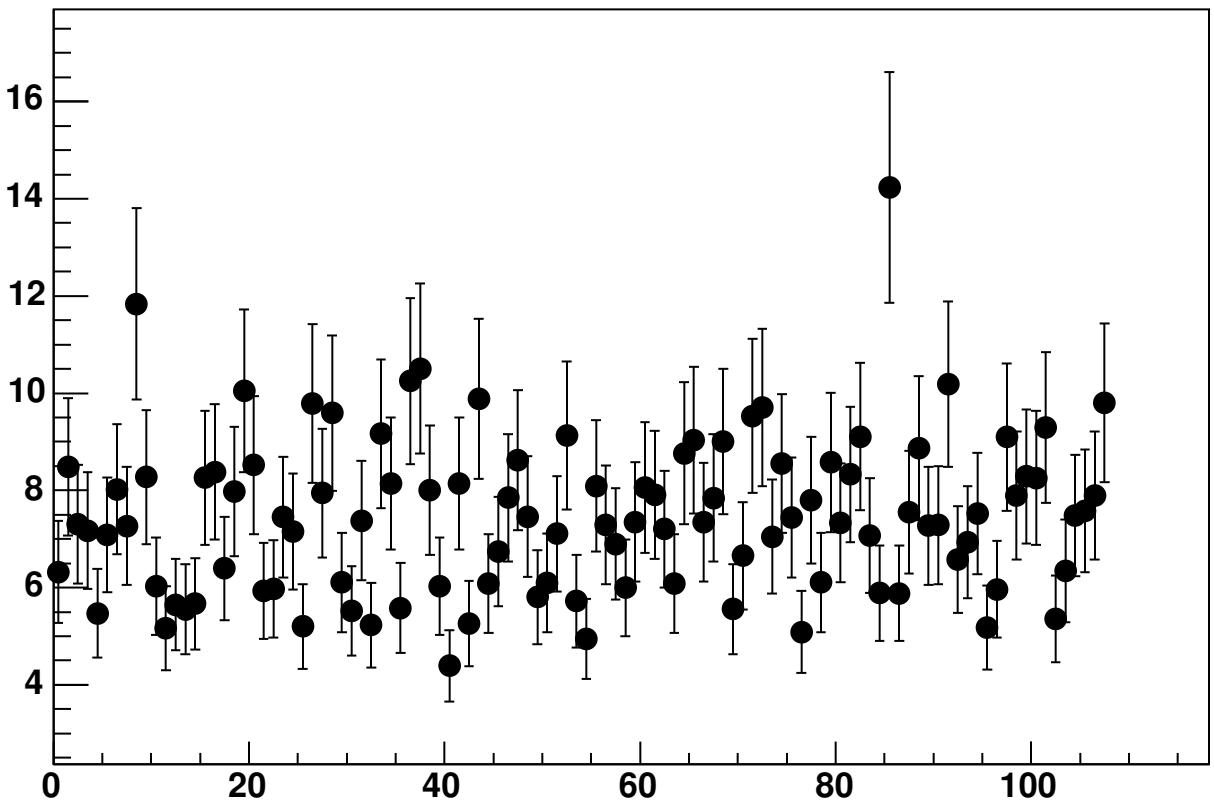
Enable 4, Hold=35, DAC=15600, ADC Noise vs 18*Chip+Chan



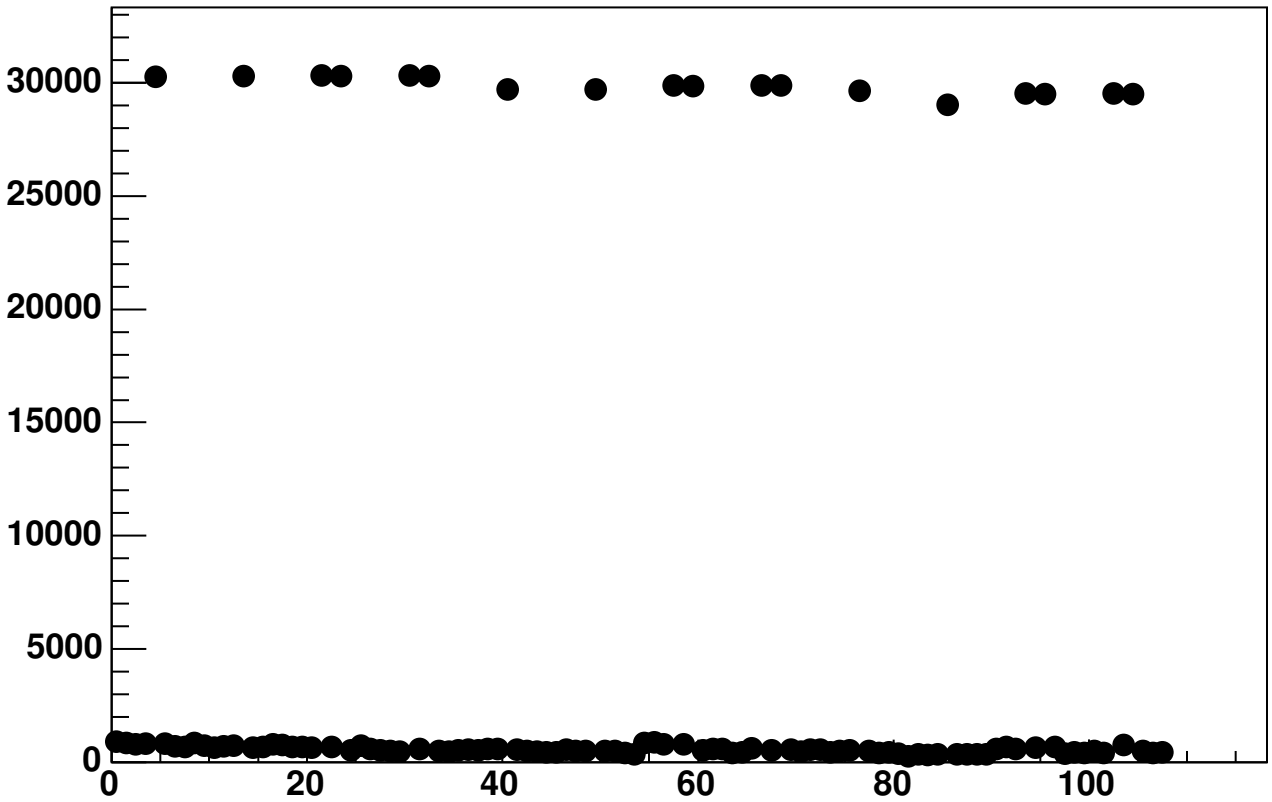
Enable 4, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



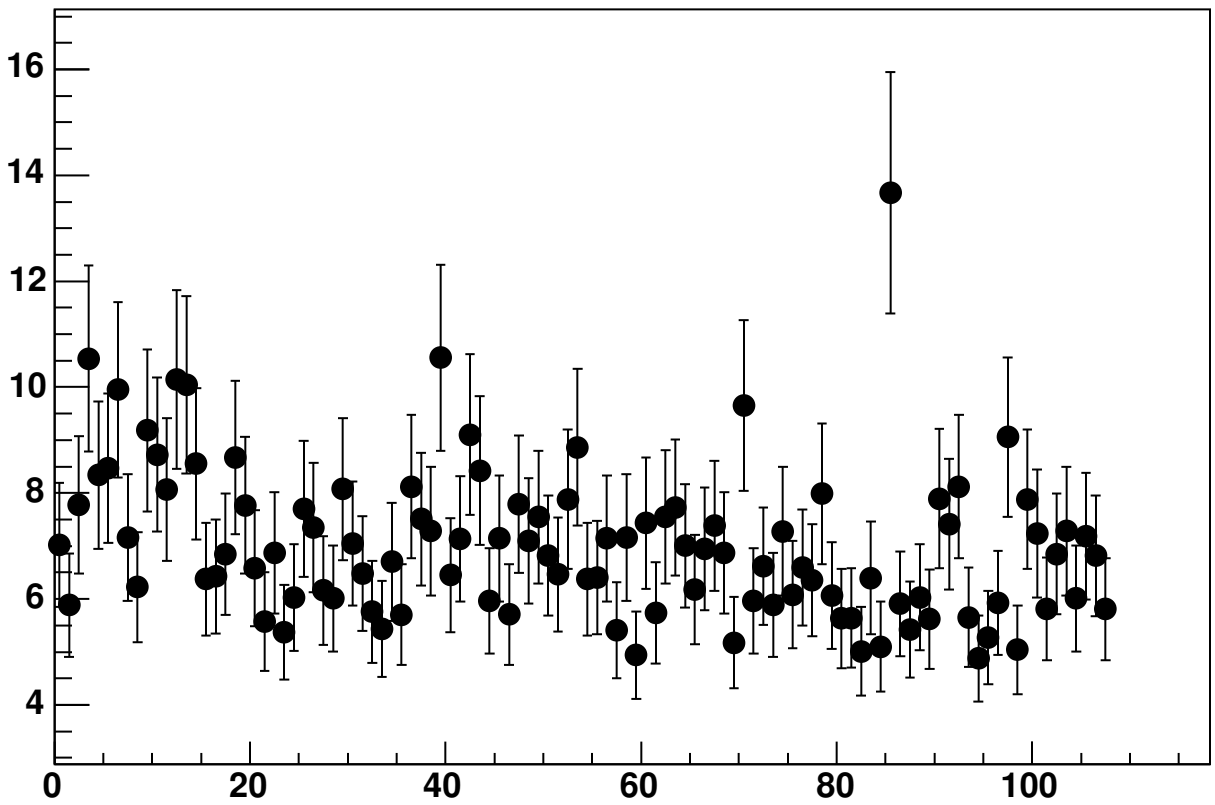
Enable 4, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



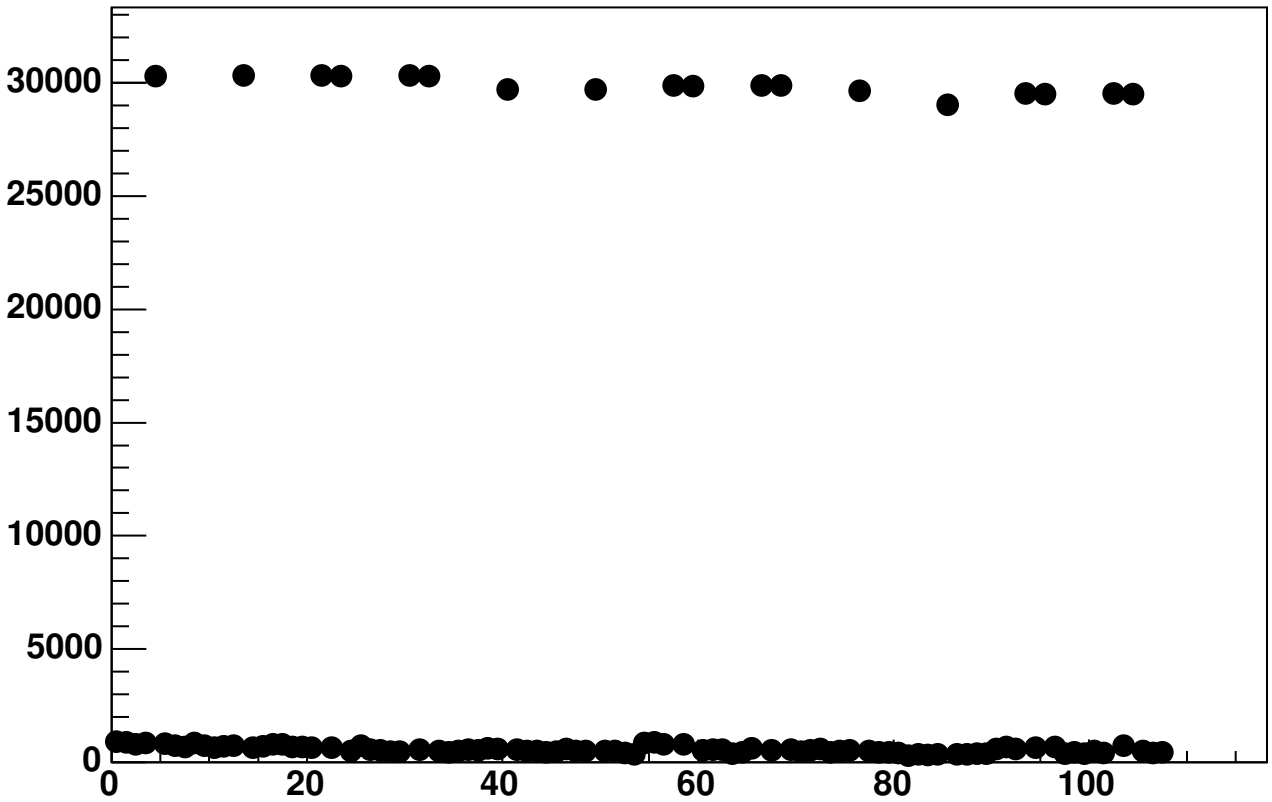
Enable 4, Hold=35, DAC=16400, ADC Mean vs 18*Chip+Chan



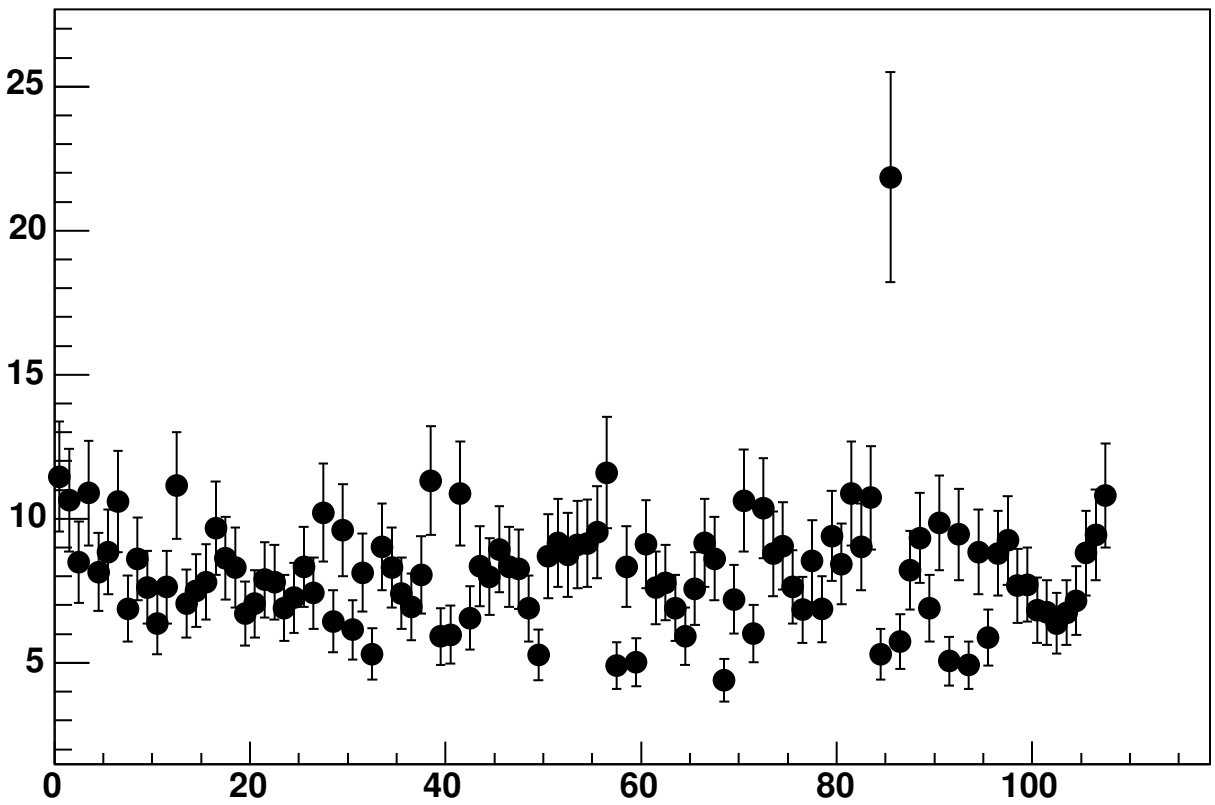
Enable 4, Hold=35, DAC=16400, ADC Noise vs 18*Chip+Chan



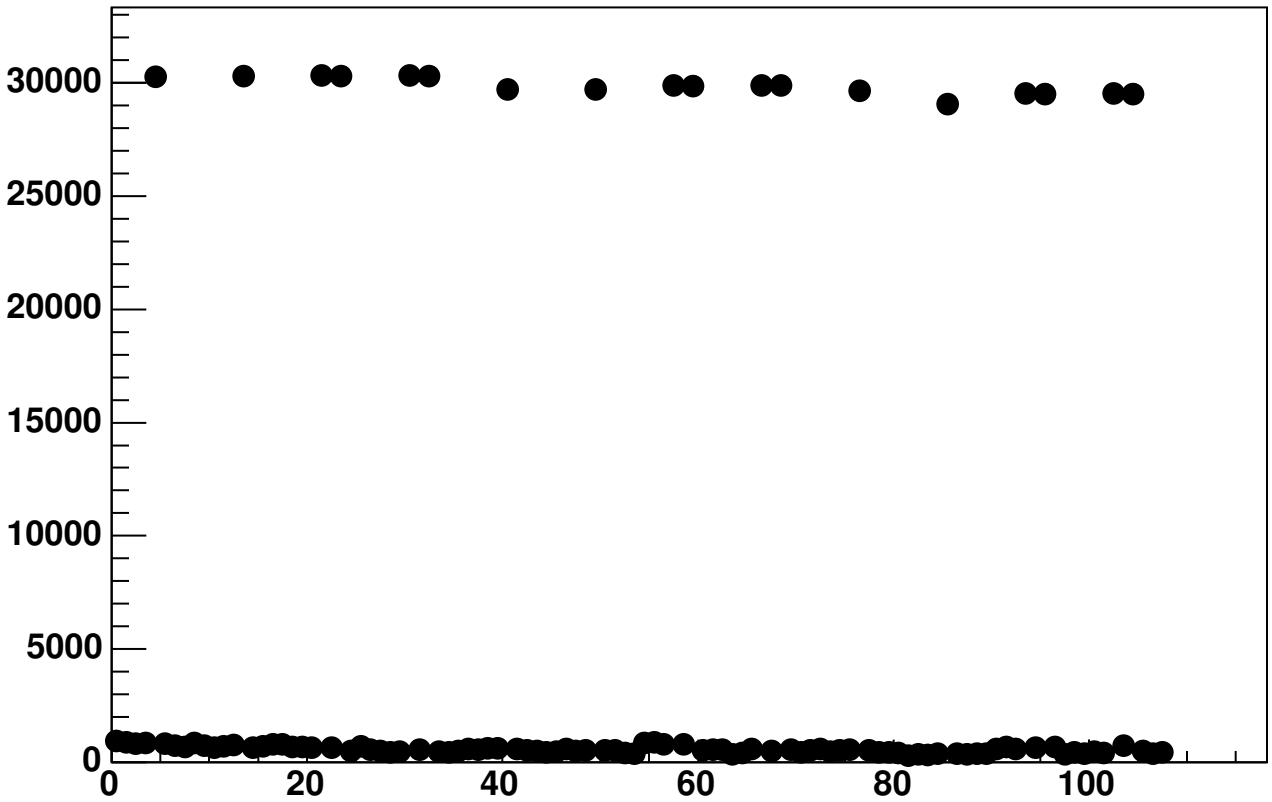
Enable 4, Hold=35, DAC=16800, ADC Mean vs 18*Chip+Chan



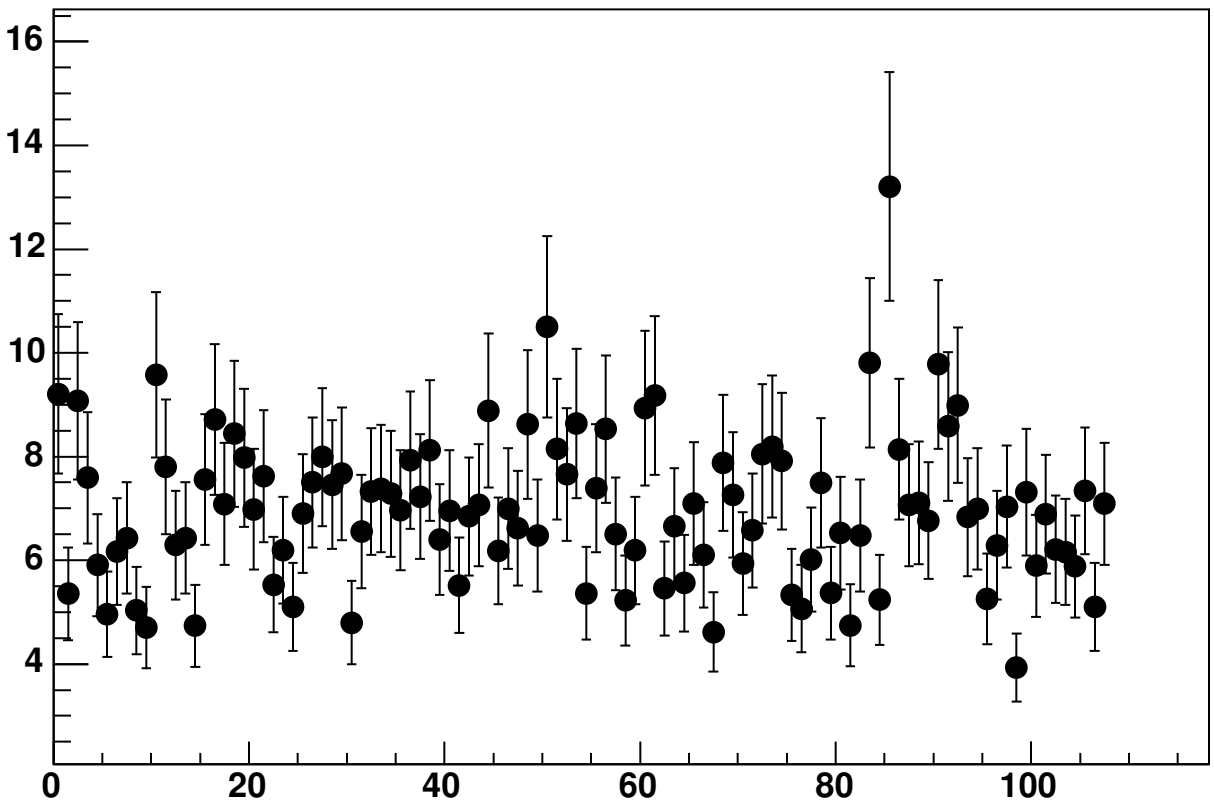
Enable 4, Hold=35, DAC=16800, ADC Noise vs 18*Chip+Chan



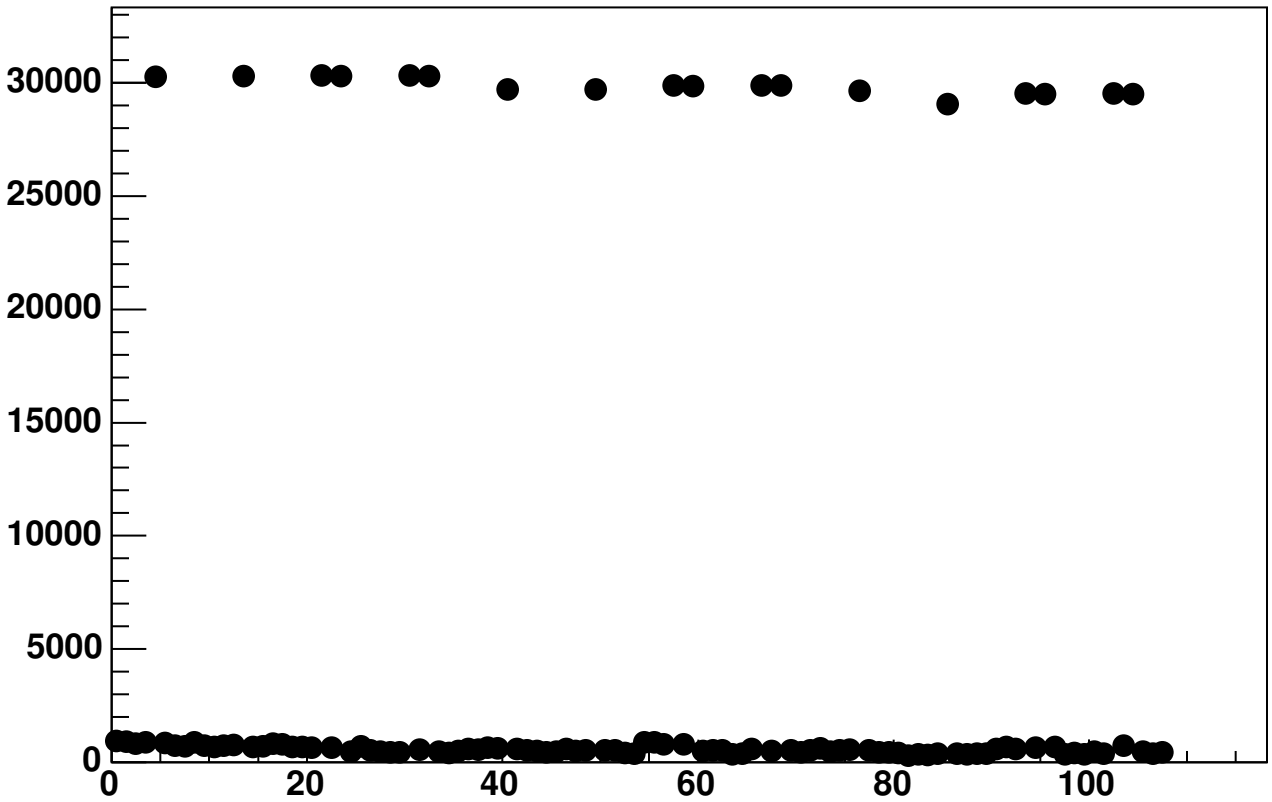
Enable 4, Hold=35, DAC=17200, ADC Mean vs 18*Chip+Chan



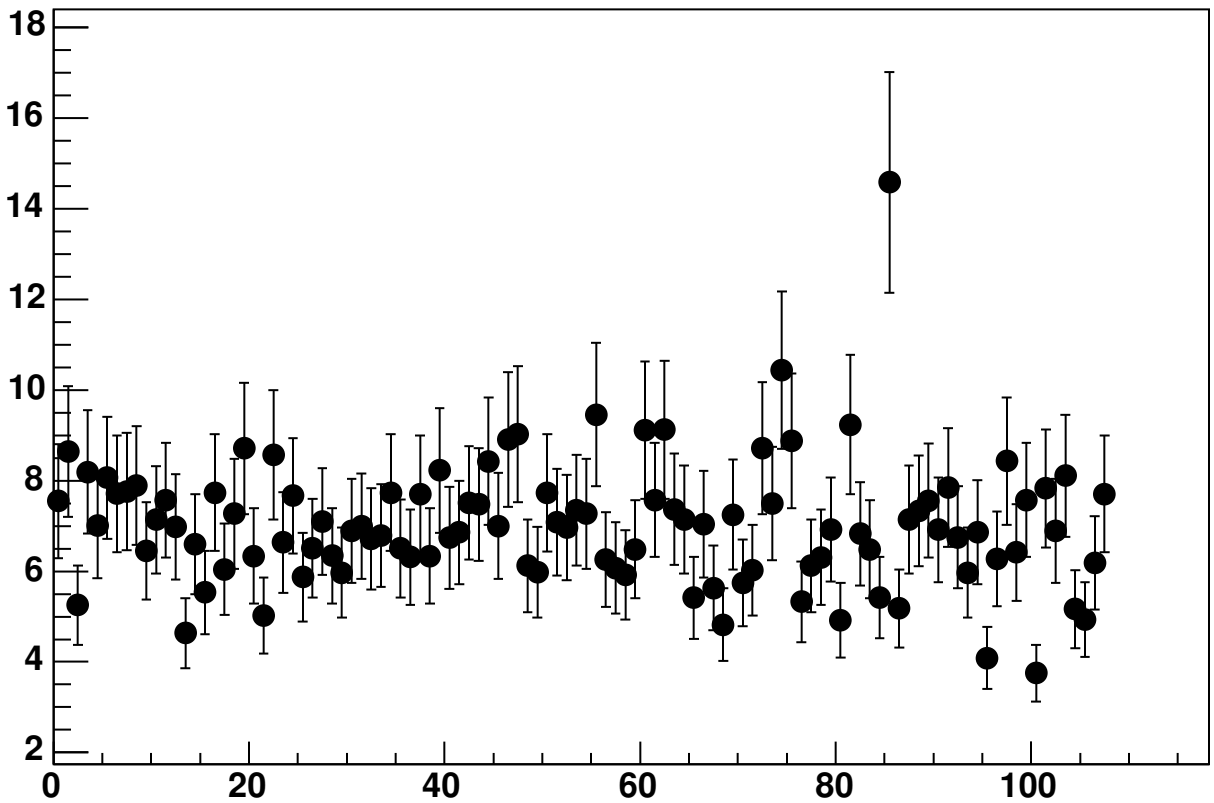
Enable 4, Hold=35, DAC=17200, ADC Noise vs 18*Chip+Chan



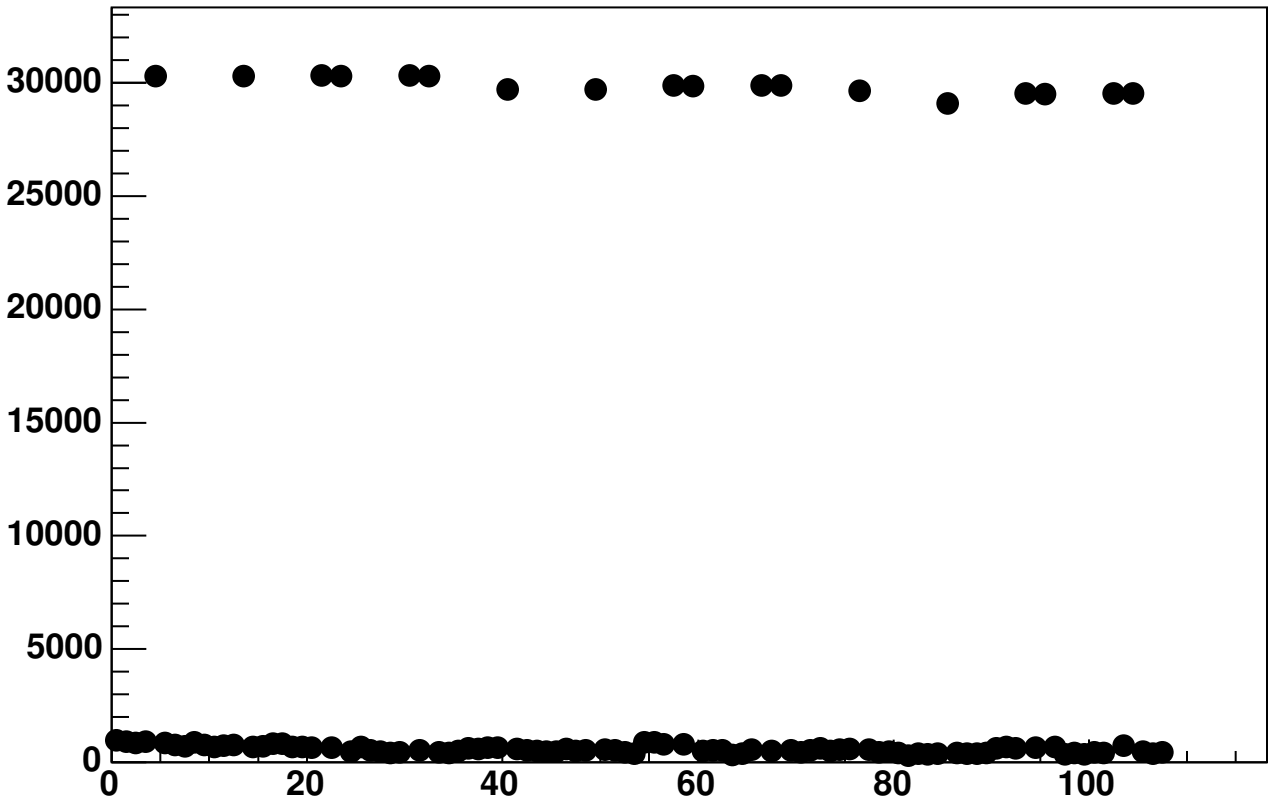
Enable 4, Hold=35, DAC=17600, ADC Mean vs 18*Chip+Chan



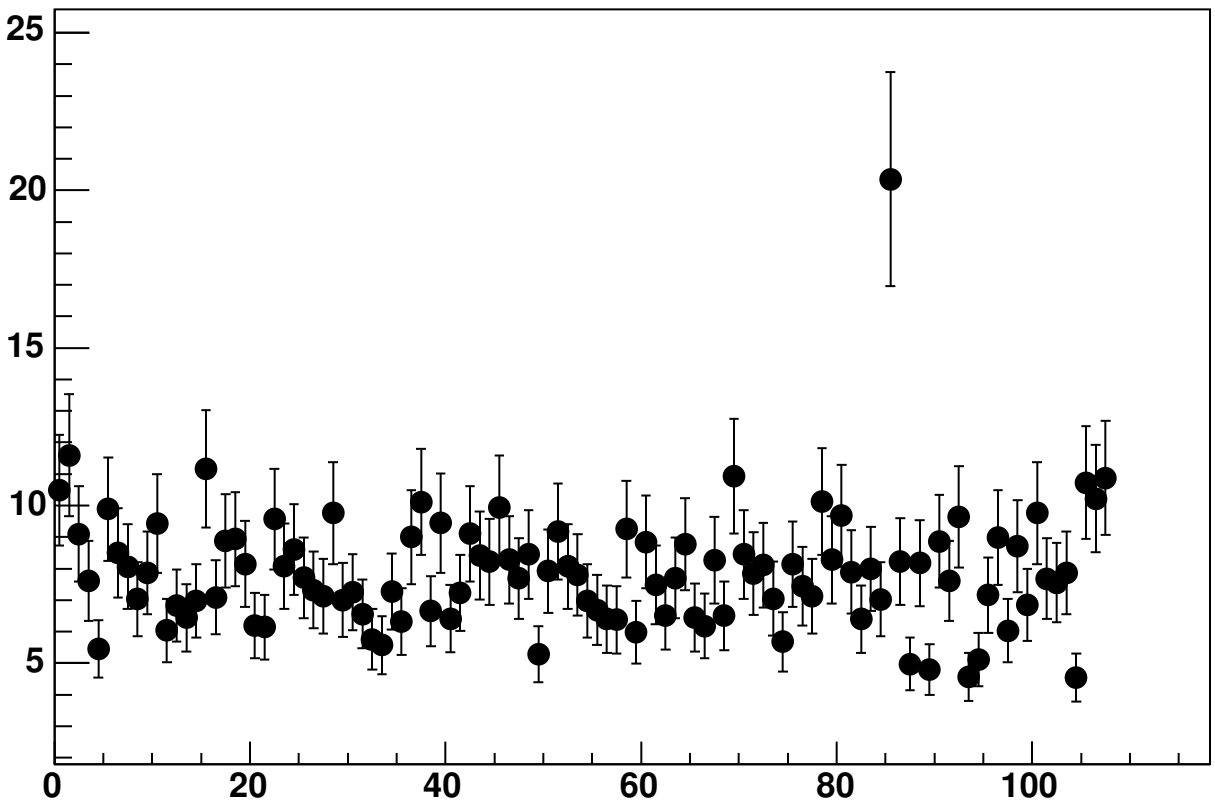
Enable 4, Hold=35, DAC=17600, ADC Noise vs 18*Chip+Chan



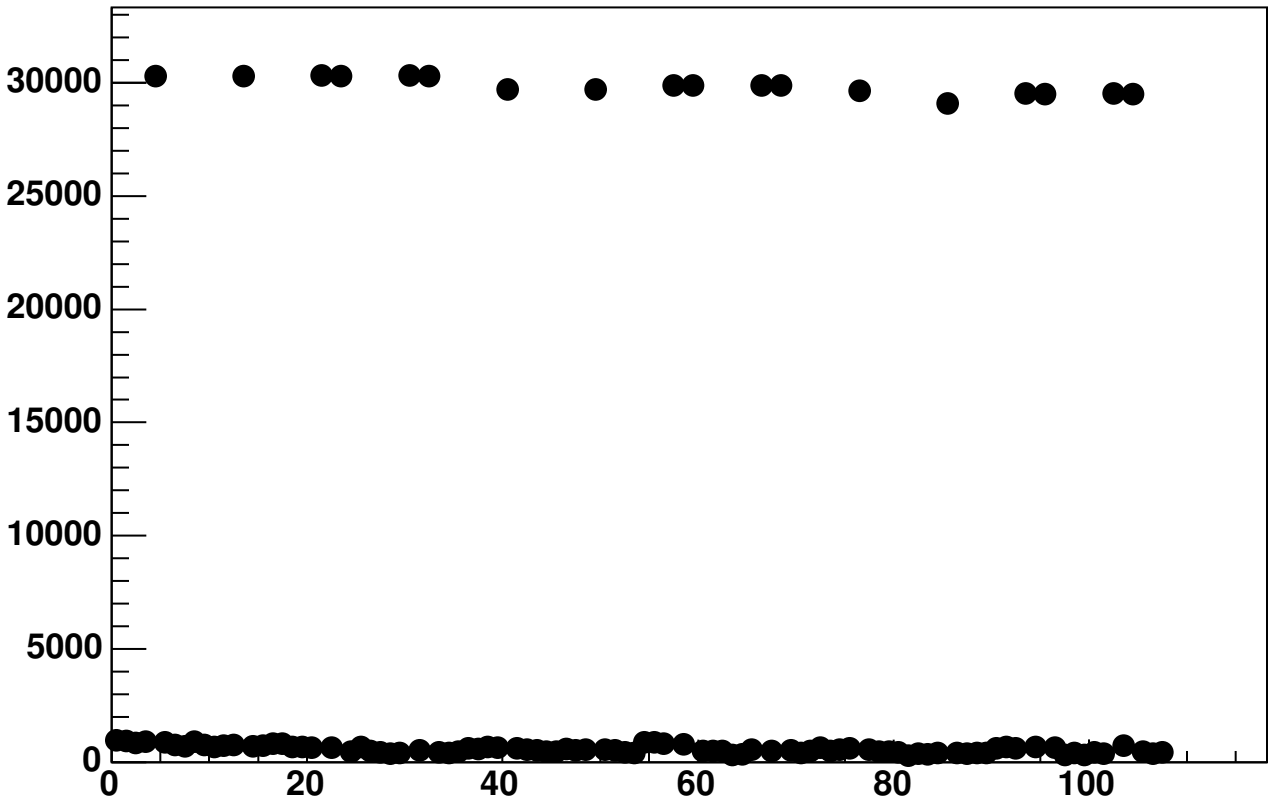
Enable 4, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



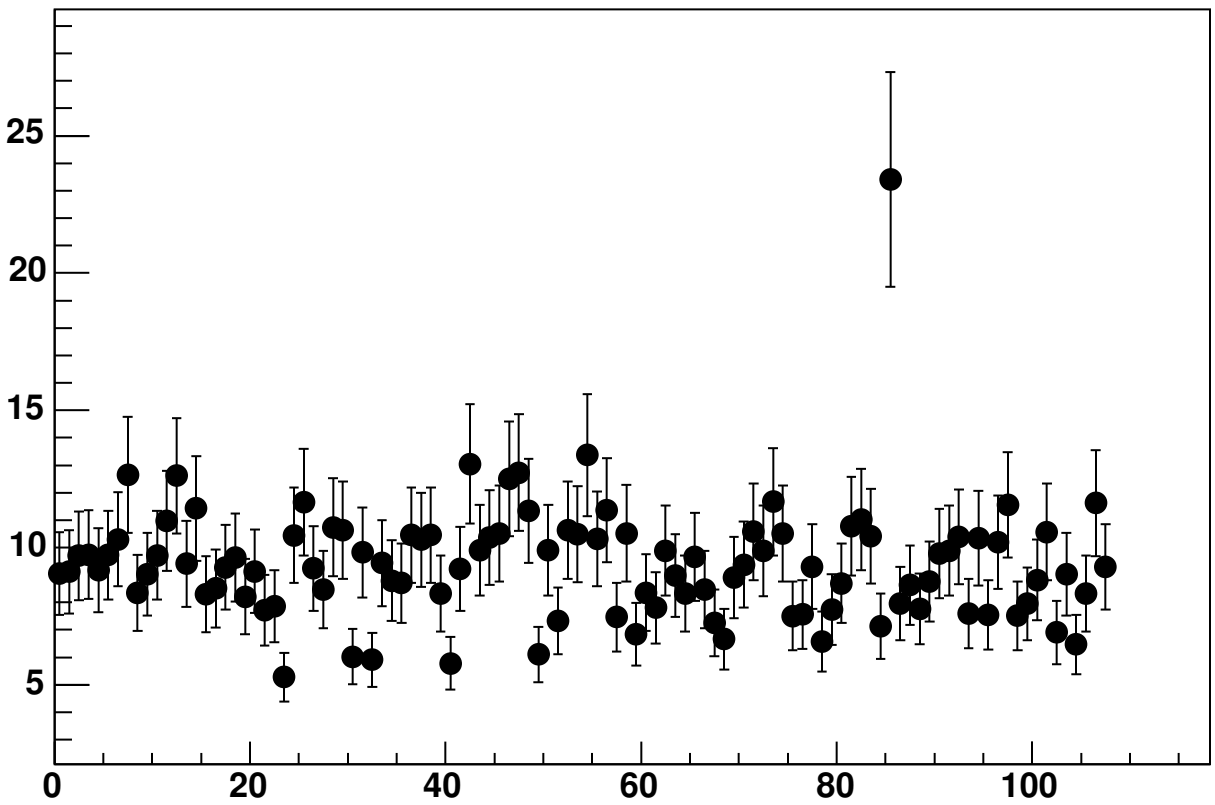
Enable 4, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



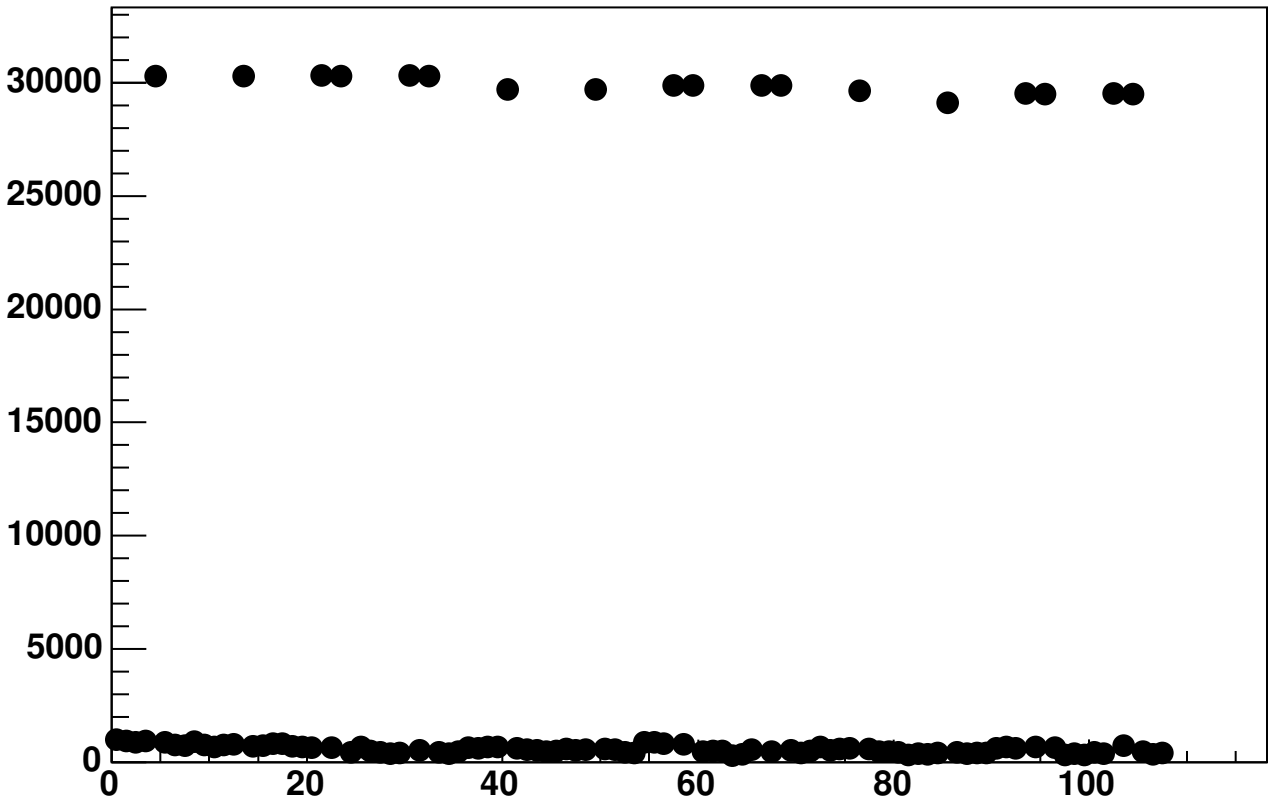
Enable 4, Hold=35, DAC=18400, ADC Mean vs 18*Chip+Chan



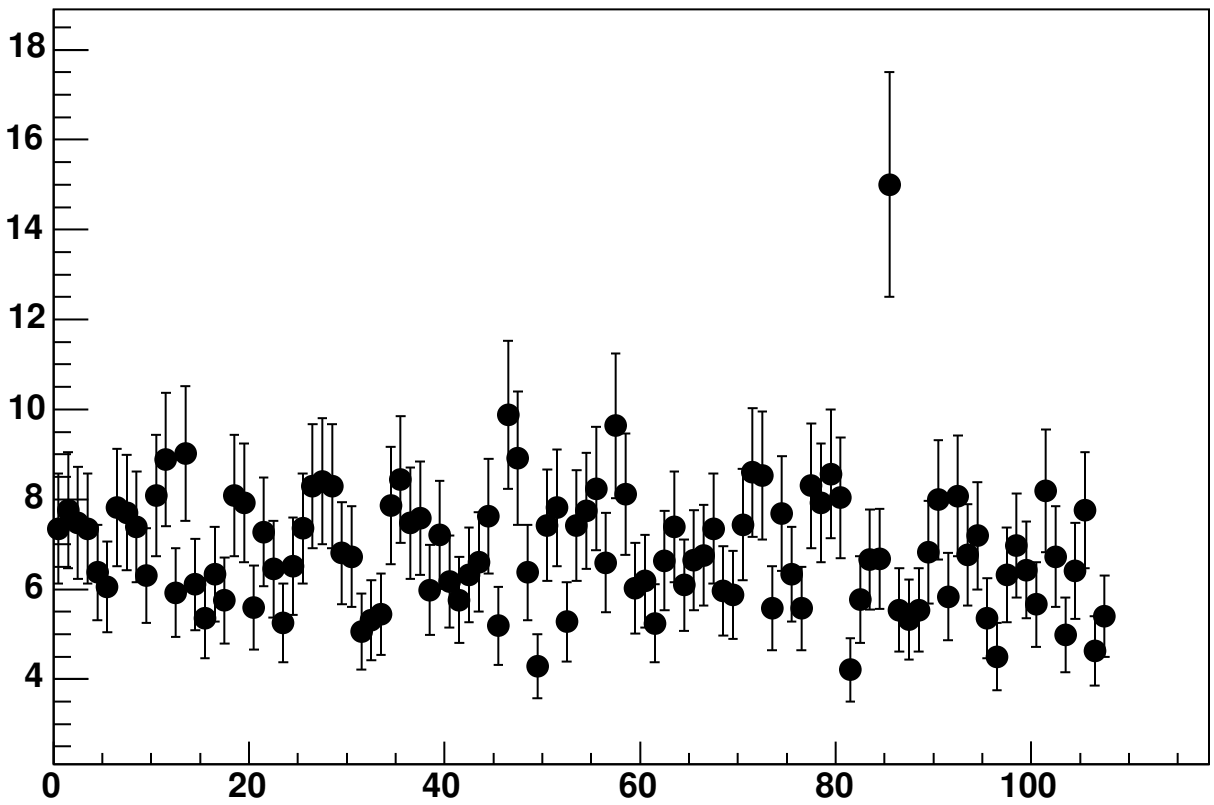
Enable 4, Hold=35, DAC=18400, ADC Noise vs 18*Chip+Chan



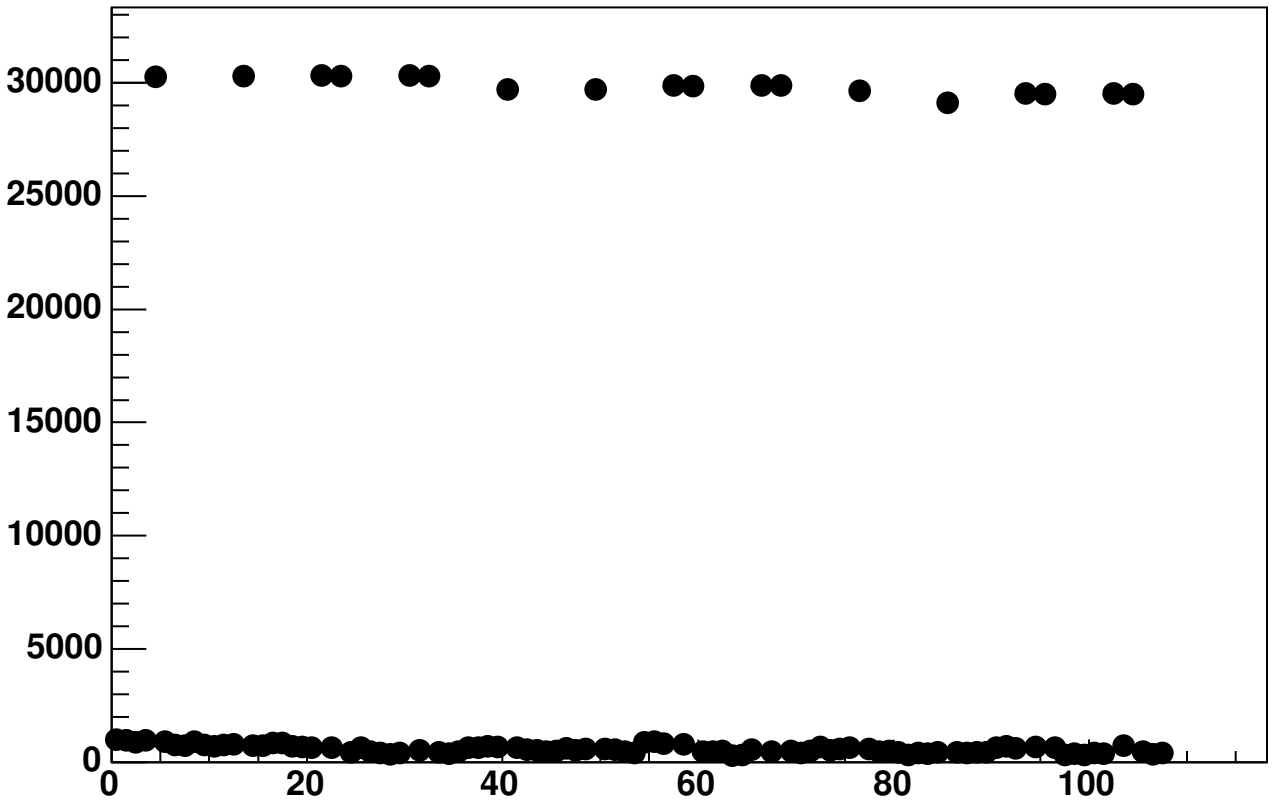
Enable 4, Hold=35, DAC=18800, ADC Mean vs 18*Chip+Chan



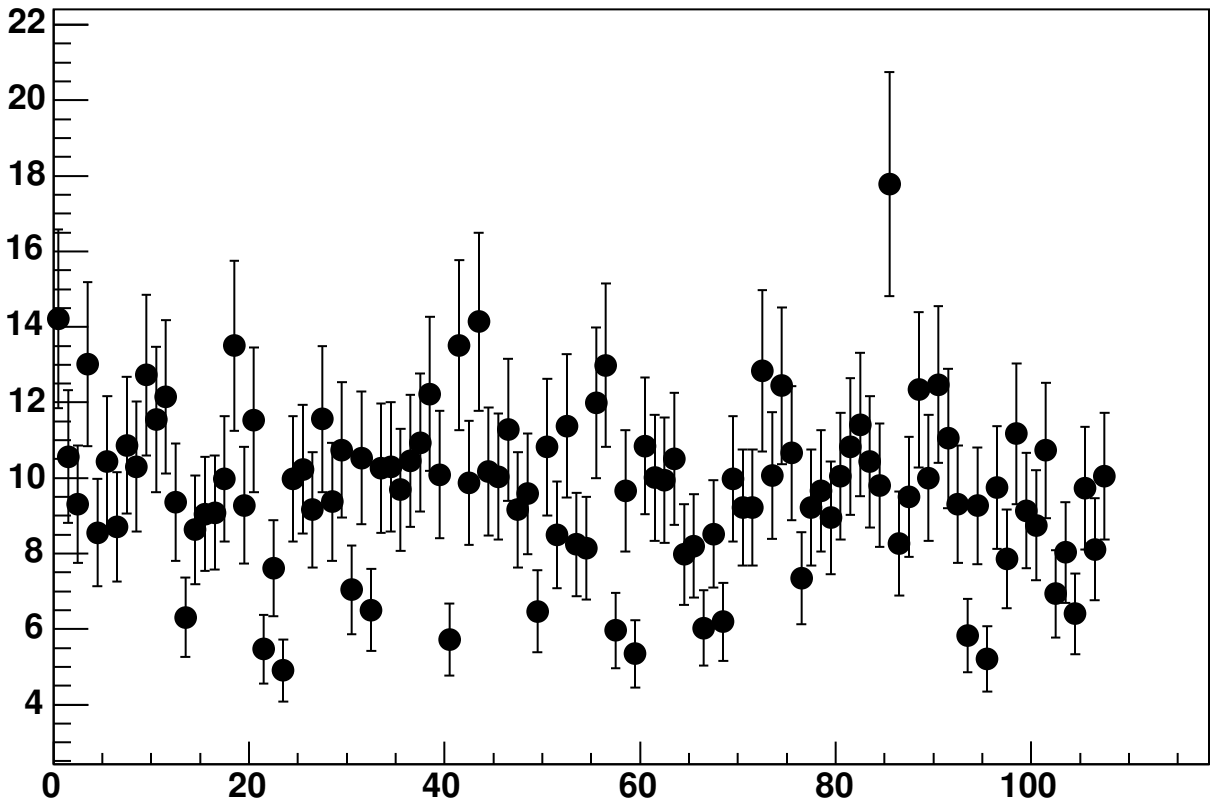
Enable 4, Hold=35, DAC=18800, ADC Noise vs 18*Chip+Chan



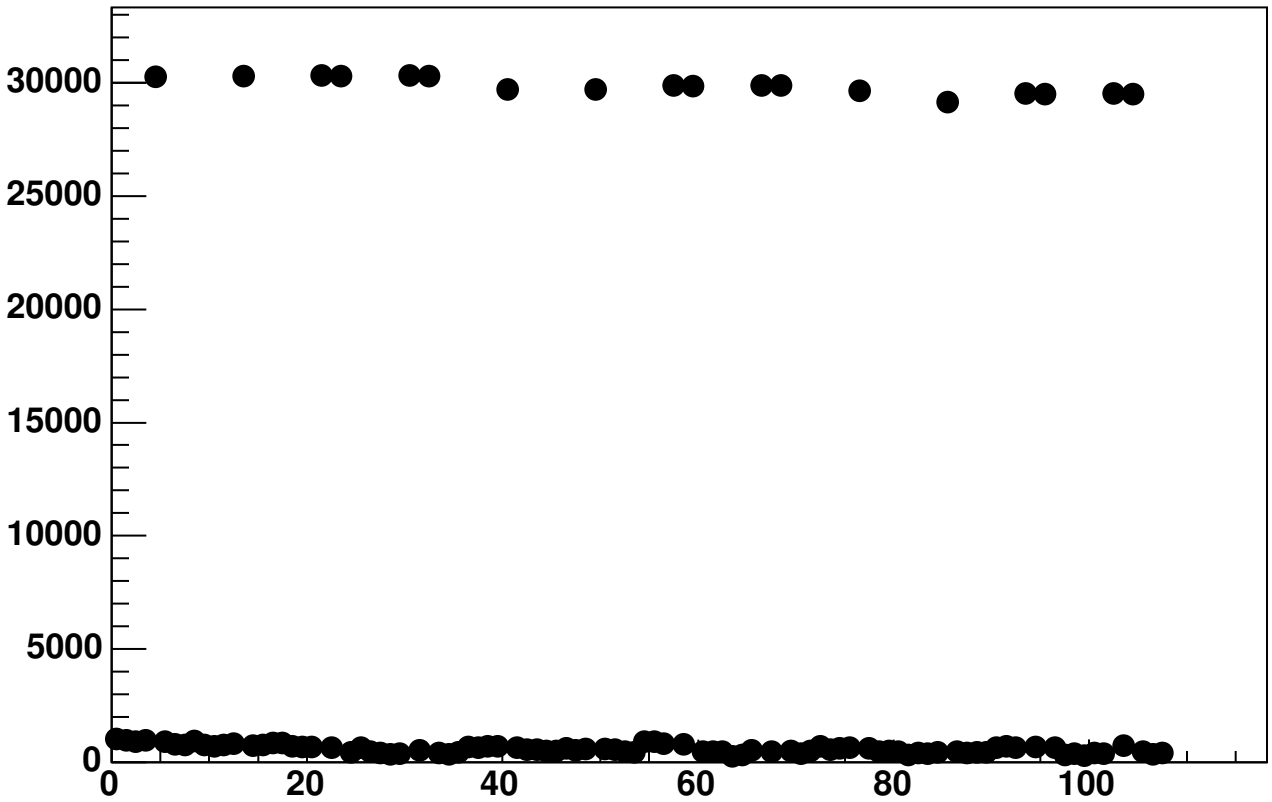
Enable 4, Hold=35, DAC=19200, ADC Mean vs 18*Chip+Chan



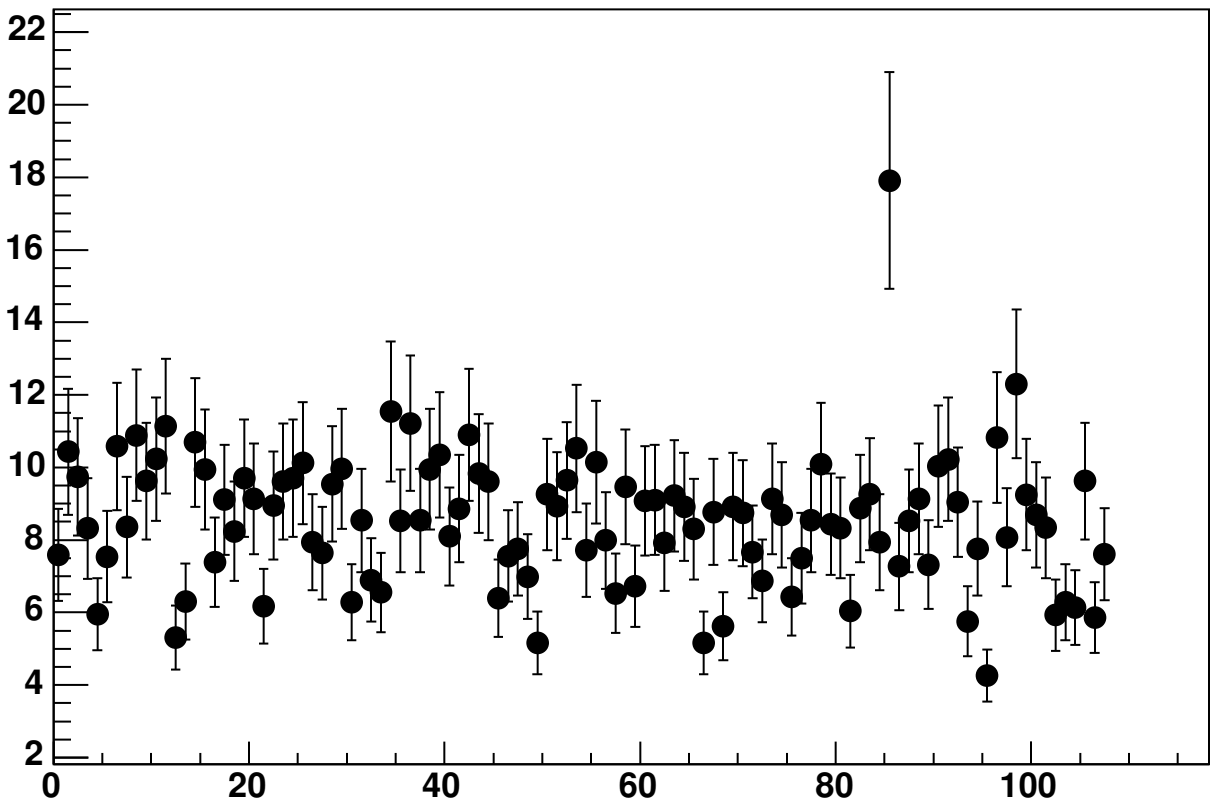
Enable 4, Hold=35, DAC=19200, ADC Noise vs 18*Chip+Chan



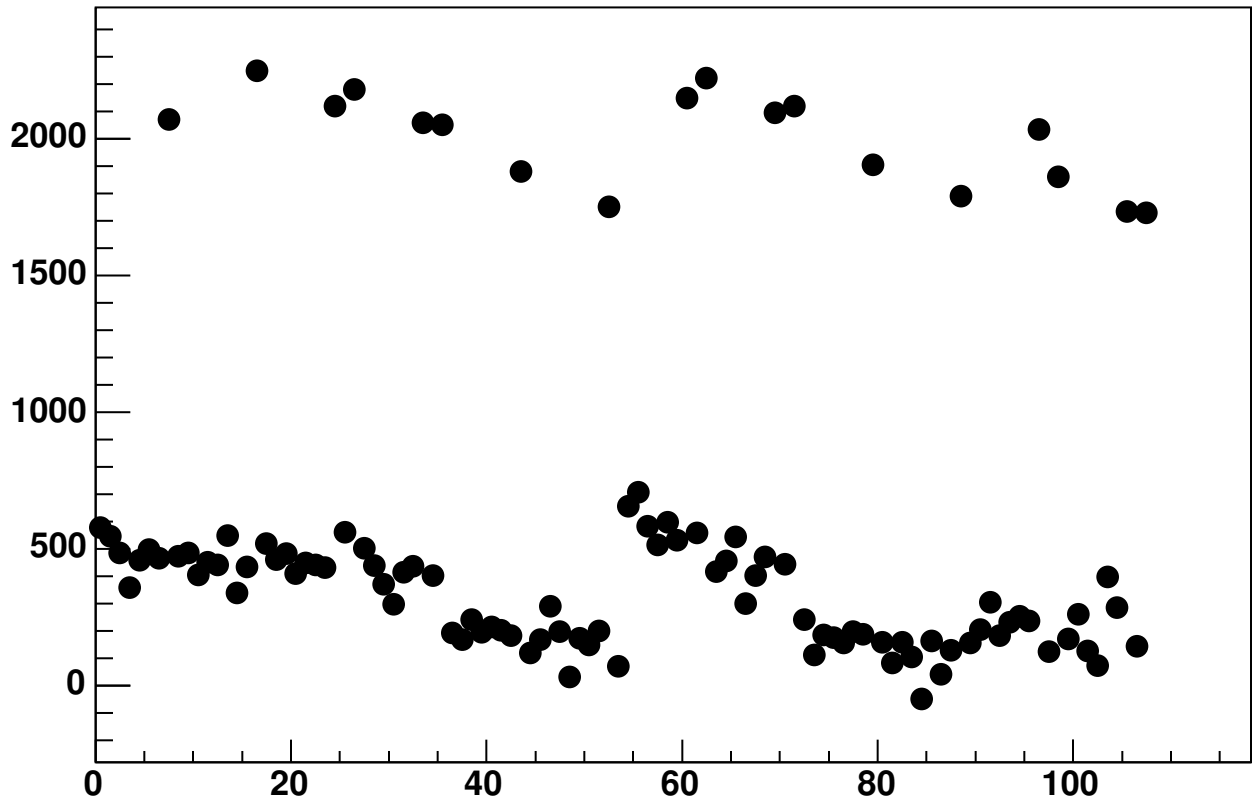
Enable 4, Hold=35, DAC=19600, ADC Mean vs 18*Chip+Chan



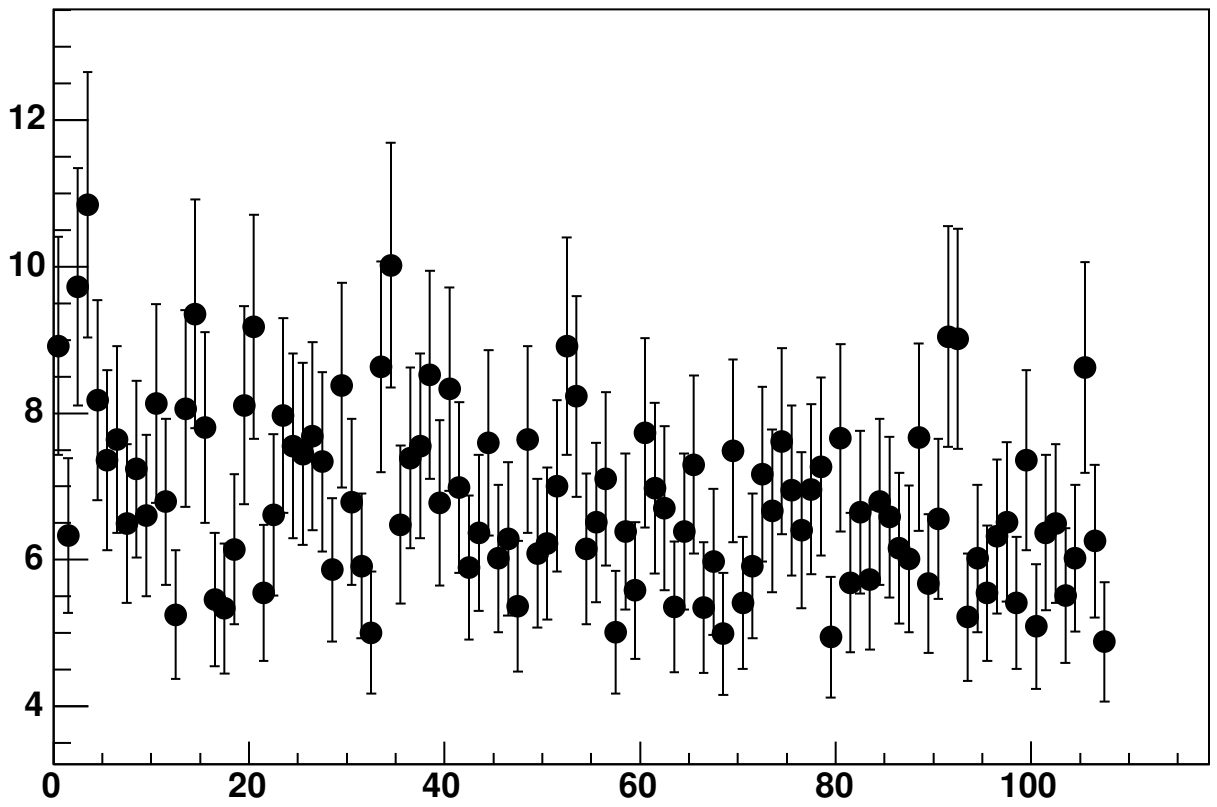
Enable 4, Hold=35, DAC=19600, ADC Noise vs 18*Chip+Chan



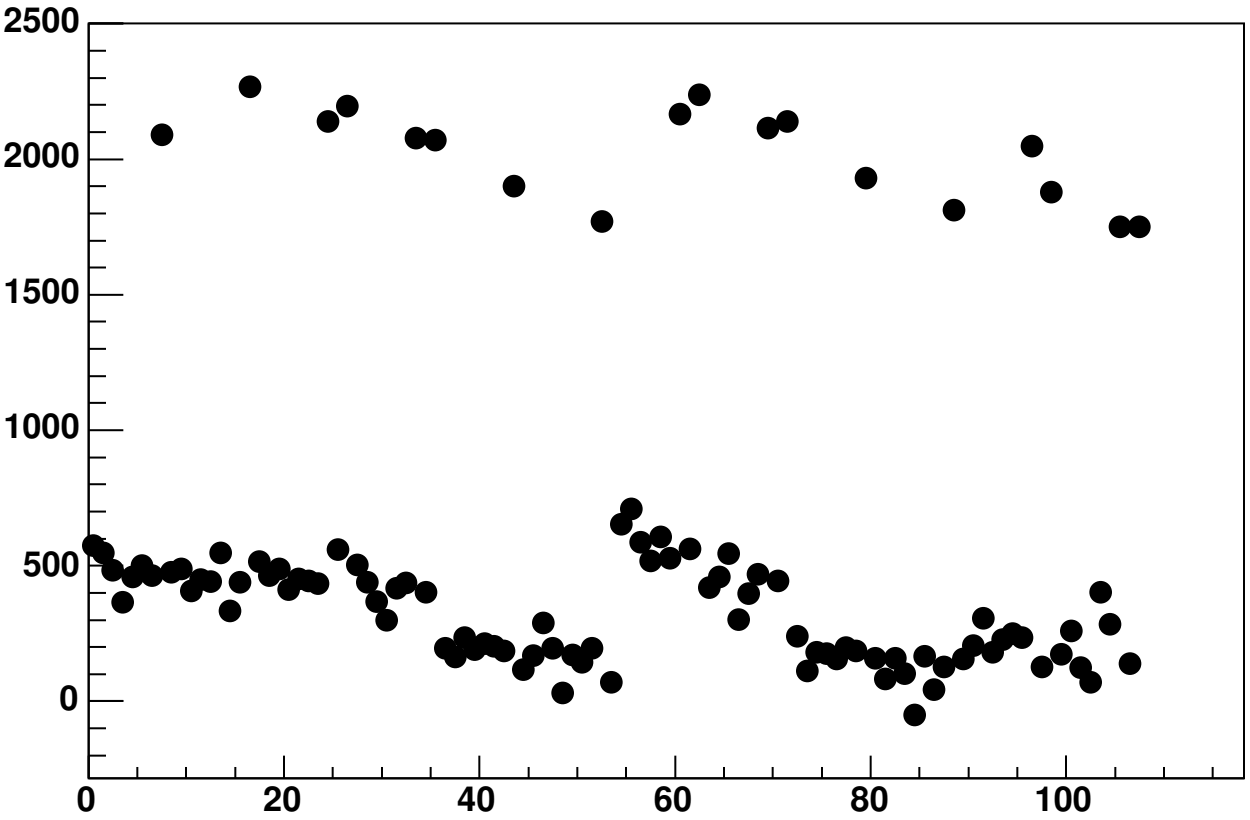
Enable 5, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



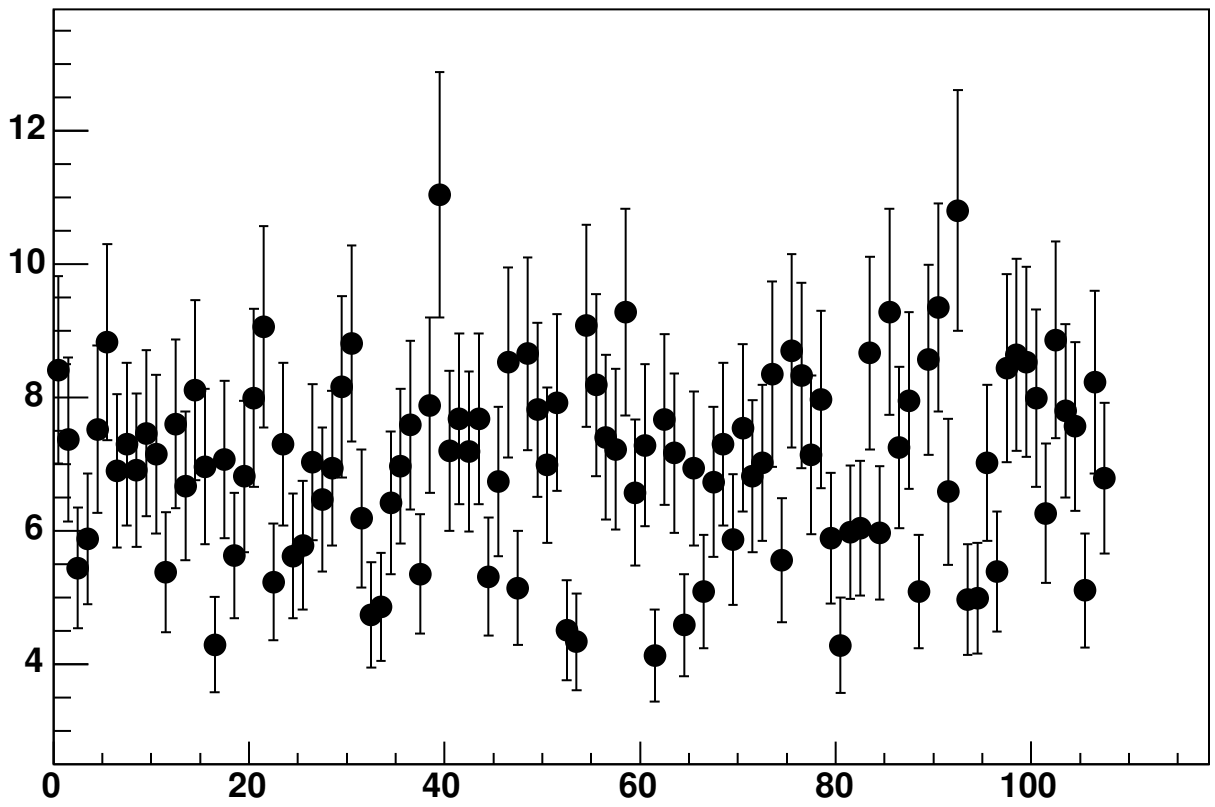
Enable 5, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



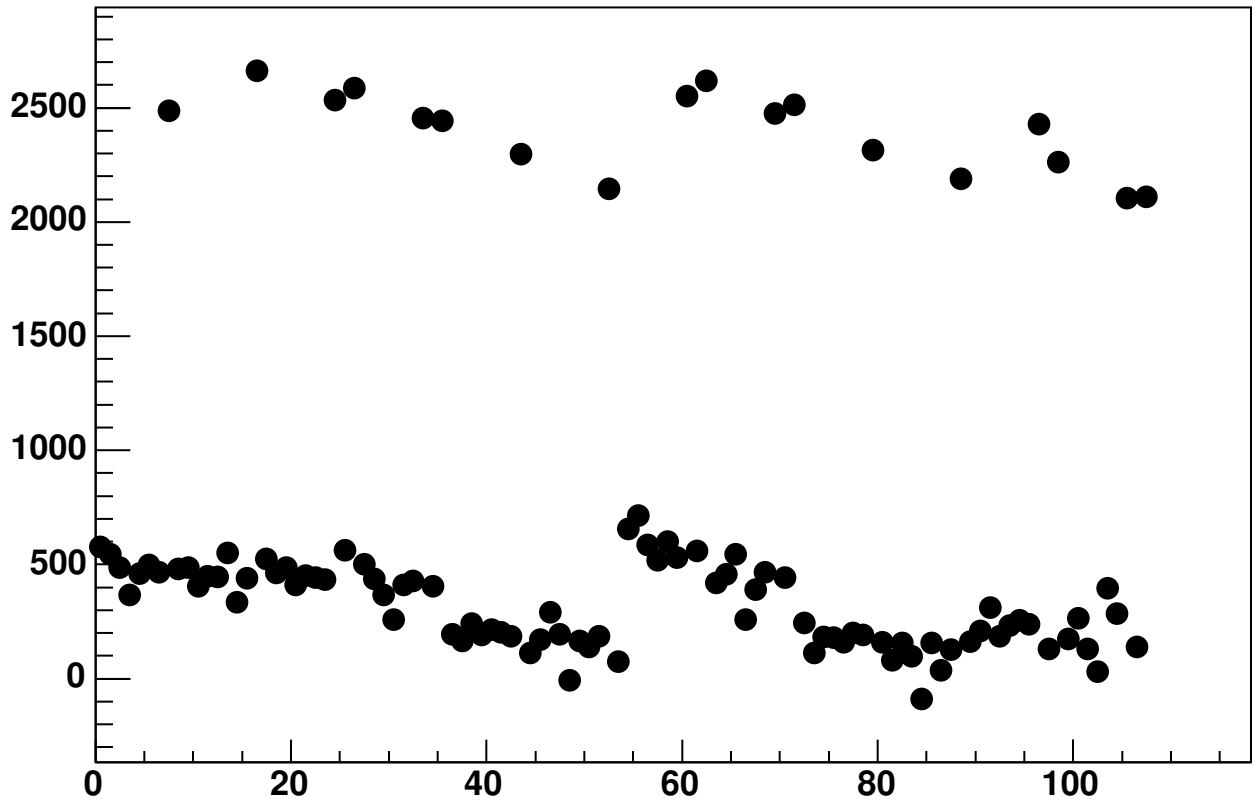
Enable 5, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



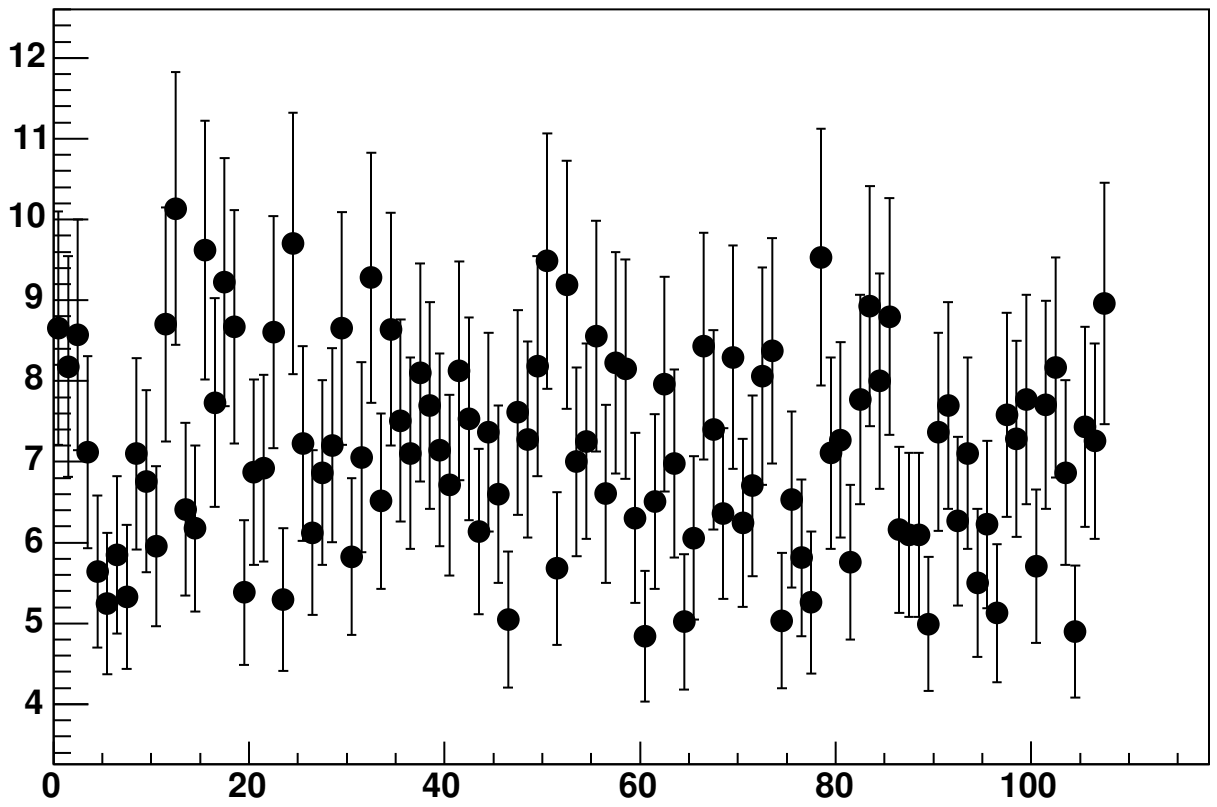
Enable 5, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



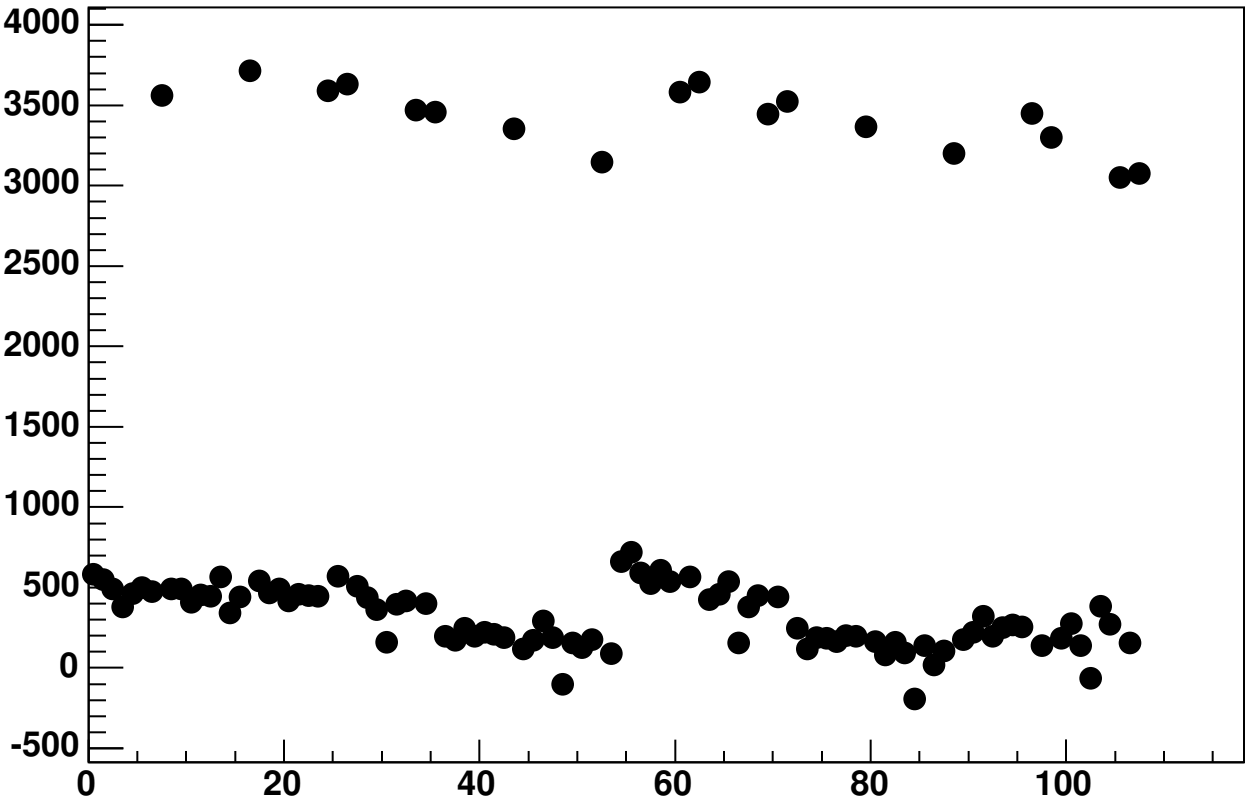
Enable 5, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



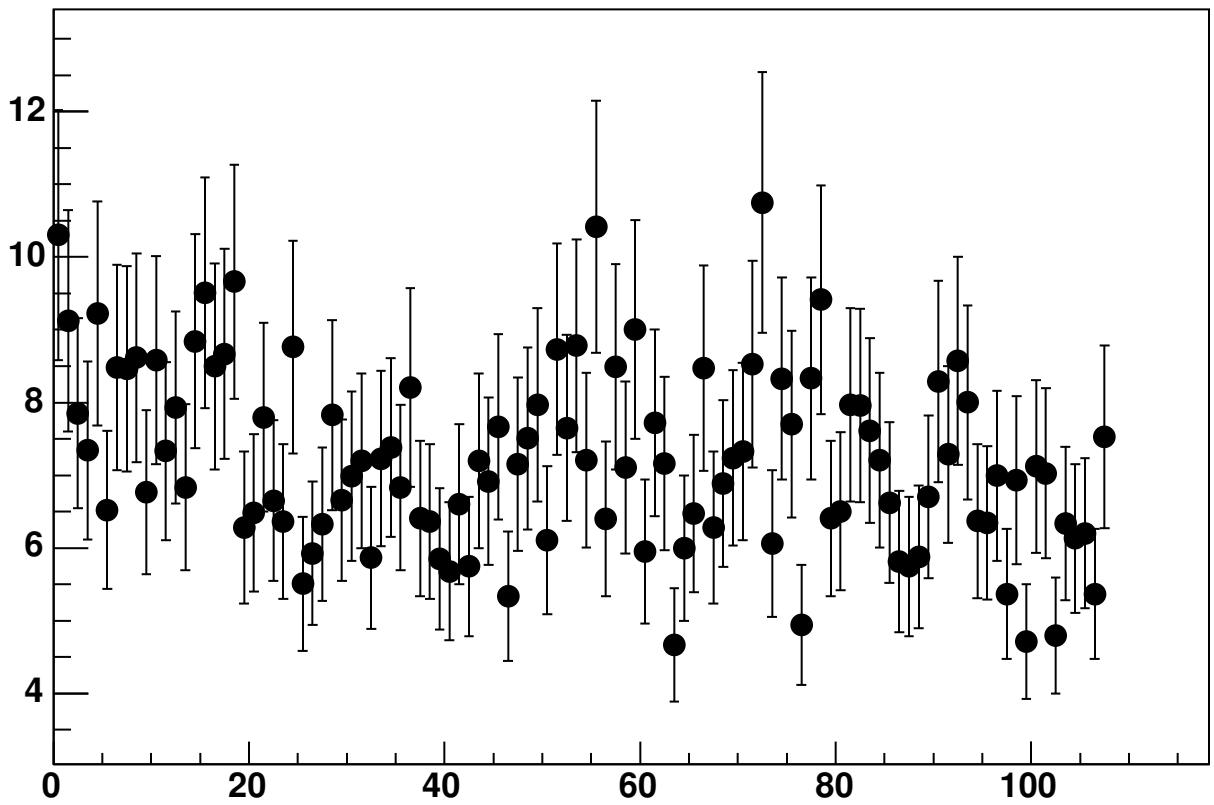
Enable 5, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



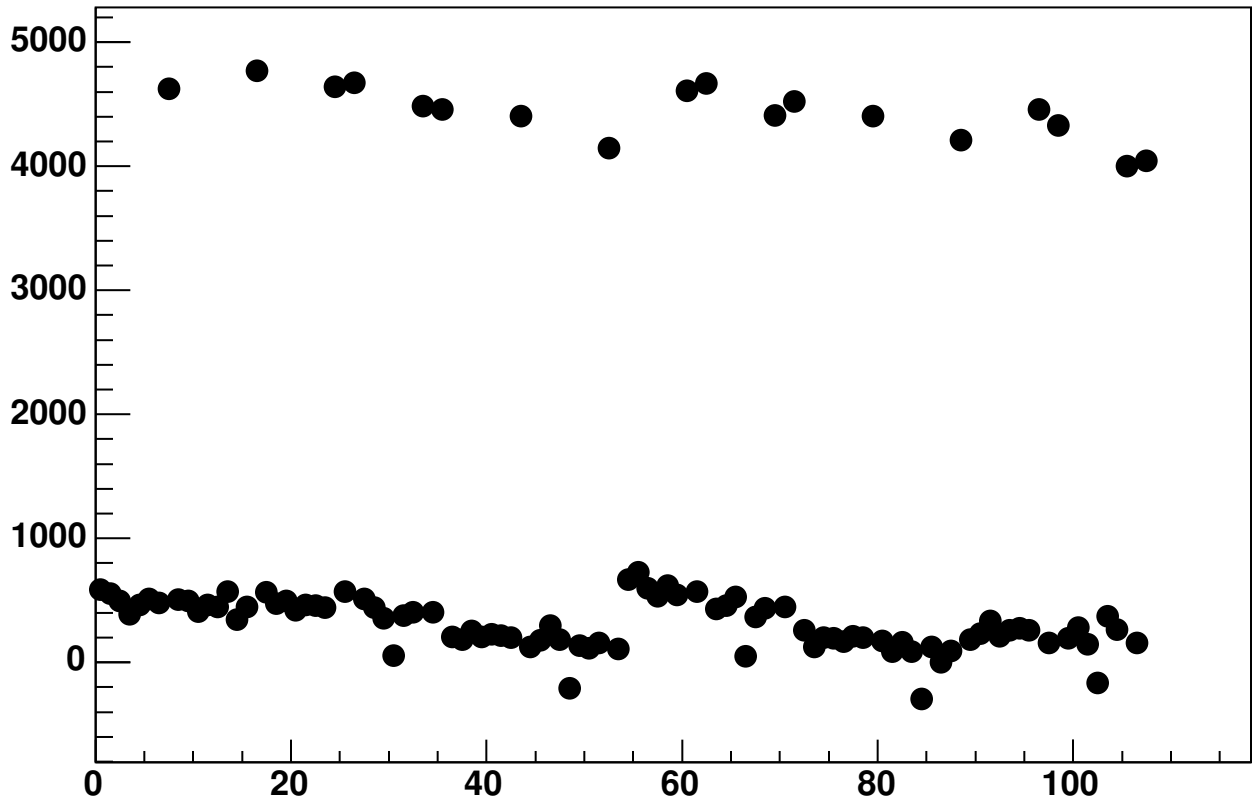
Enable 5, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



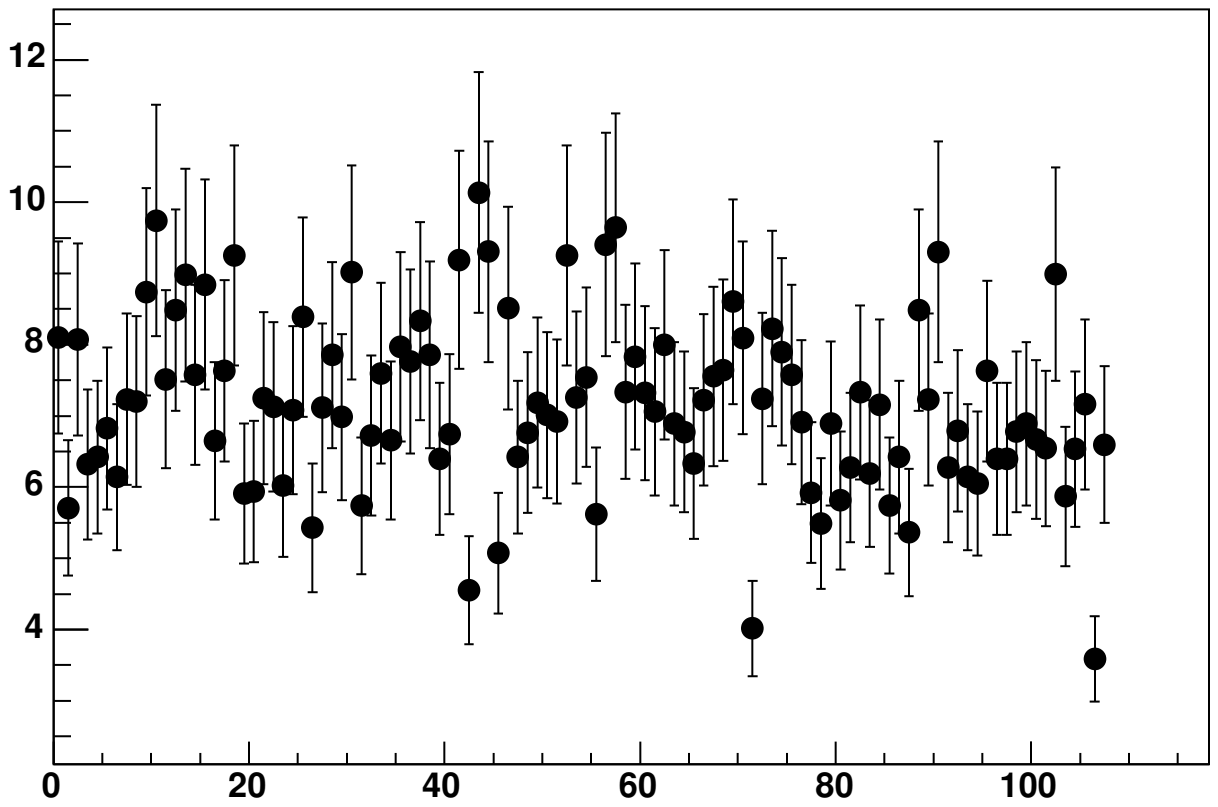
Enable 5, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



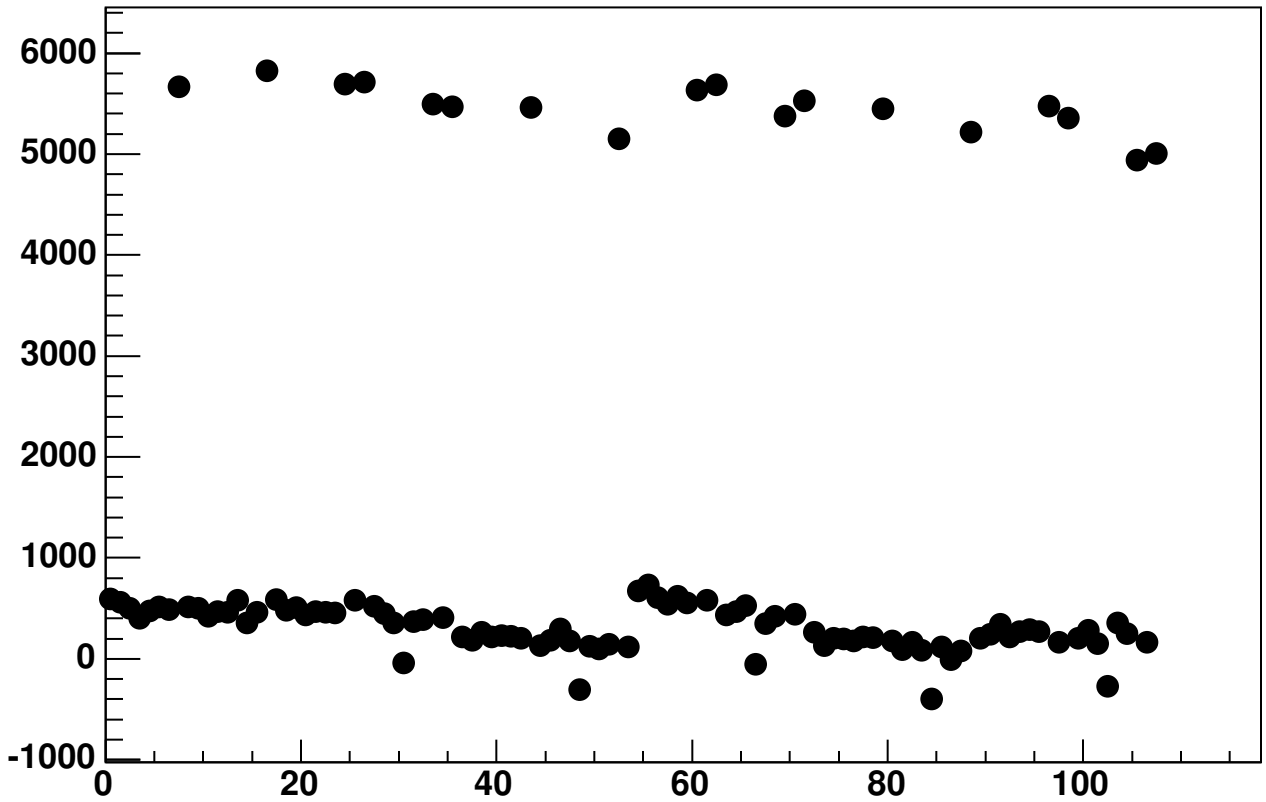
Enable 5, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



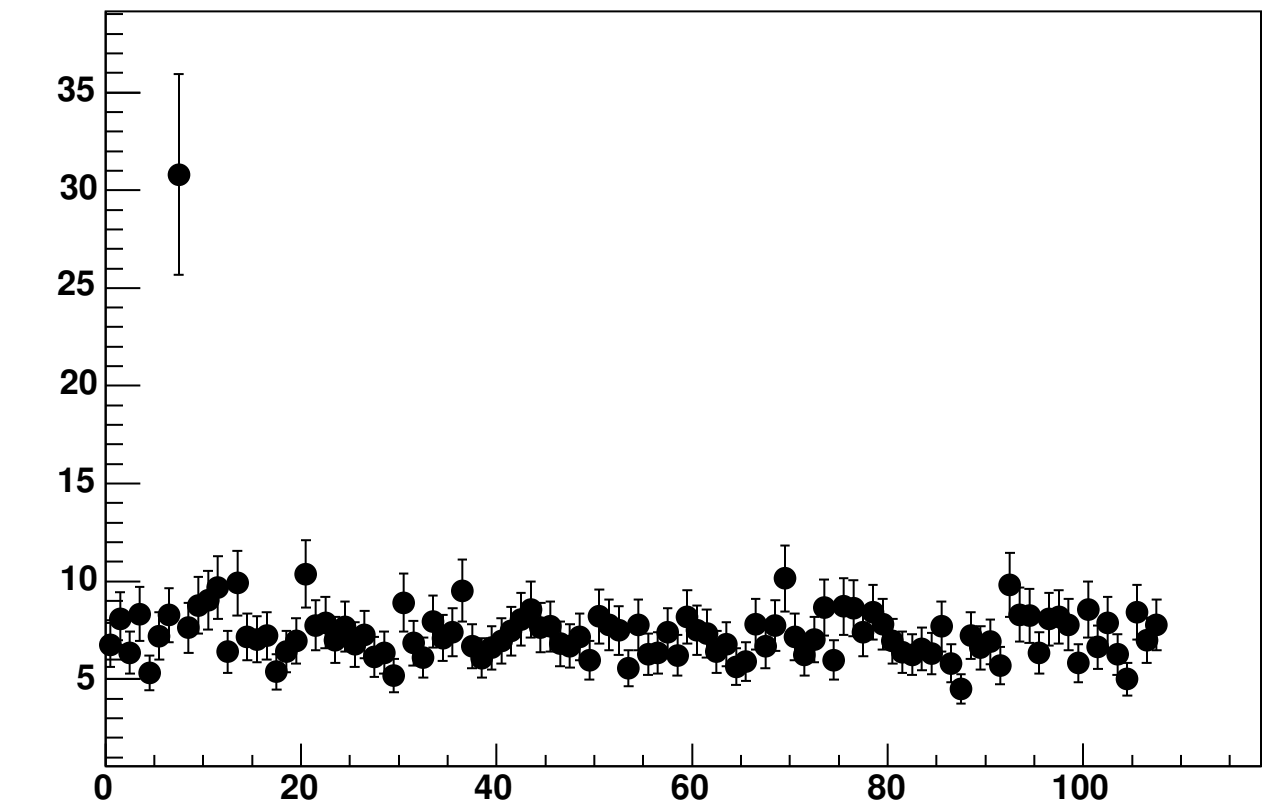
Enable 5, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



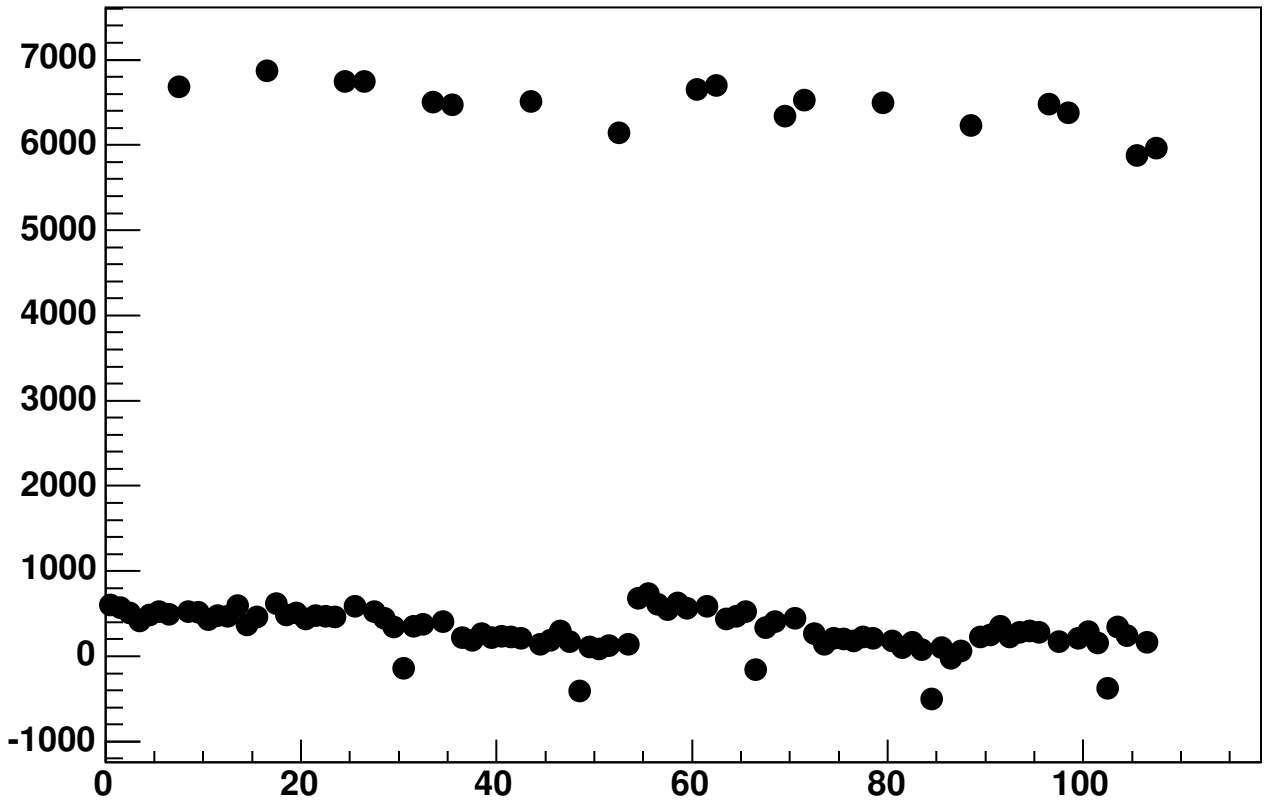
Enable 5, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



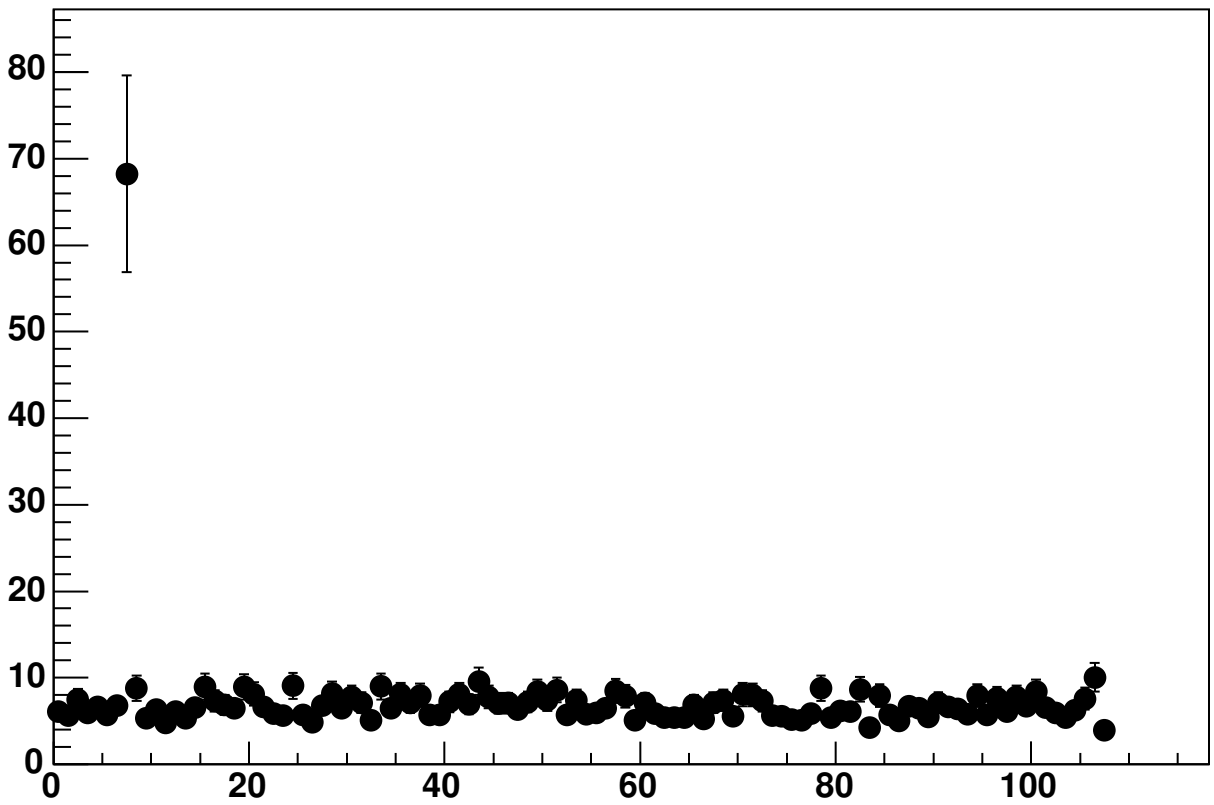
Enable 5, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



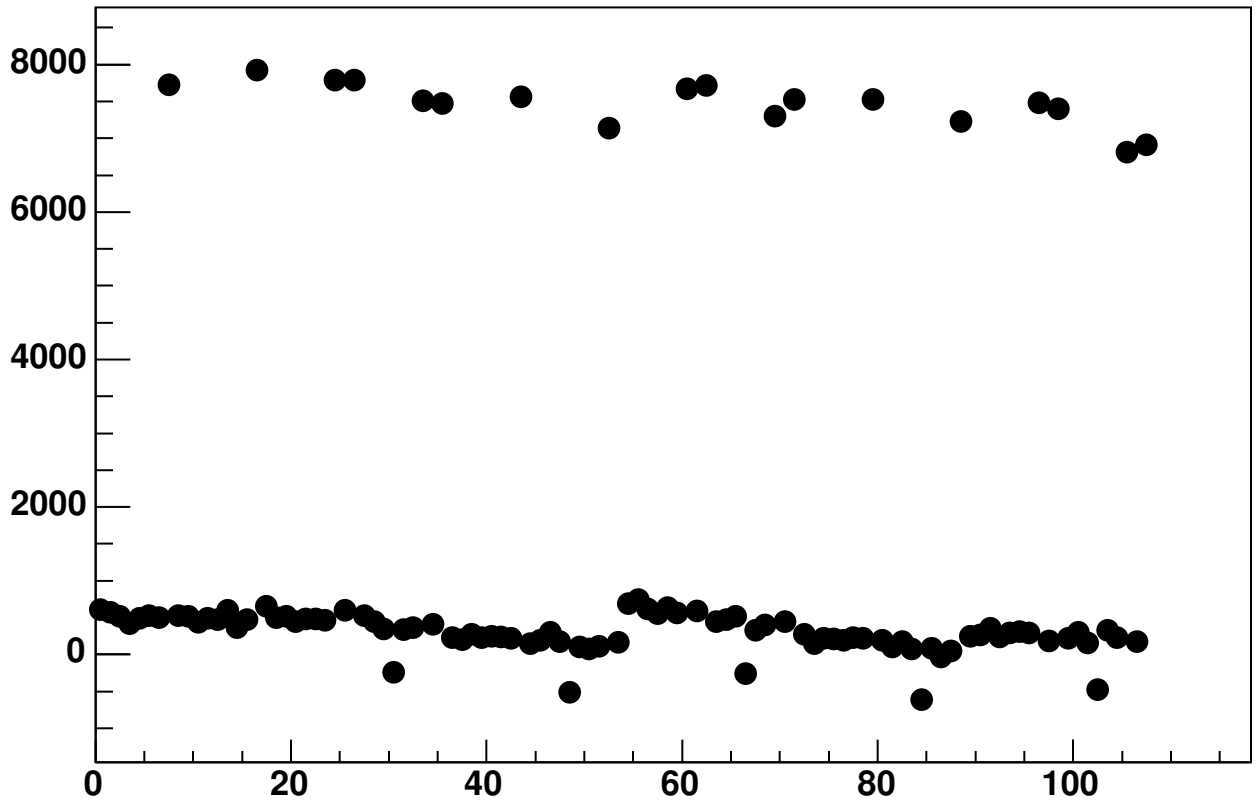
Enable 5, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



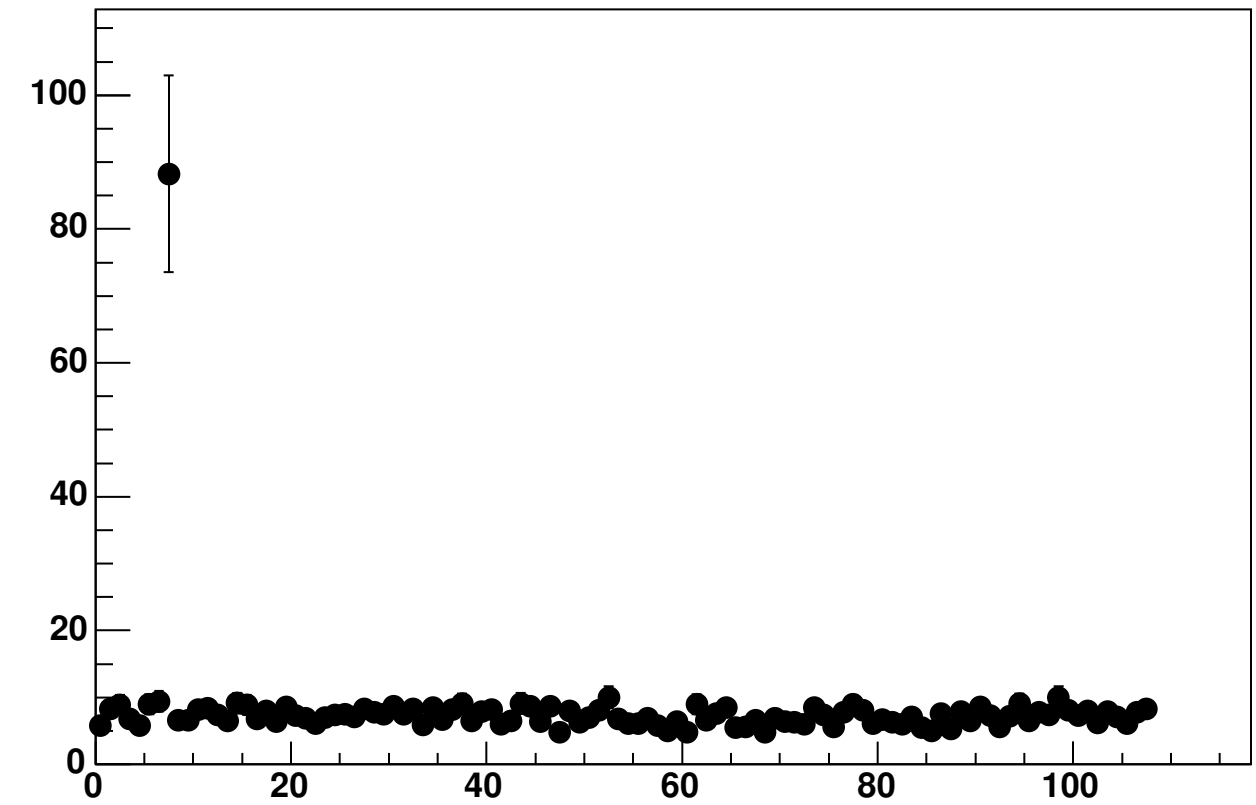
Enable 5, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



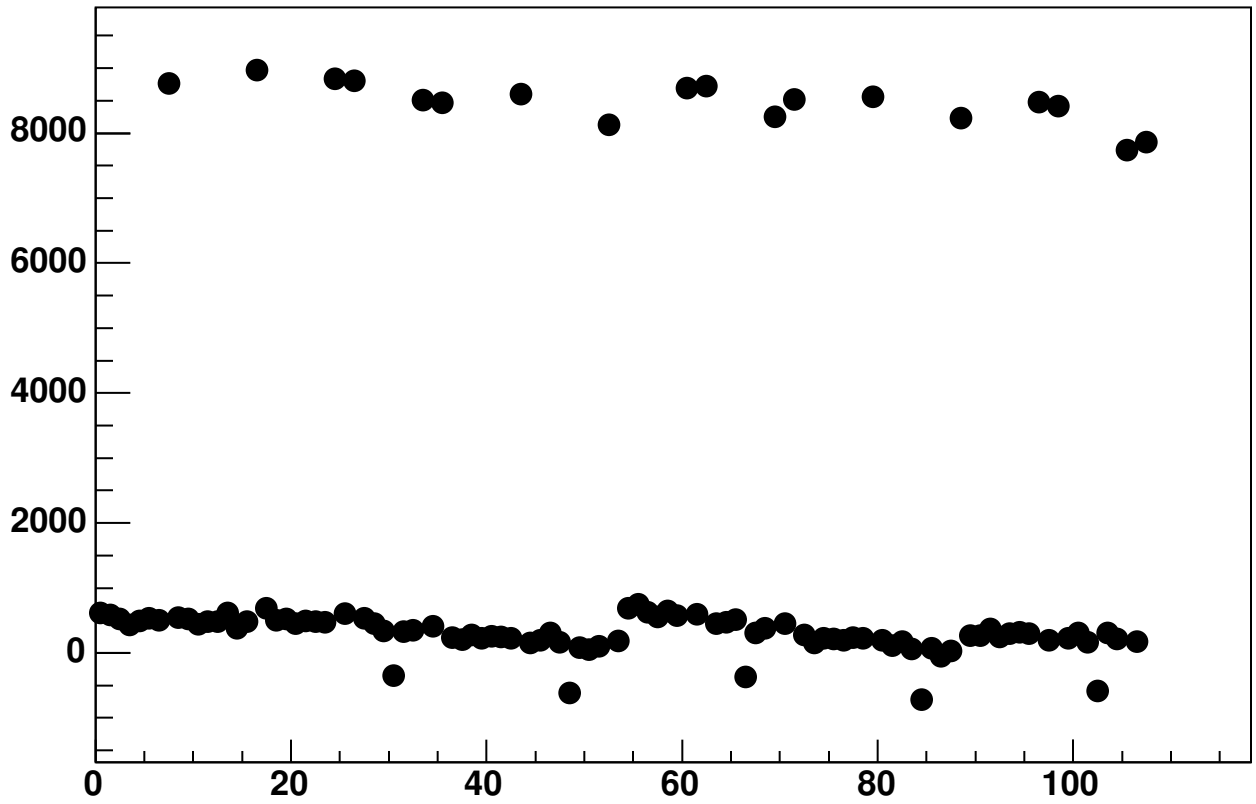
Enable 5, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



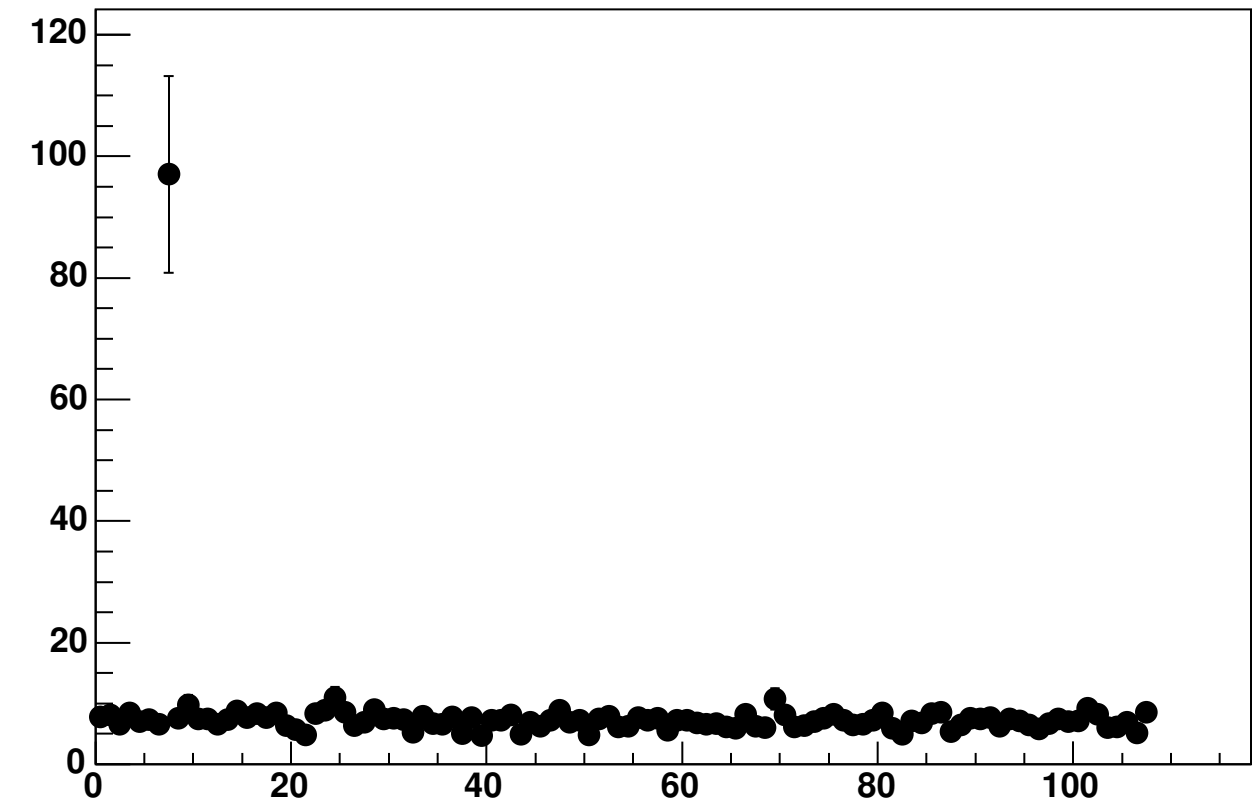
Enable 5, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



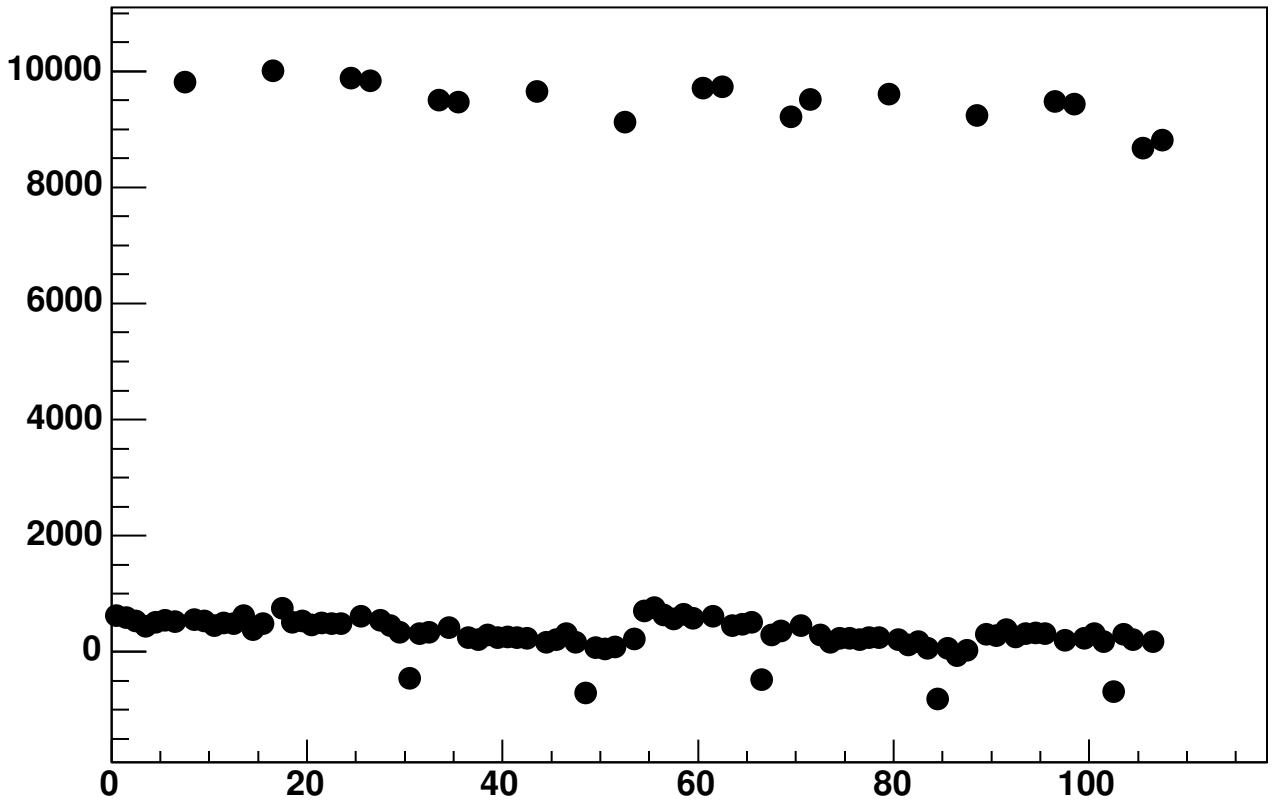
Enable 5, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



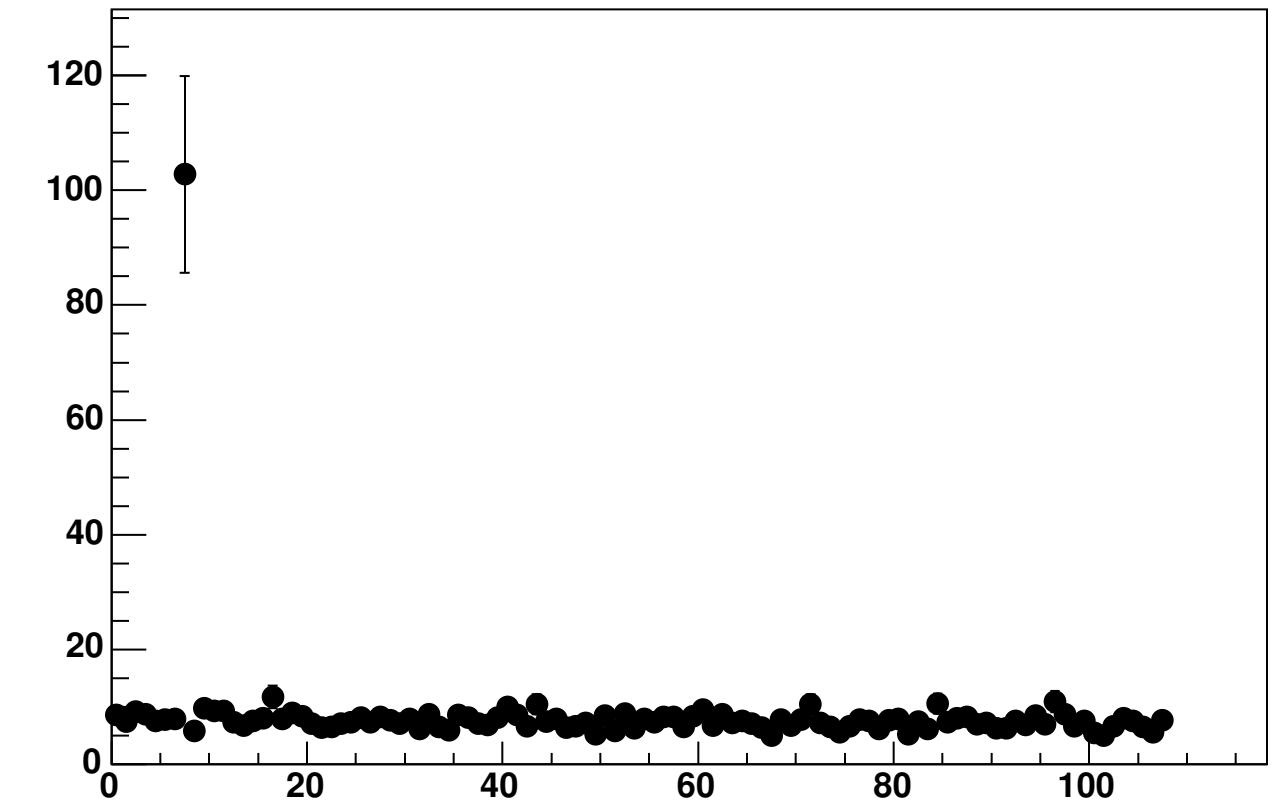
Enable 5, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



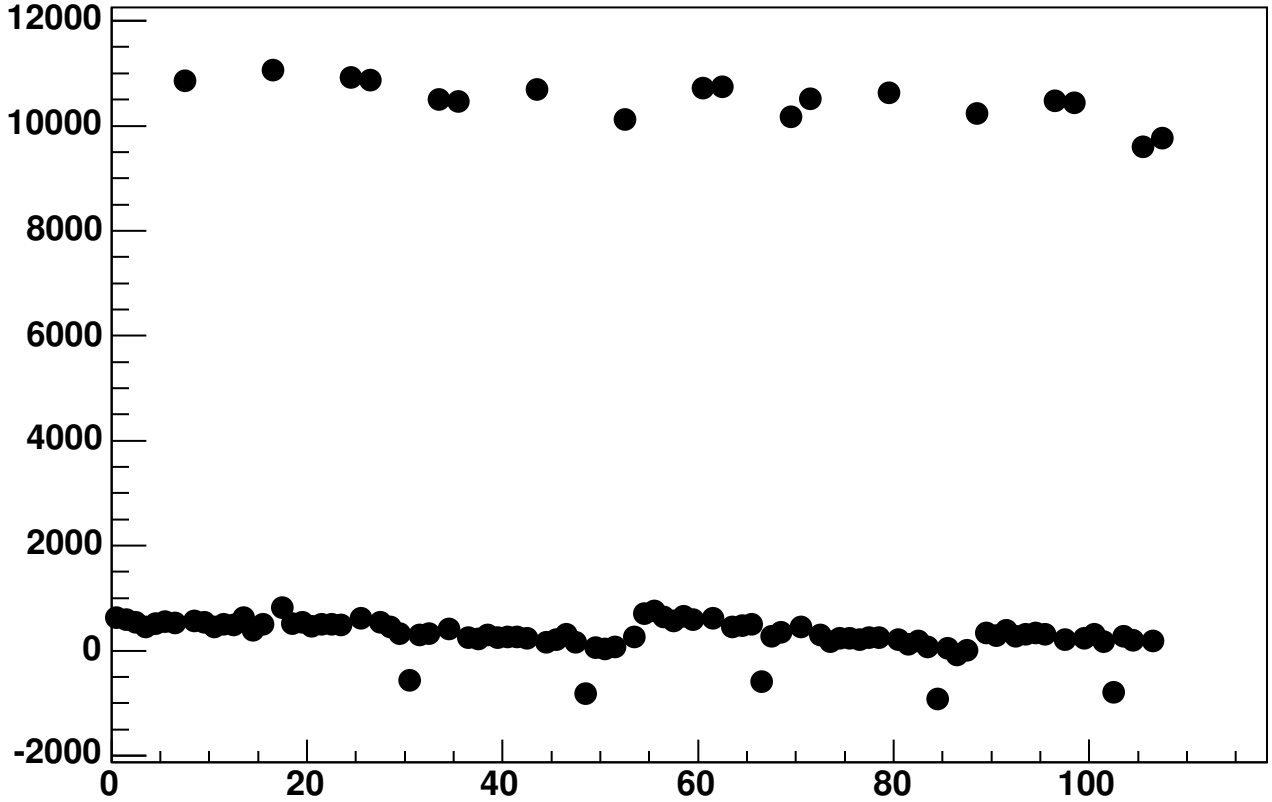
Enable 5, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



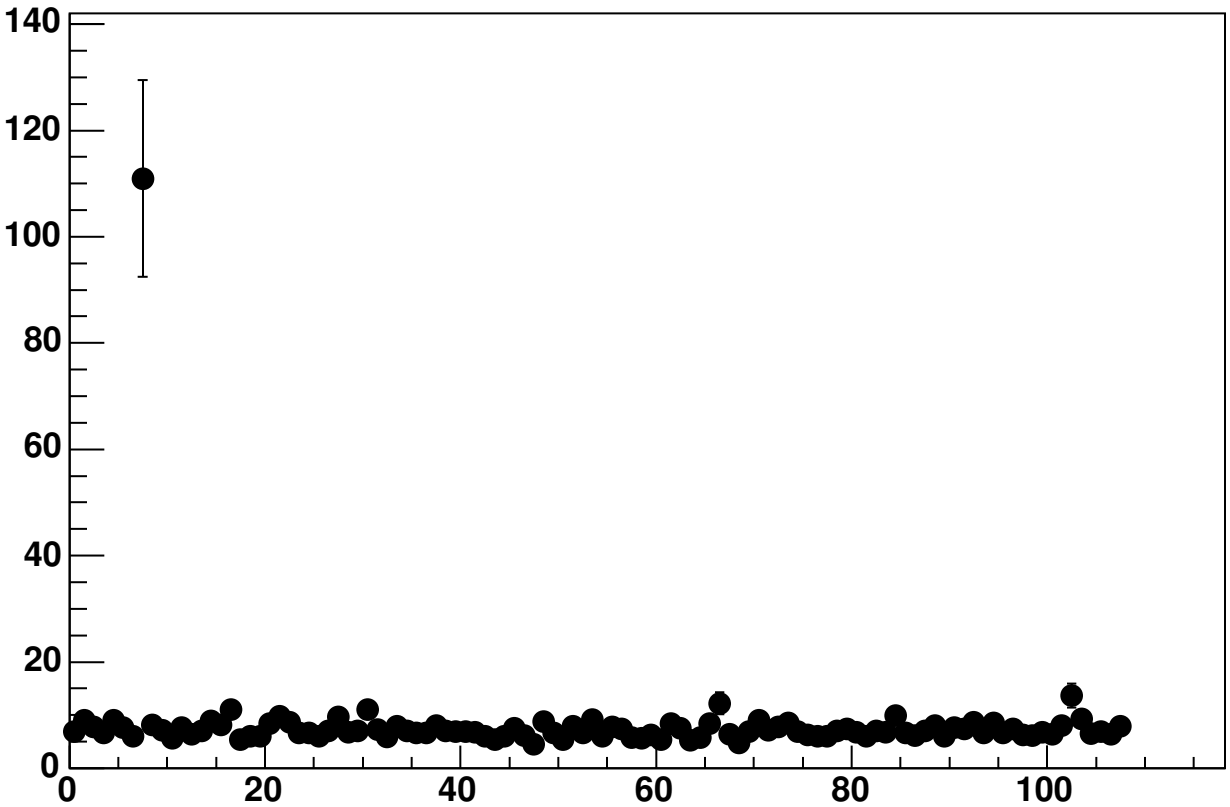
Enable 5, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



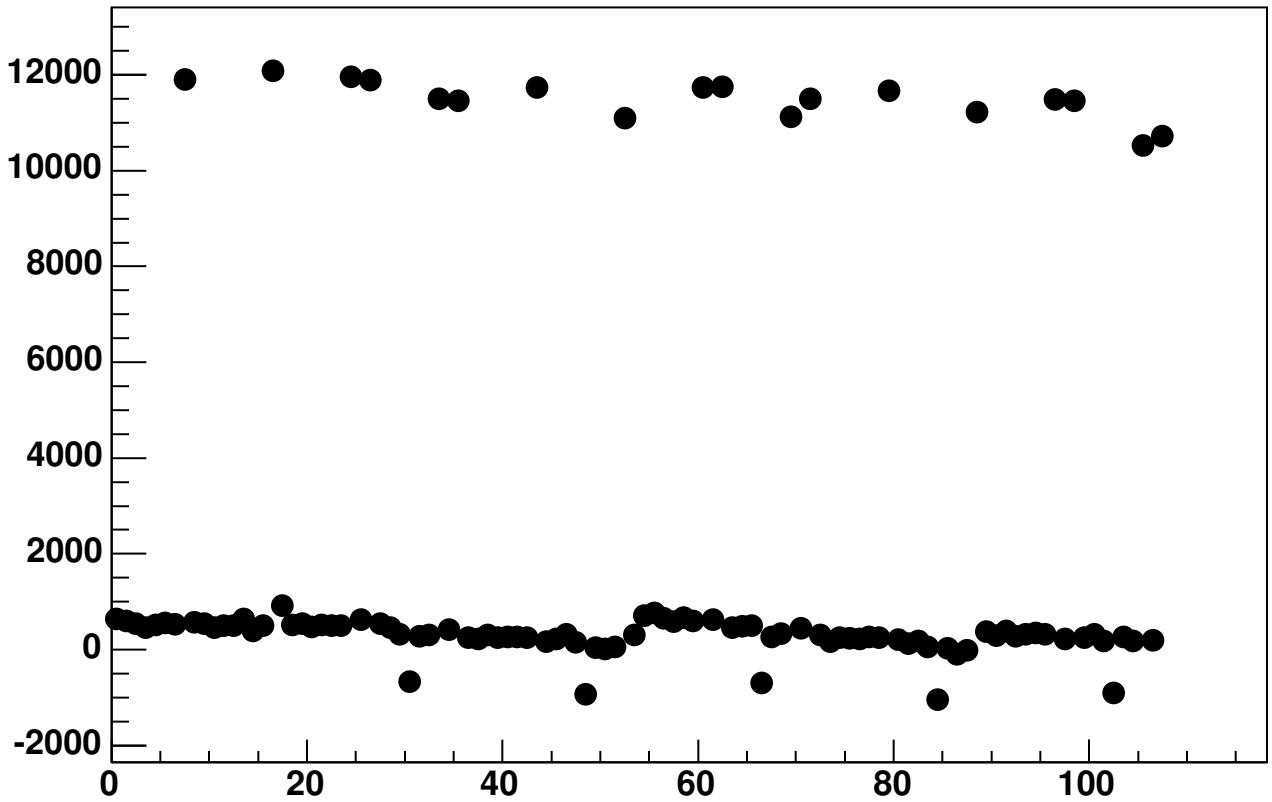
Enable 5, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



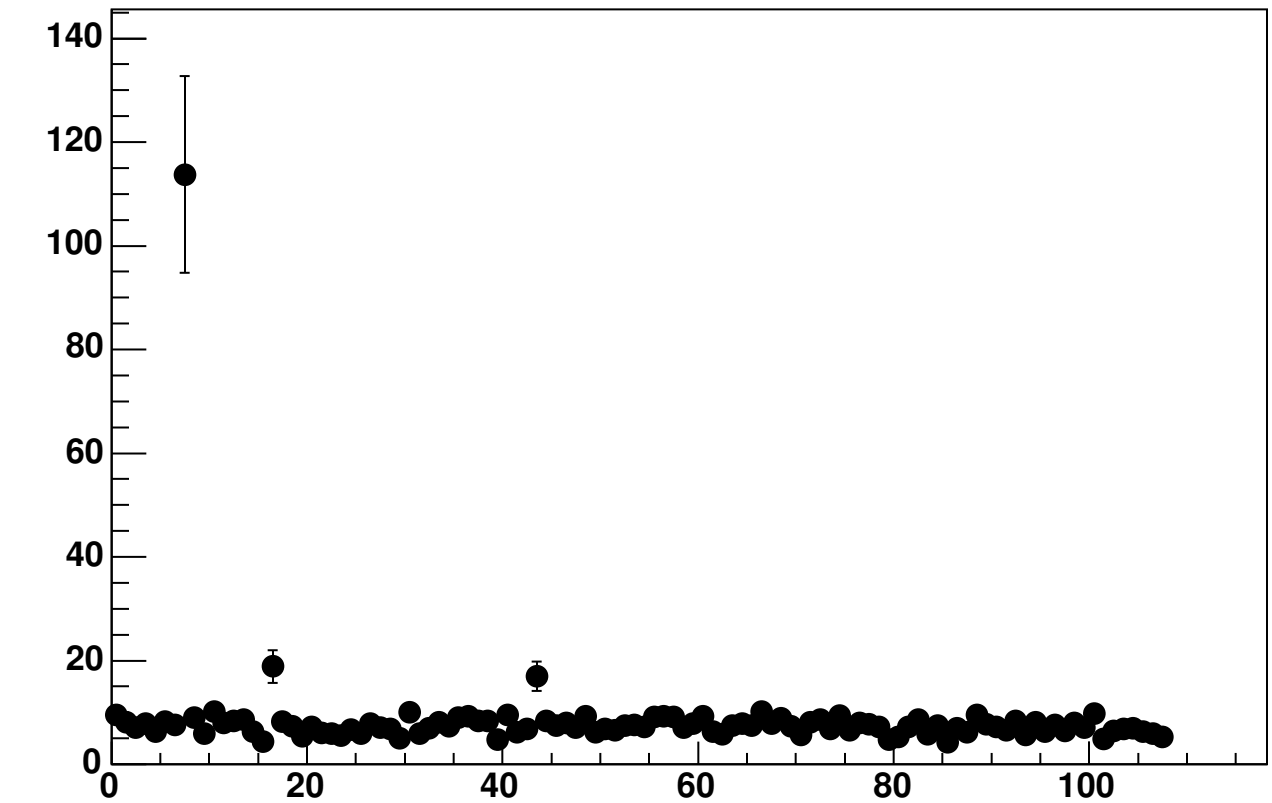
Enable 5, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



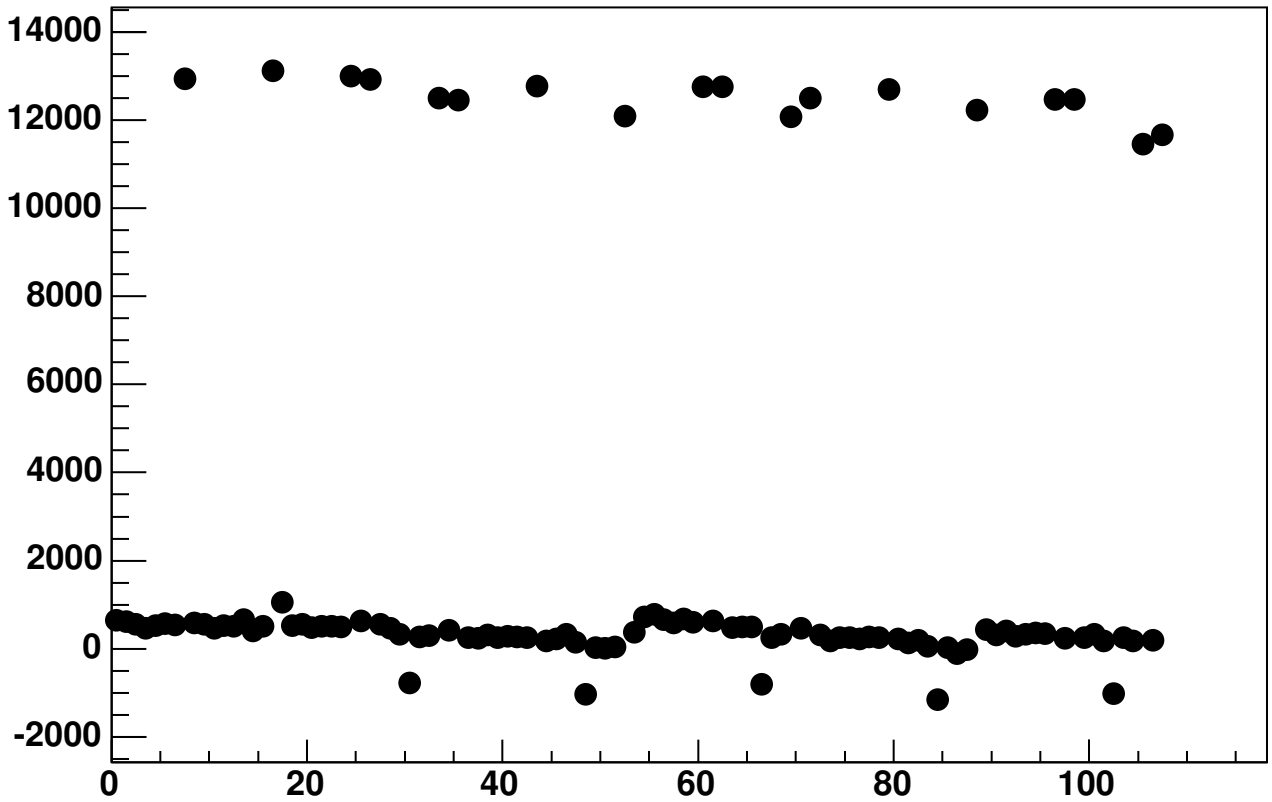
Enable 5, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



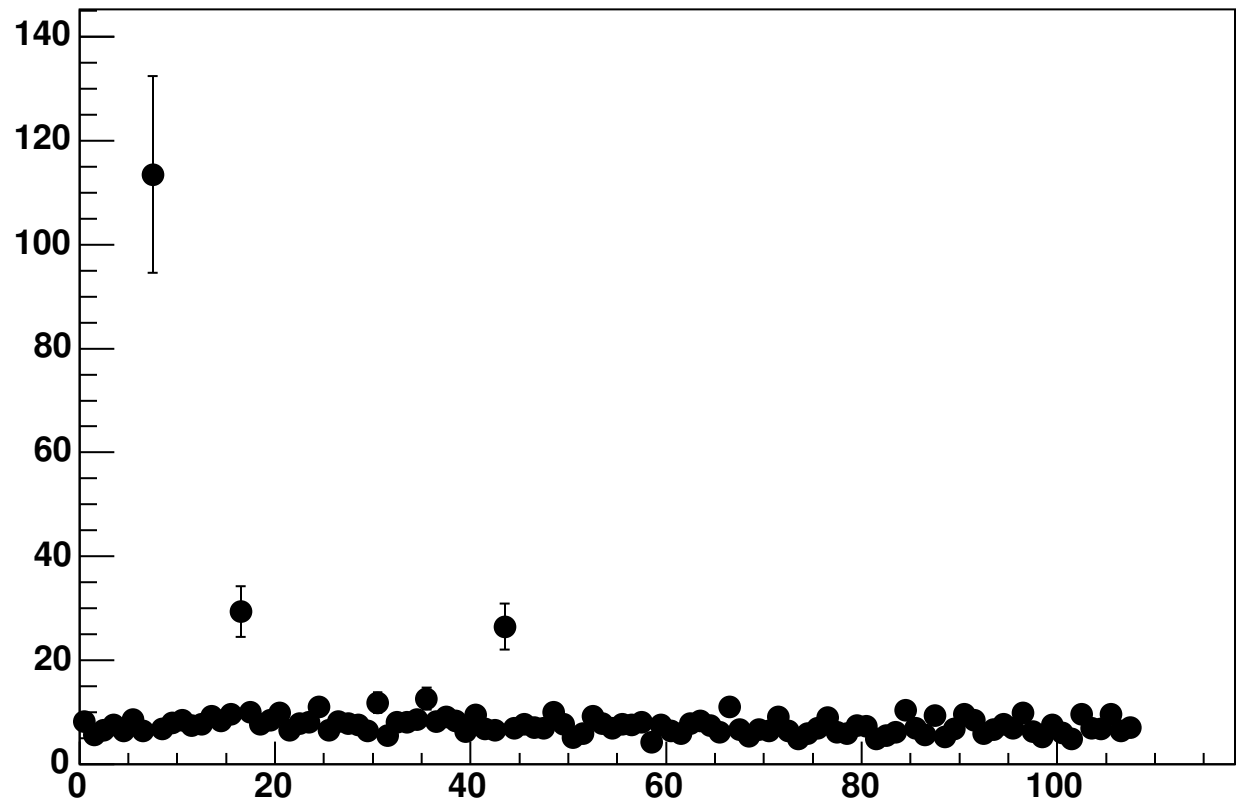
Enable 5, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



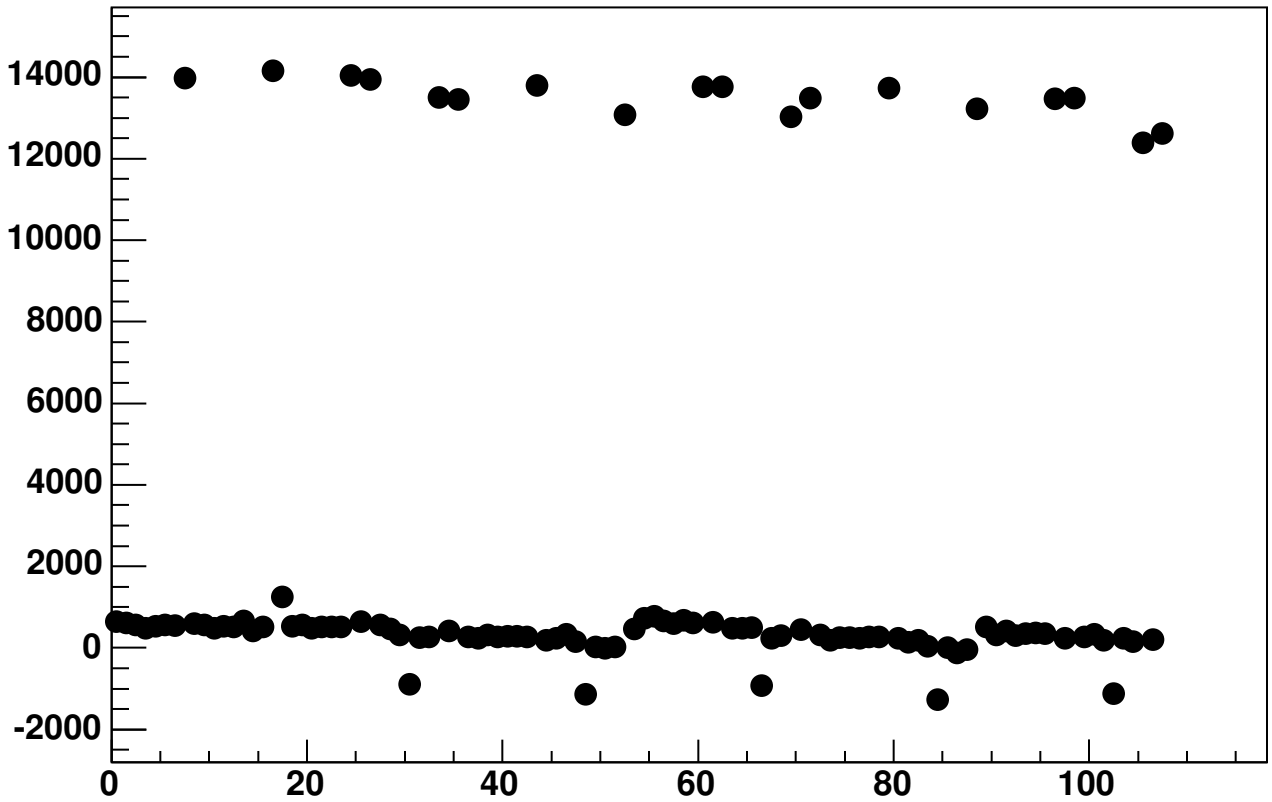
Enable 5, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



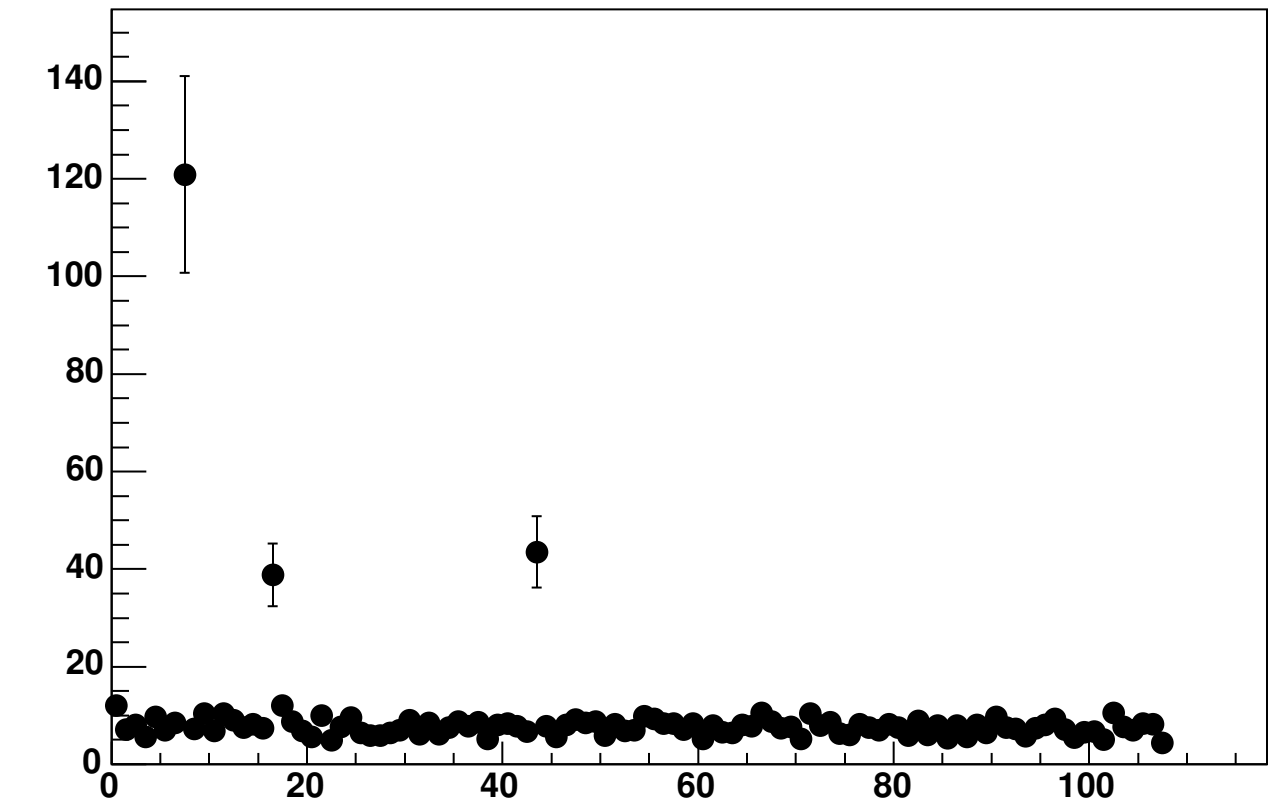
Enable 5, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



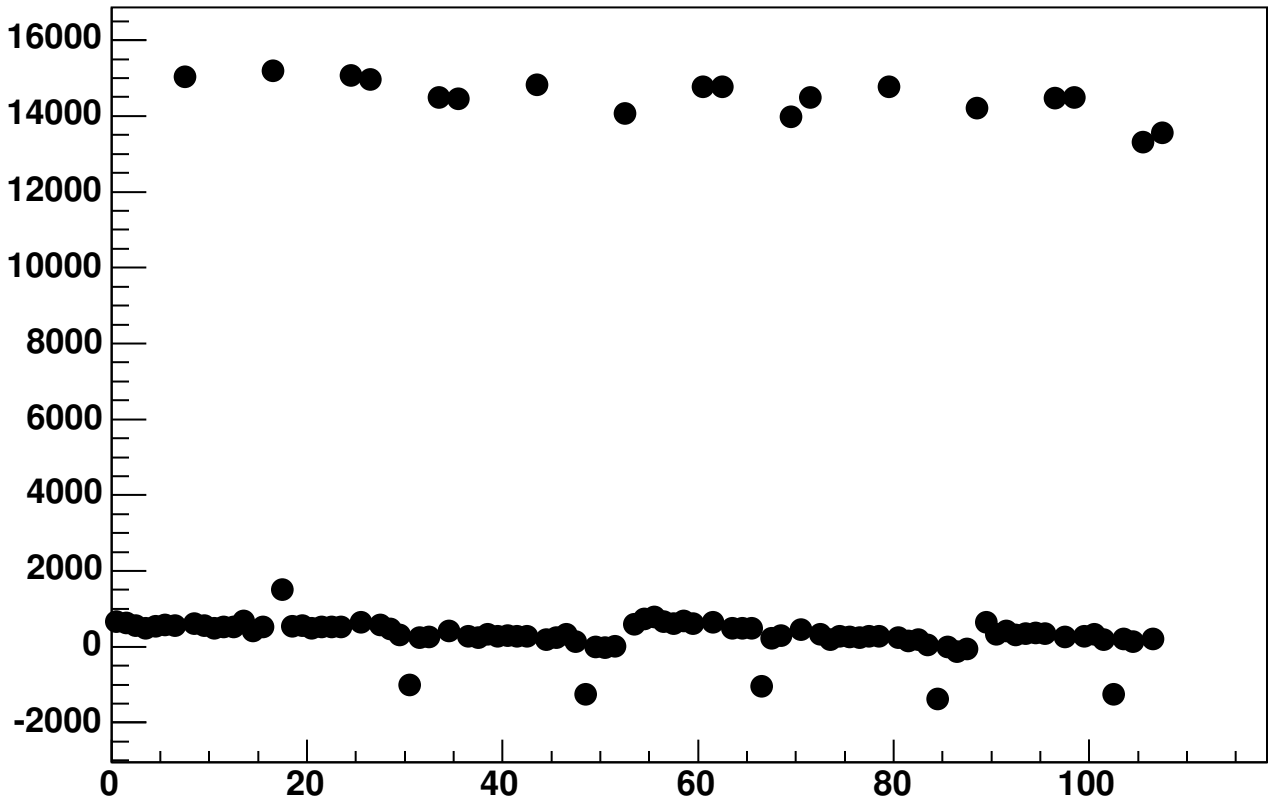
Enable 5, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



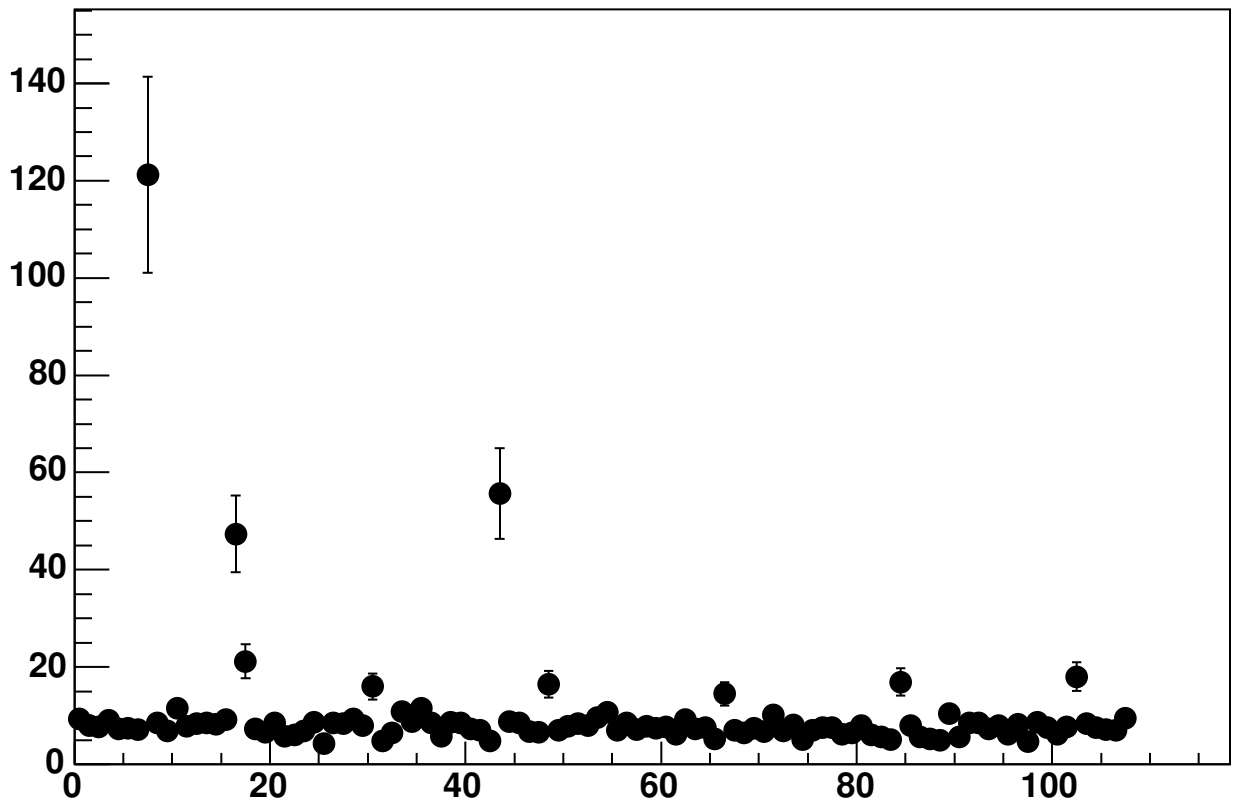
Enable 5, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



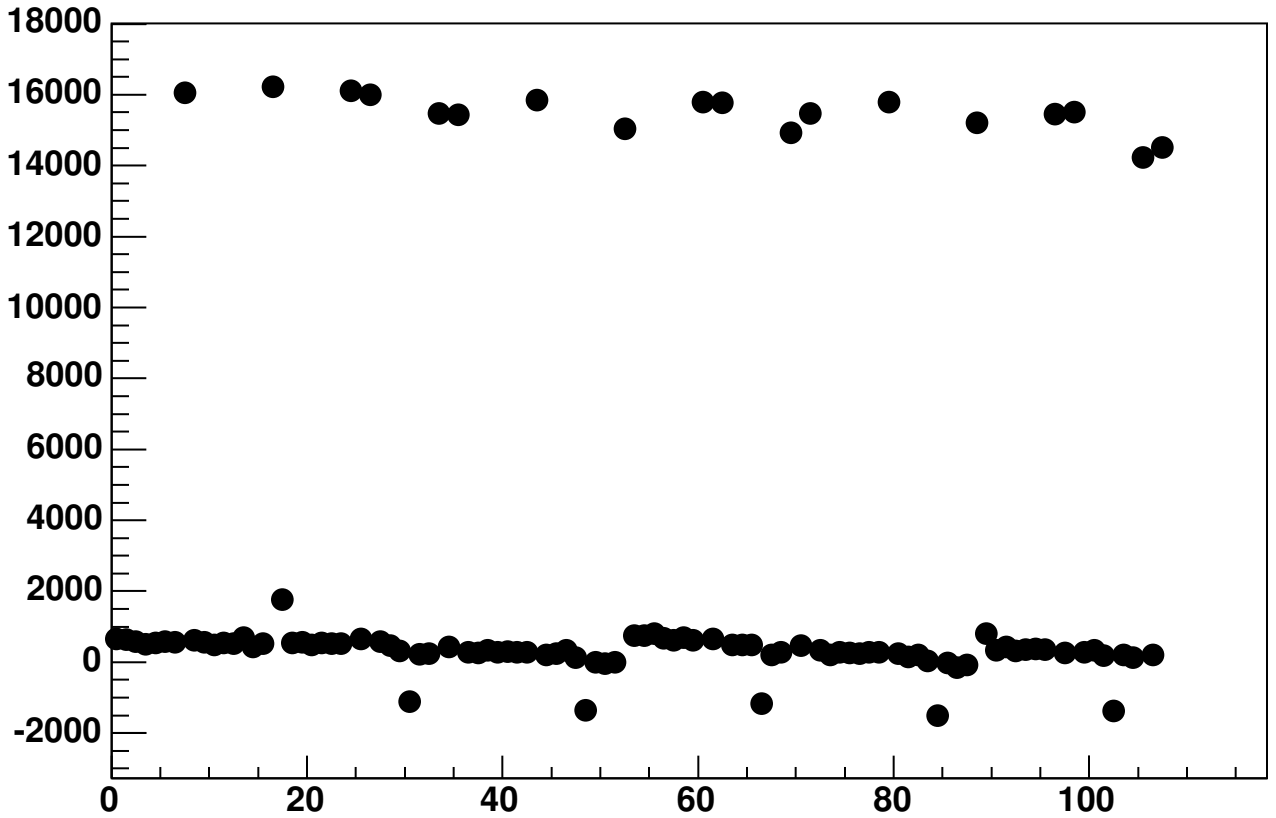
Enable 5, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



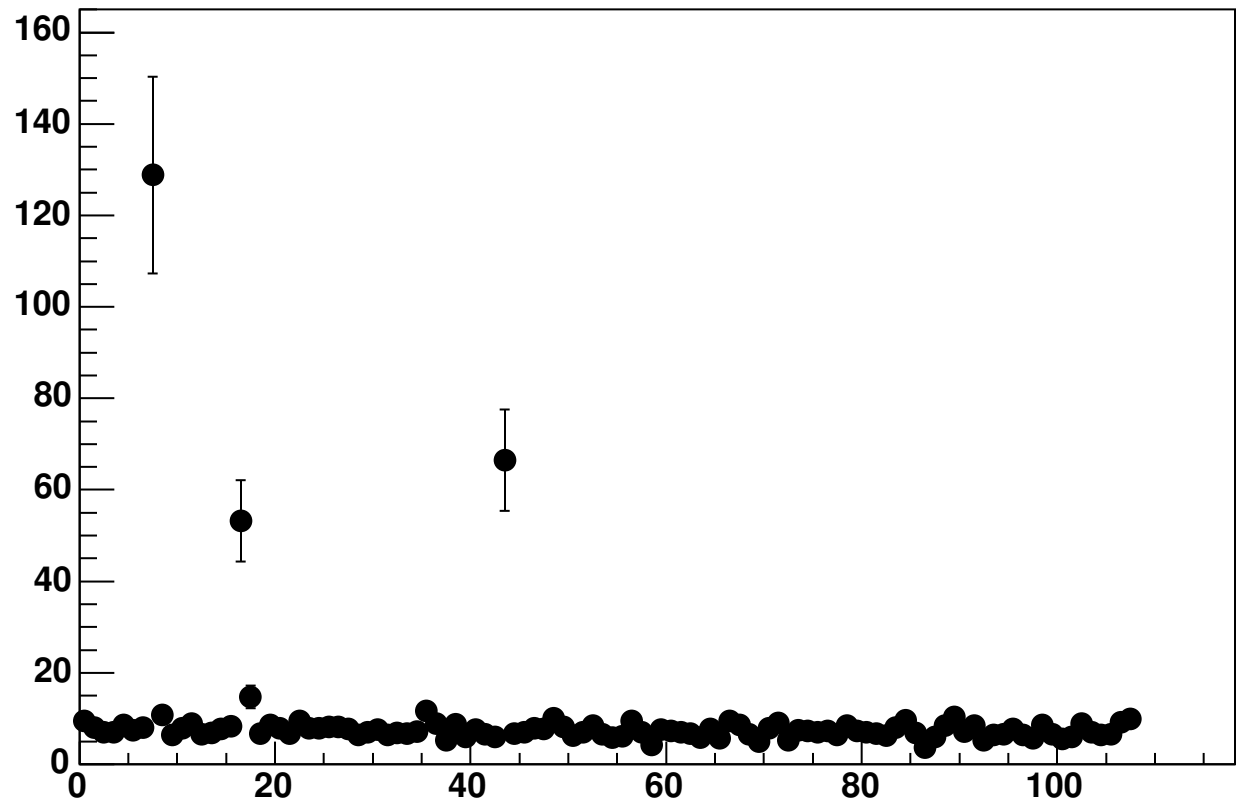
Enable 5, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



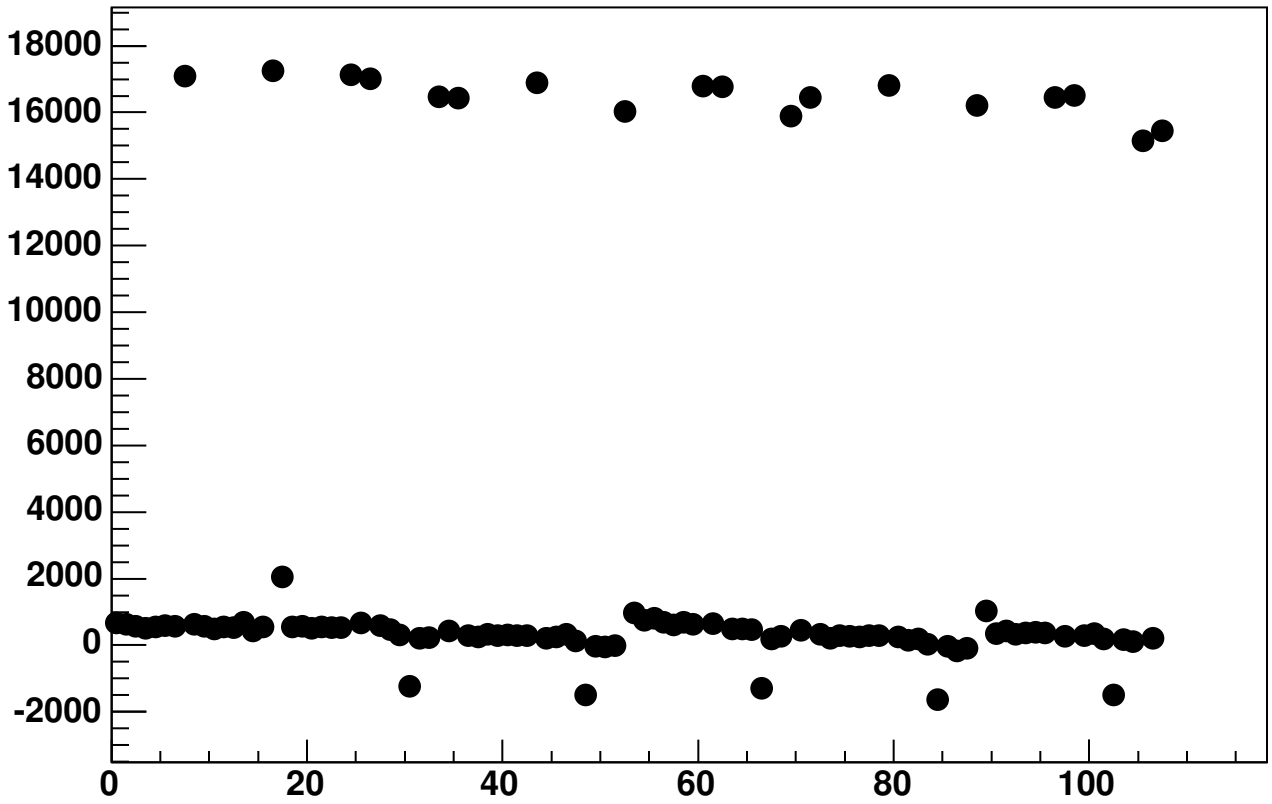
Enable 5, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



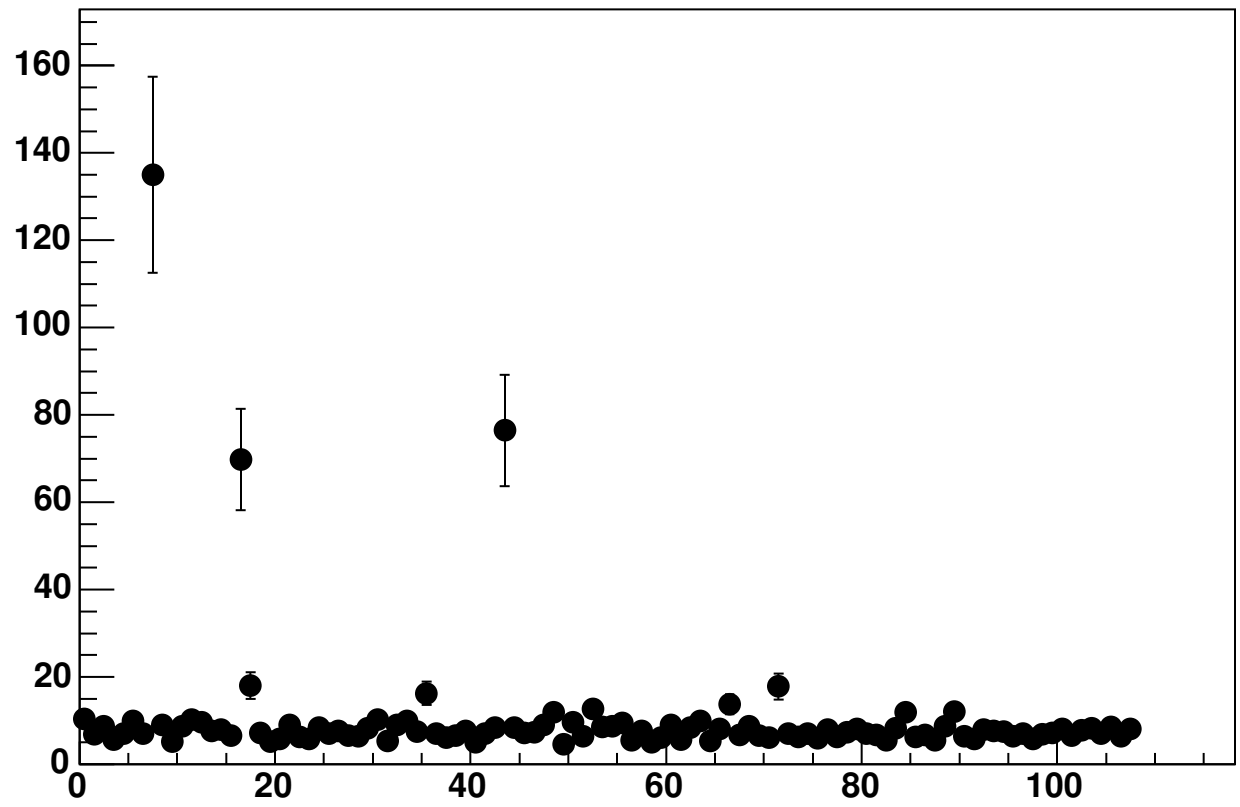
Enable 5, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



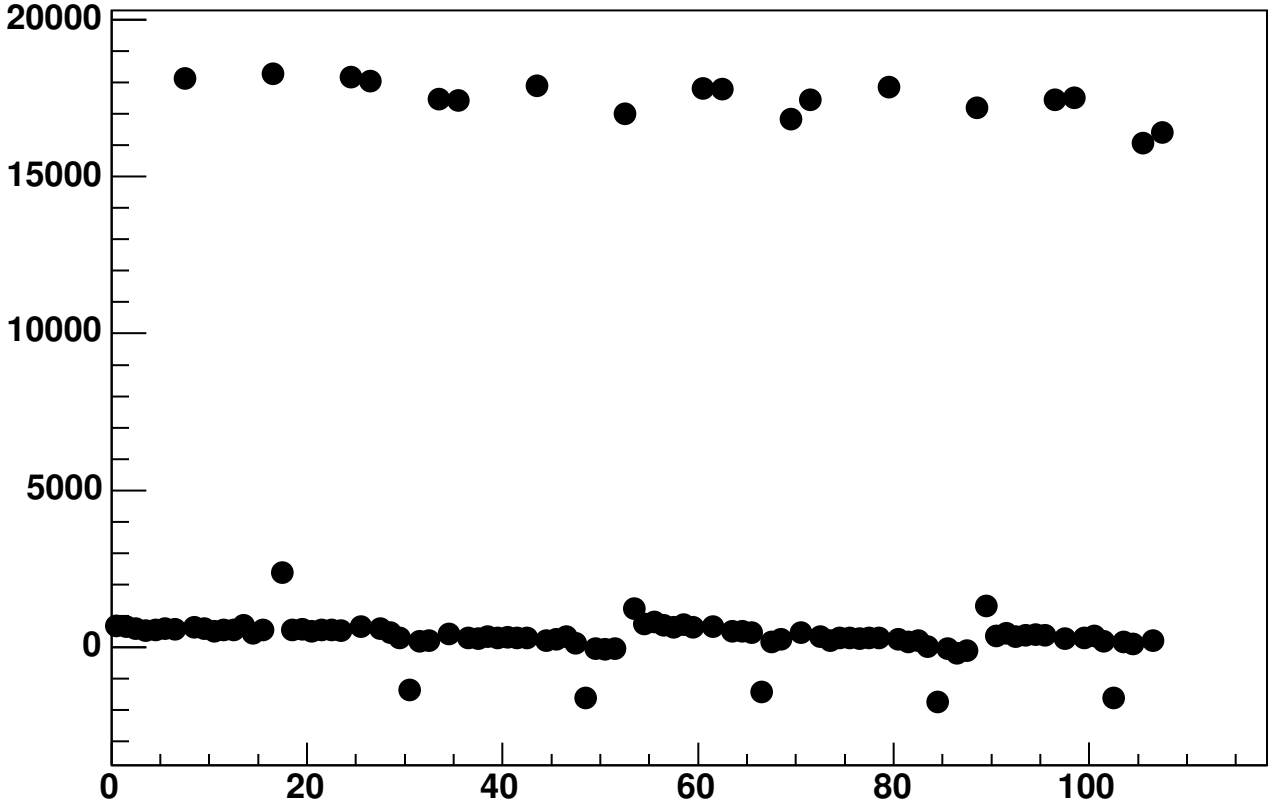
Enable 5, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



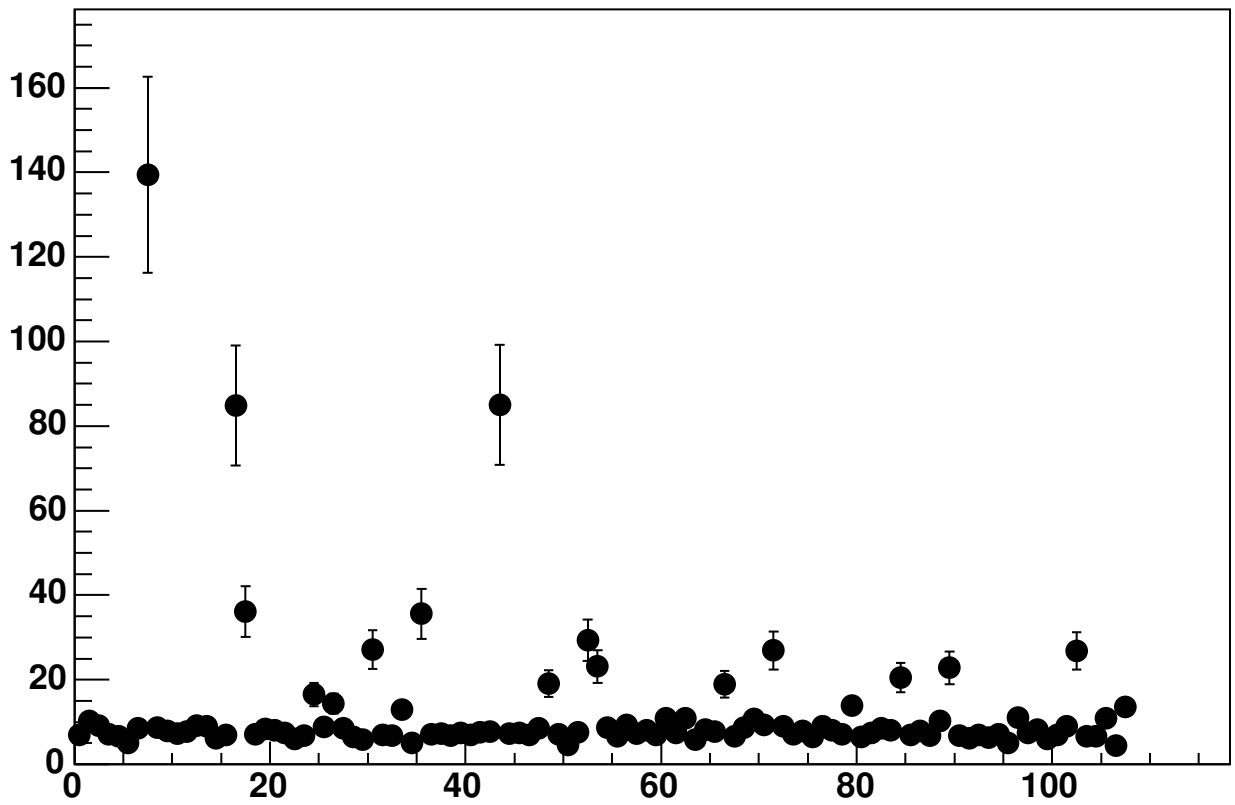
Enable 5, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



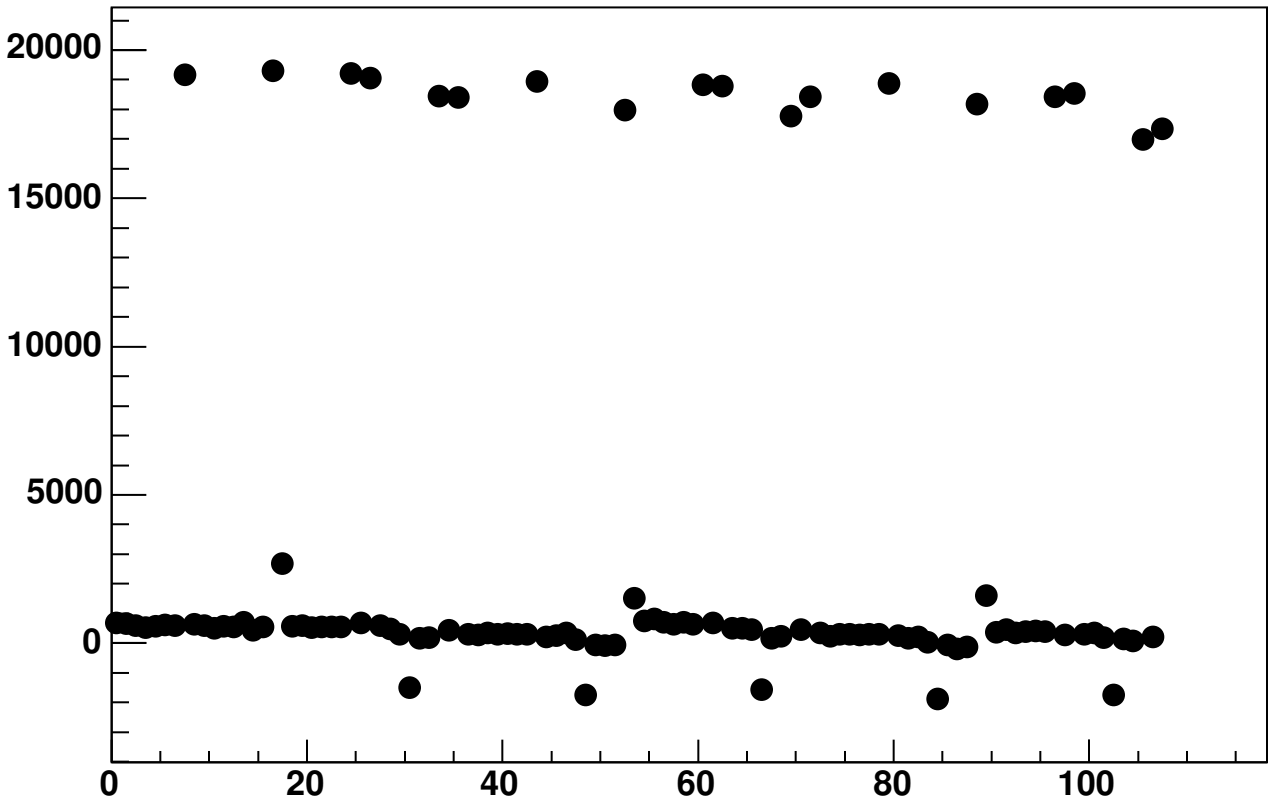
Enable 5, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



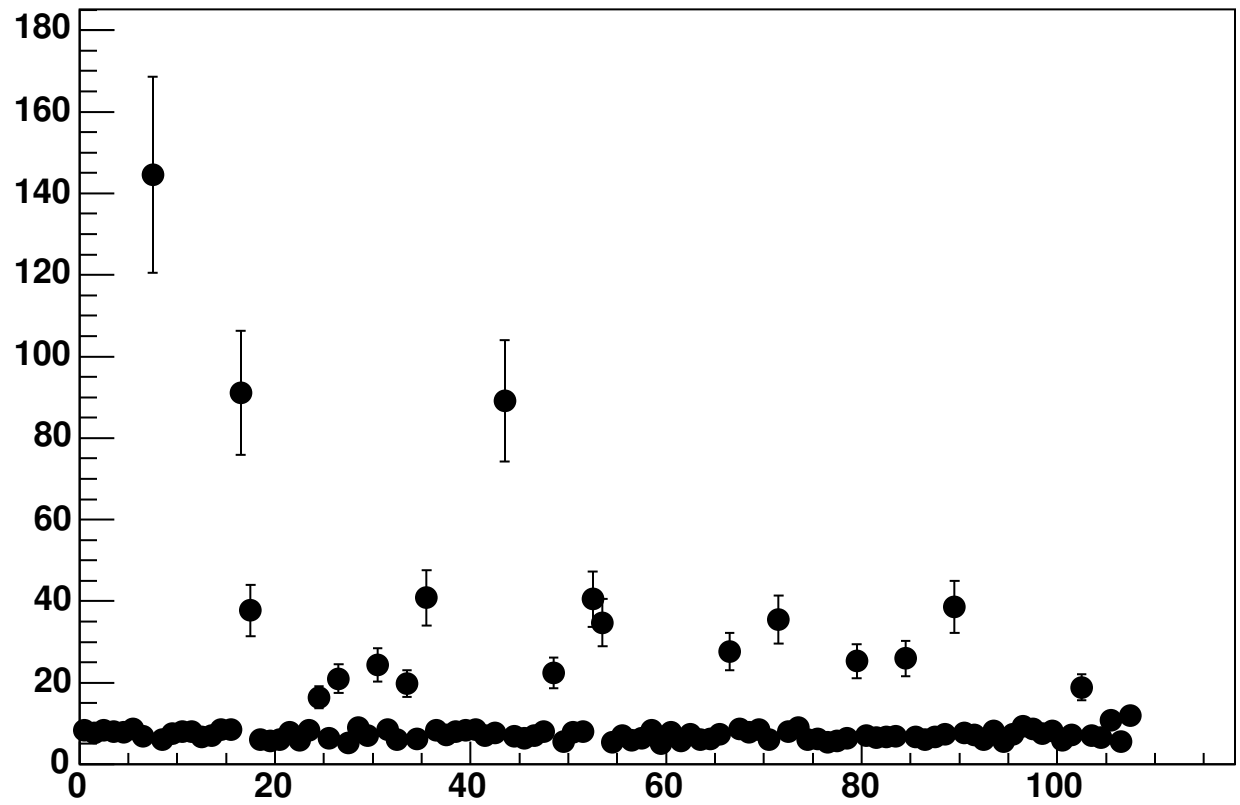
Enable 5, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



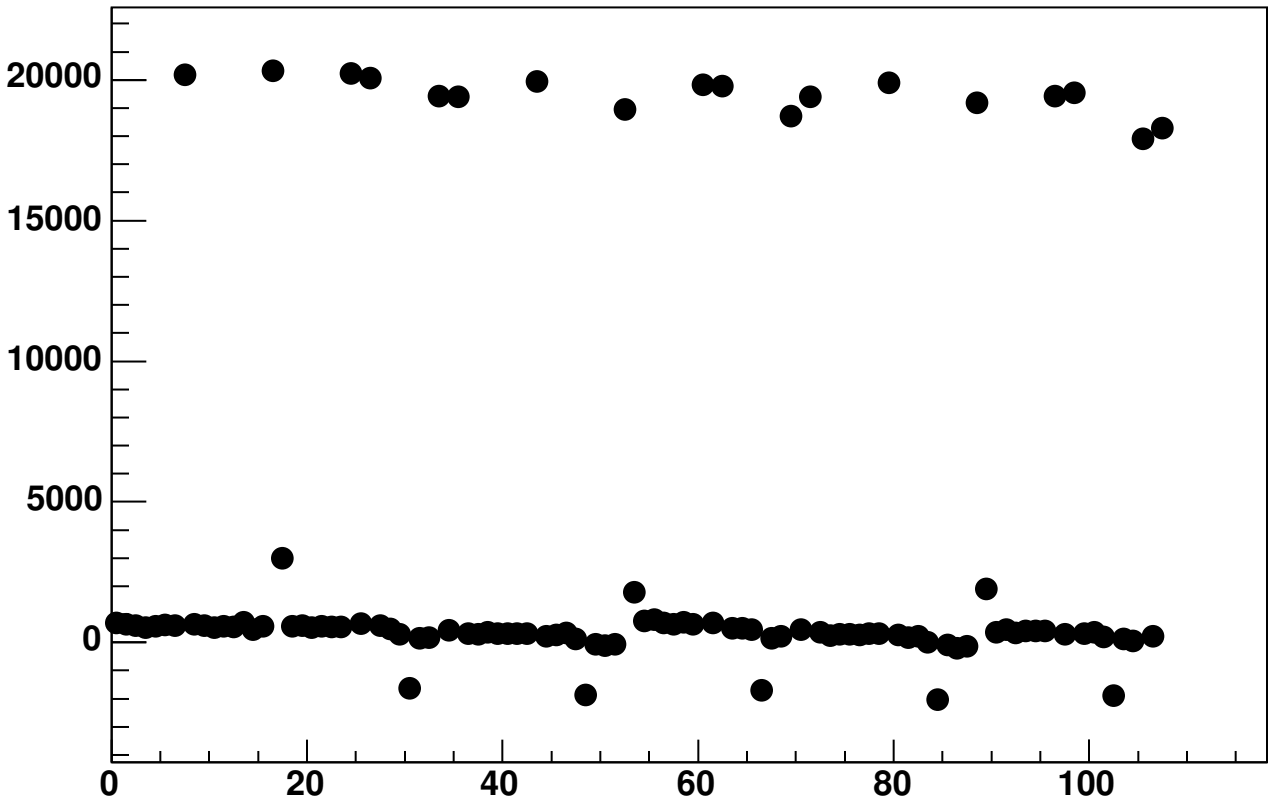
Enable 5, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



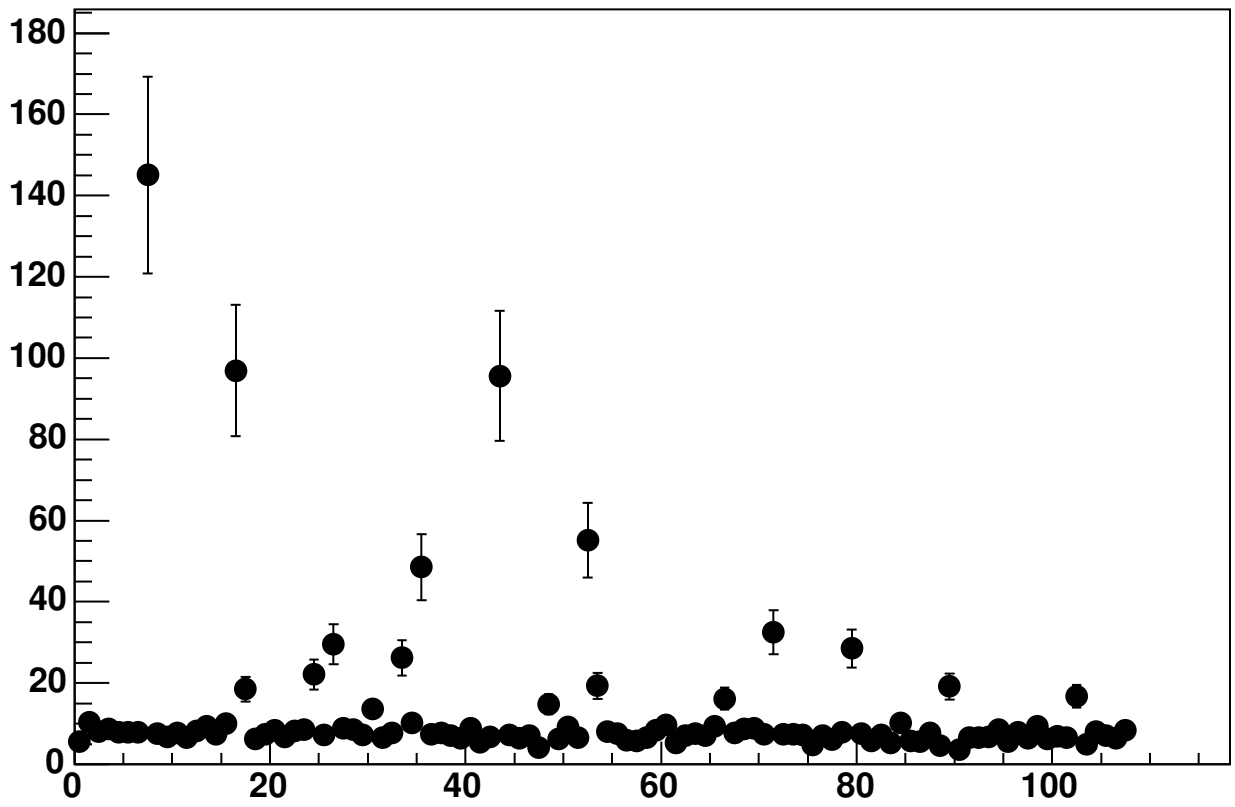
Enable 5, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



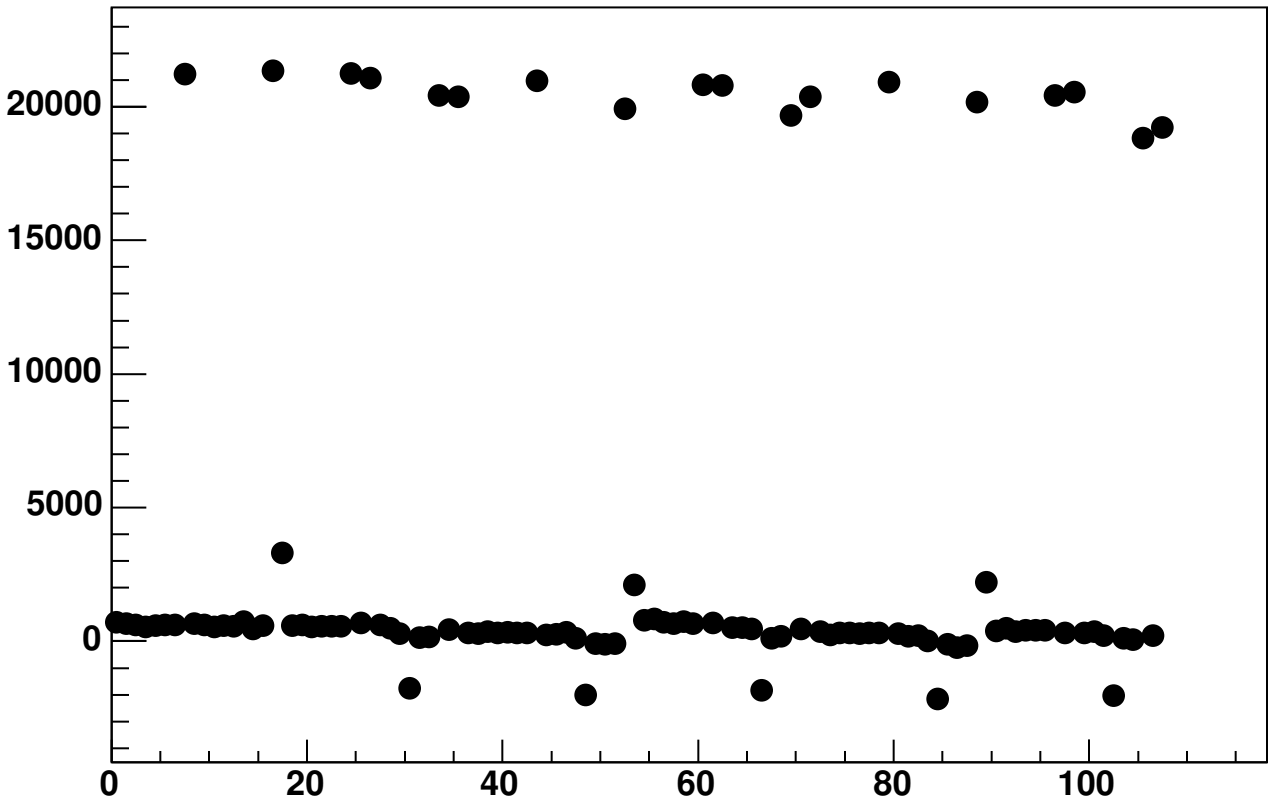
Enable 5, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



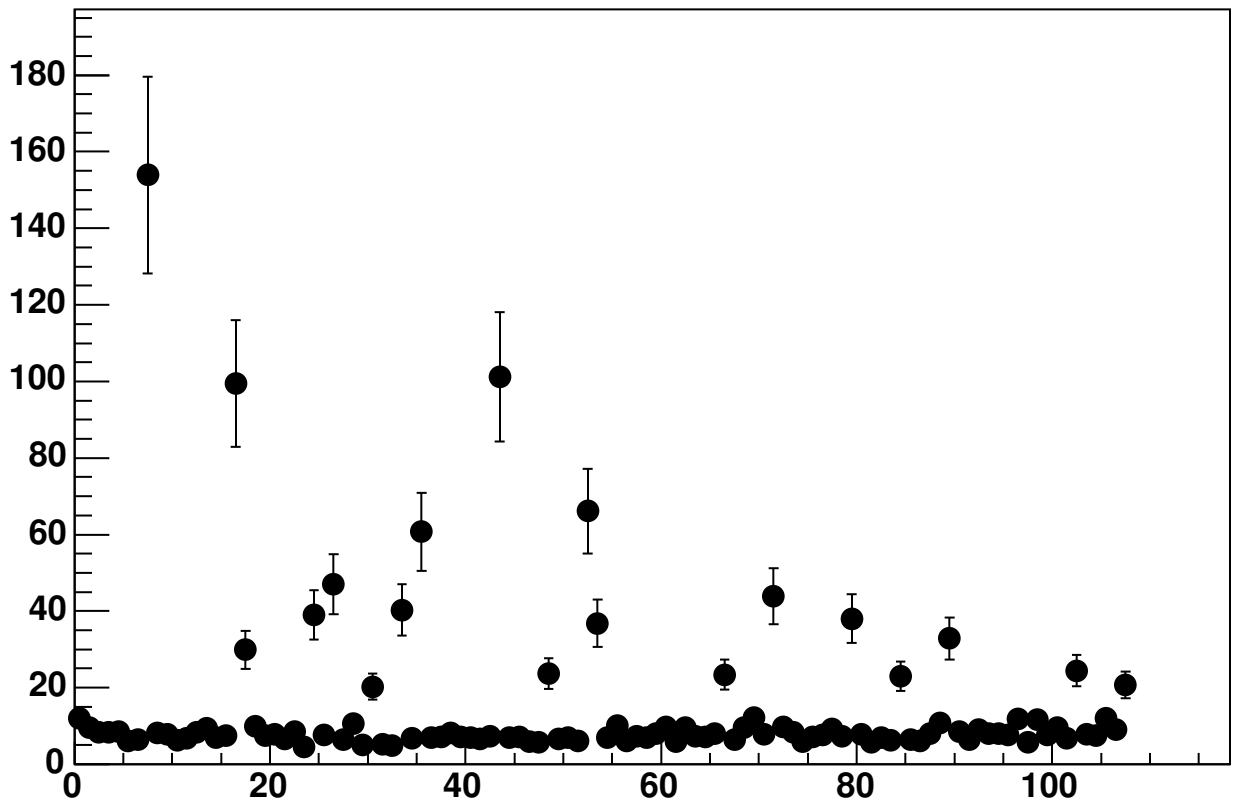
Enable 5, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



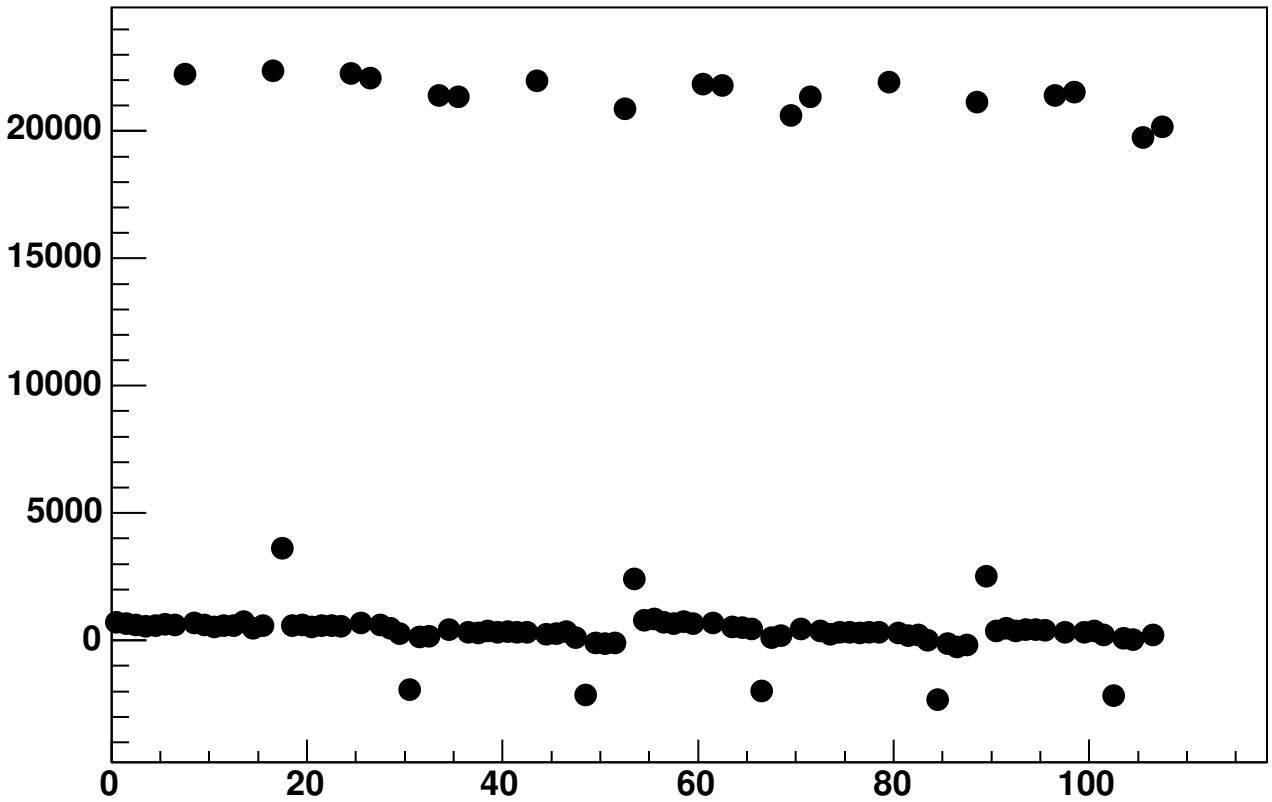
Enable 5, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



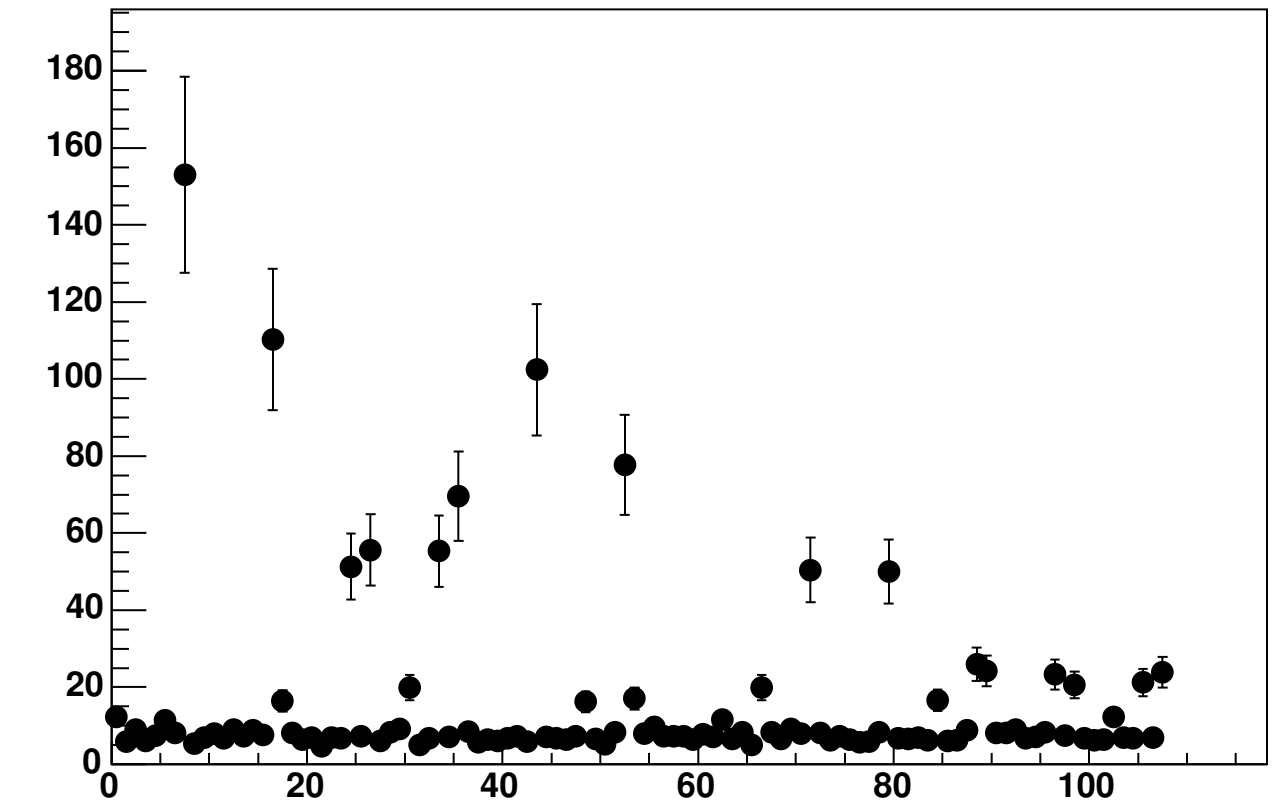
Enable 5, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



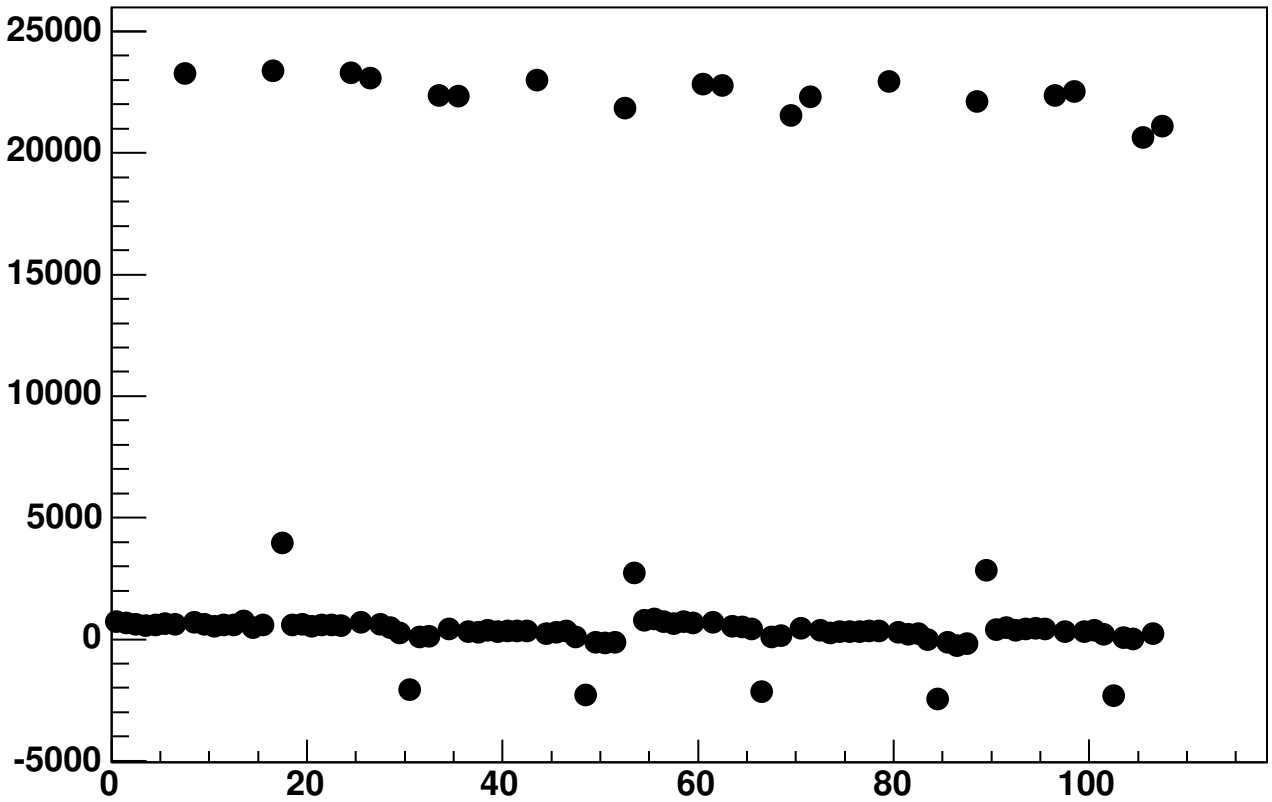
Enable 5, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



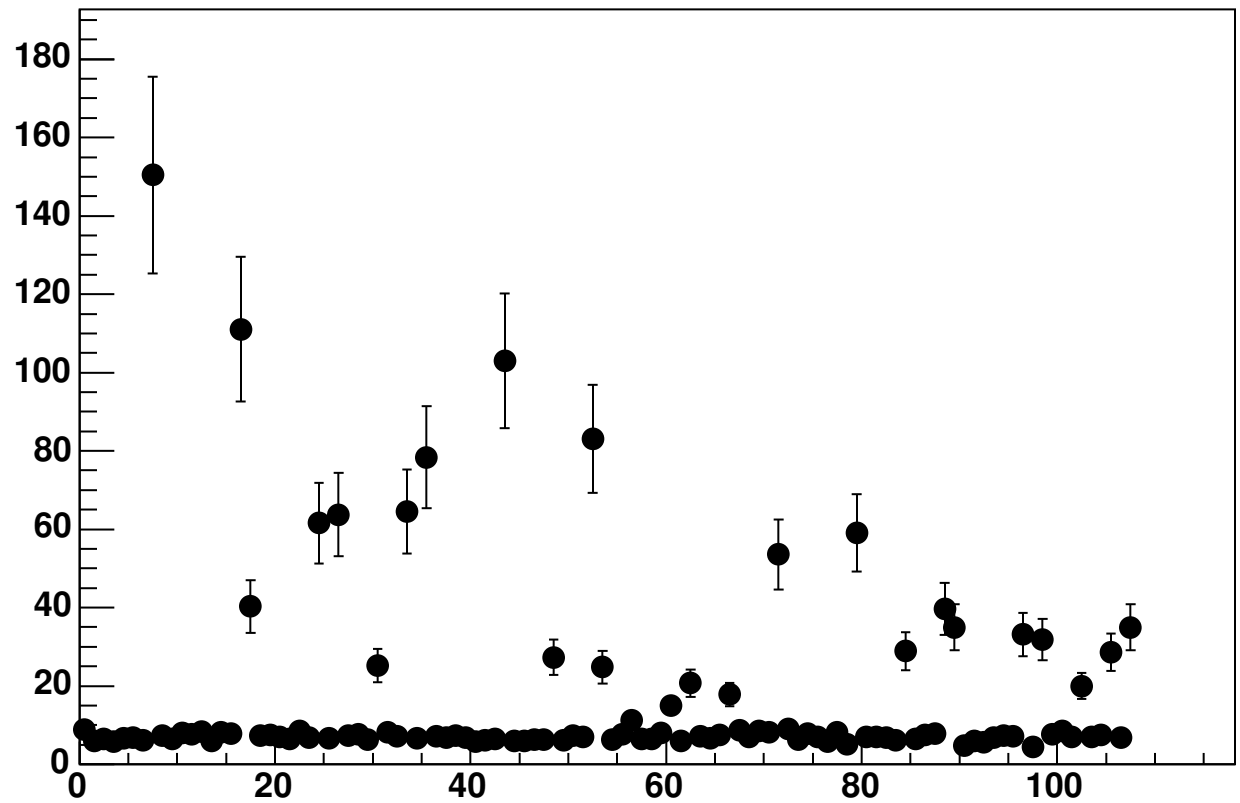
Enable 5, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



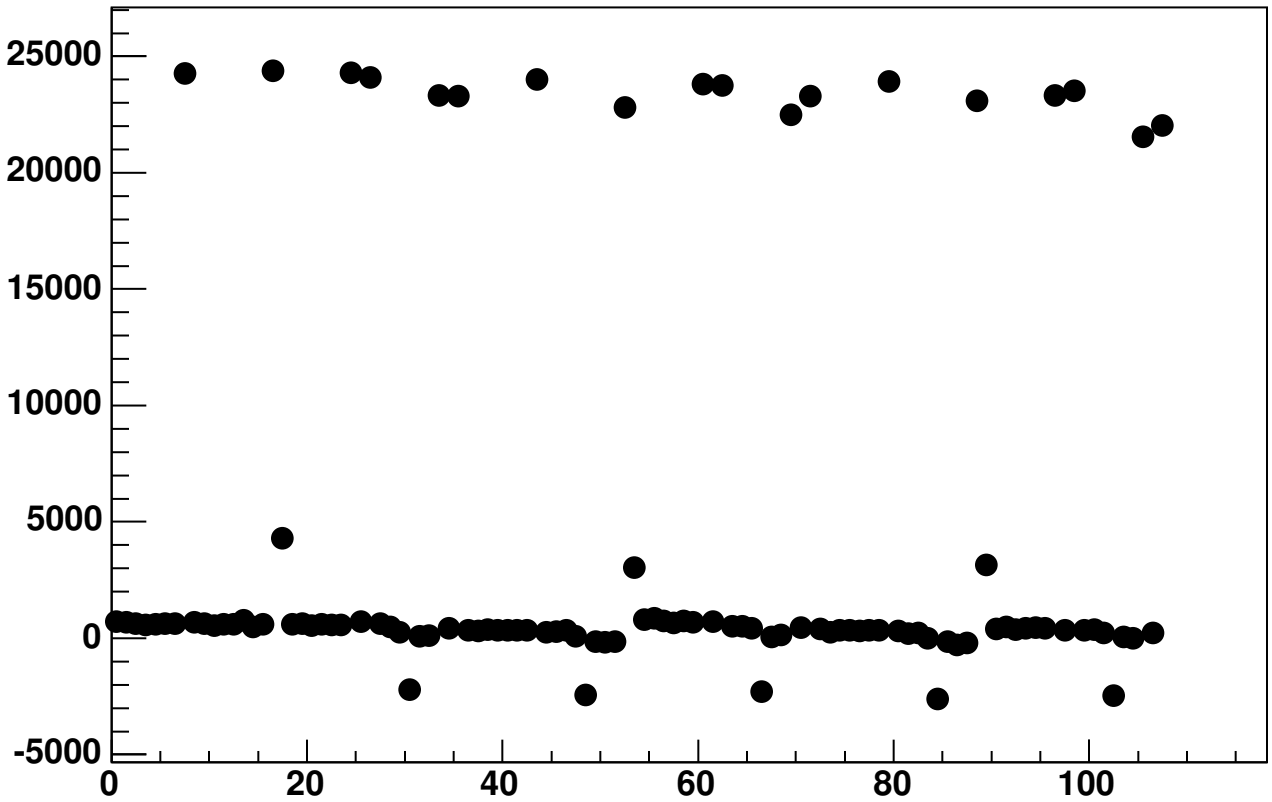
Enable 5, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



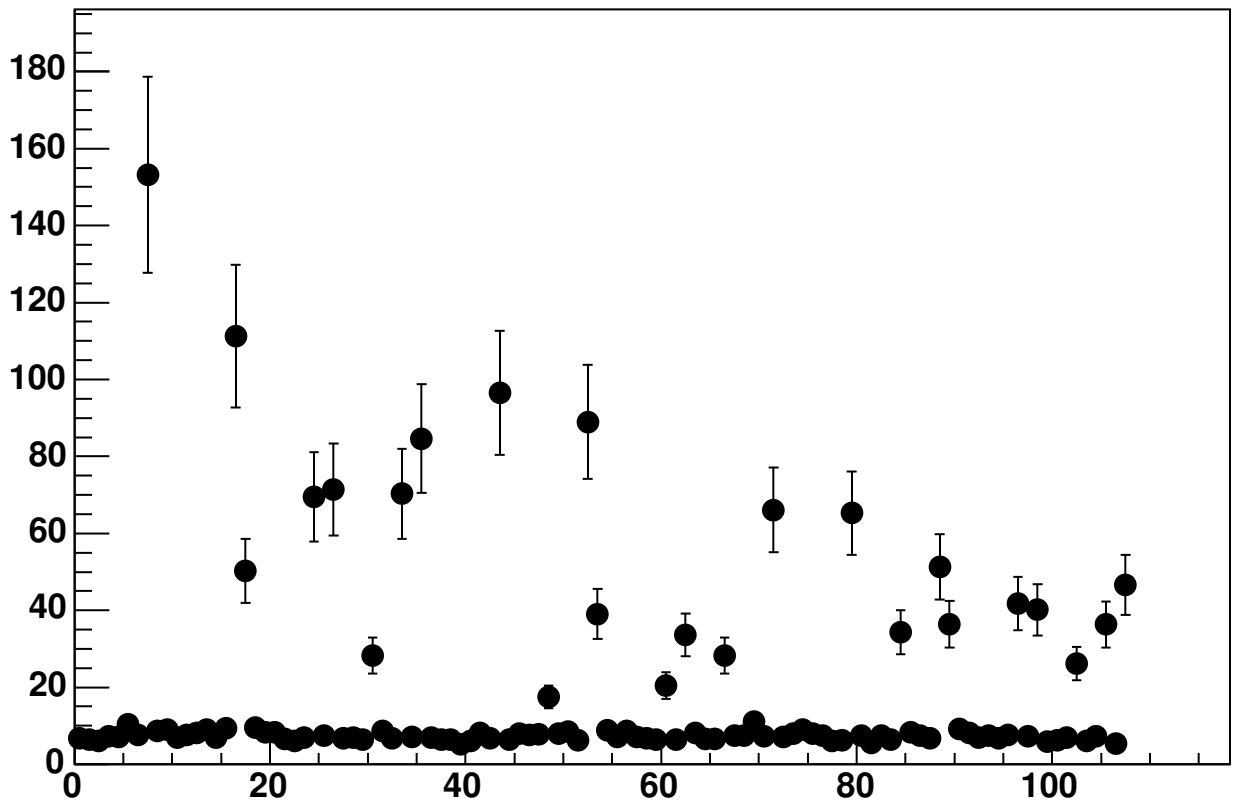
Enable 5, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



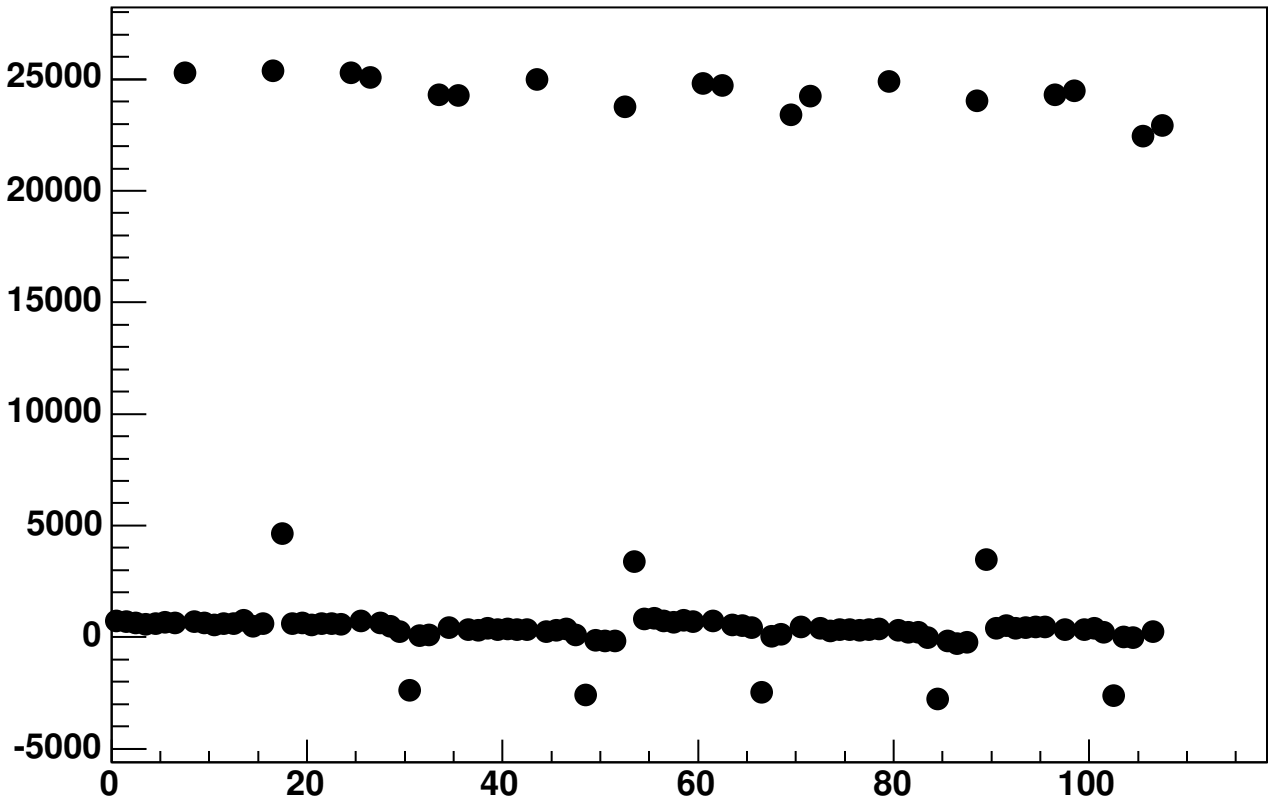
Enable 5, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



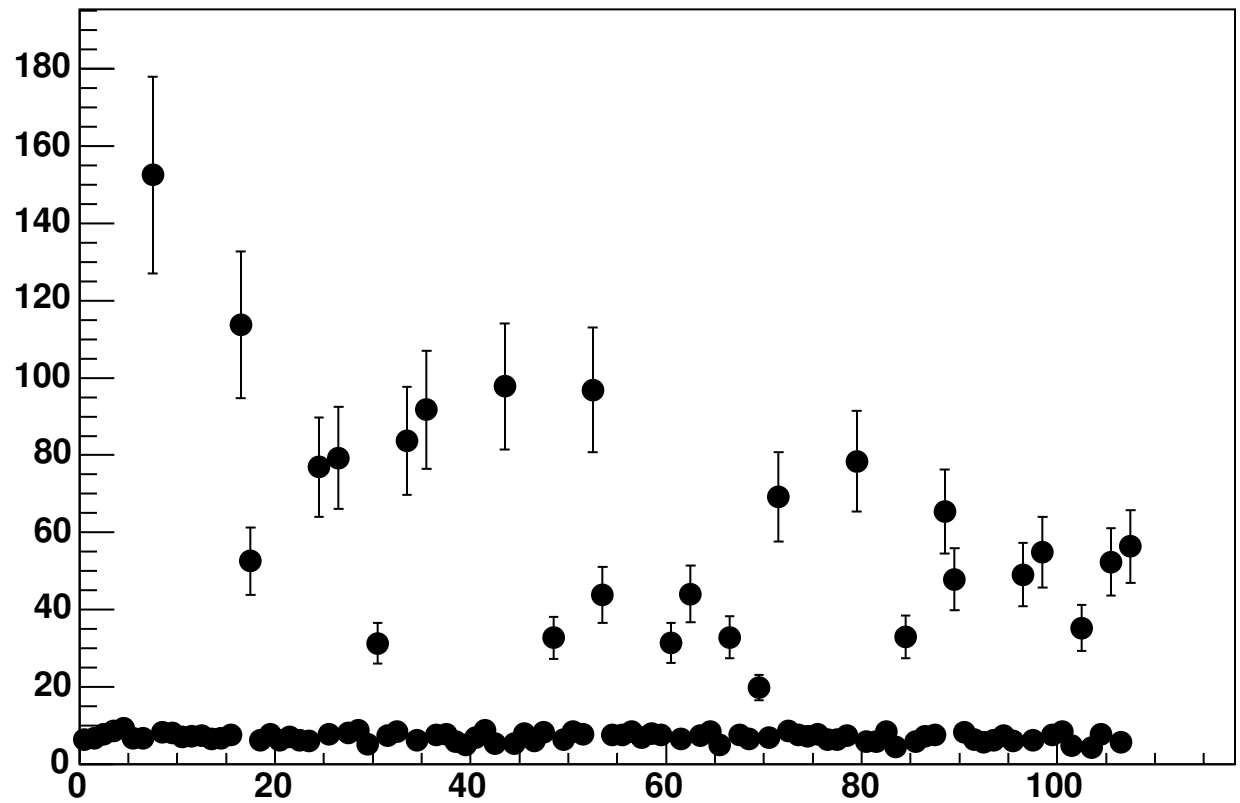
Enable 5, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



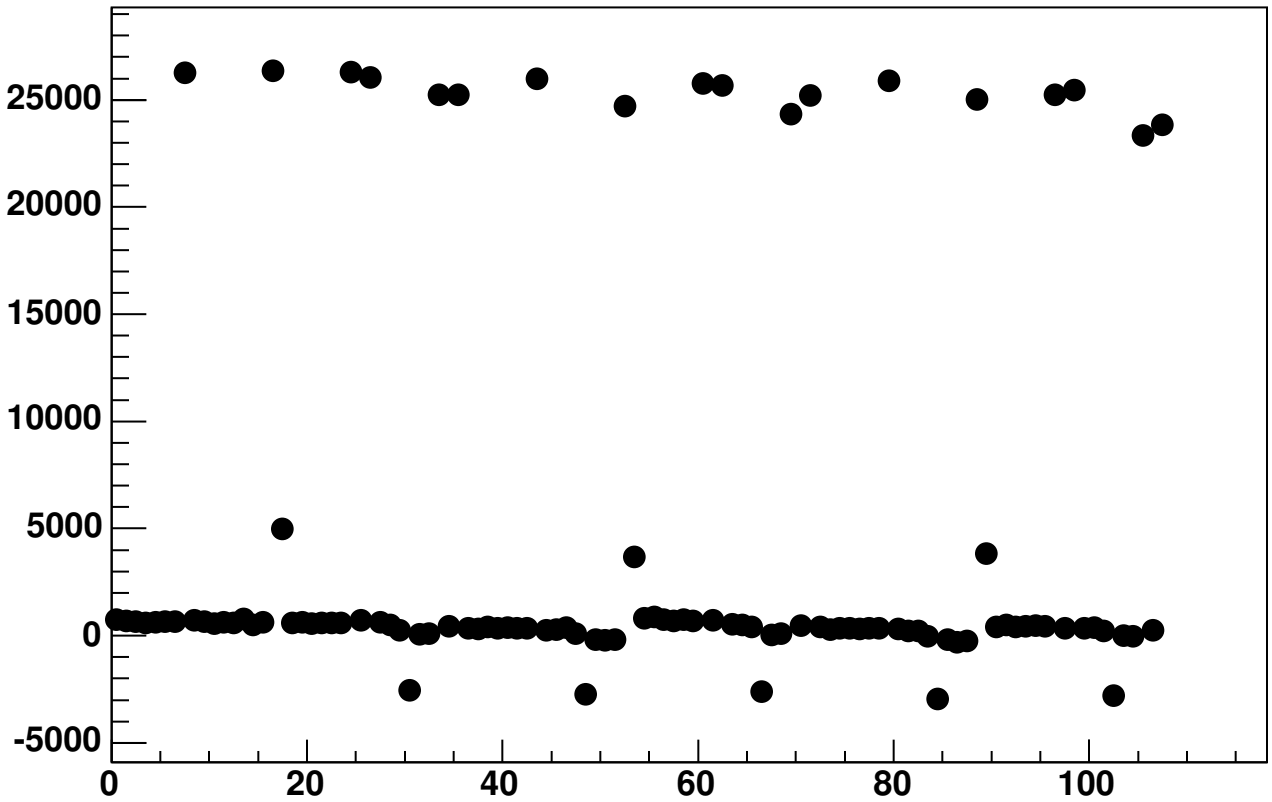
Enable 5, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



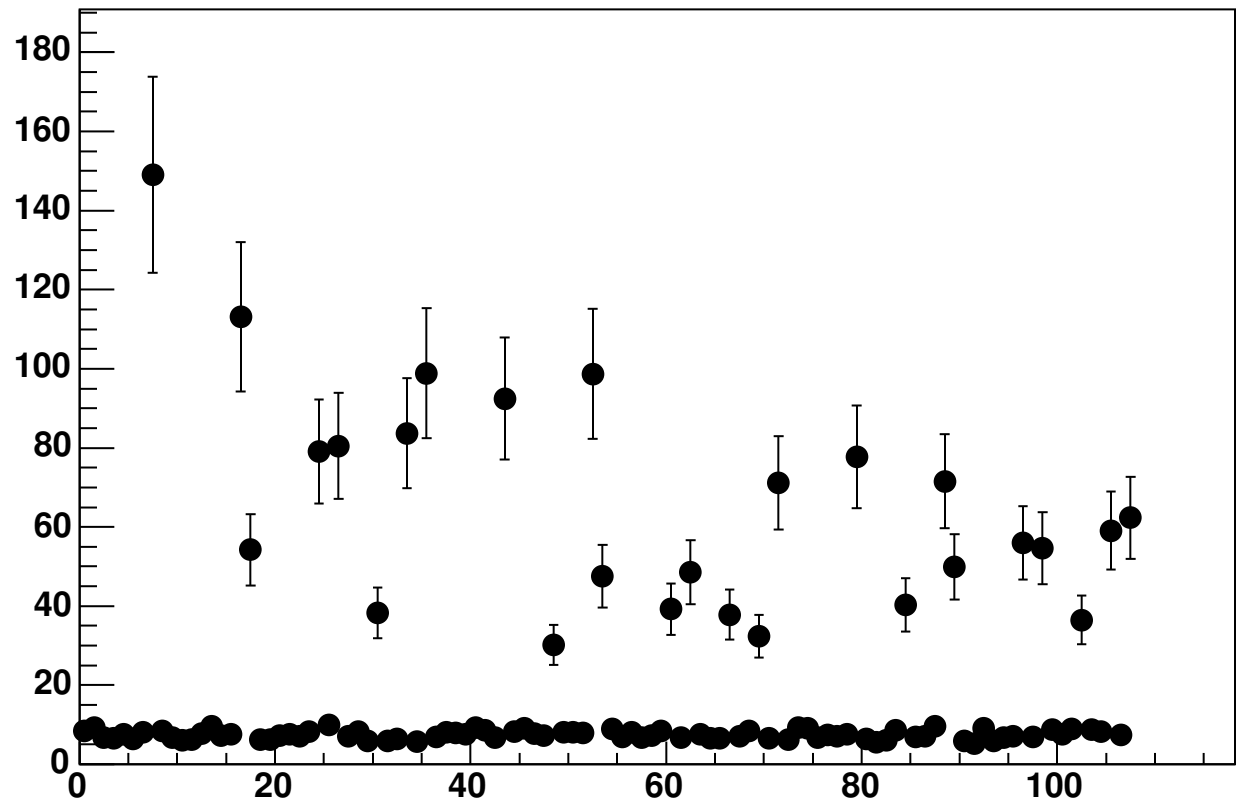
Enable 5, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



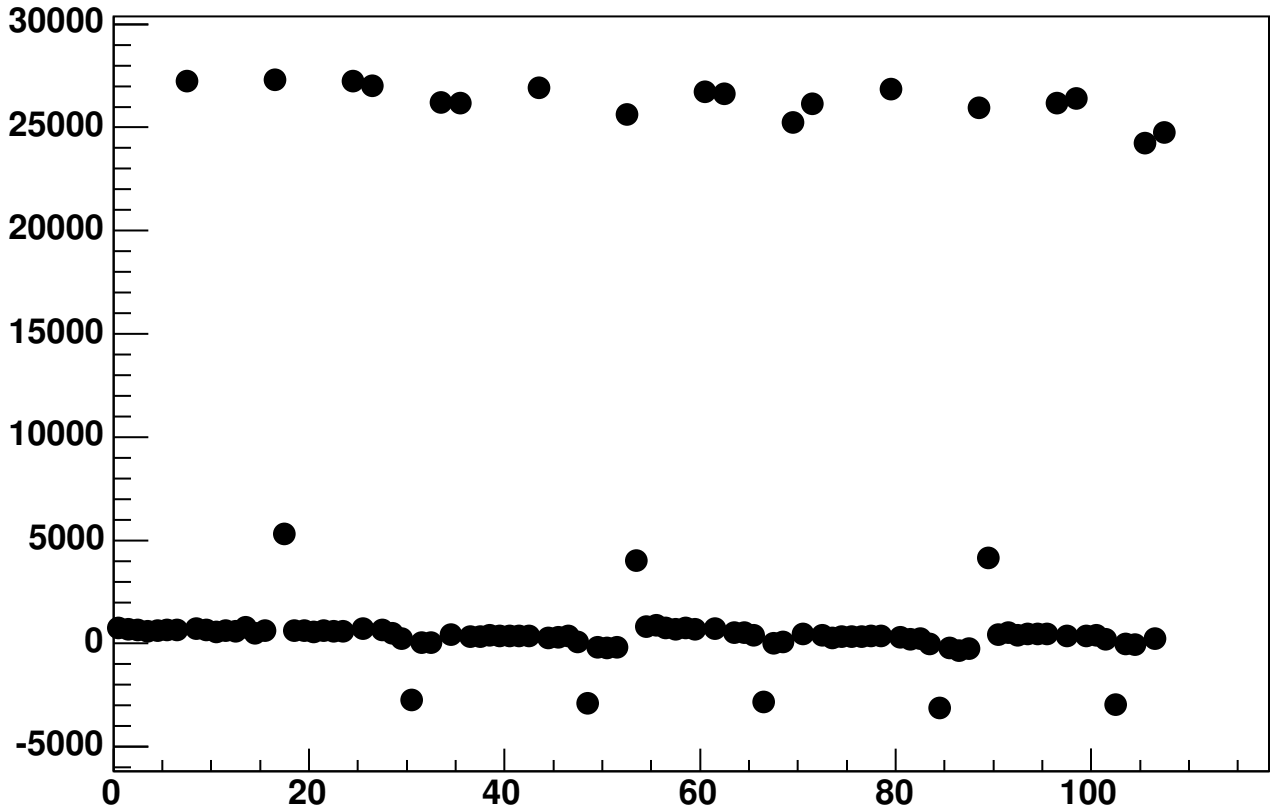
Enable 5, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



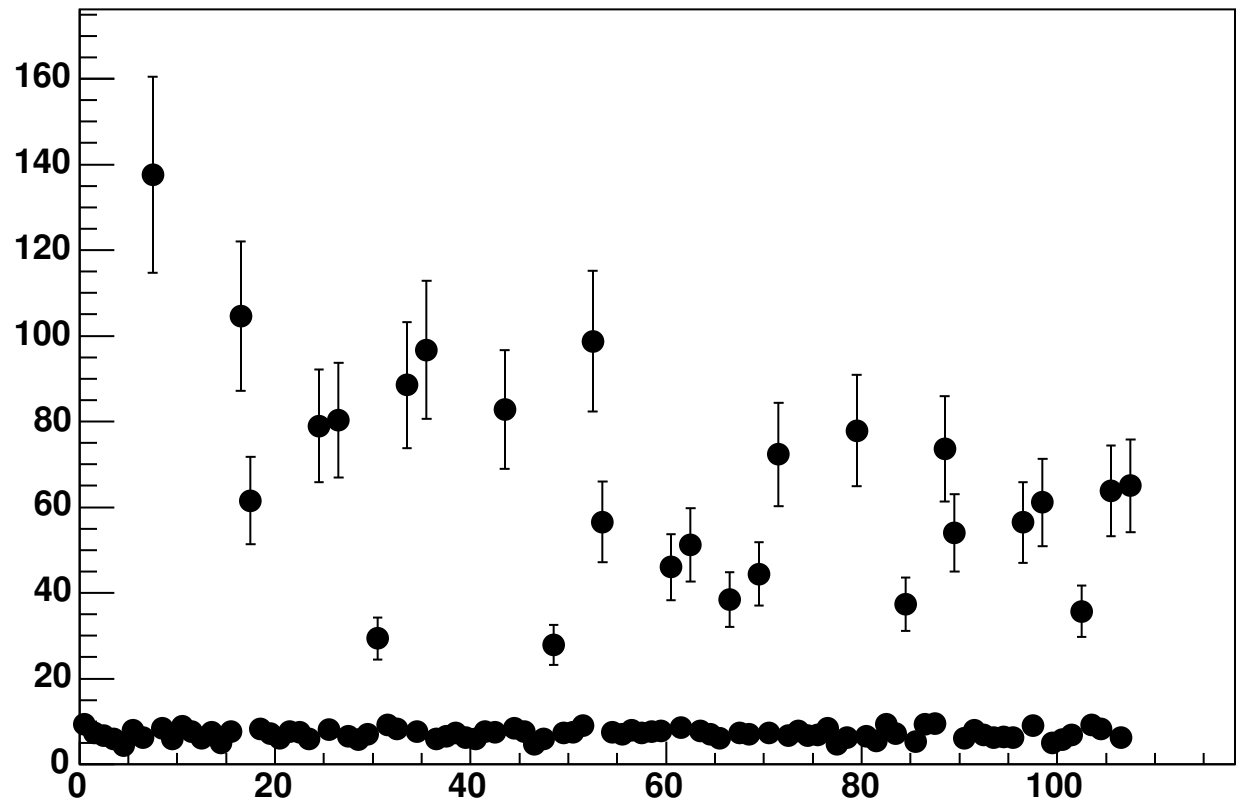
Enable 5, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



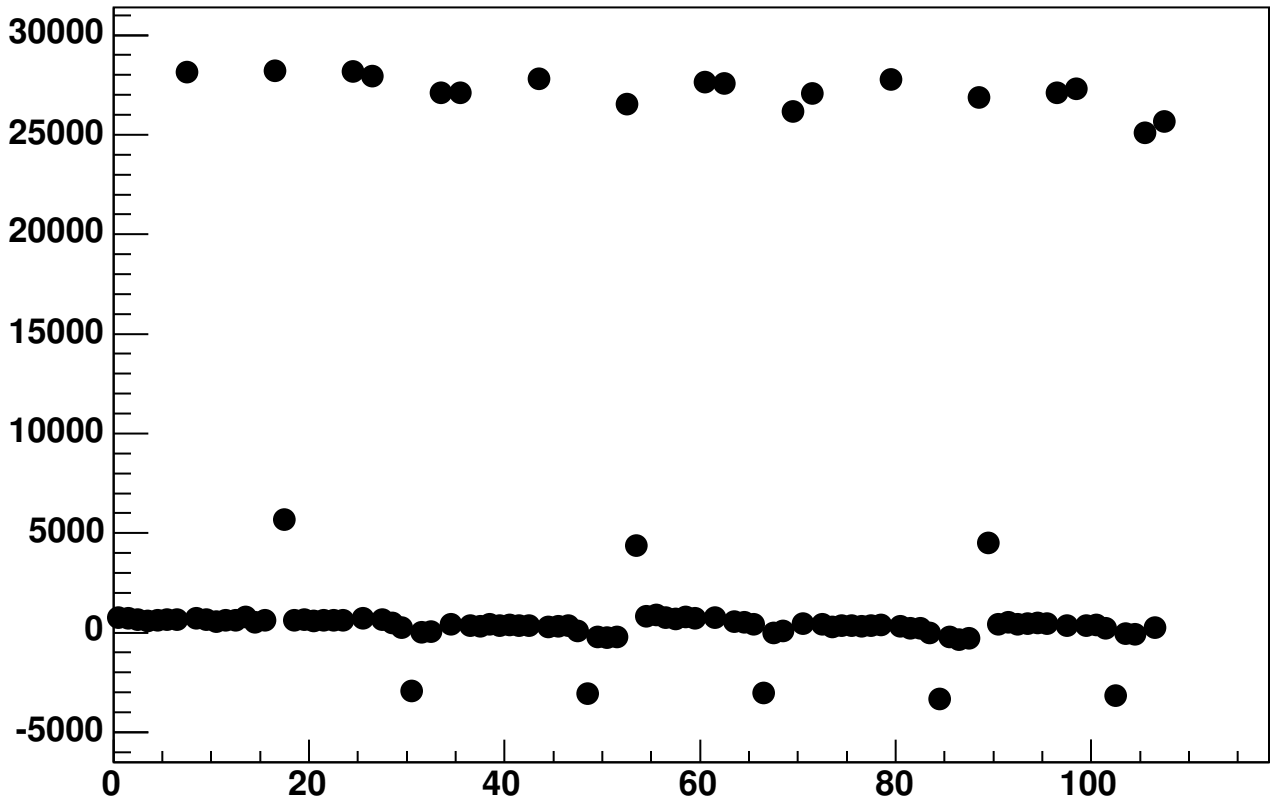
Enable 5, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



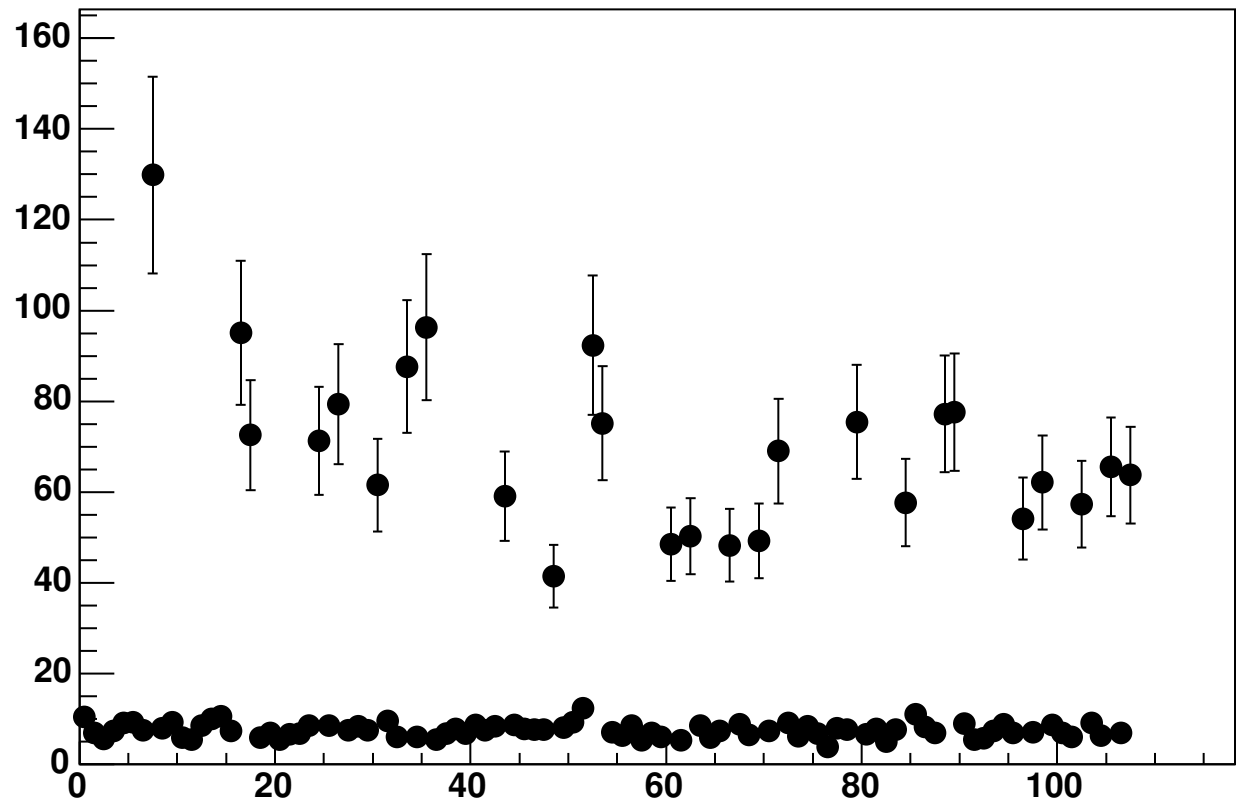
Enable 5, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



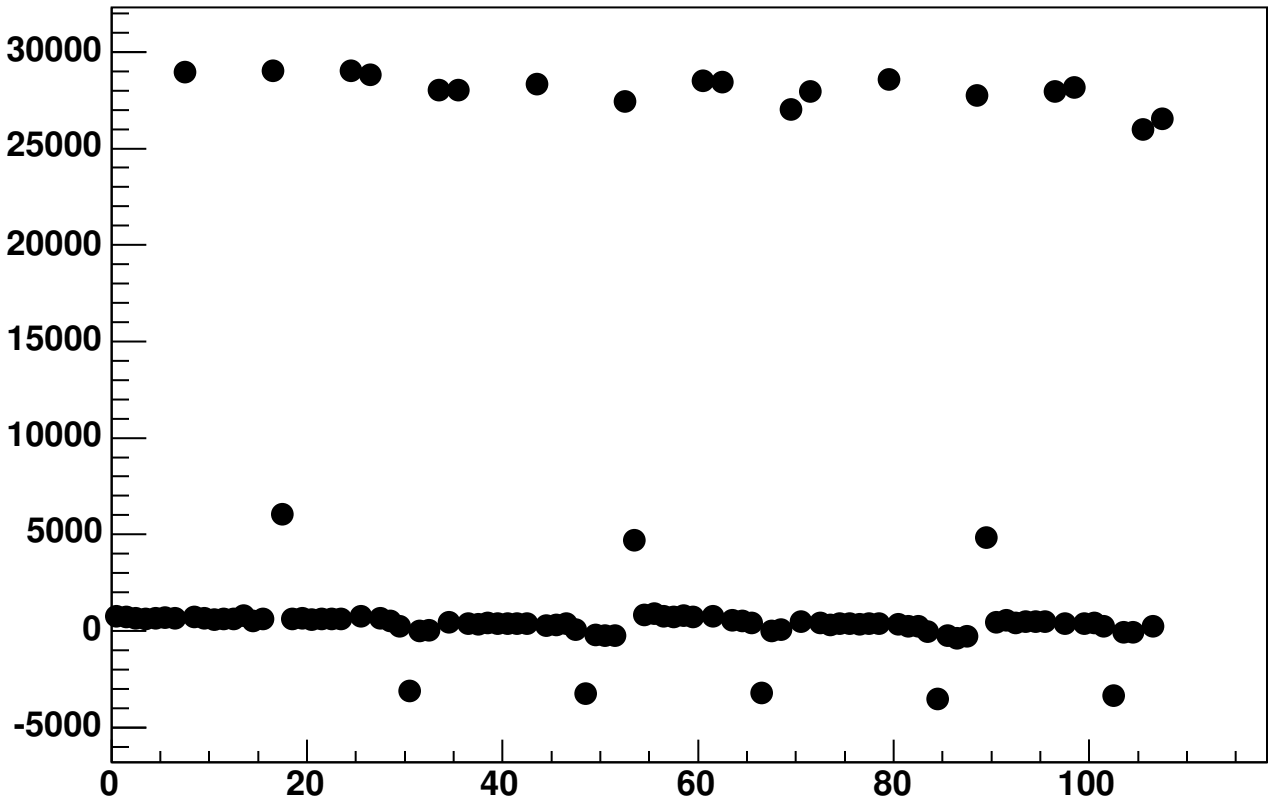
Enable 5, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



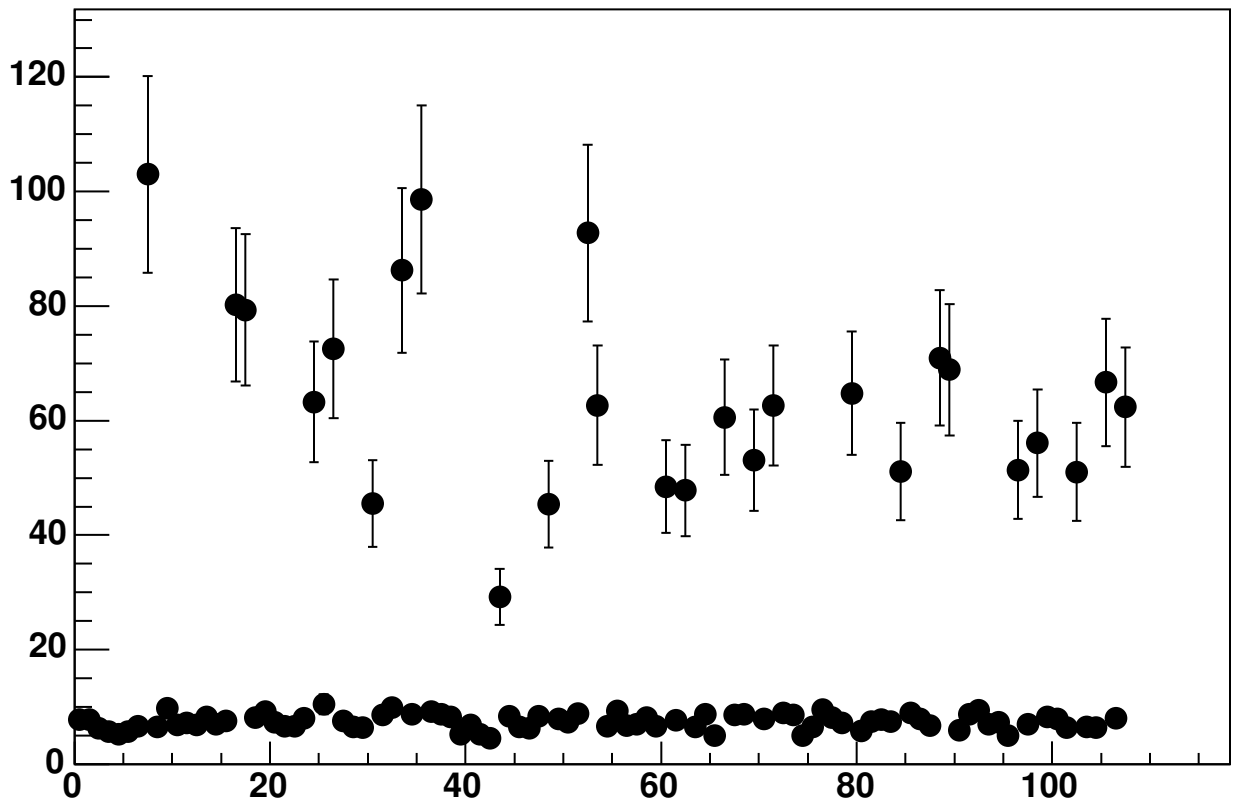
Enable 5, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



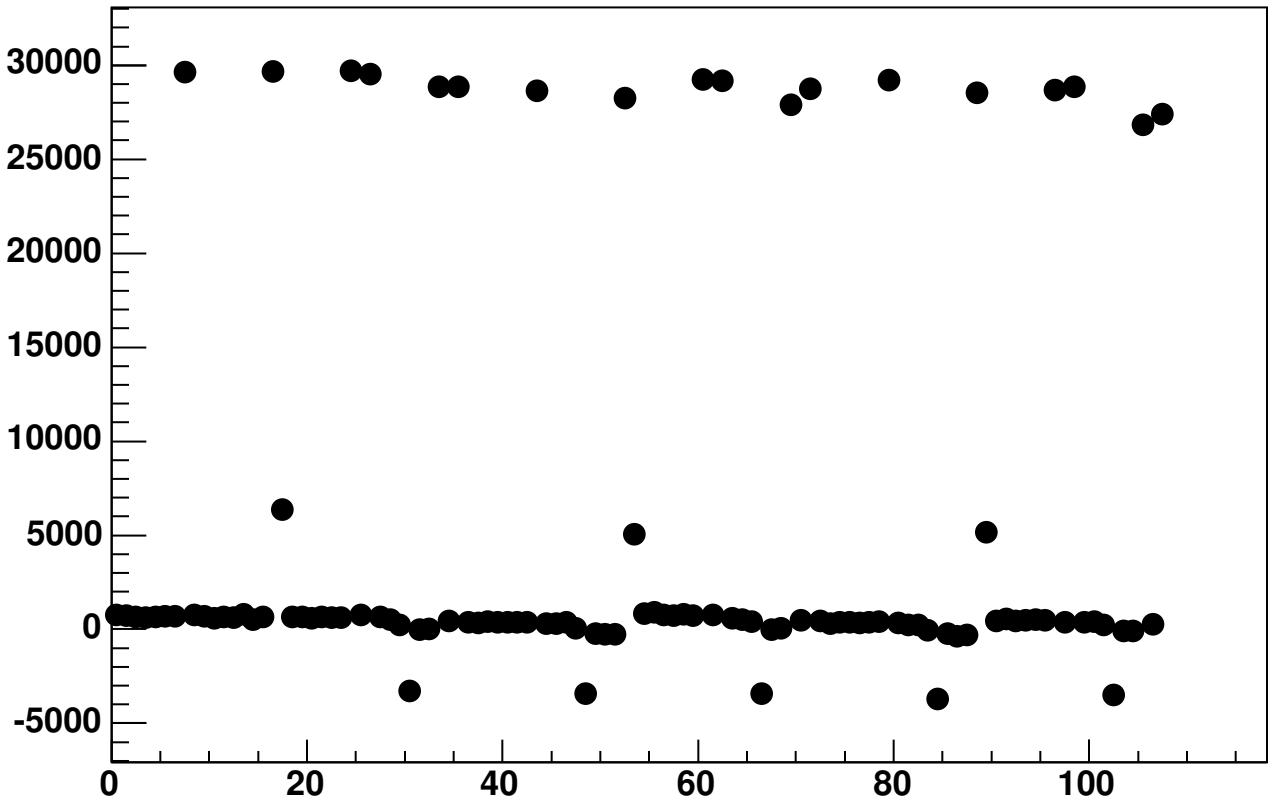
Enable 5, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



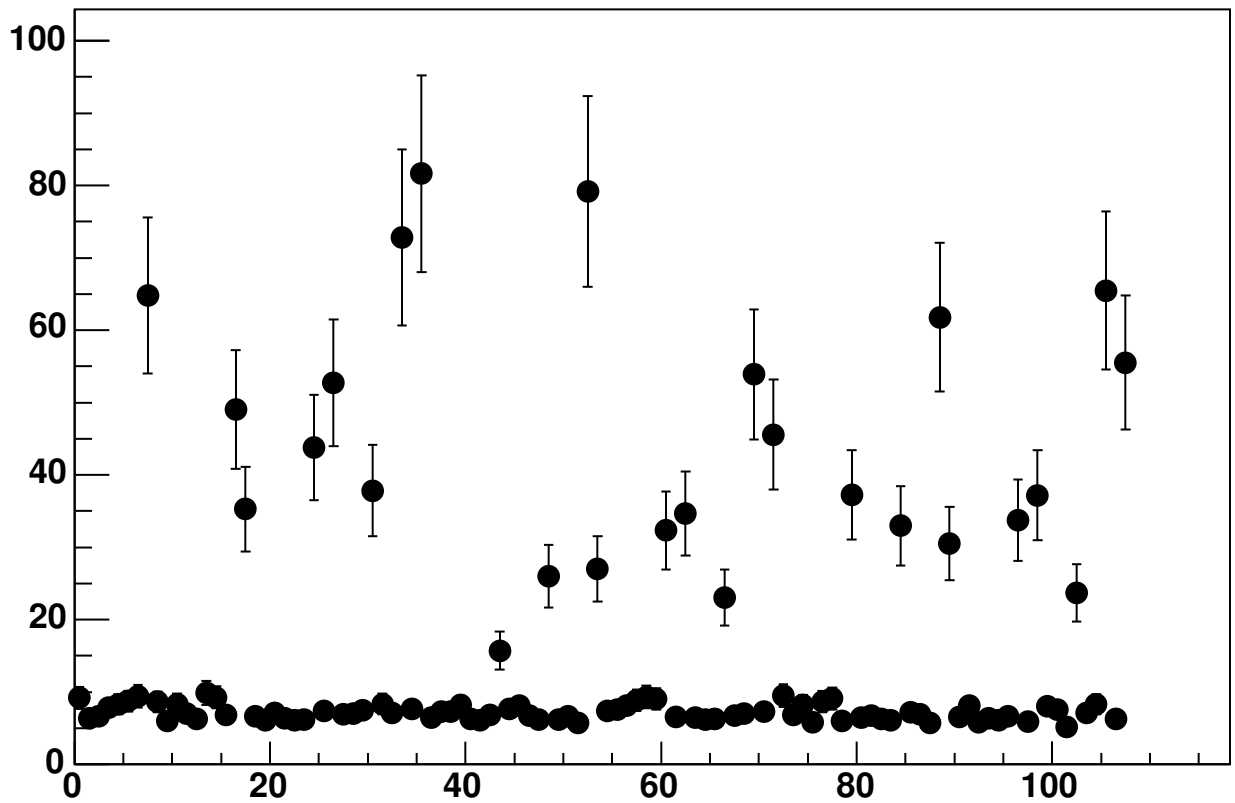
Enable 5, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



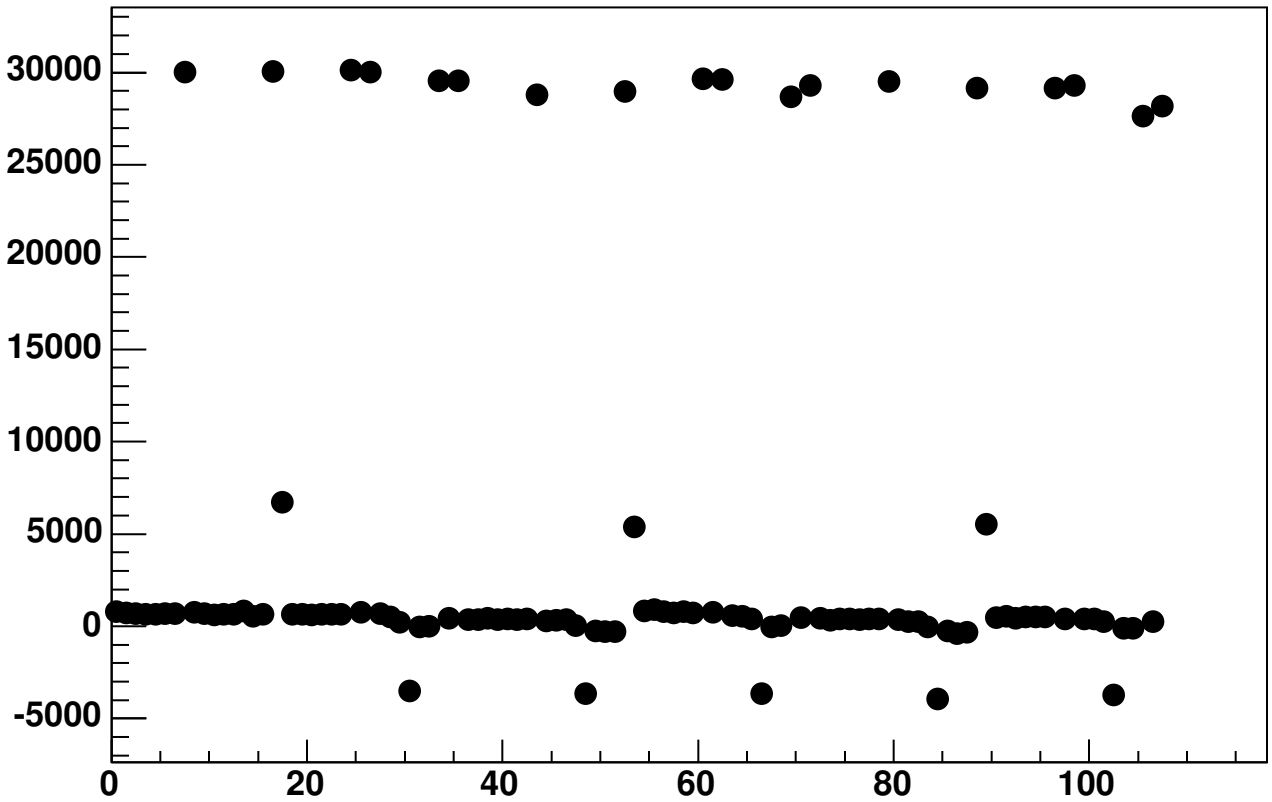
Enable 5, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



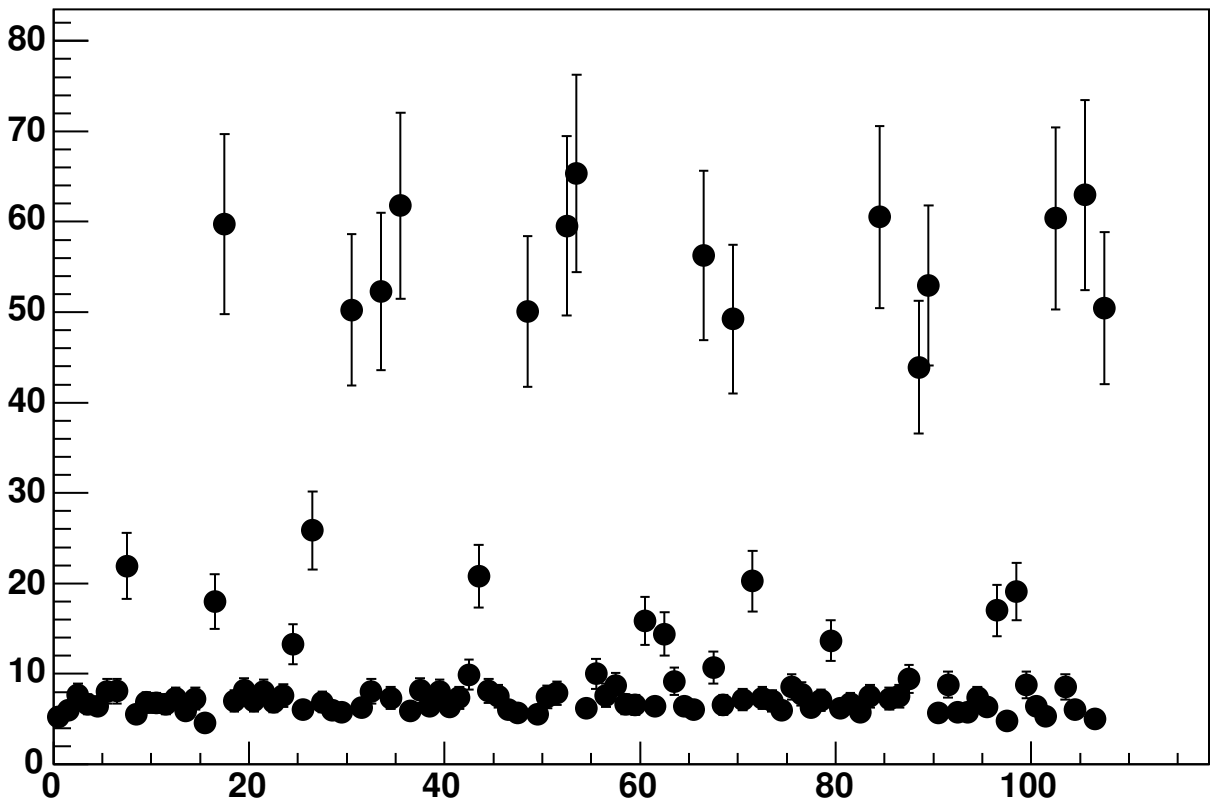
Enable 5, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



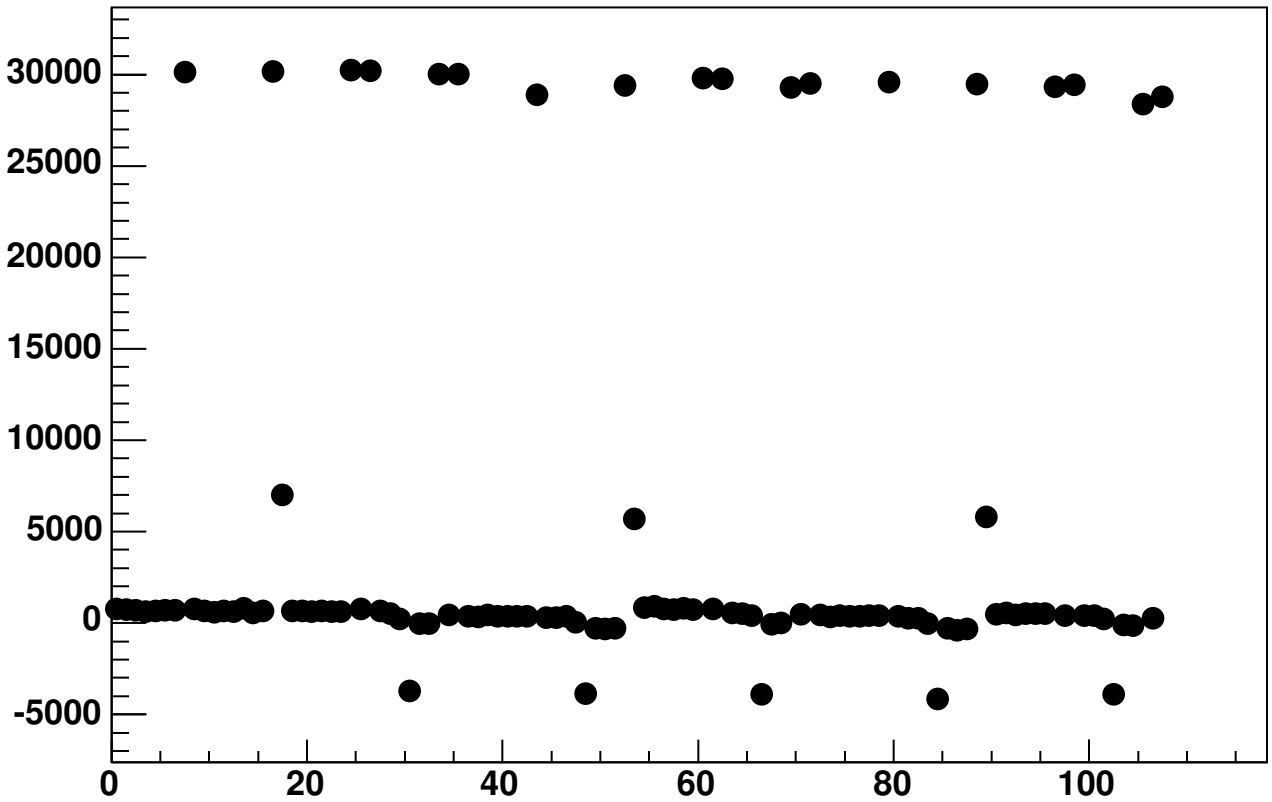
Enable 5, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



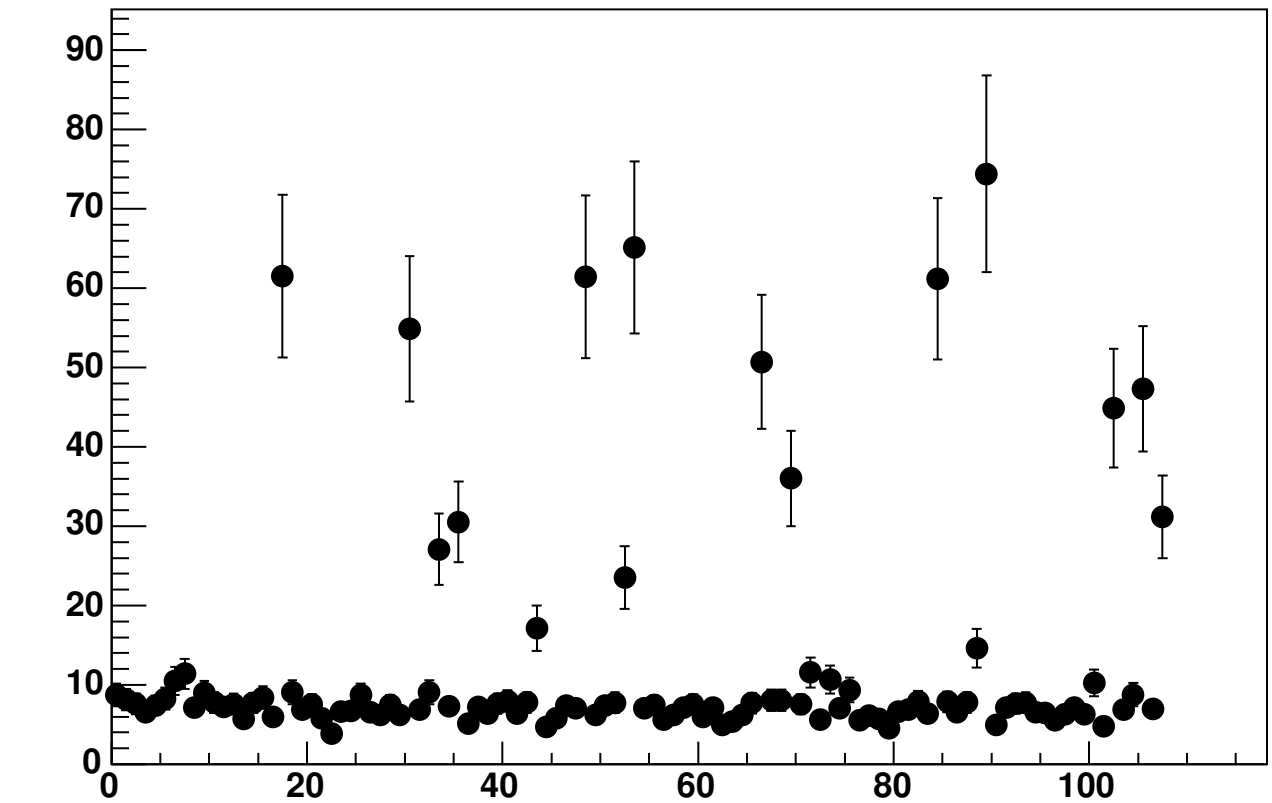
Enable 5, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



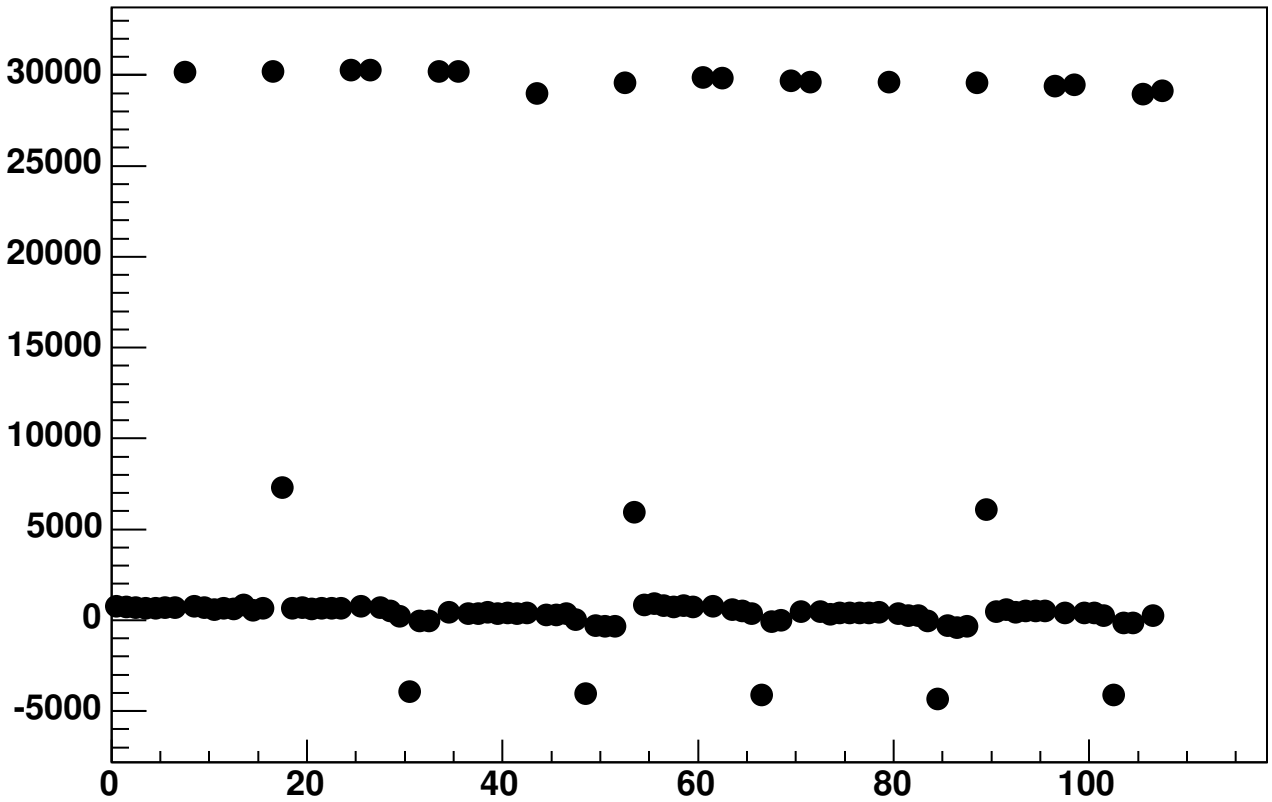
Enable 5, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



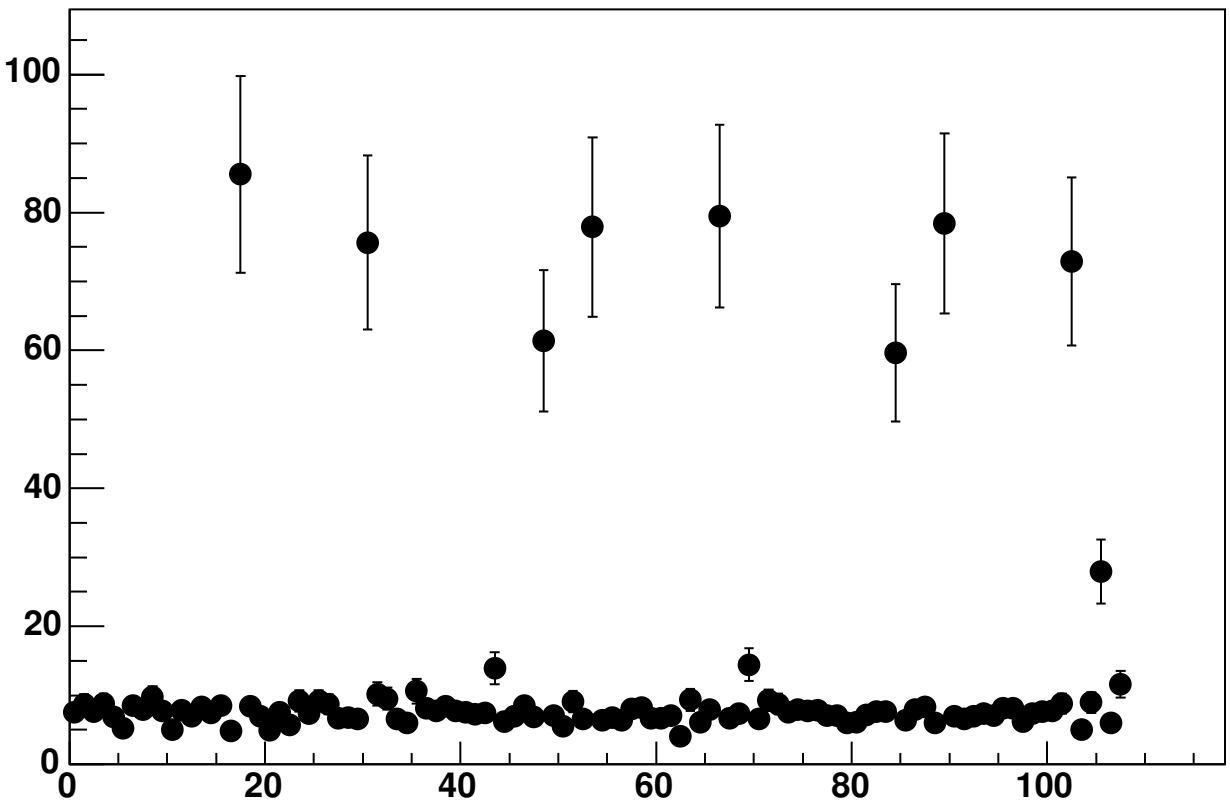
Enable 5, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



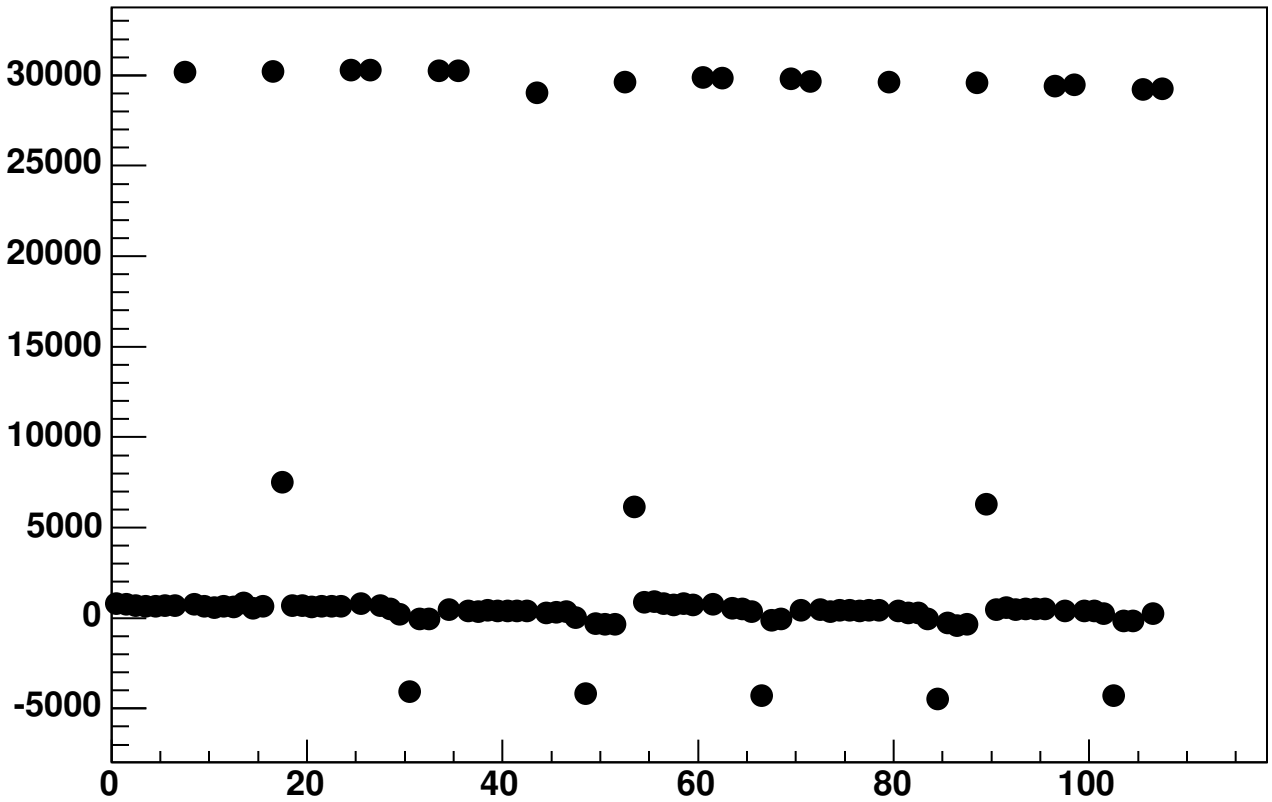
Enable 5, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



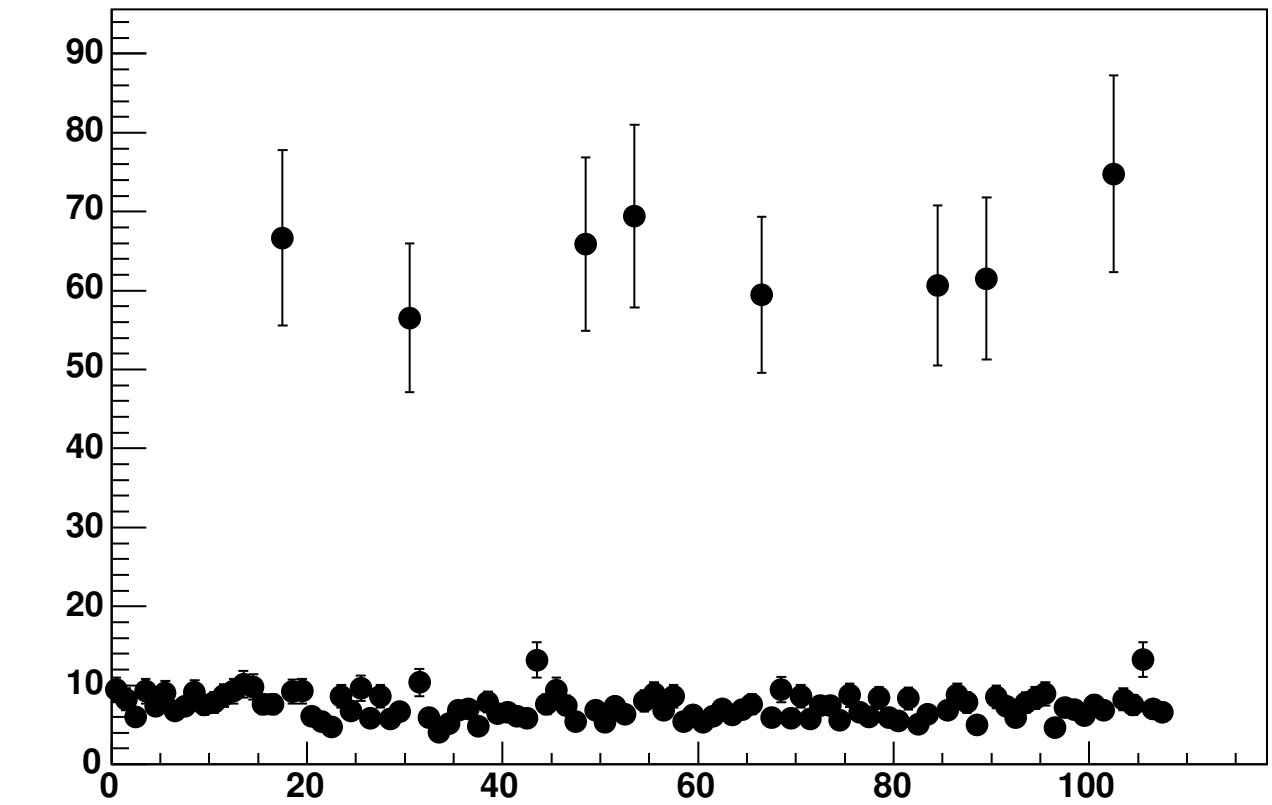
Enable 5, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



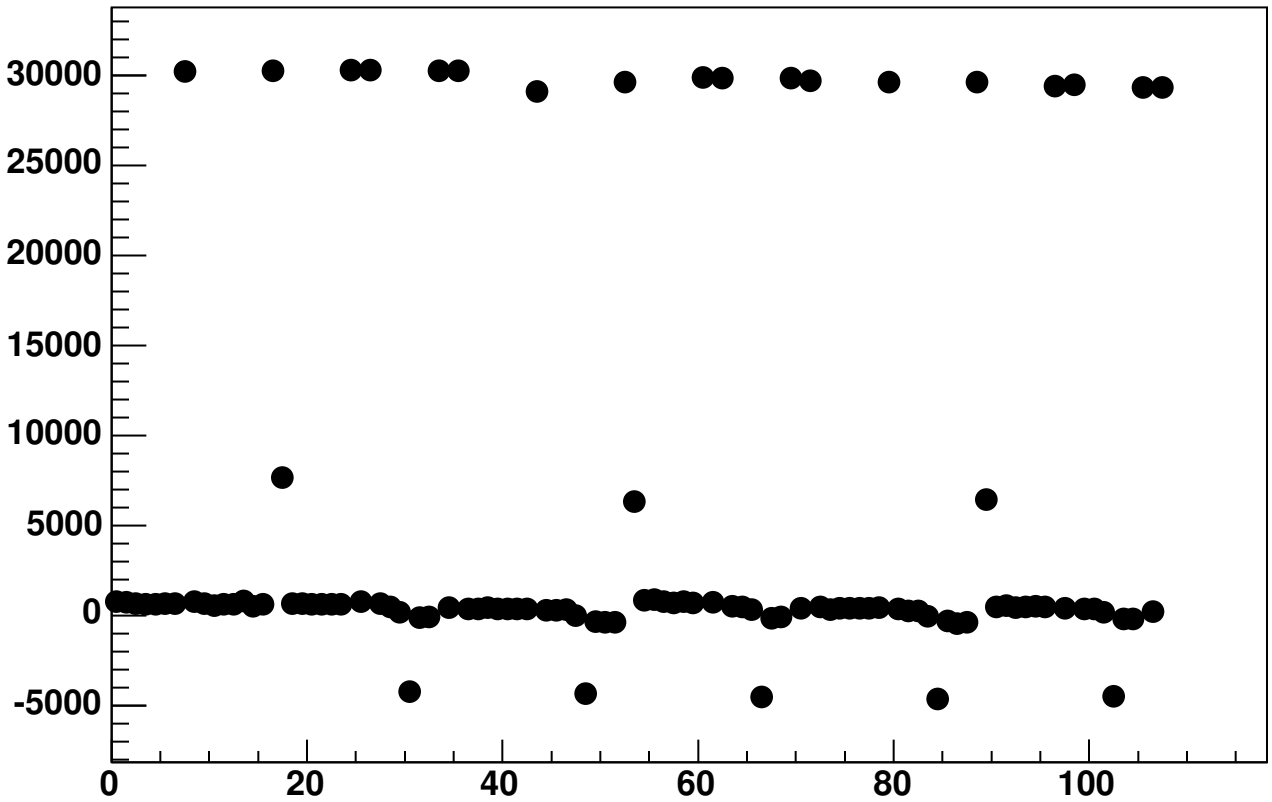
Enable 5, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



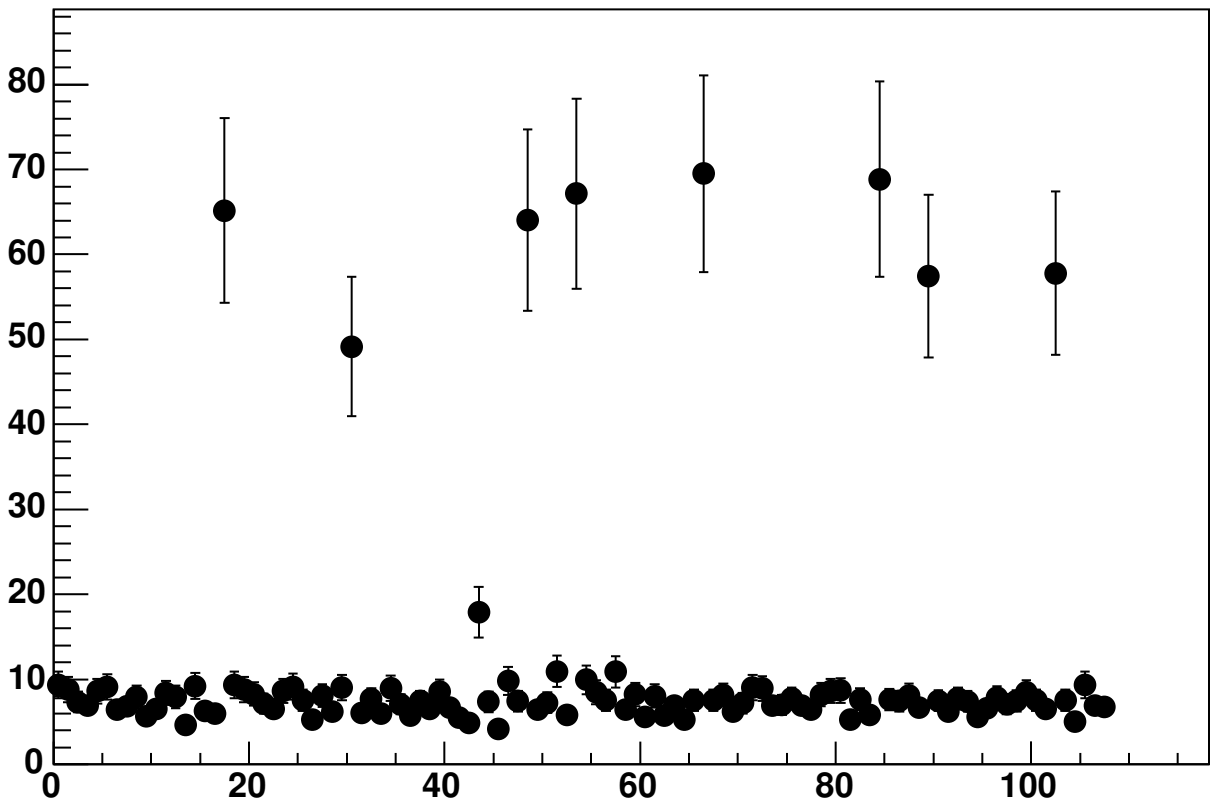
Enable 5, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



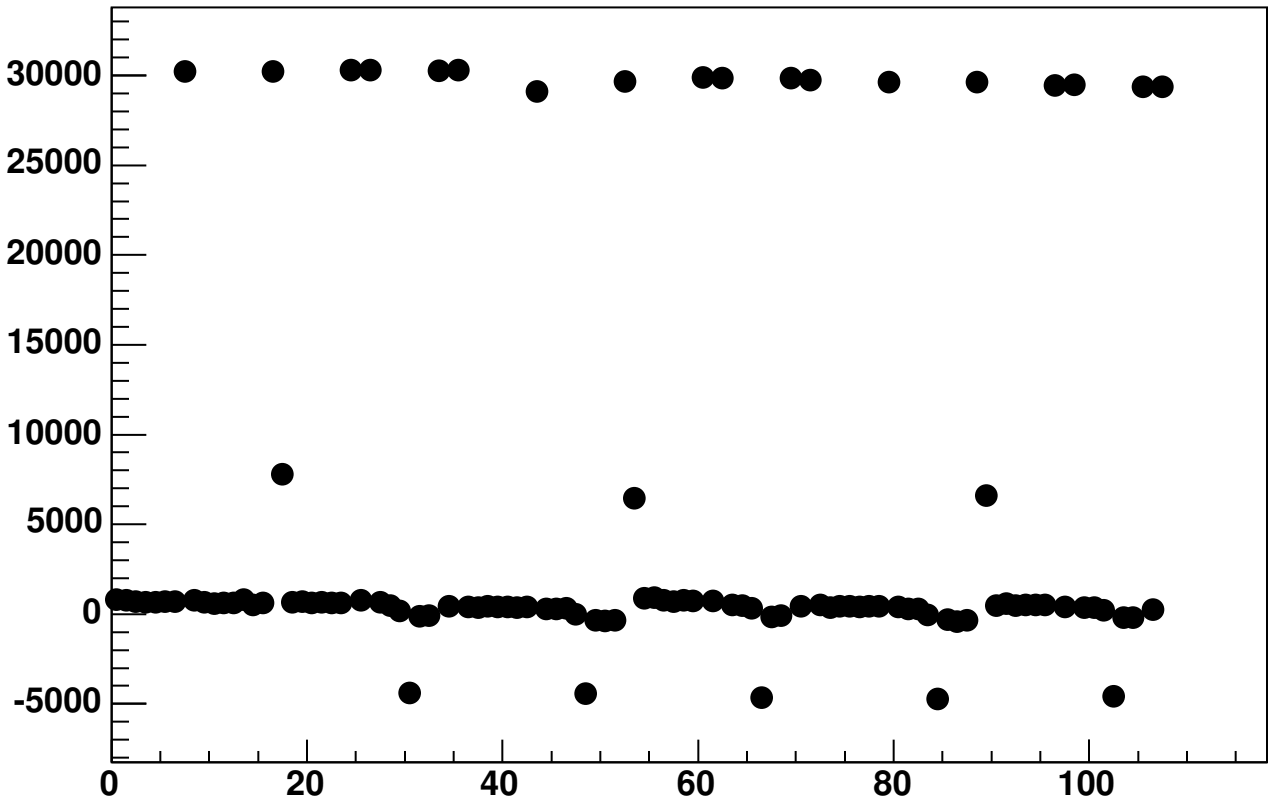
Enable 5, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



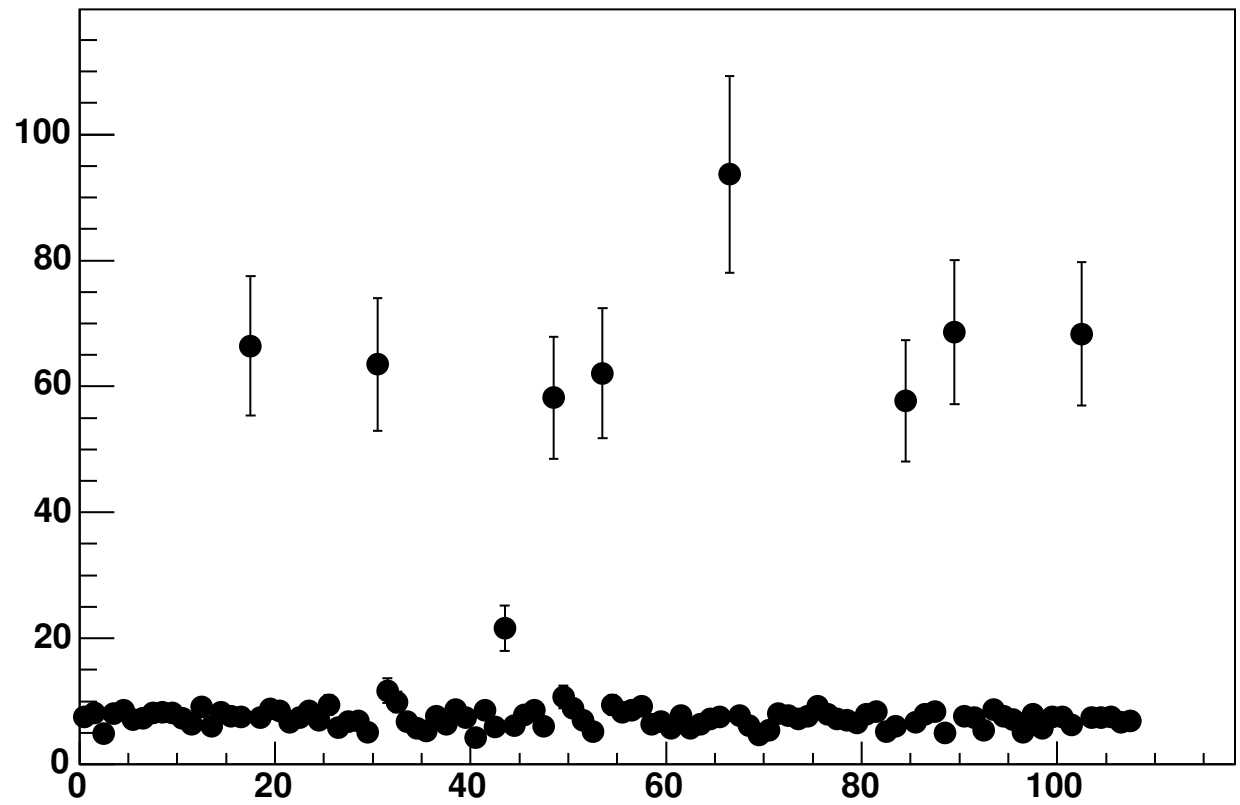
Enable 5, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



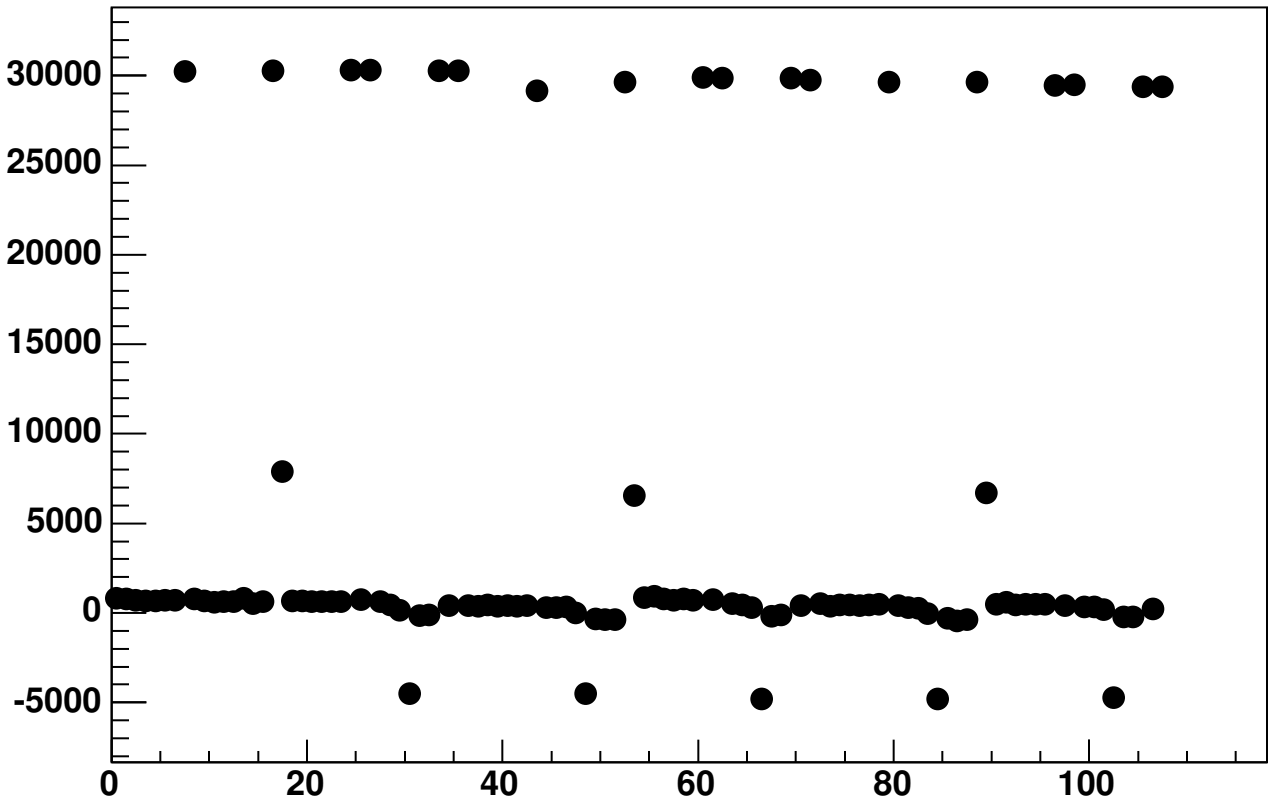
Enable 5, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



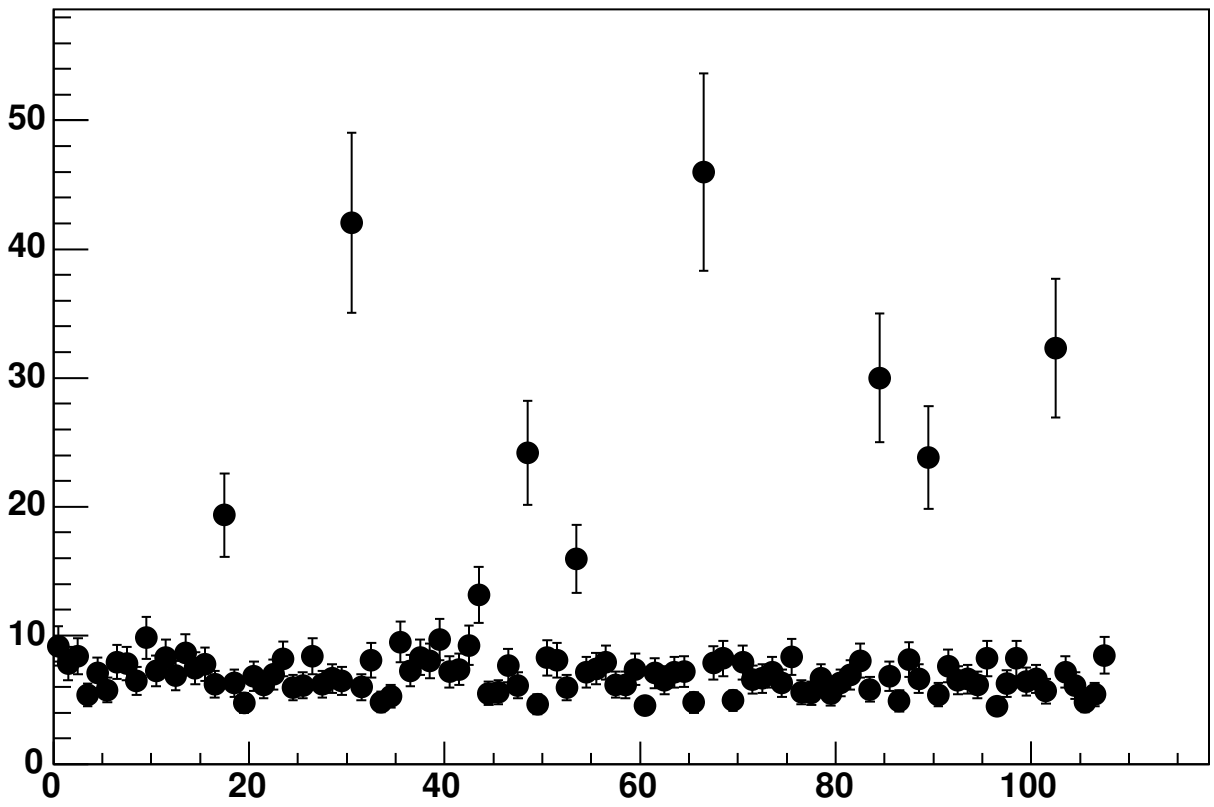
Enable 5, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



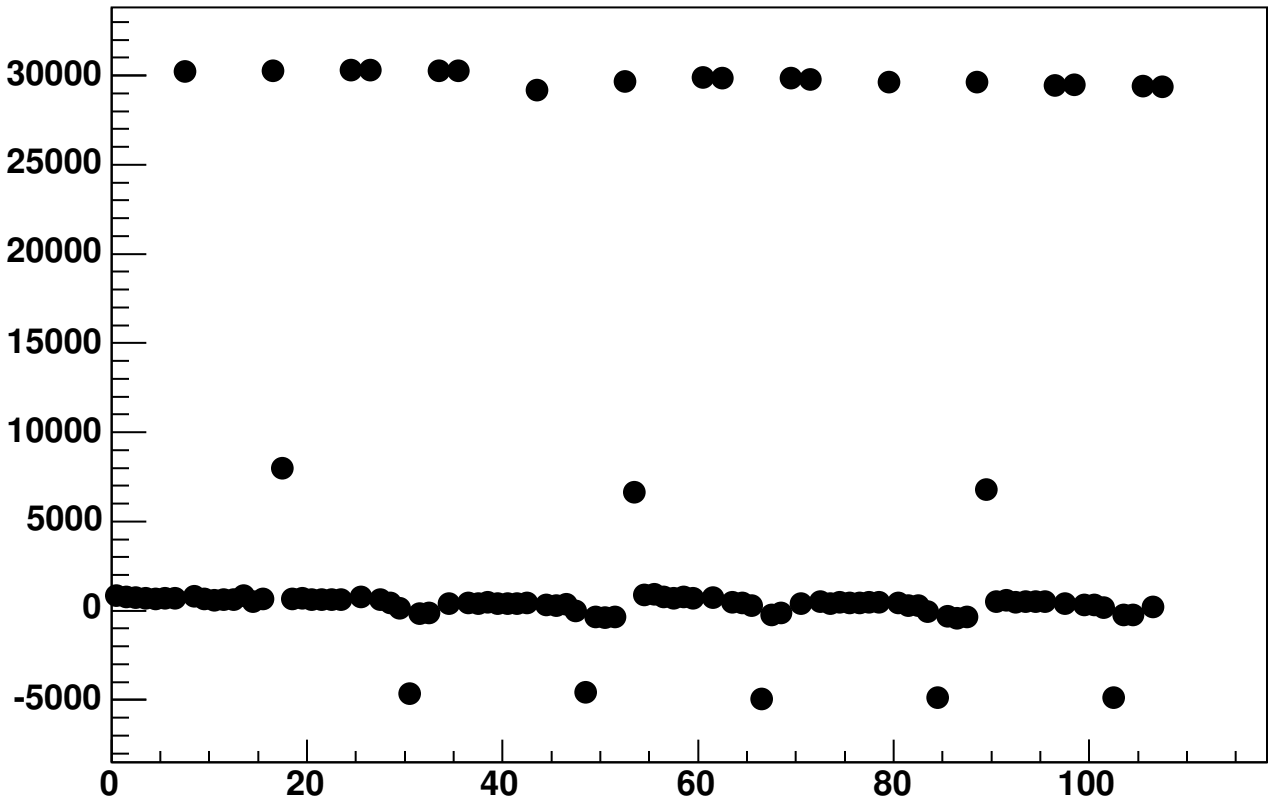
Enable 5, Hold=35, DAC=14400, ADC Mean vs 18*Chip+Chan



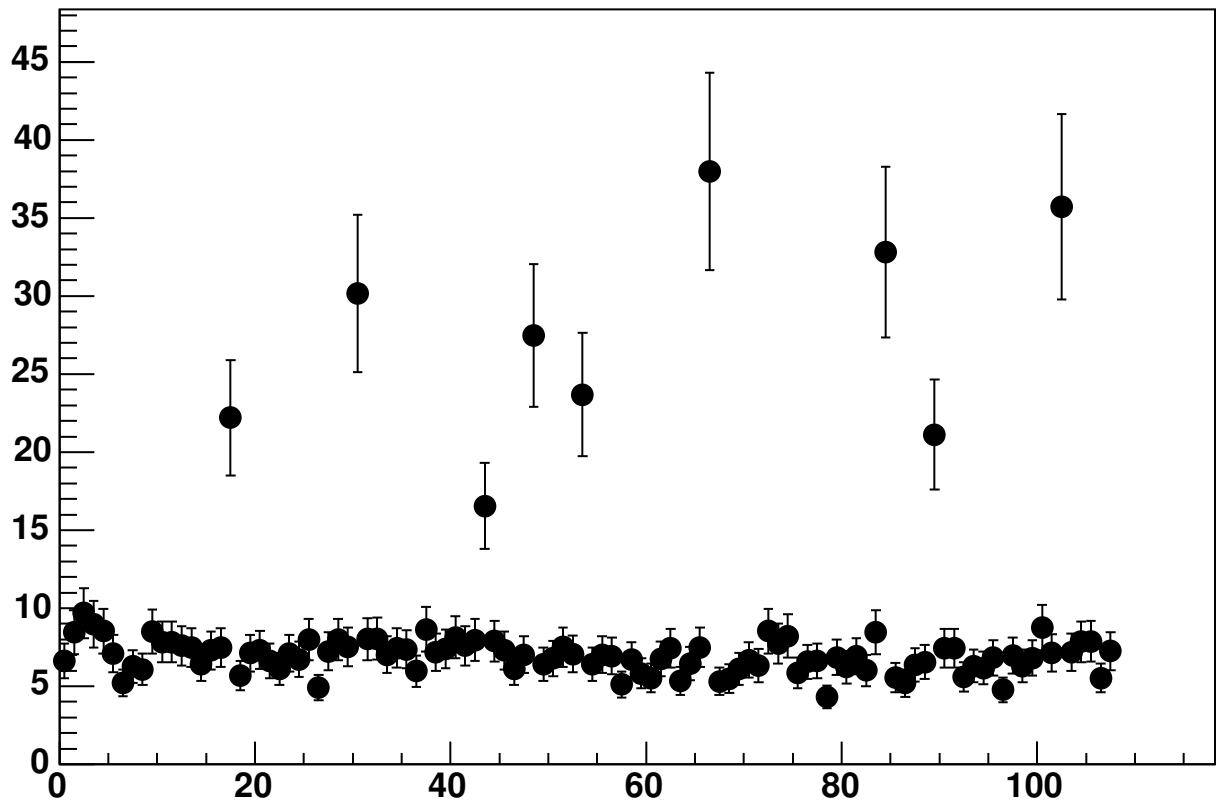
Enable 5, Hold=35, DAC=14400, ADC Noise vs 18*Chip+Chan



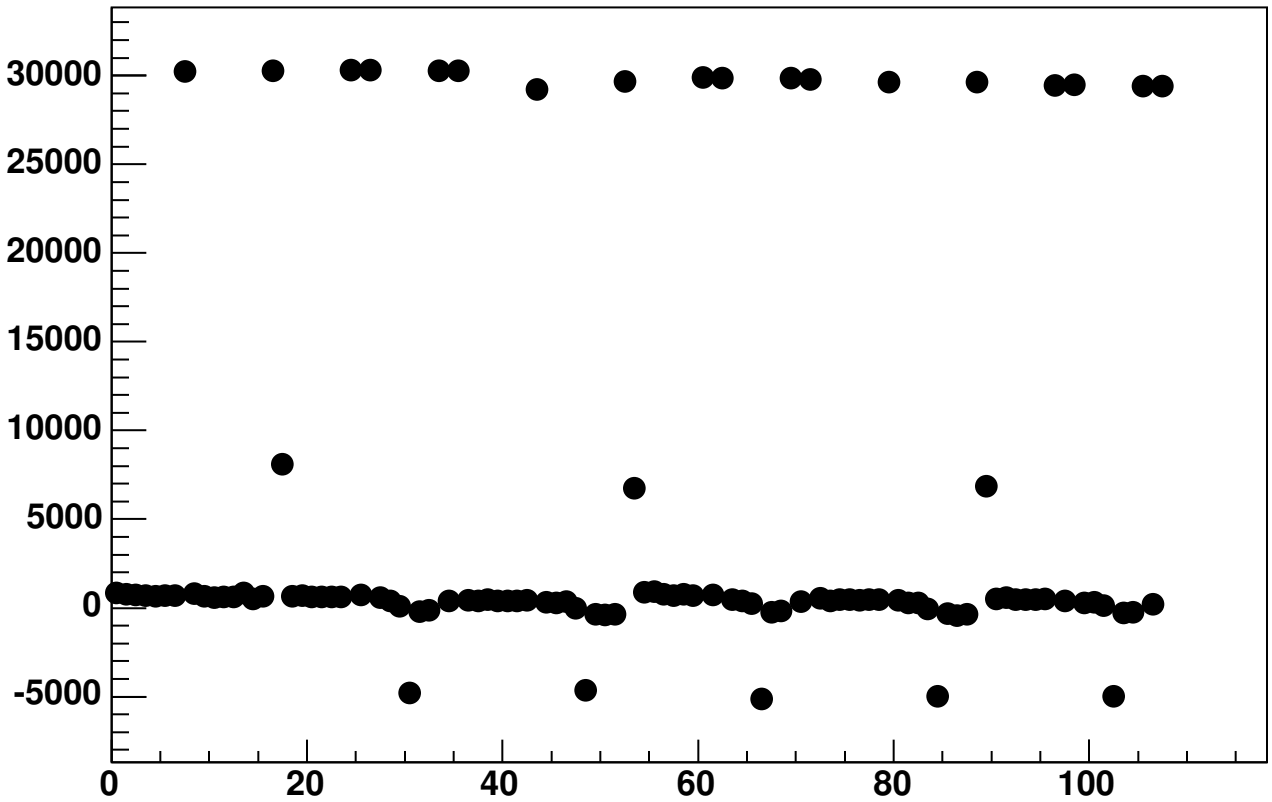
Enable 5, Hold=35, DAC=14800, ADC Mean vs 18*Chip+Chan



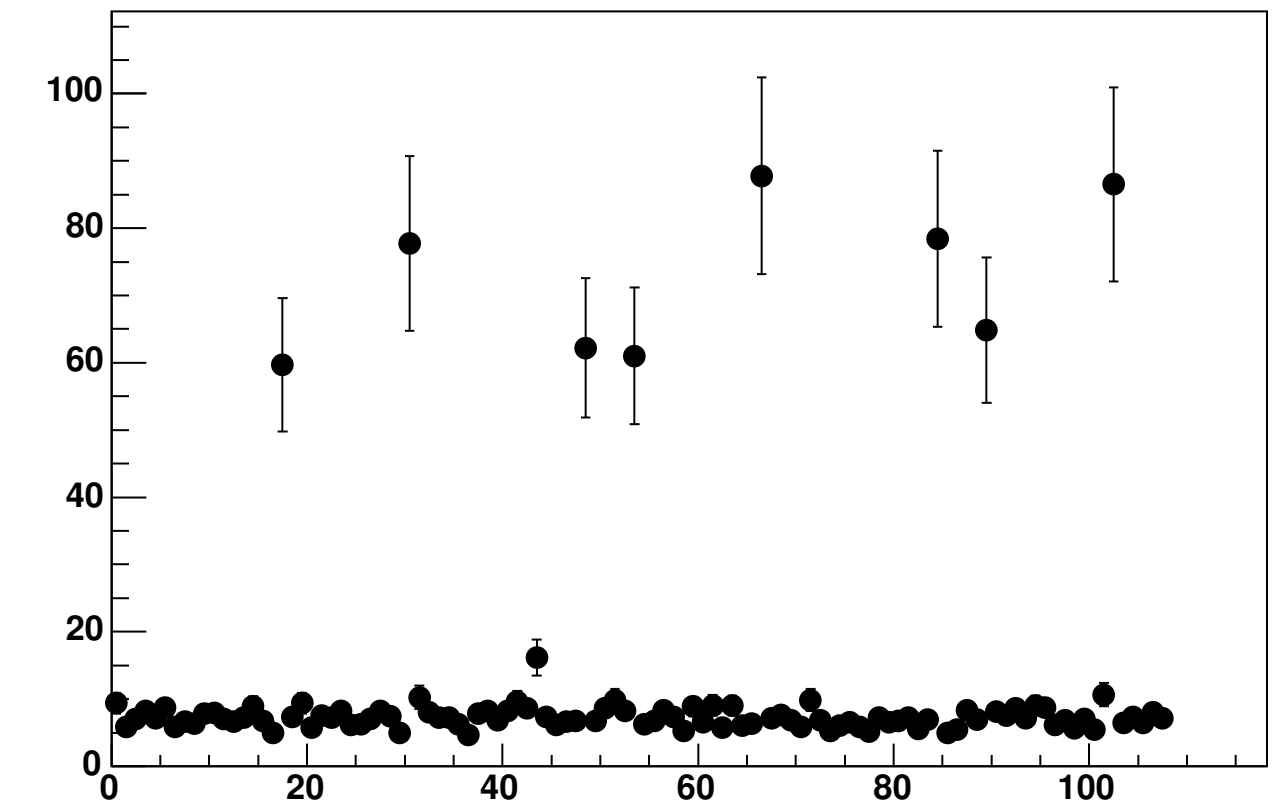
Enable 5, Hold=35, DAC=14800, ADC Noise vs 18*Chip+Chan



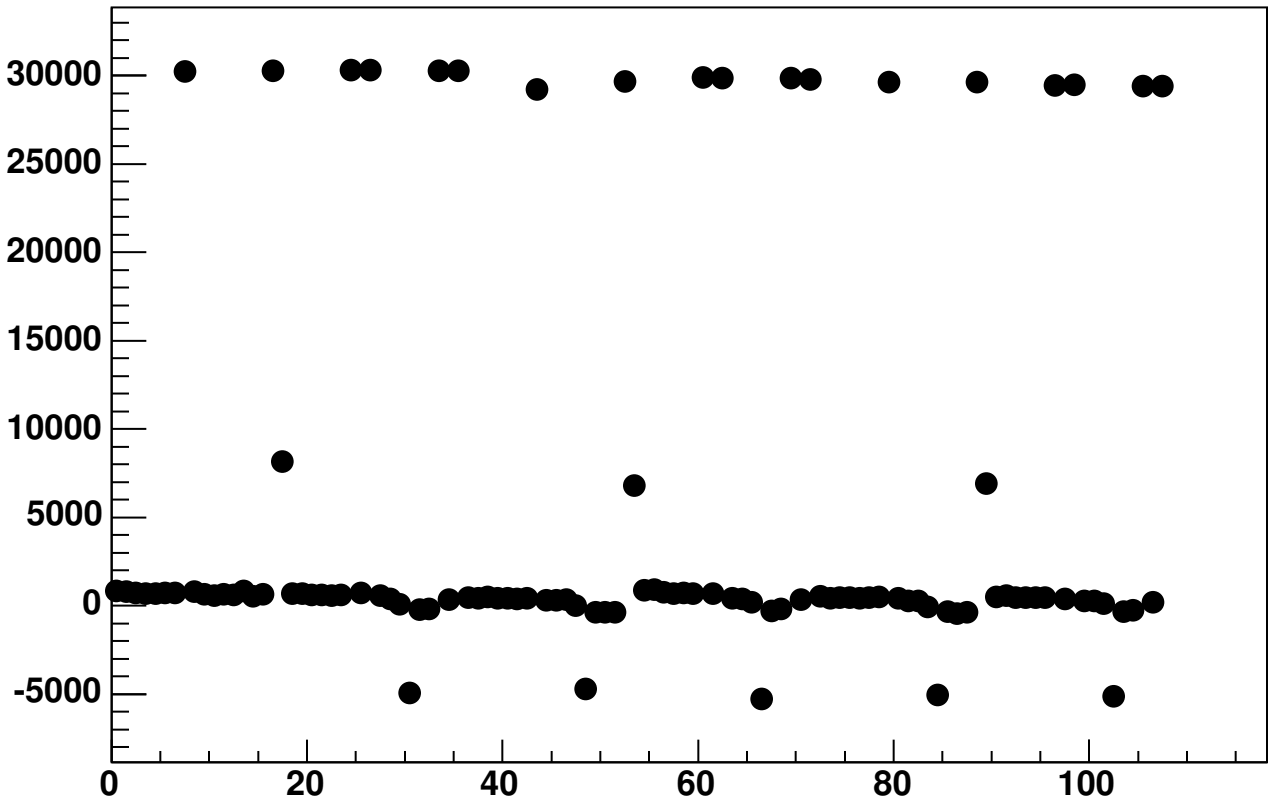
Enable 5, Hold=35, DAC=15200, ADC Mean vs 18*Chip+Chan



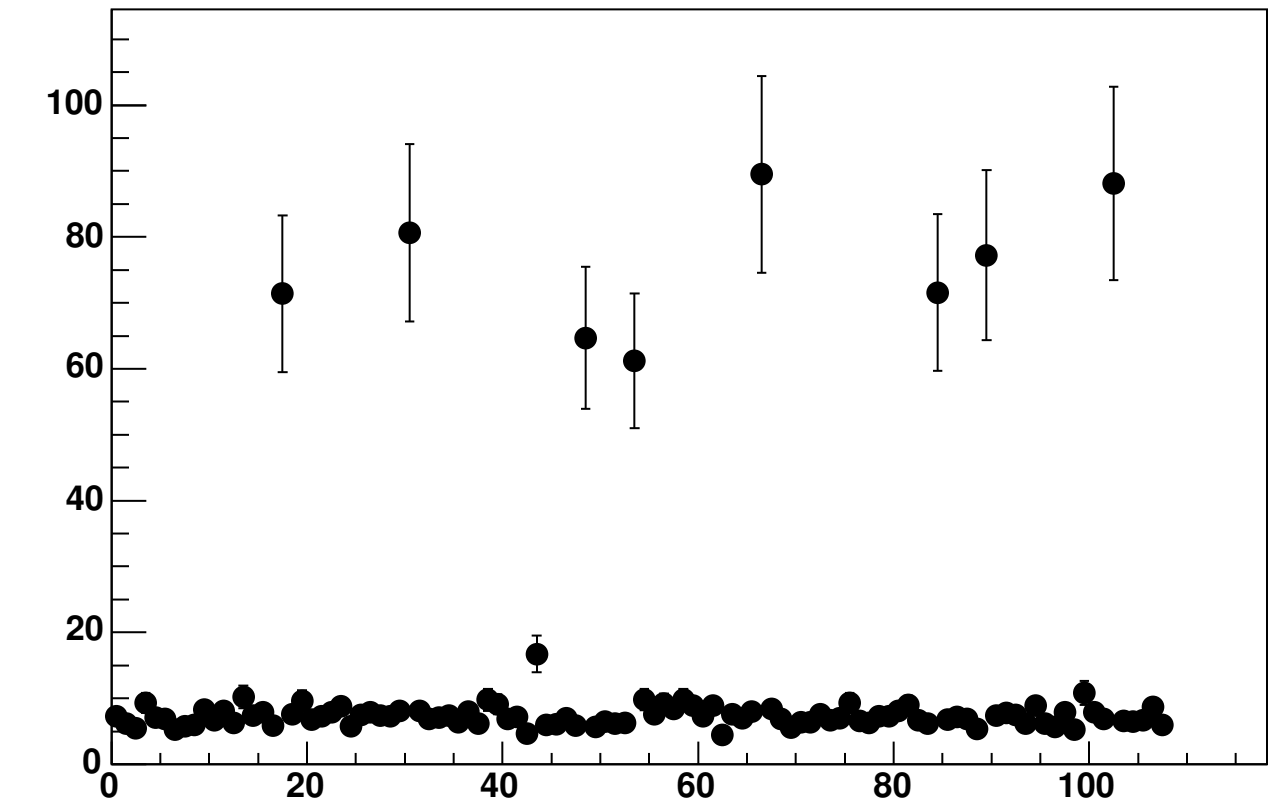
Enable 5, Hold=35, DAC=15200, ADC Noise vs 18*Chip+Chan



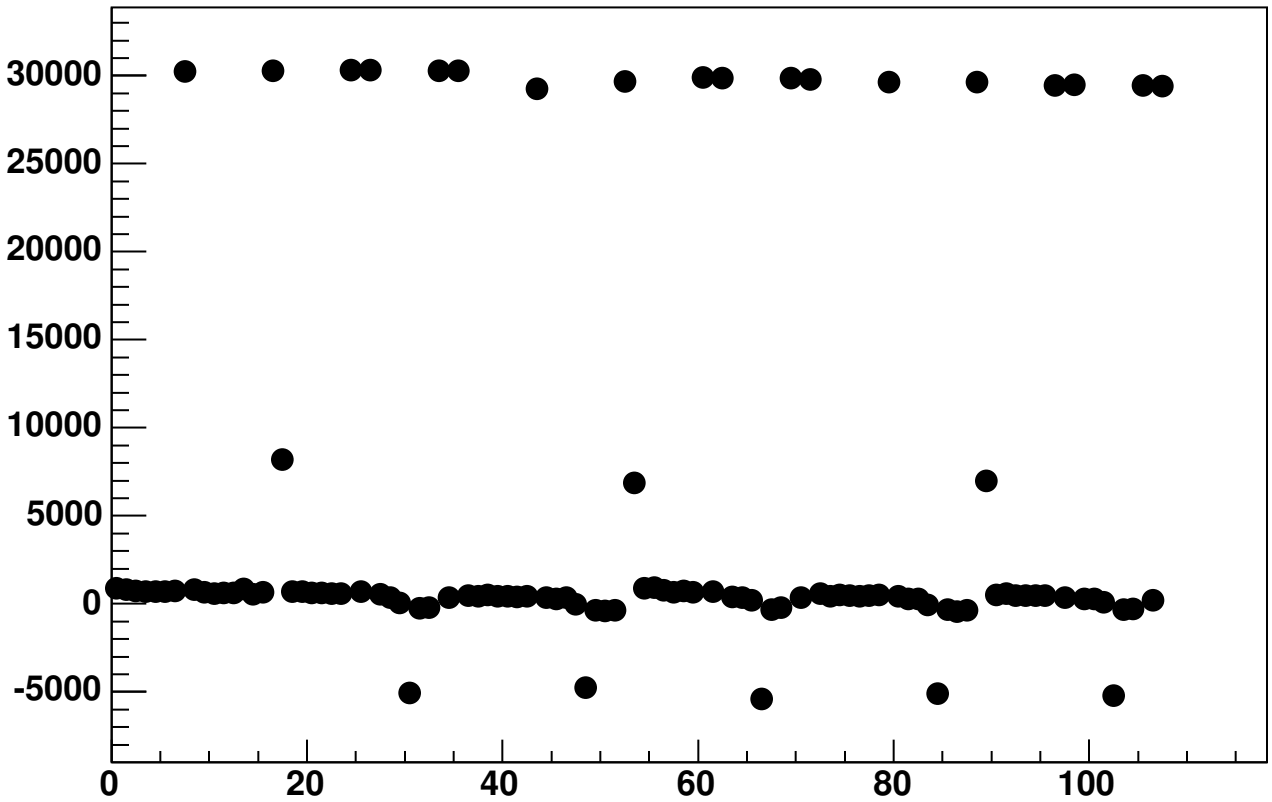
Enable 5, Hold=35, DAC=15600, ADC Mean vs 18*Chip+Chan



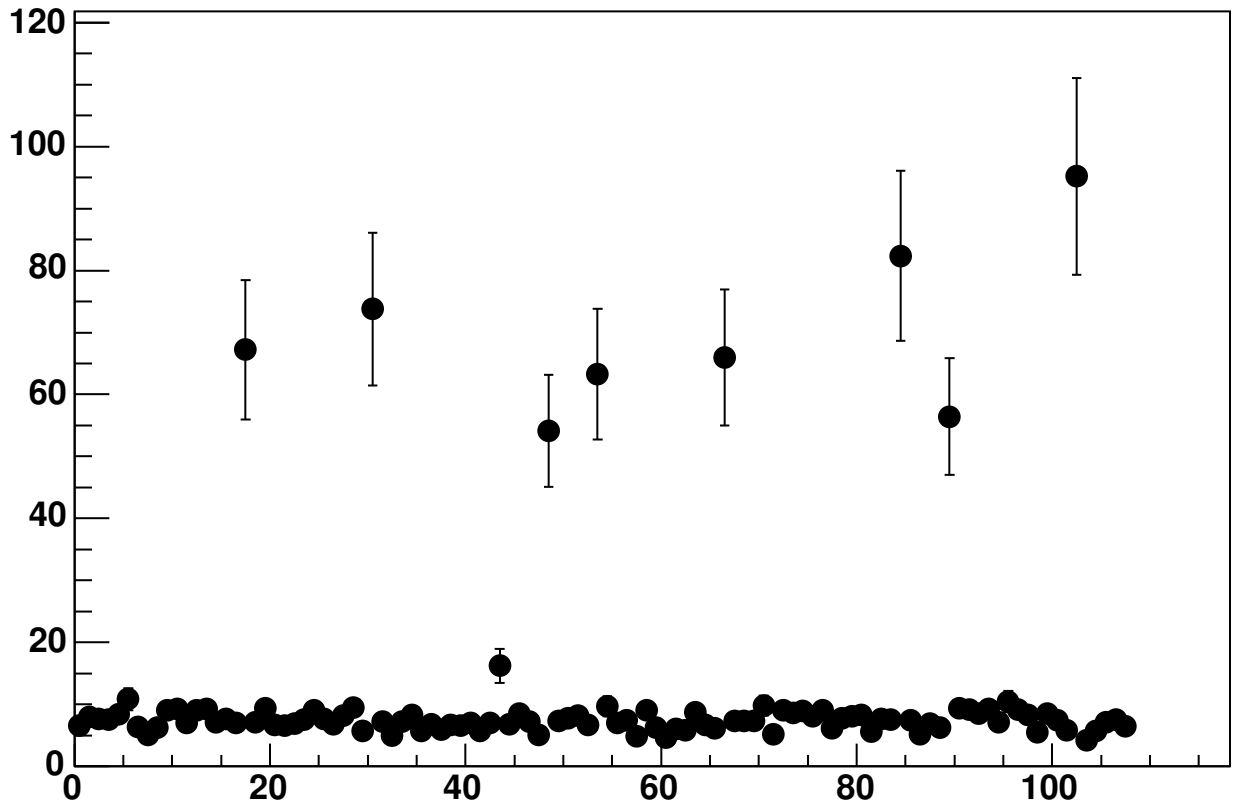
Enable 5, Hold=35, DAC=15600, ADC Noise vs 18*Chip+Chan



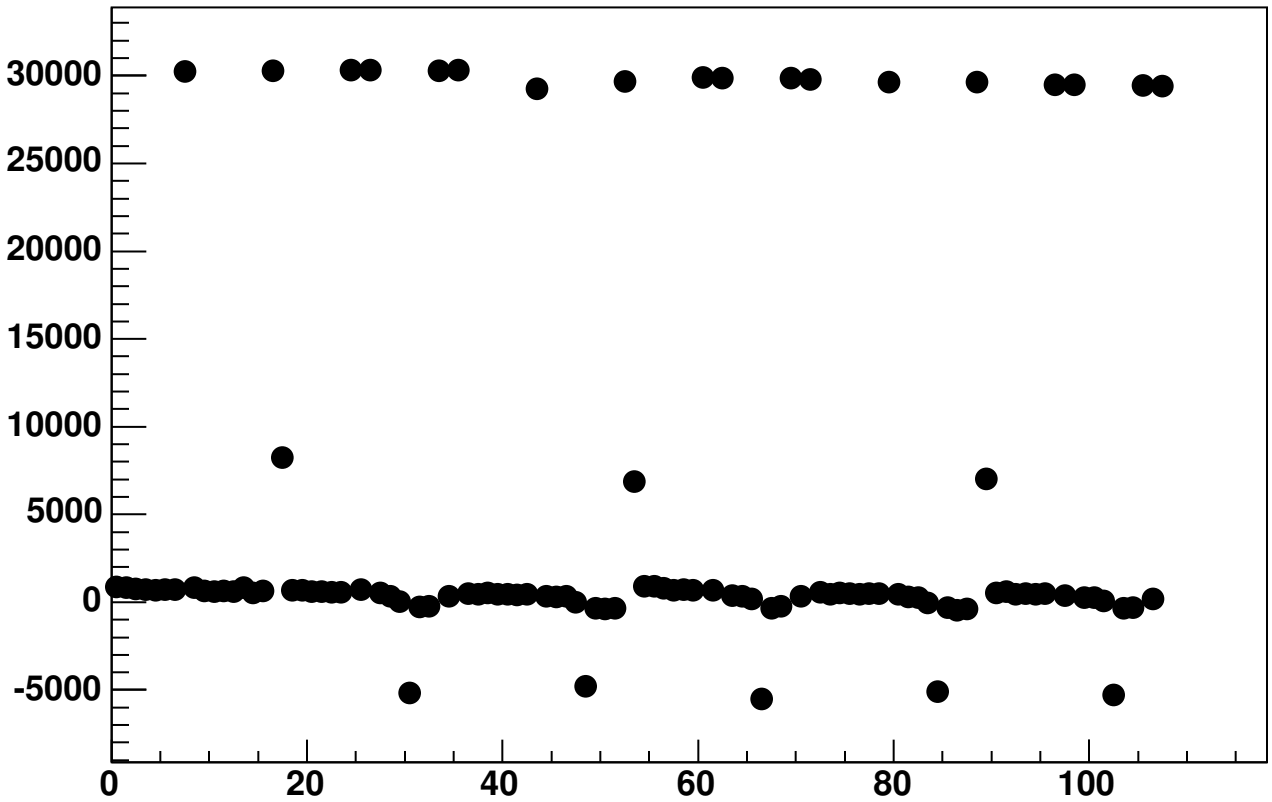
Enable 5, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



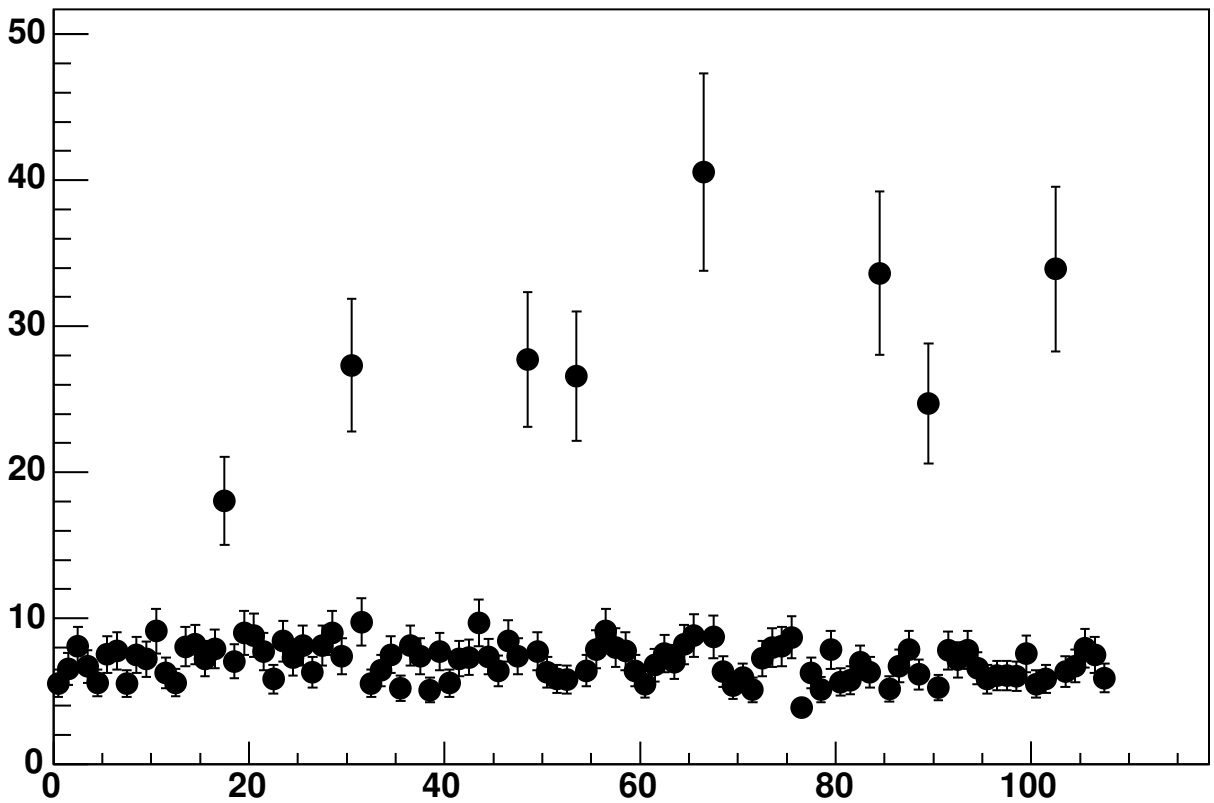
Enable 5, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



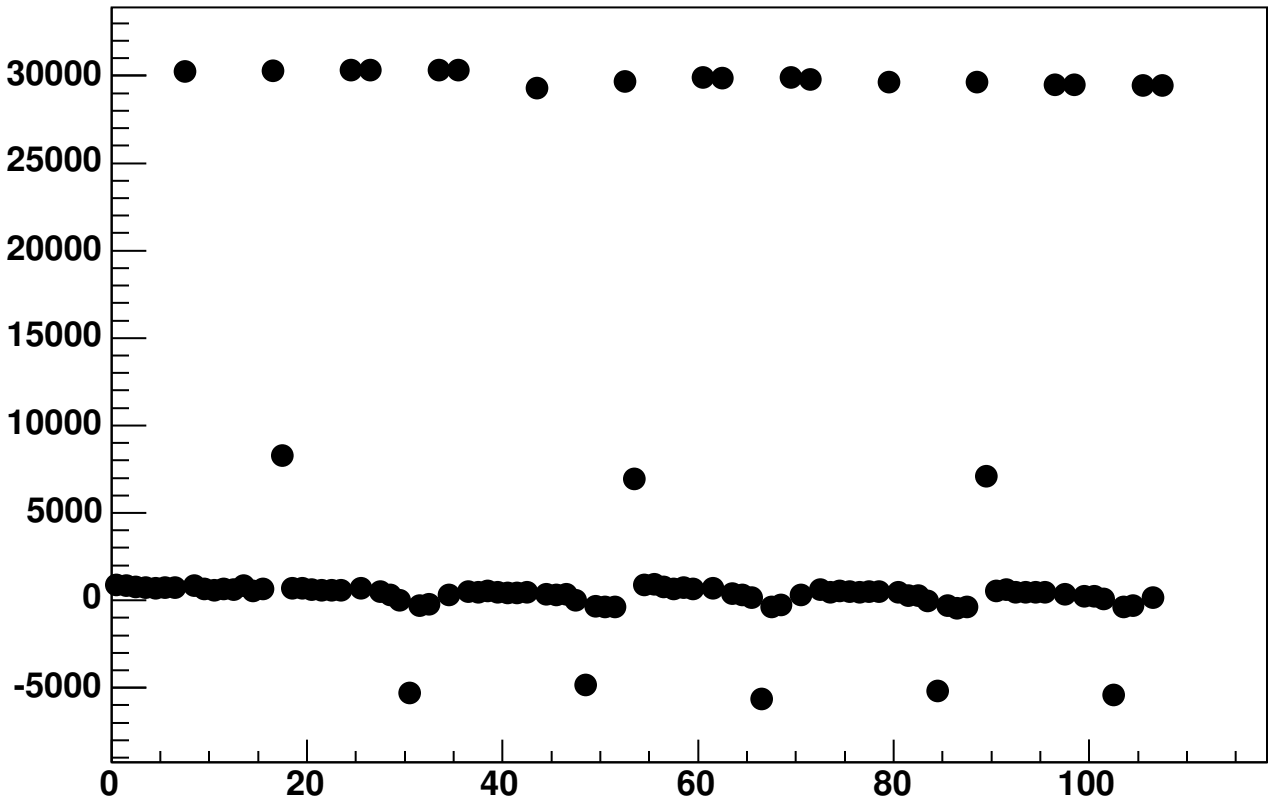
Enable 5, Hold=35, DAC=16400, ADC Mean vs 18*Chip+Chan



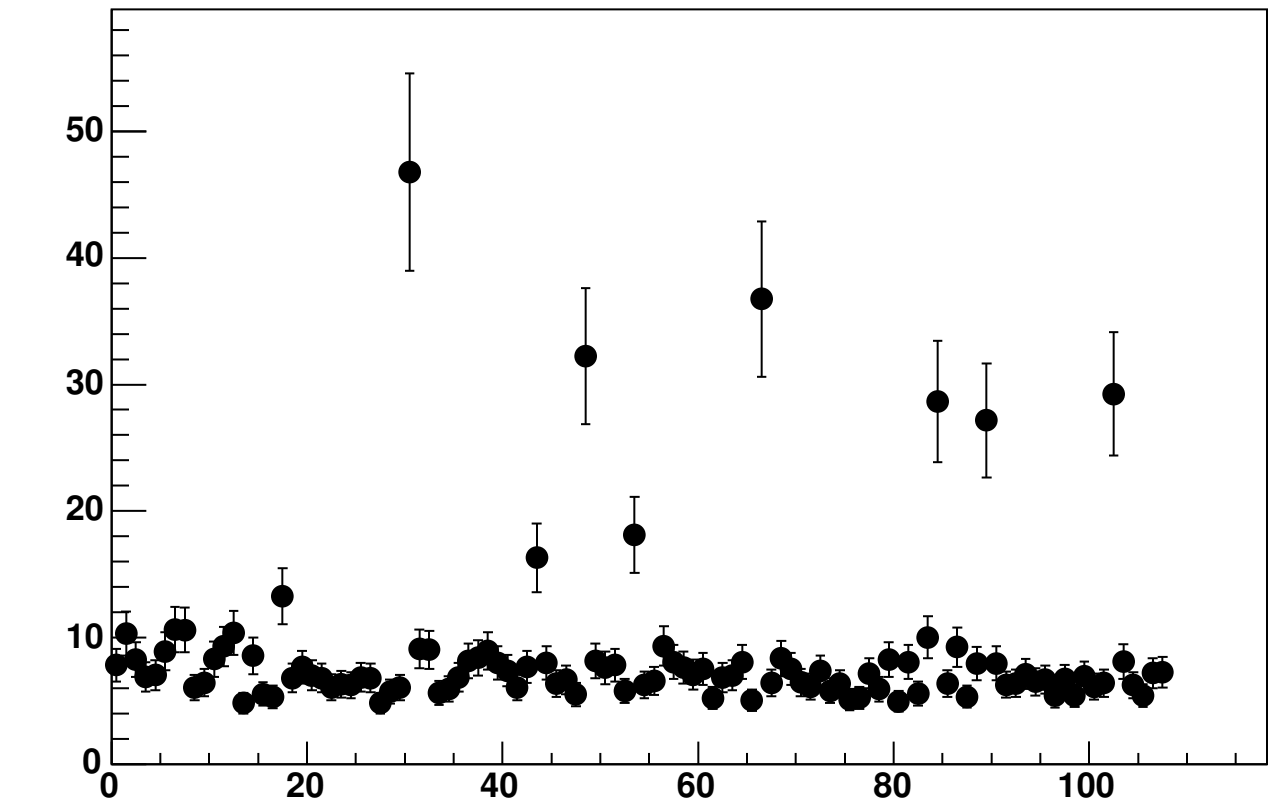
Enable 5, Hold=35, DAC=16400, ADC Noise vs 18*Chip+Chan



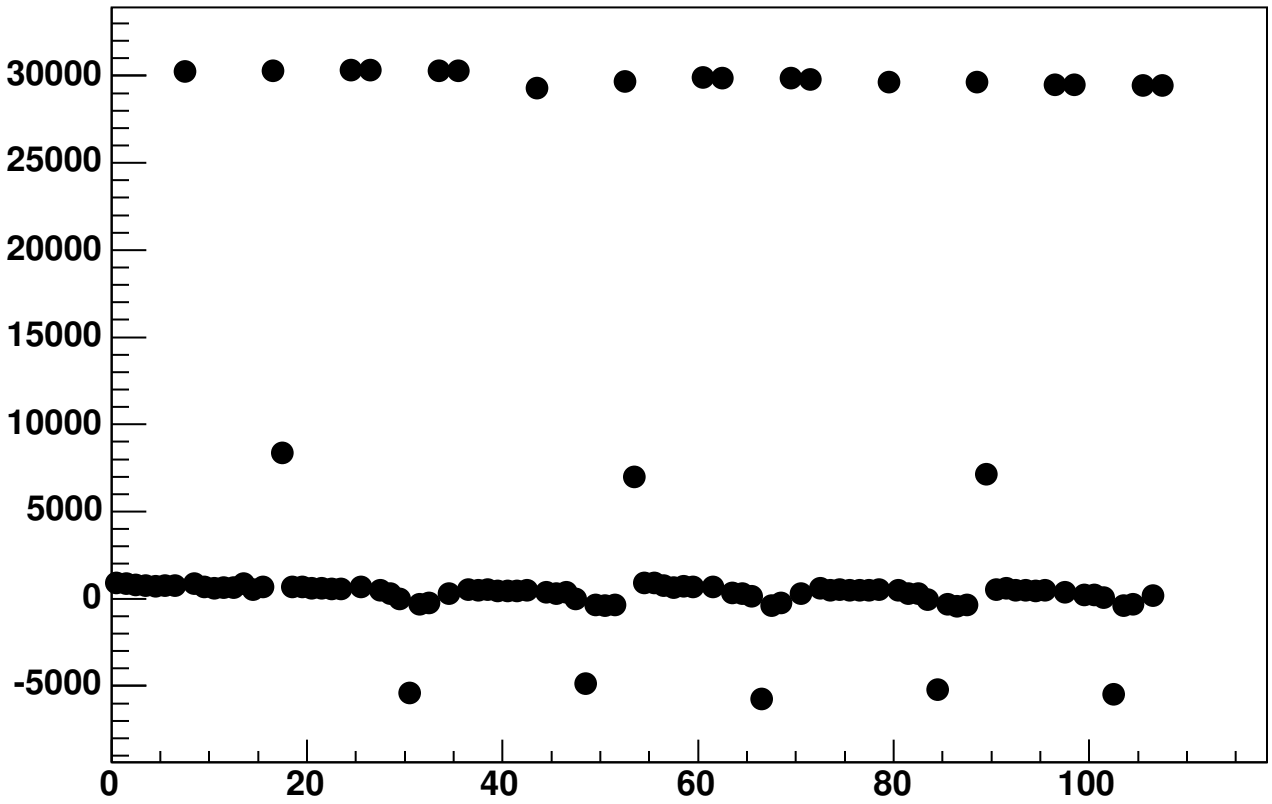
Enable 5, Hold=35, DAC=16800, ADC Mean vs 18*Chip+Chan



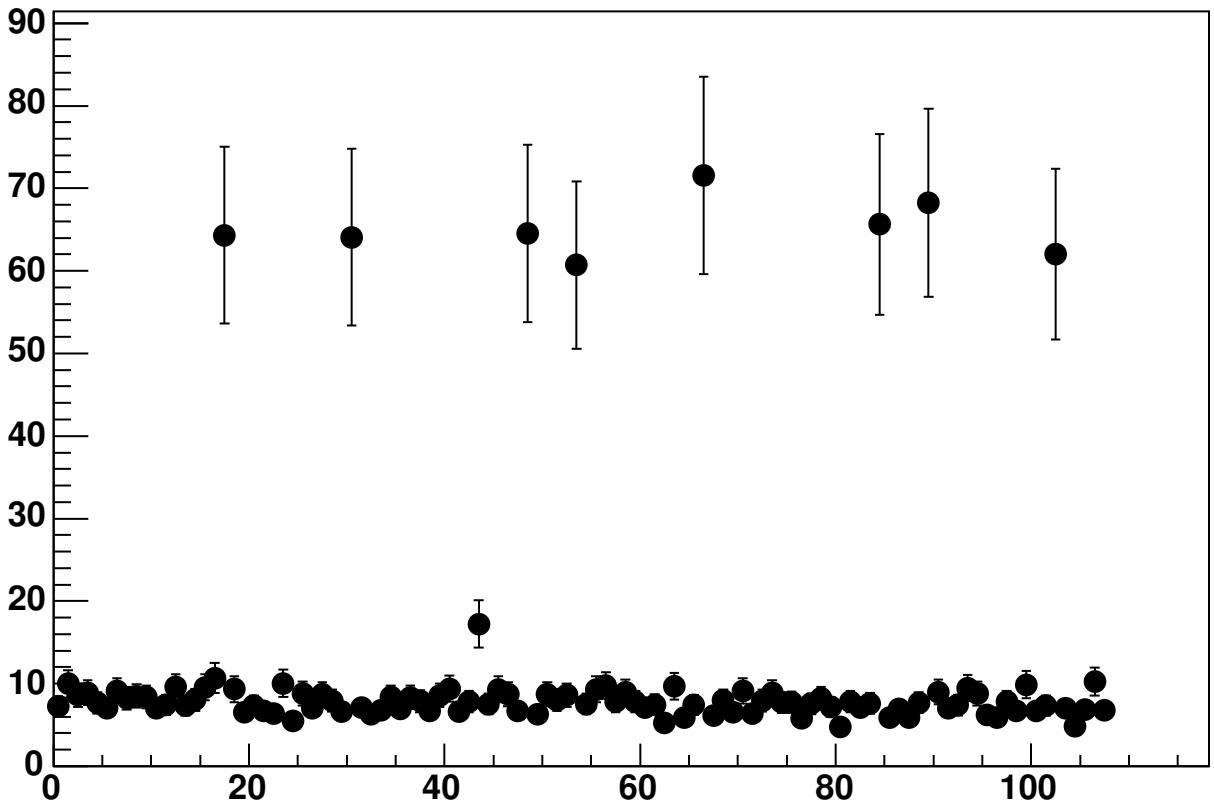
Enable 5, Hold=35, DAC=16800, ADC Noise vs 18*Chip+Chan



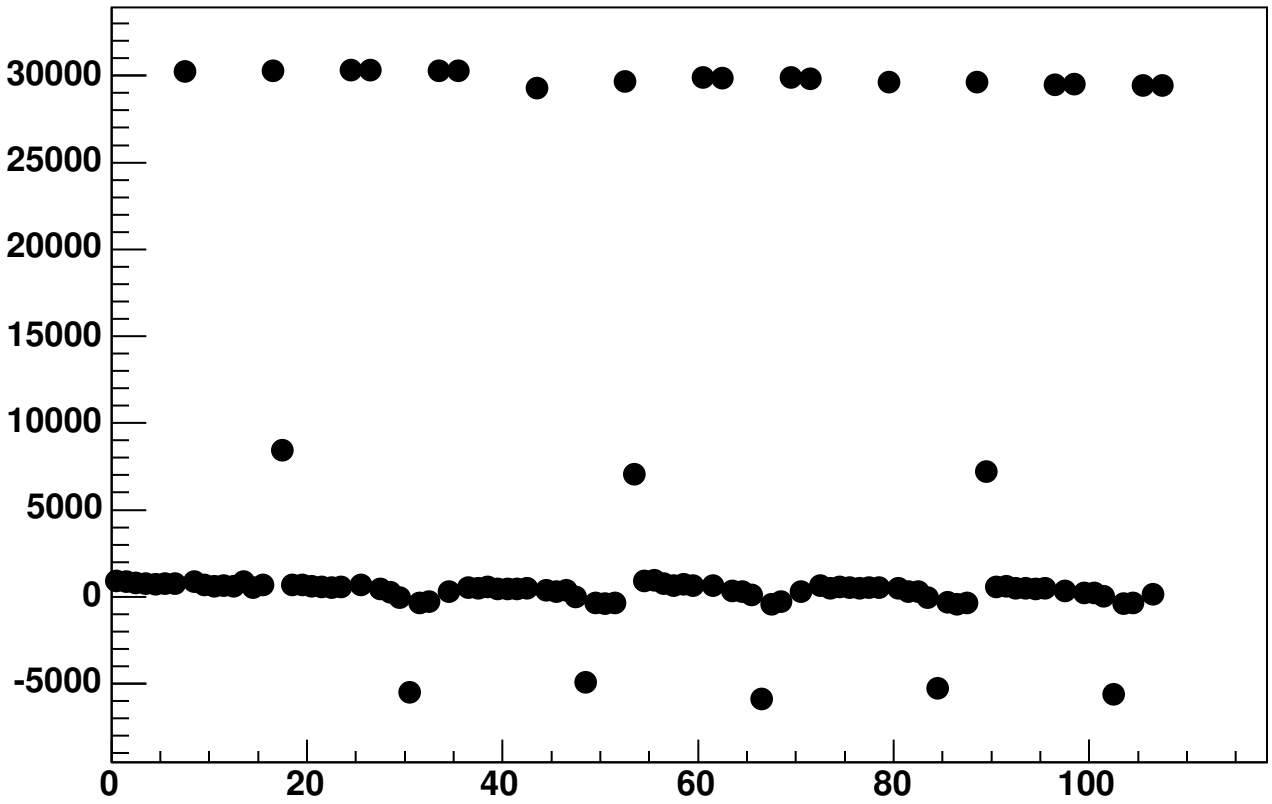
Enable 5, Hold=35, DAC=17200, ADC Mean vs 18*Chip+Chan



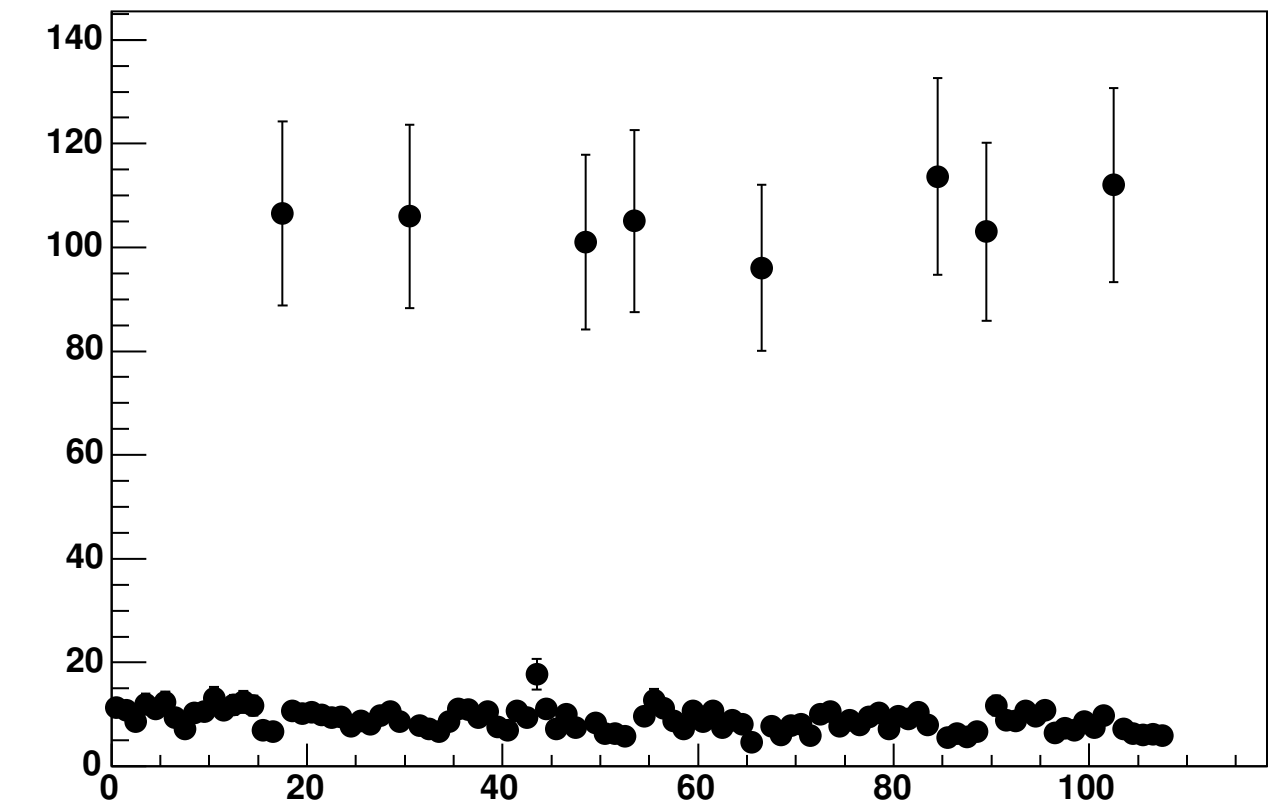
Enable 5, Hold=35, DAC=17200, ADC Noise vs 18*Chip+Chan



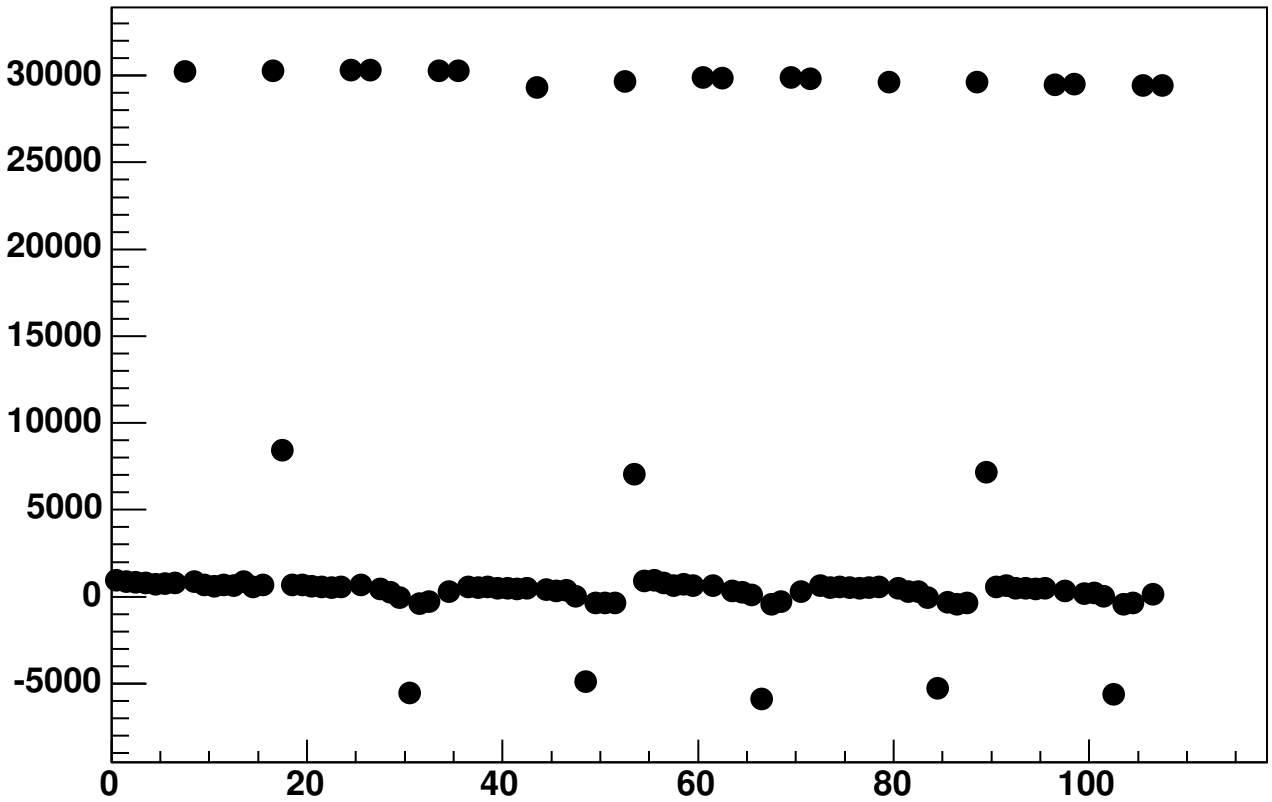
Enable 5, Hold=35, DAC=17600, ADC Mean vs 18*Chip+Chan



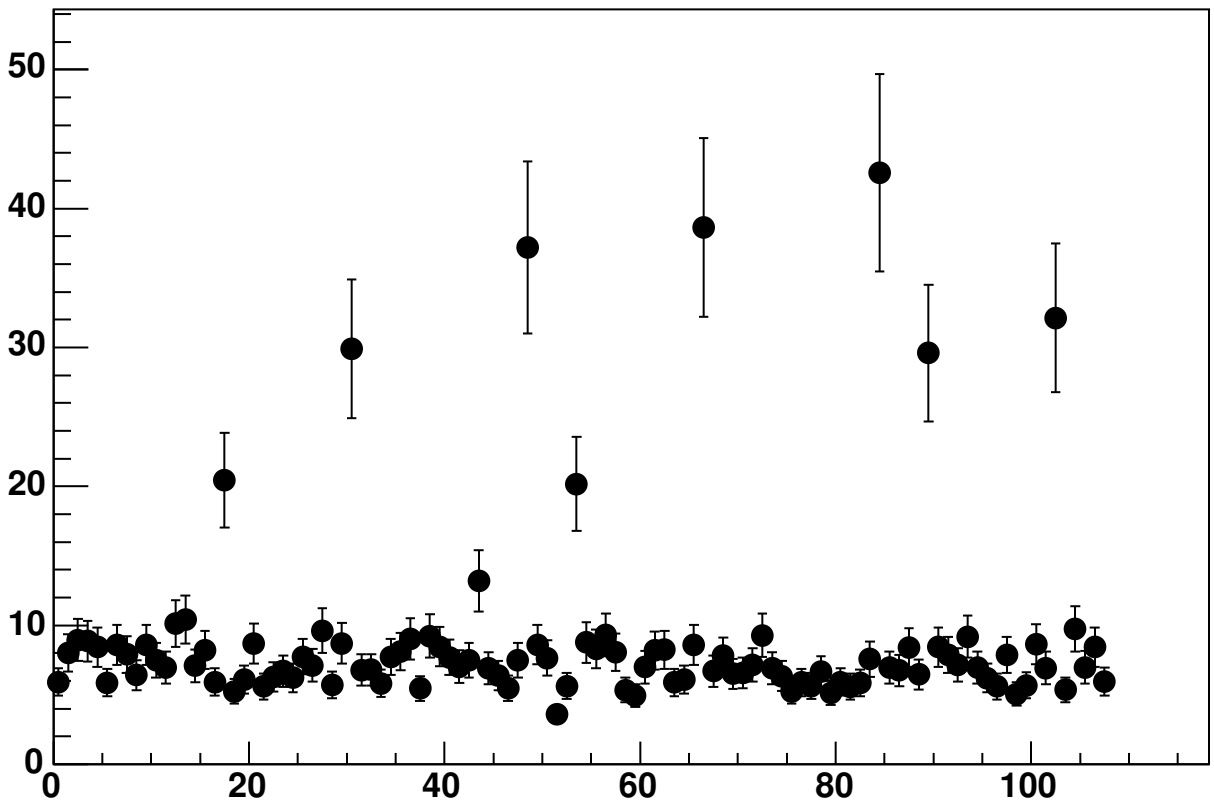
Enable 5, Hold=35, DAC=17600, ADC Noise vs 18*Chip+Chan



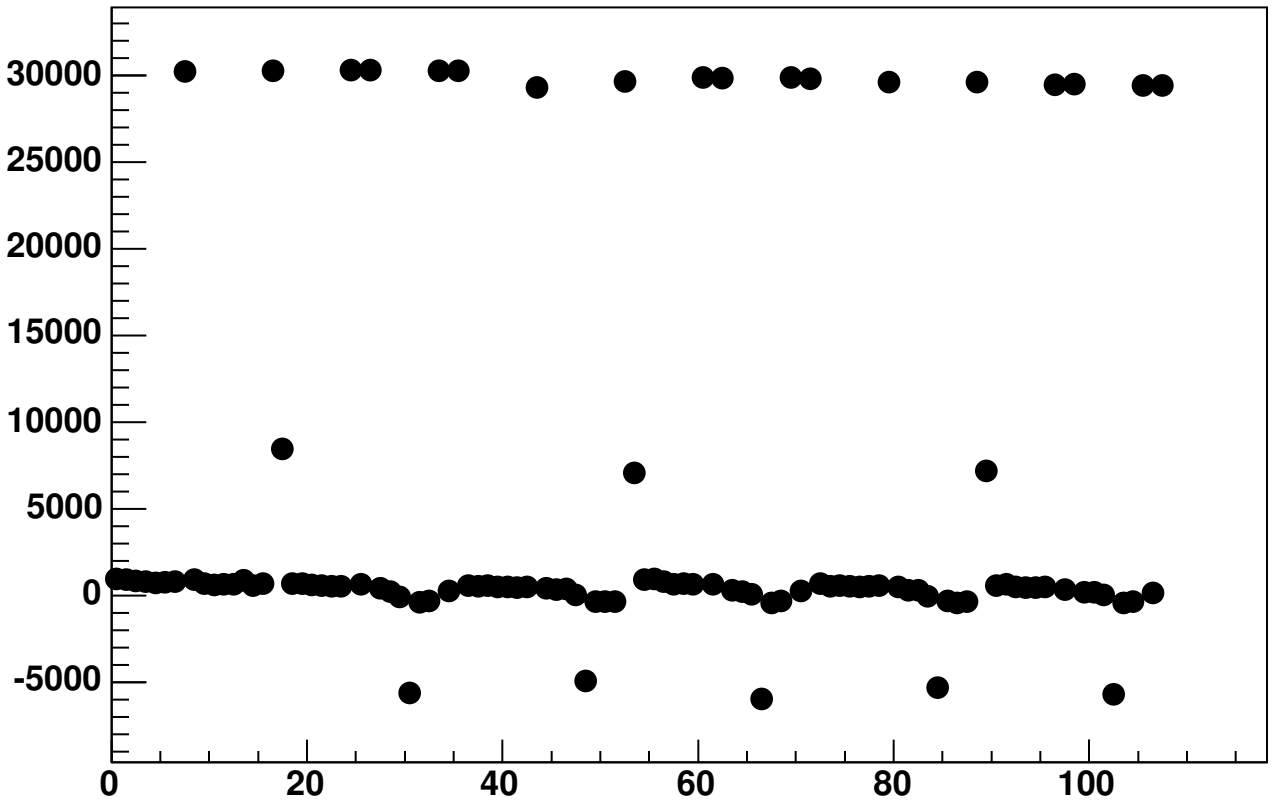
Enable 5, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



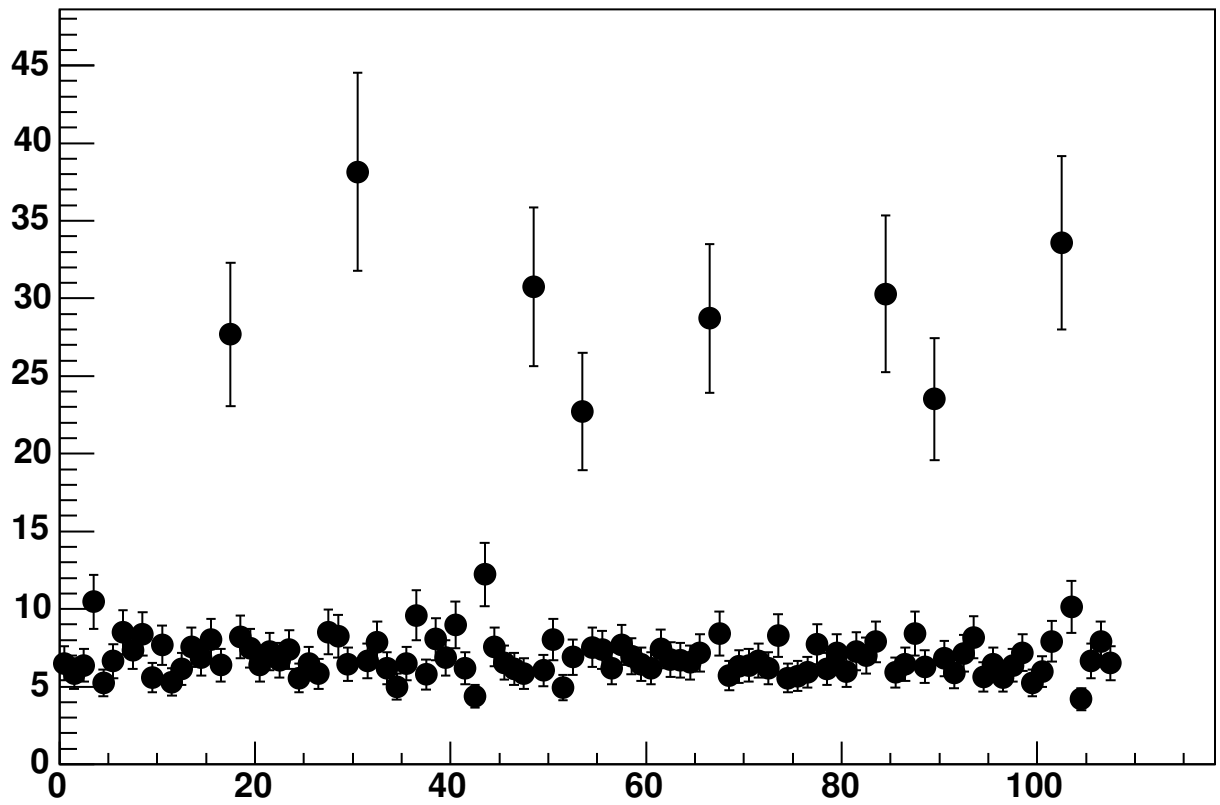
Enable 5, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



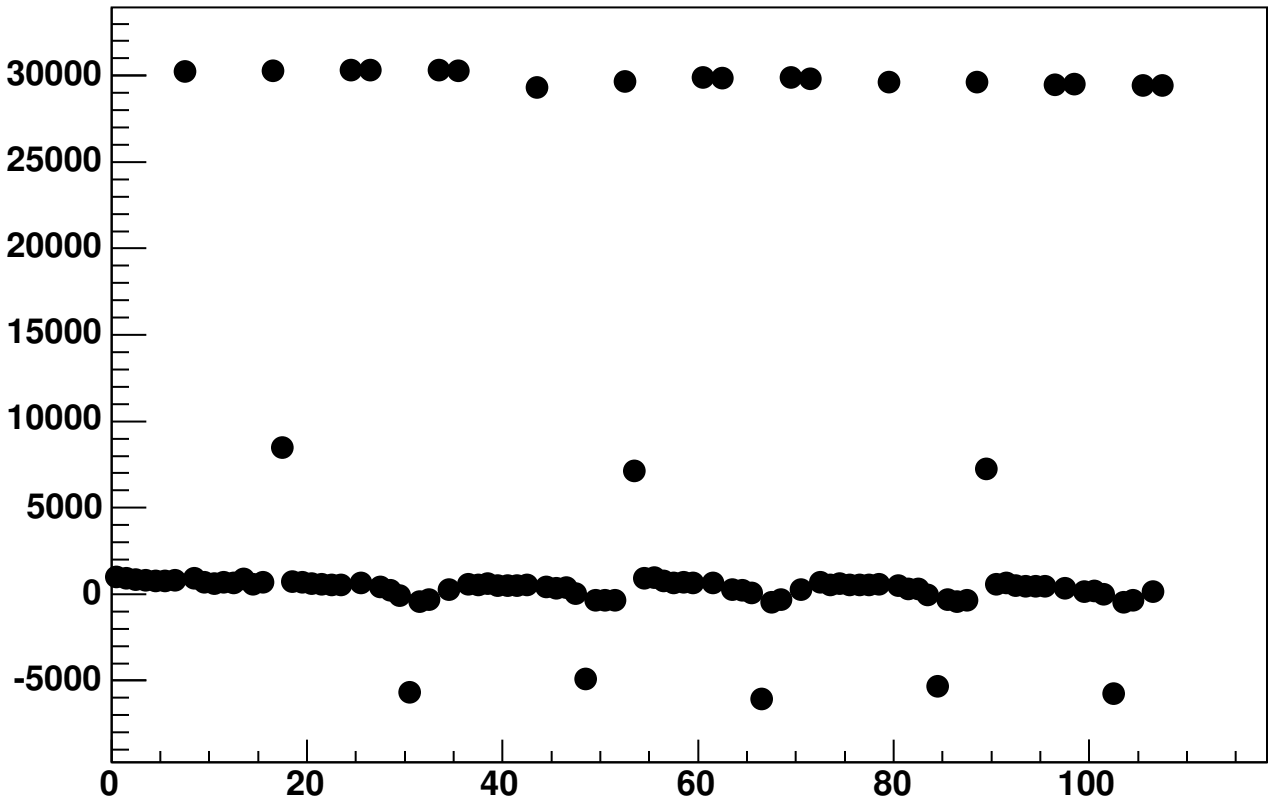
Enable 5, Hold=35, DAC=18400, ADC Mean vs 18*Chip+Chan



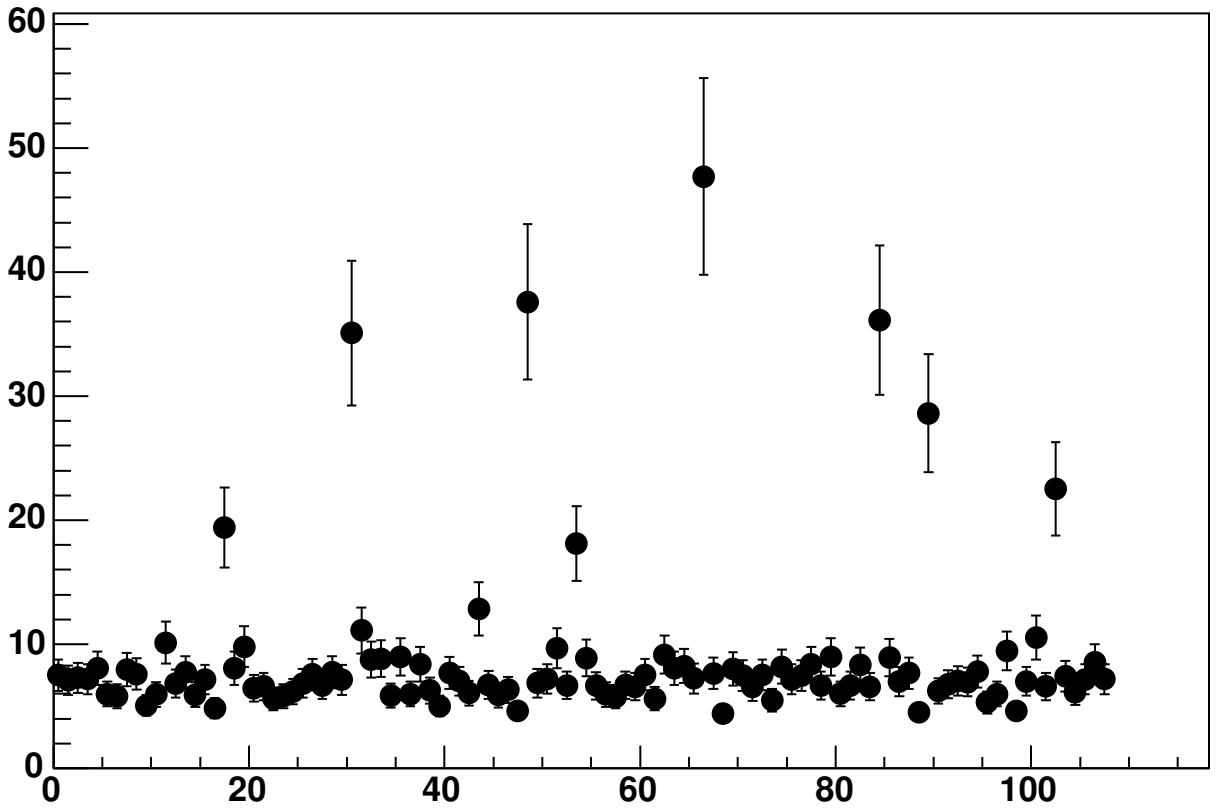
Enable 5, Hold=35, DAC=18400, ADC Noise vs 18*Chip+Chan



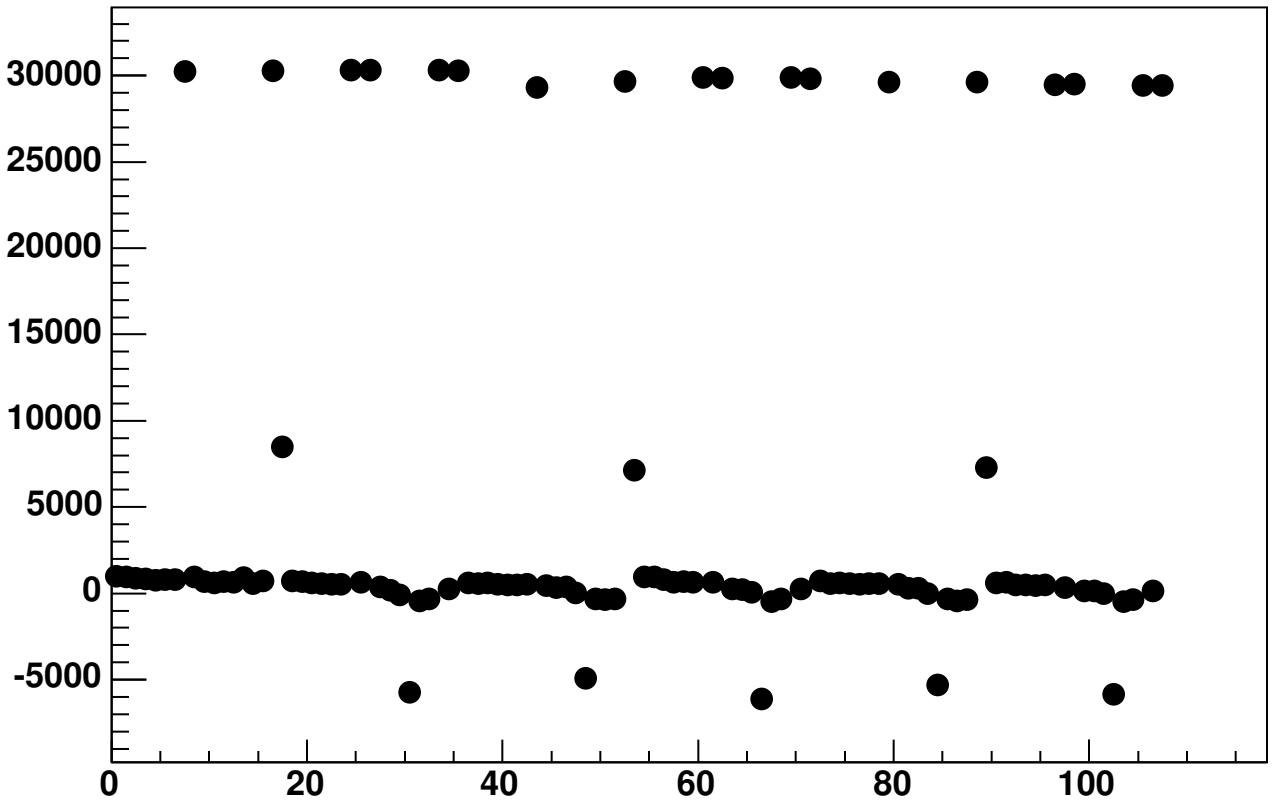
Enable 5, Hold=35, DAC=18800, ADC Mean vs 18*Chip+Chan



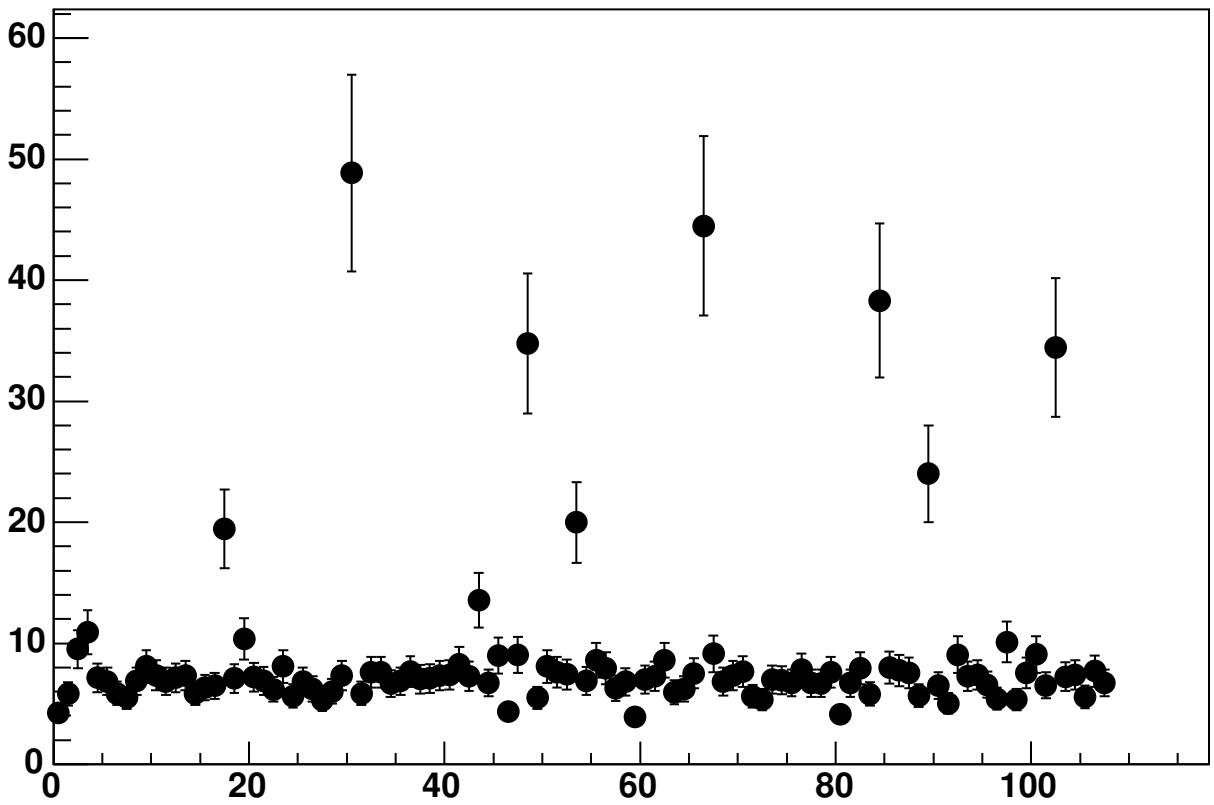
Enable 5, Hold=35, DAC=18800, ADC Noise vs 18*Chip+Chan

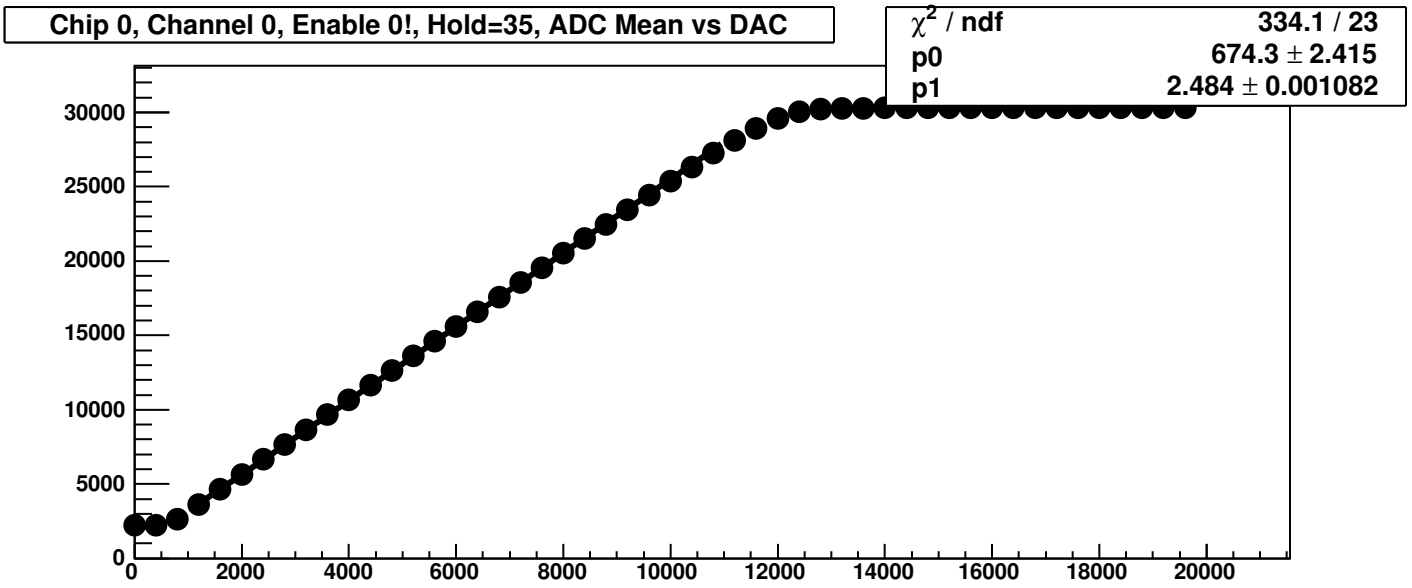


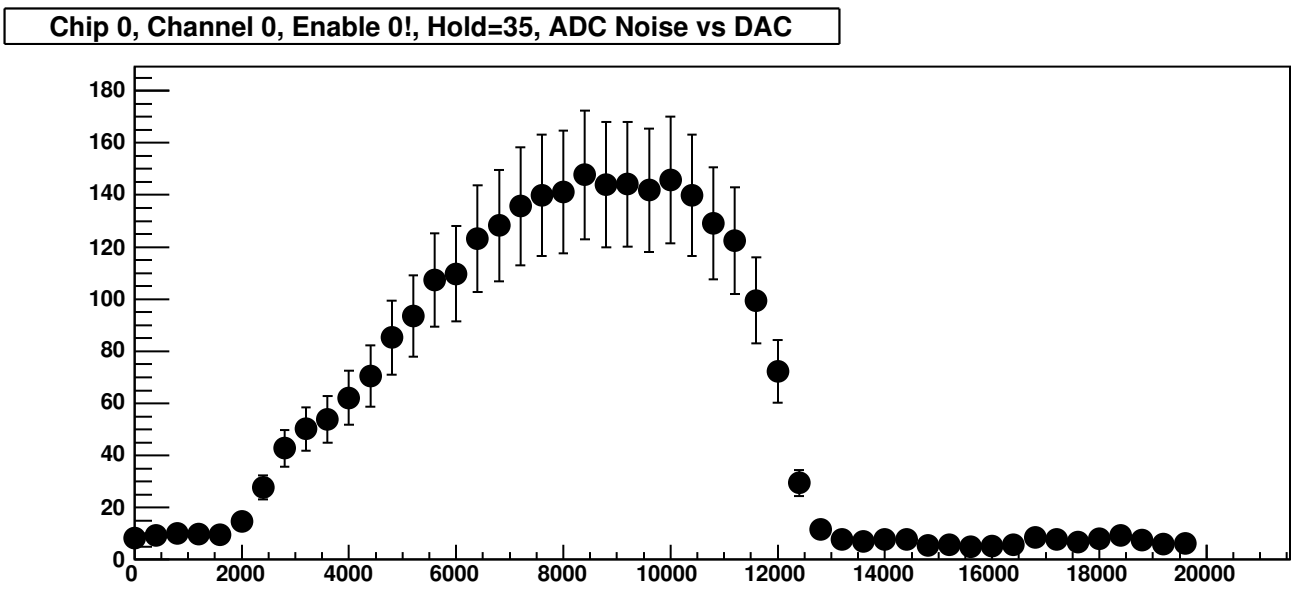
Enable 5, Hold=35, DAC=19200, ADC Mean vs 18*Chip+Chan

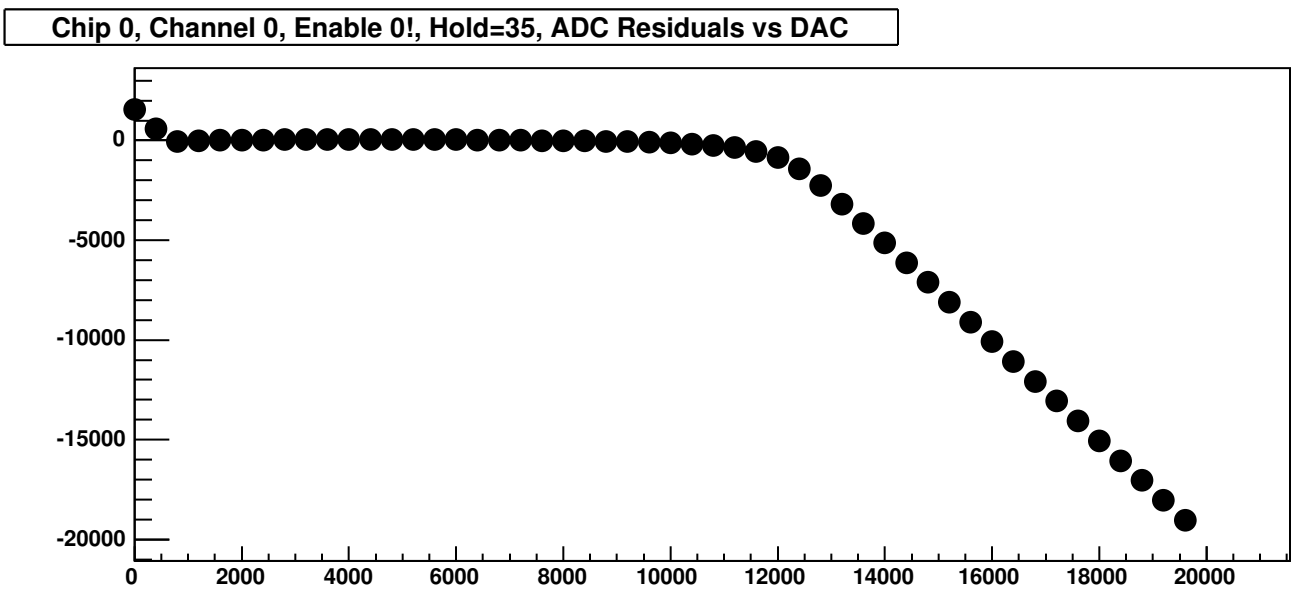


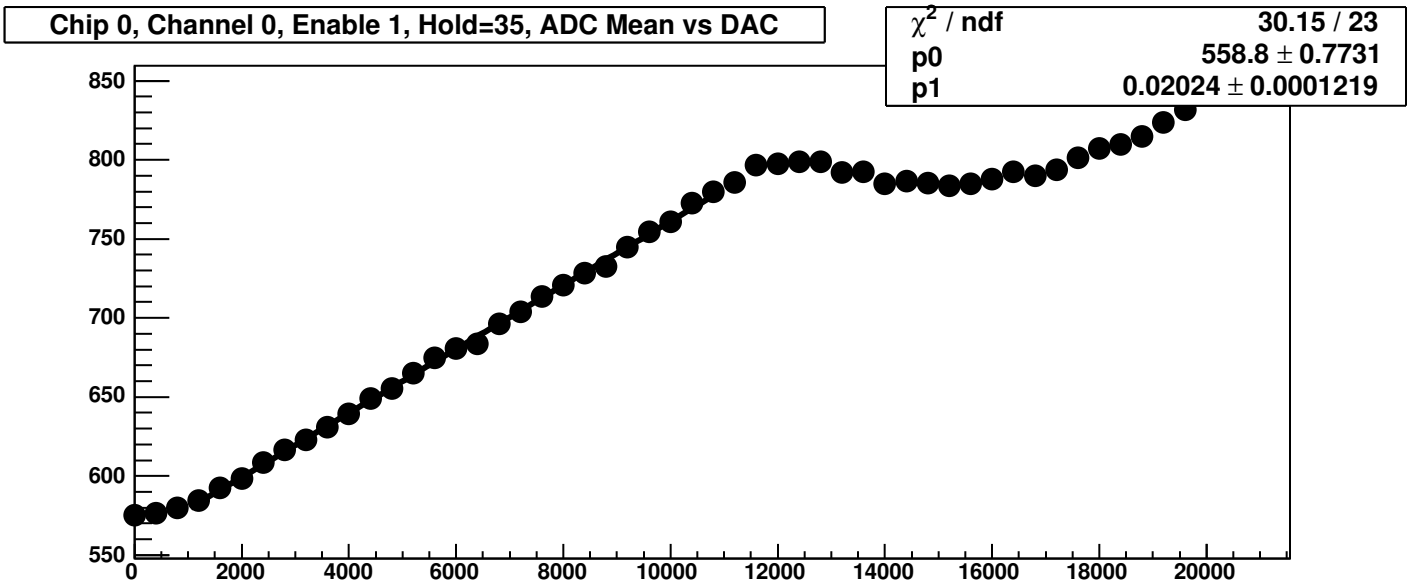
Enable 5, Hold=35, DAC=19200, ADC Noise vs 18*Chip+Chan

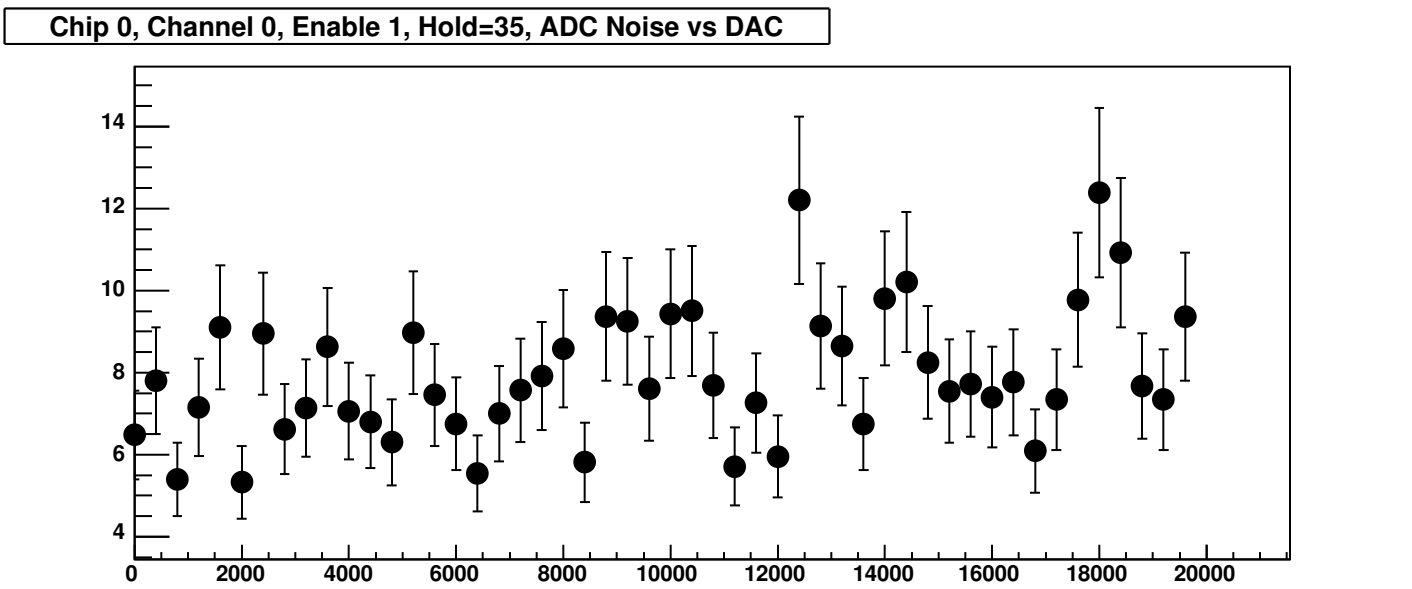


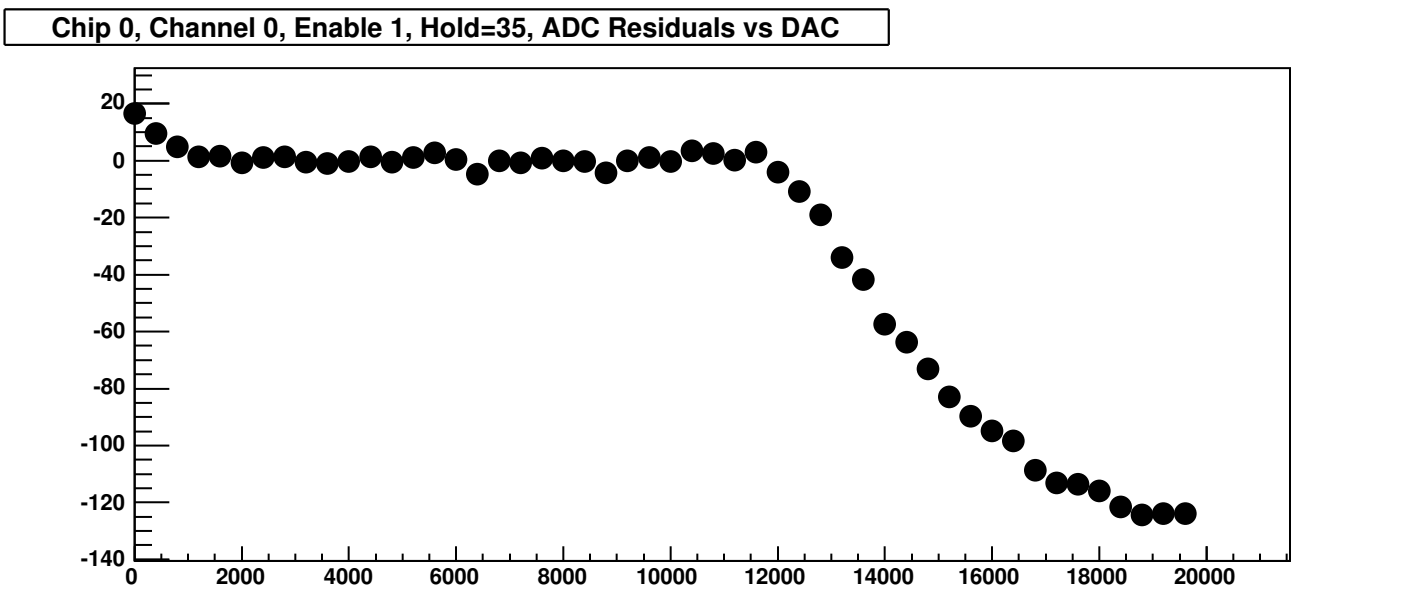

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 334.1 / 23
p0 674.3 ± 2.415
p1 2.484 ± 0.001082
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

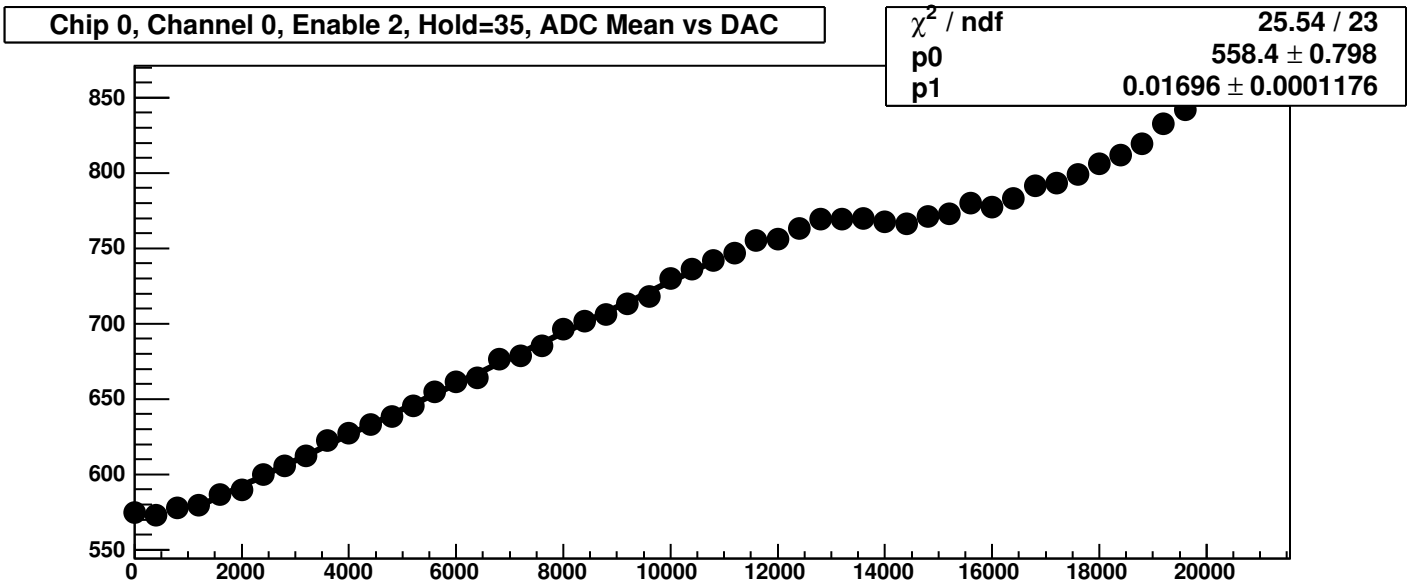

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

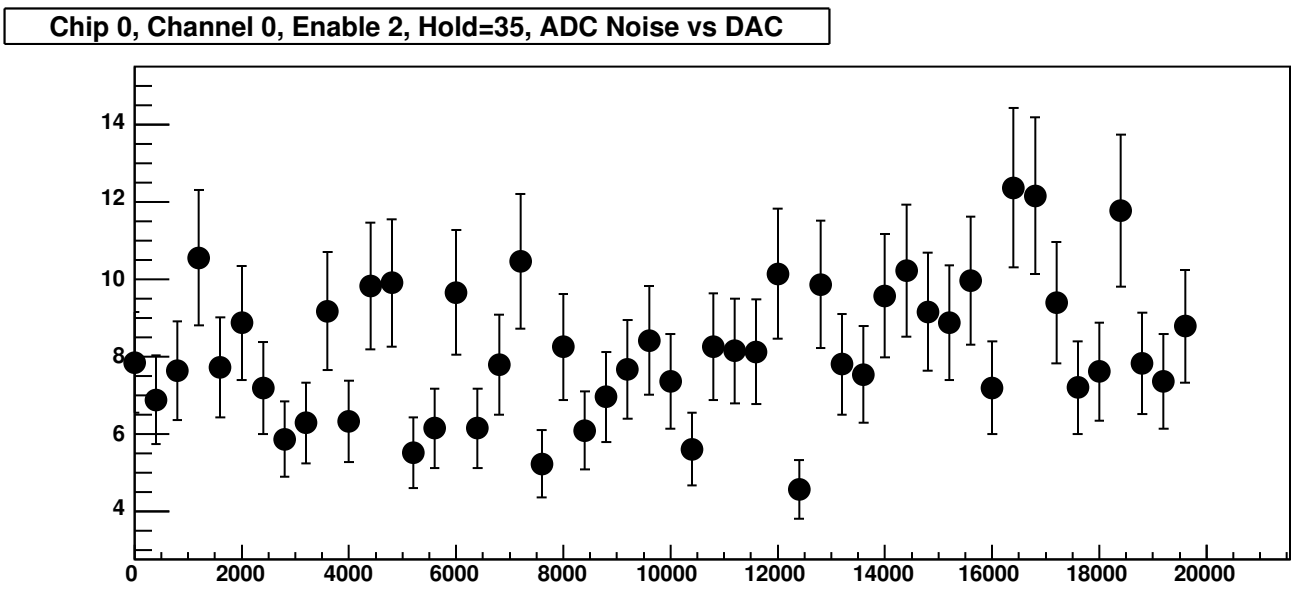

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

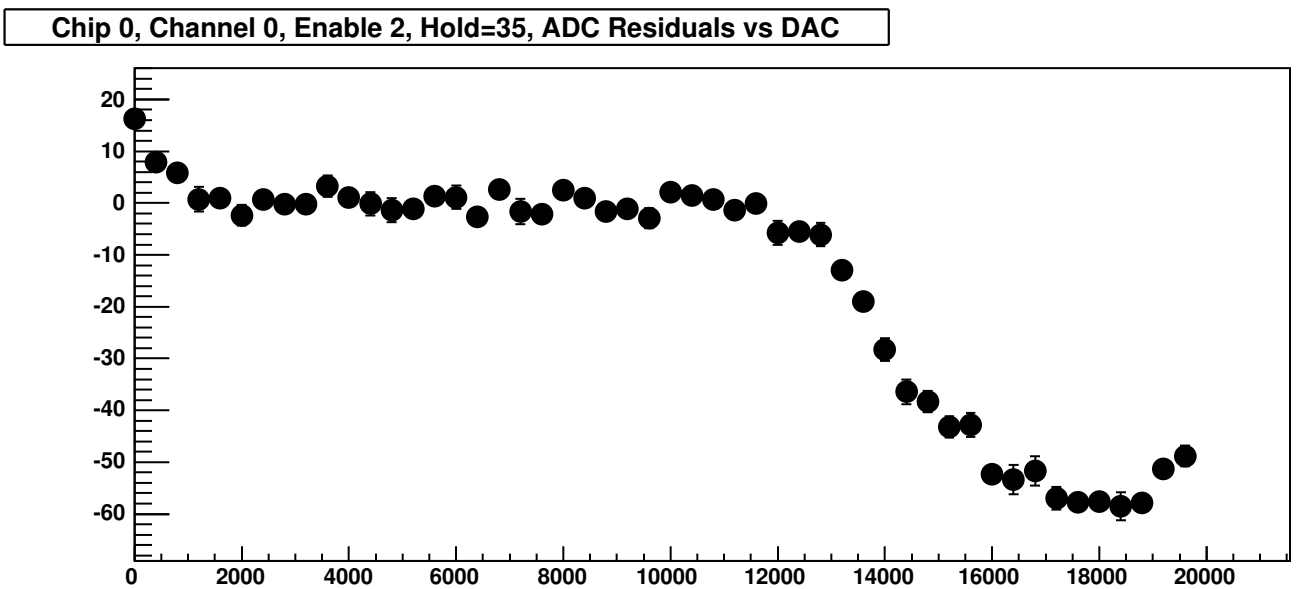

Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.15 / 23
p0 558.8 ± 0.7731
p1 0.02024 ± 0.0001219
850
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

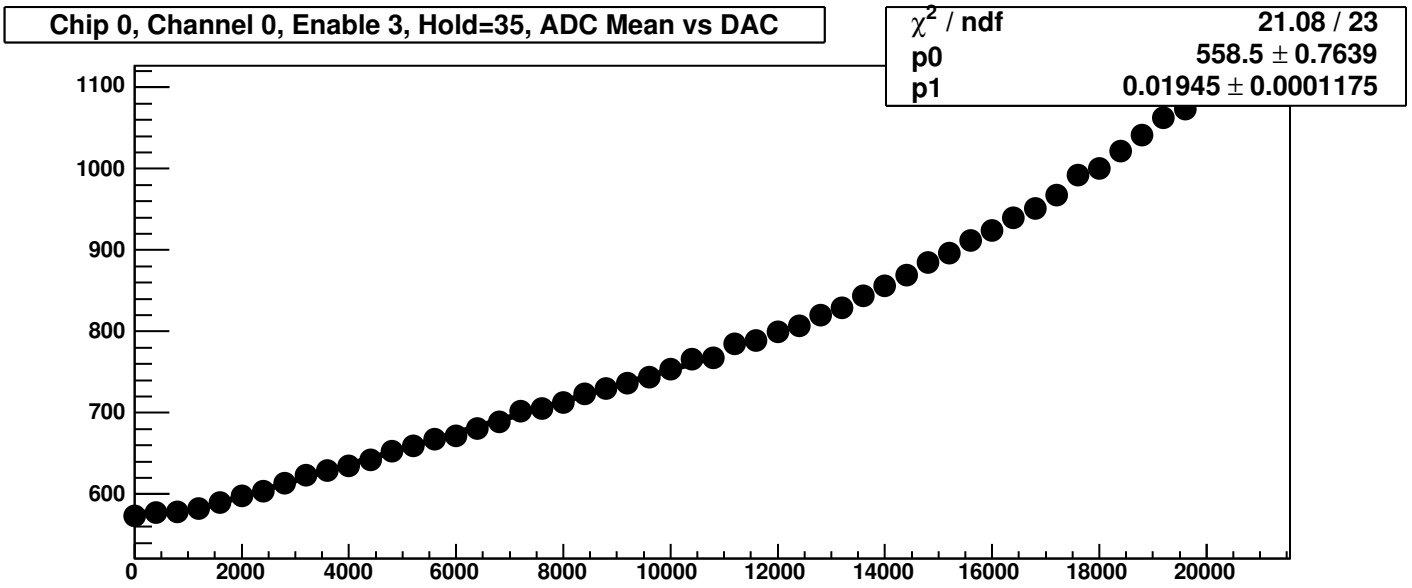

Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

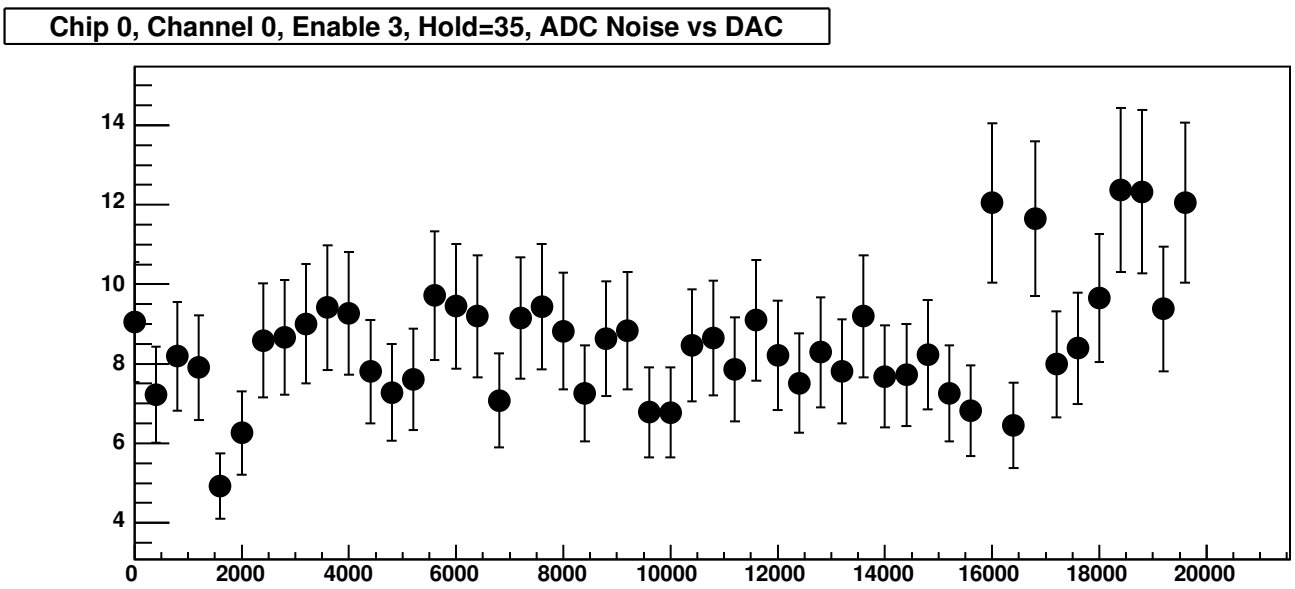

Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

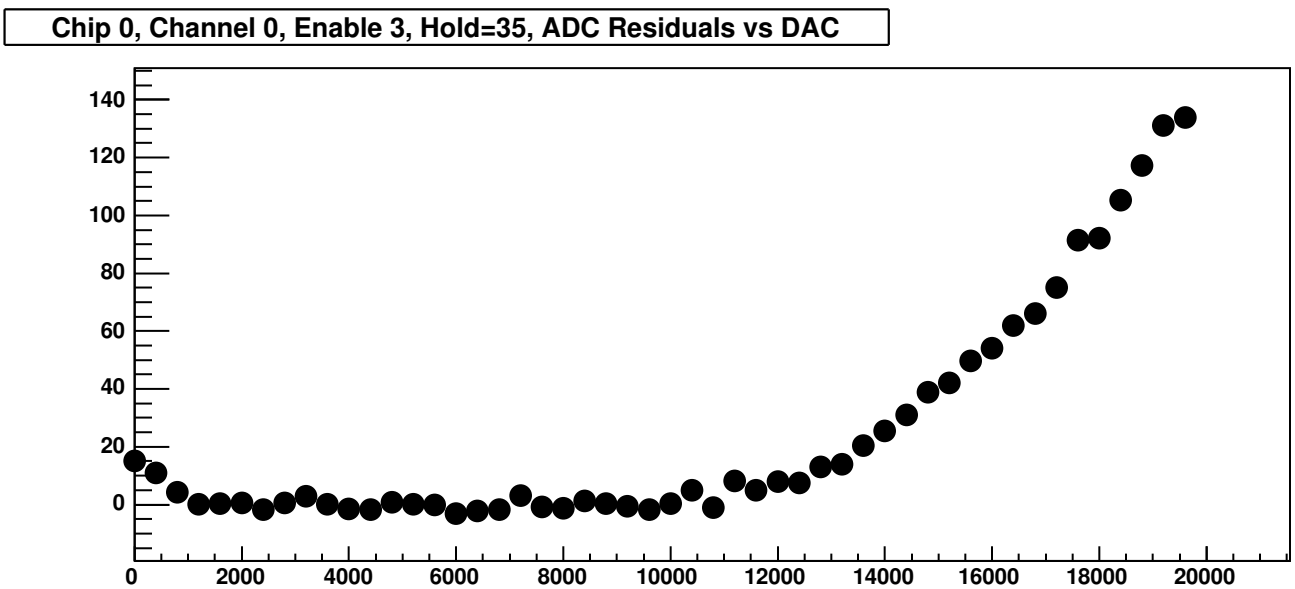

Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.54 / 23
p0 558.4 ± 0.798
p1 0.01696 ± 0.0001176
850
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

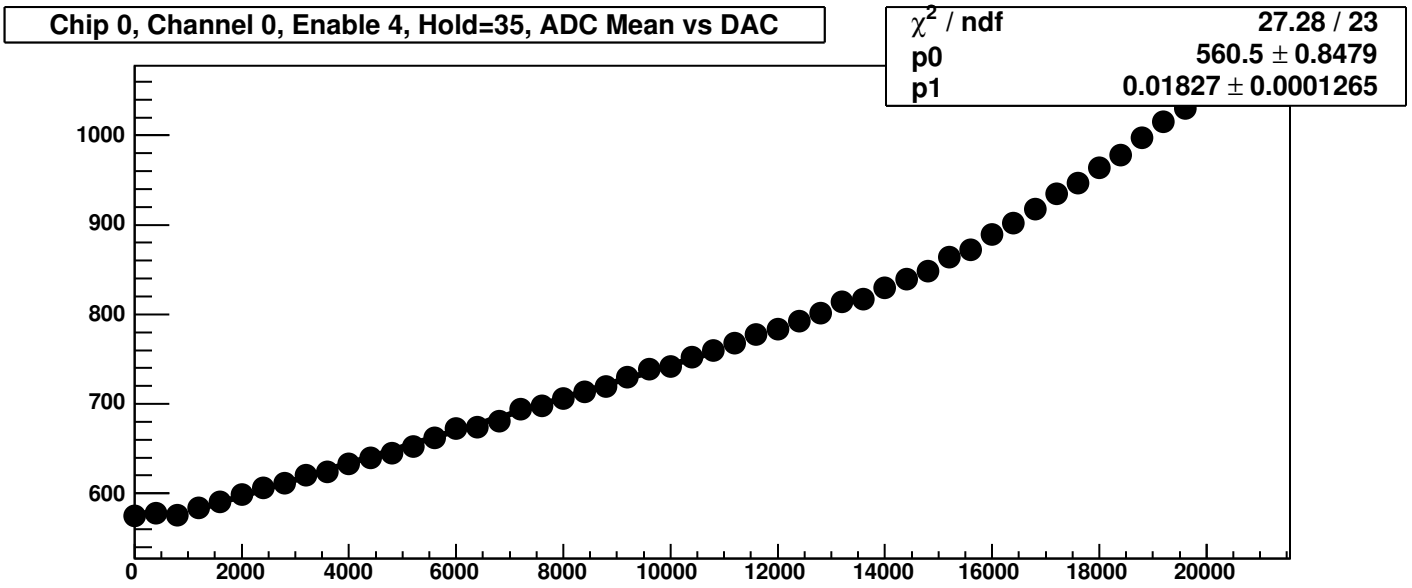

Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

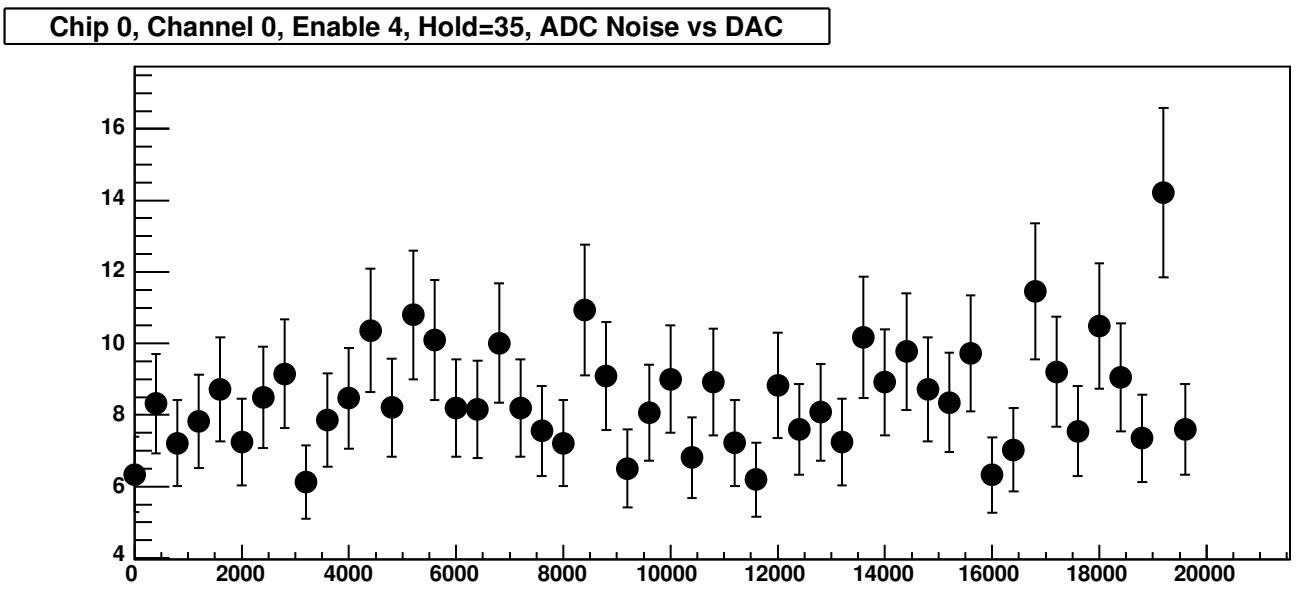

Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
-60
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

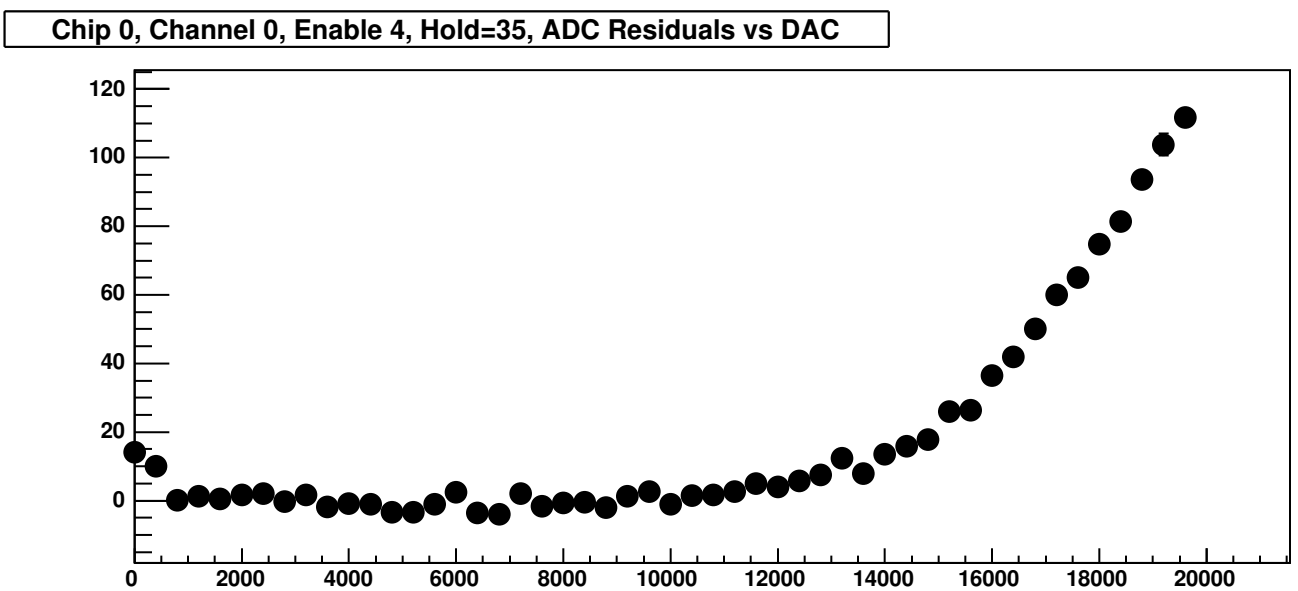

Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.08 / 23
p0 558.5 ± 0.7639
p1 0.01945 ± 0.0001175
1100
1000
900
800
700
600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

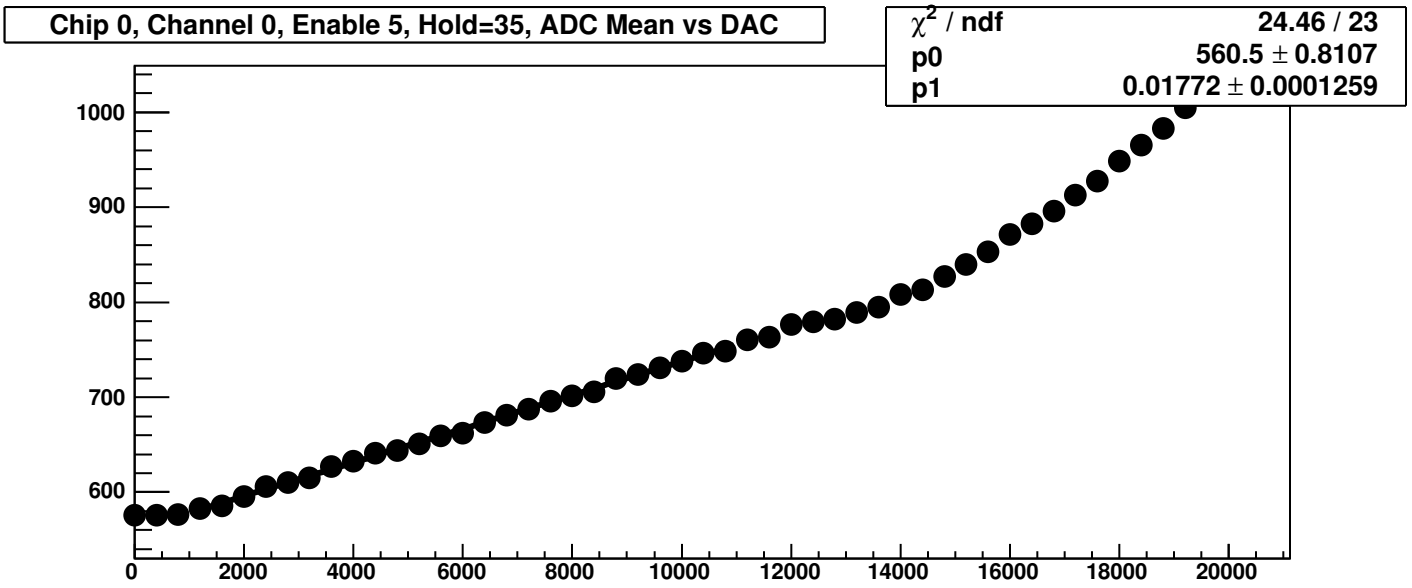

Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

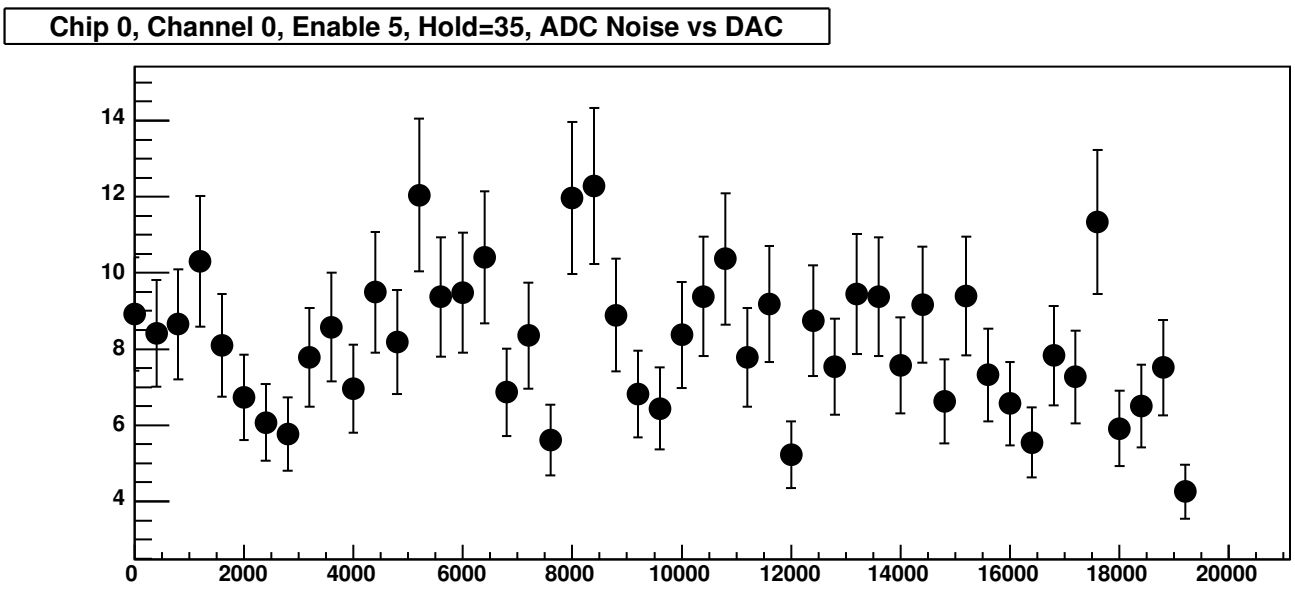

Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

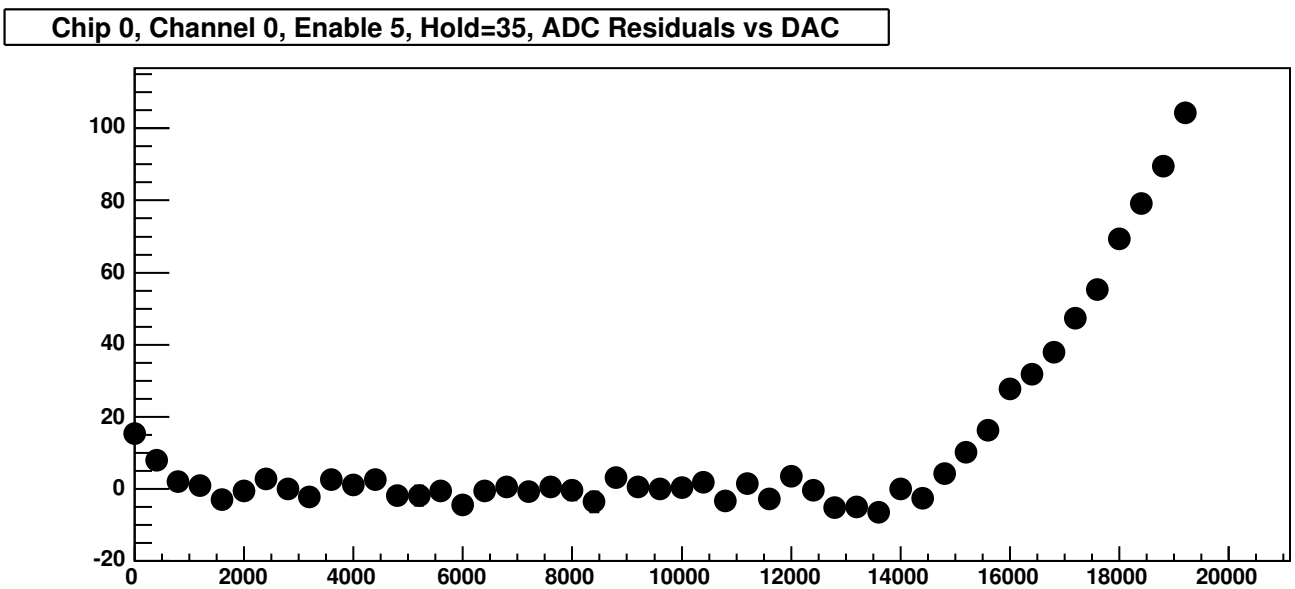

Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.28 / 23
p0 560.5 ± 0.8479
p1 0.01827 ± 0.0001265
1000
900
800
700
600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

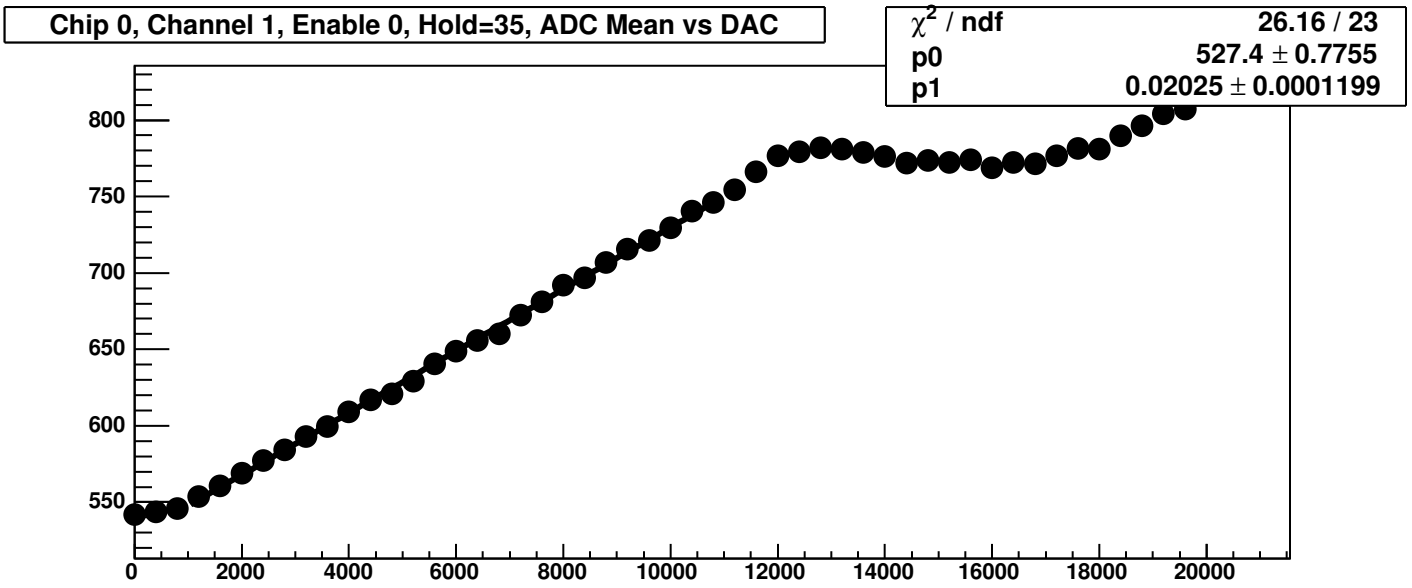

Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

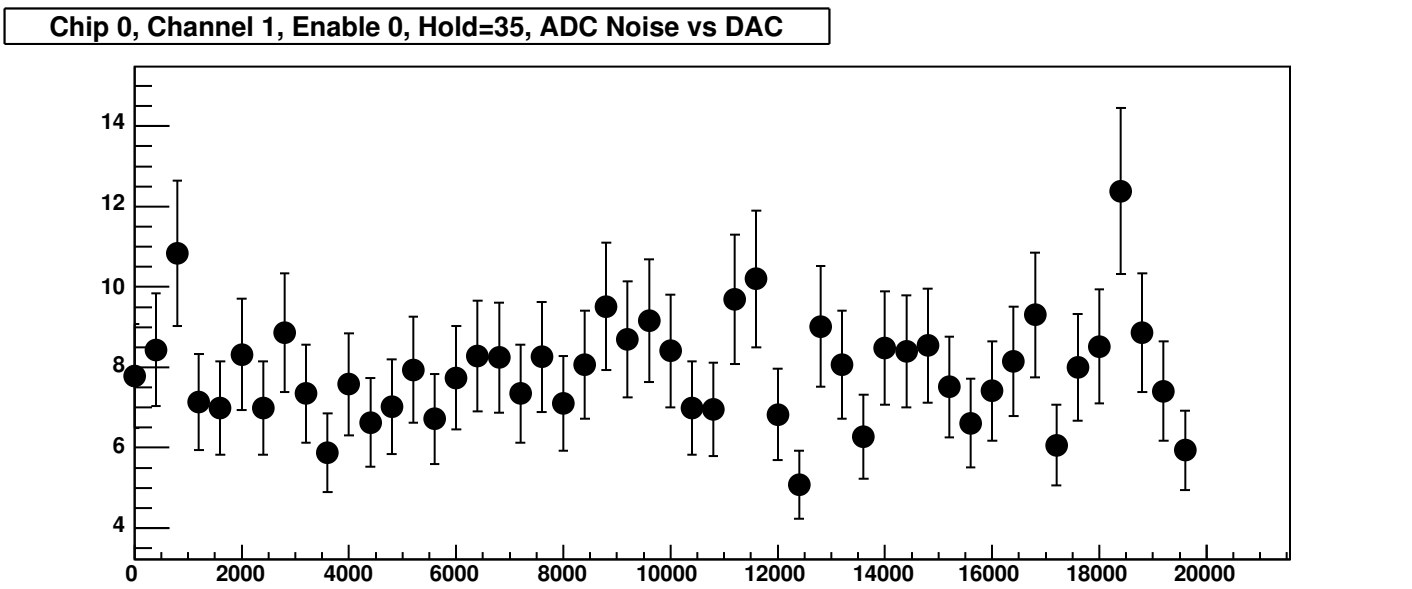

Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

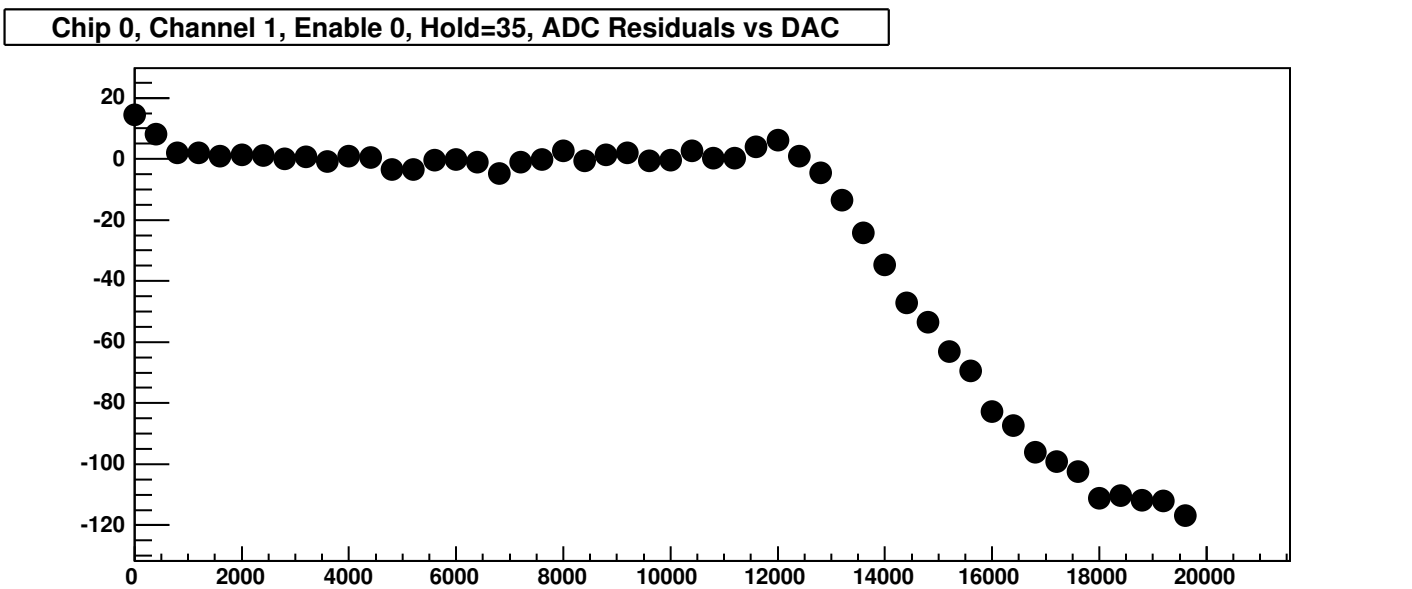

Chip 0, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 24.46 / 23
p0 560.5 ± 0.8107
p1 0.01772 ± 0.0001259
1000
900
800
700
600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

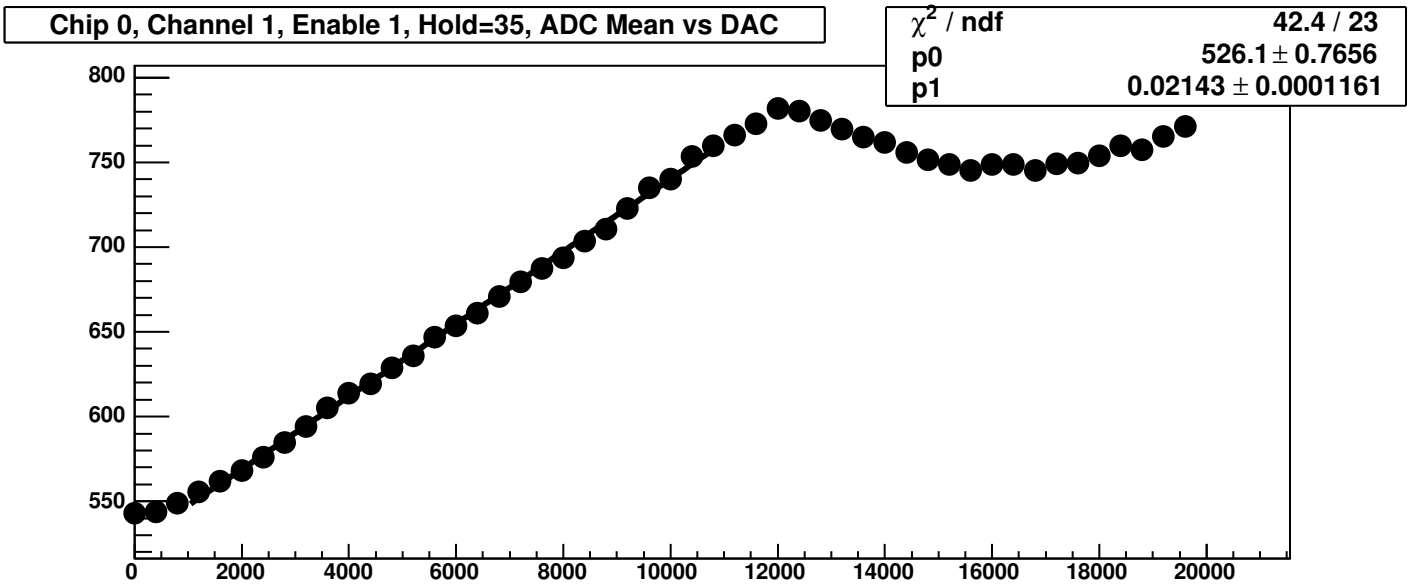

Chip 0, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

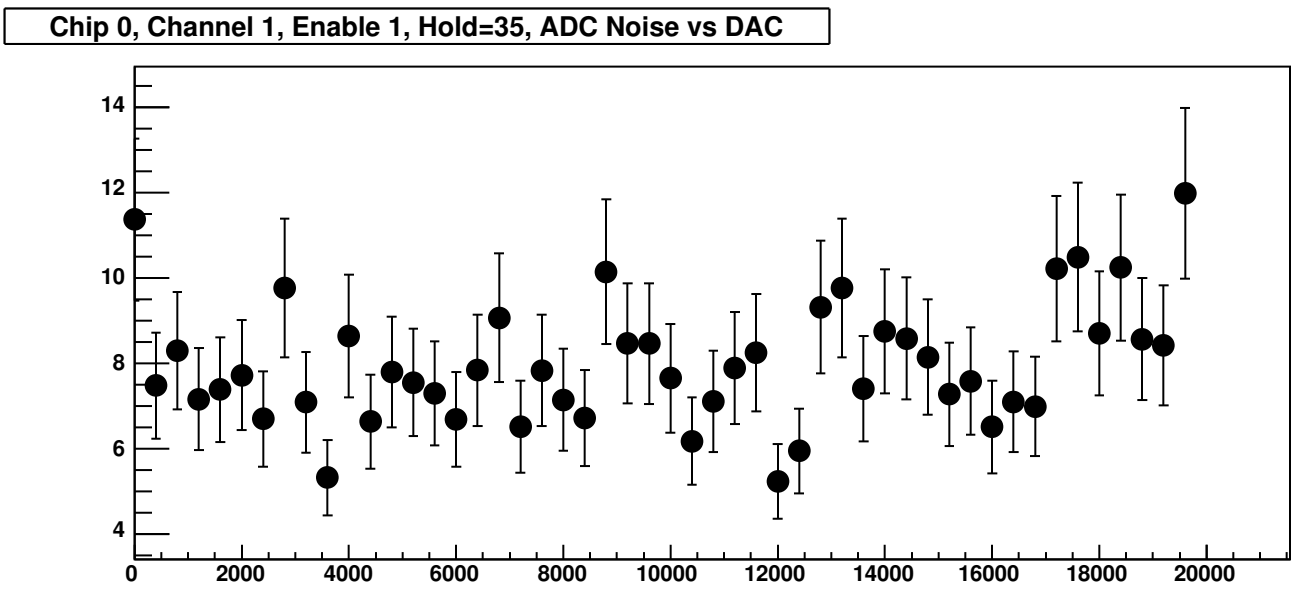

Chip 0, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

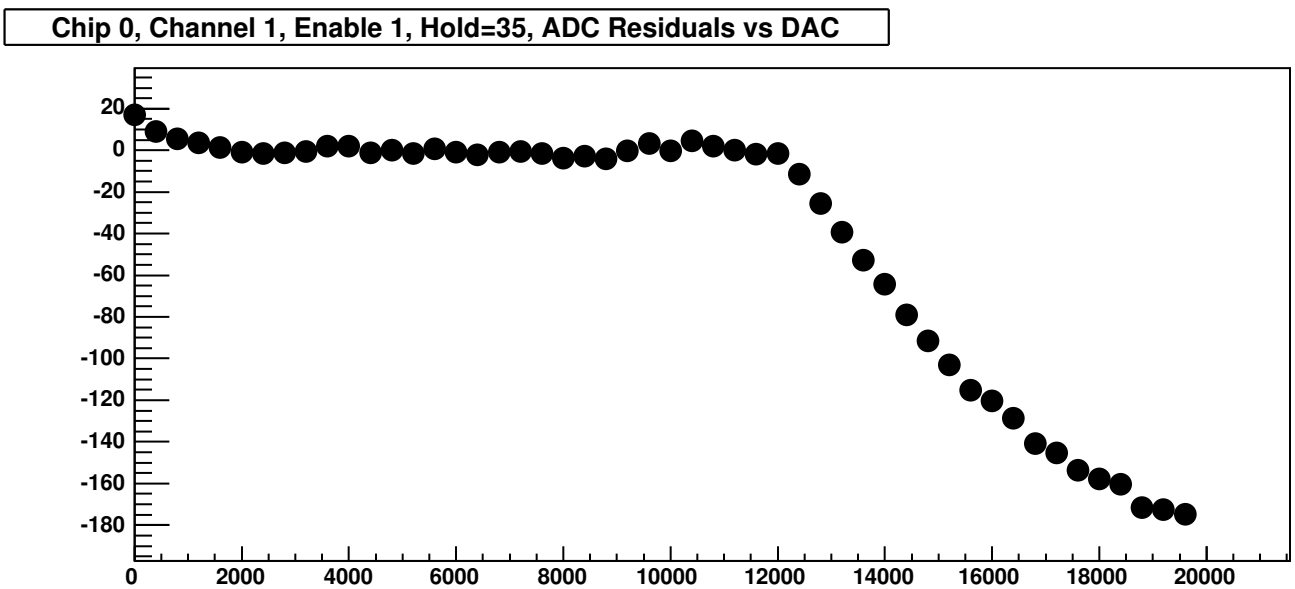

Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 26.16 / 23
p0 527.4 ± 0.7755
p1 0.02025 ± 0.0001199
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

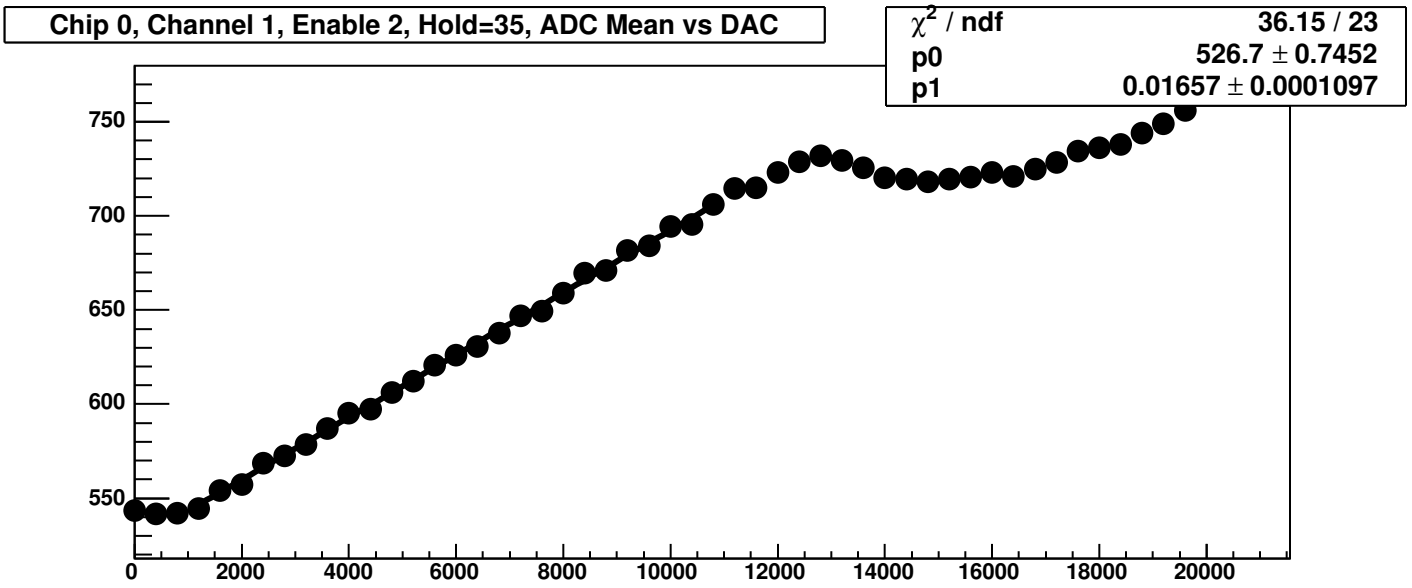

Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

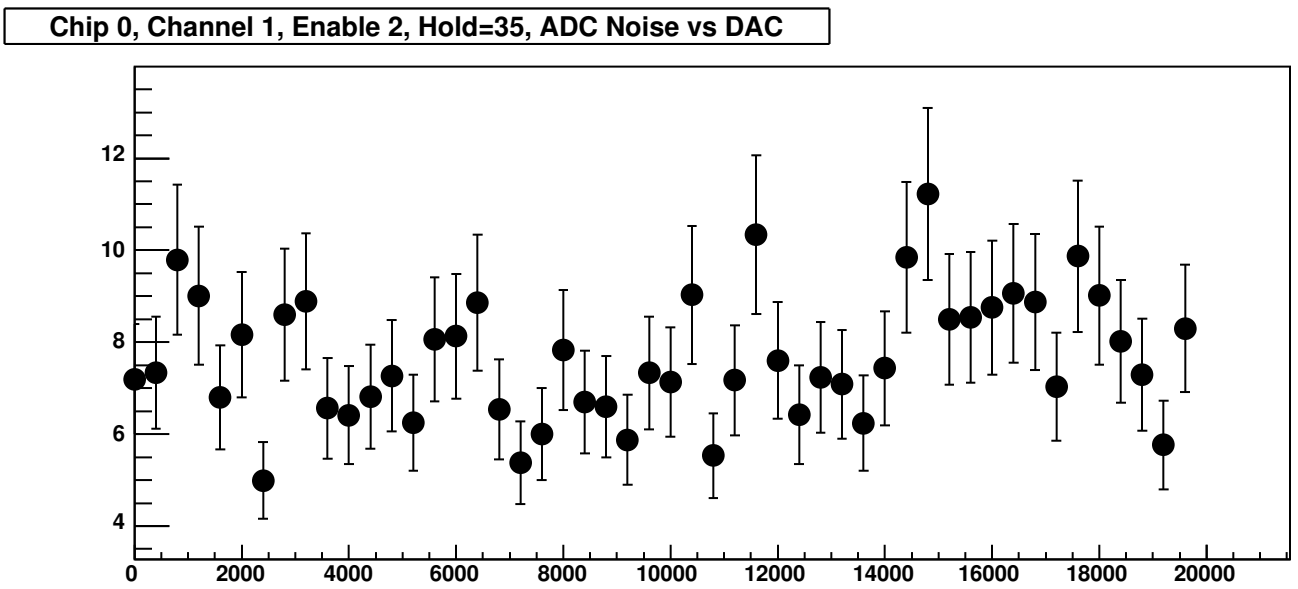

Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

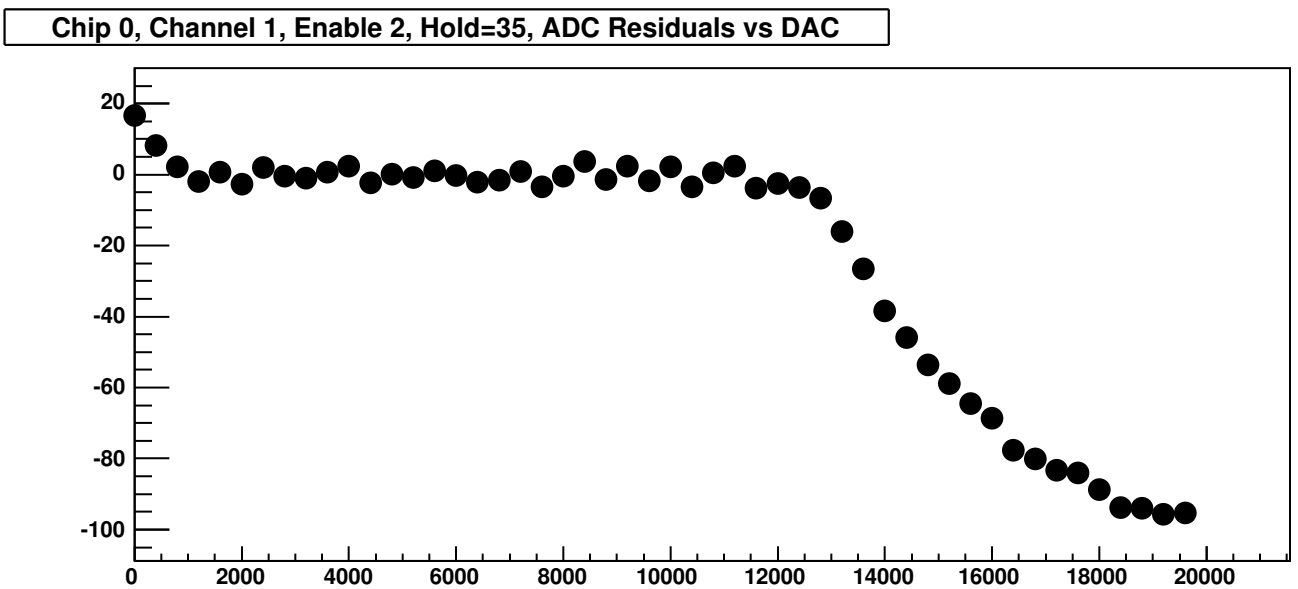

Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.4 / 23
p0 526.1 ± 0.7656
p1 0.02143 ± 0.0001161
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

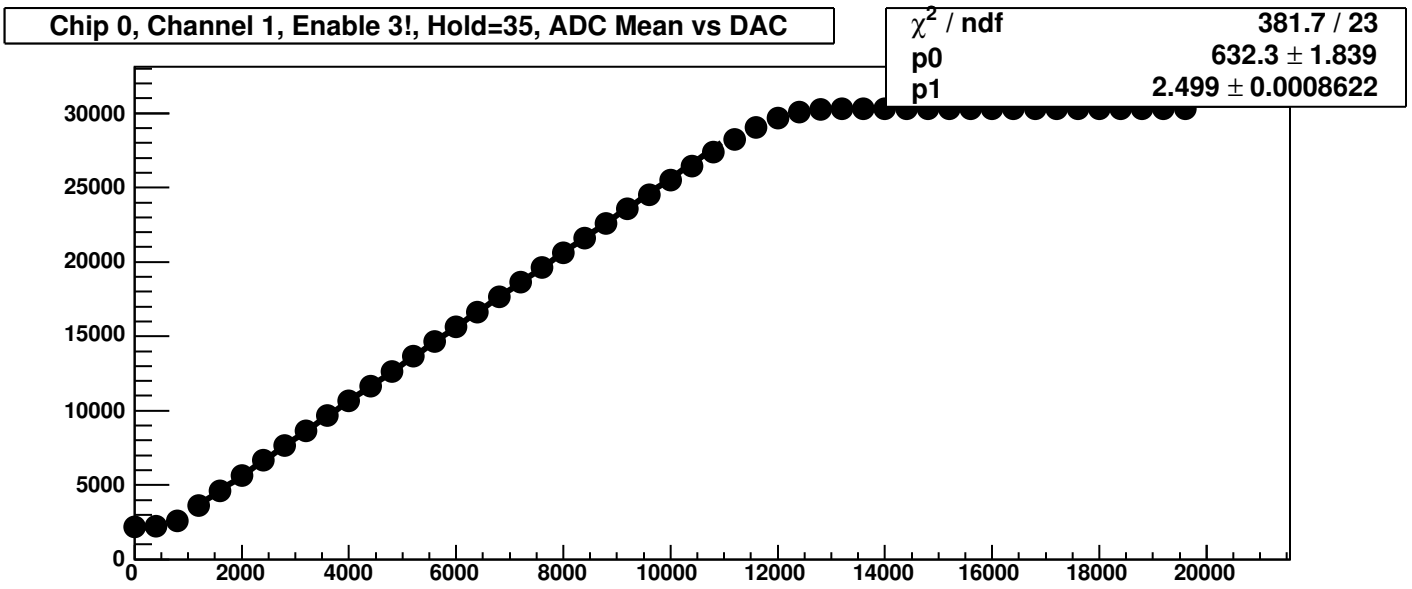

Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

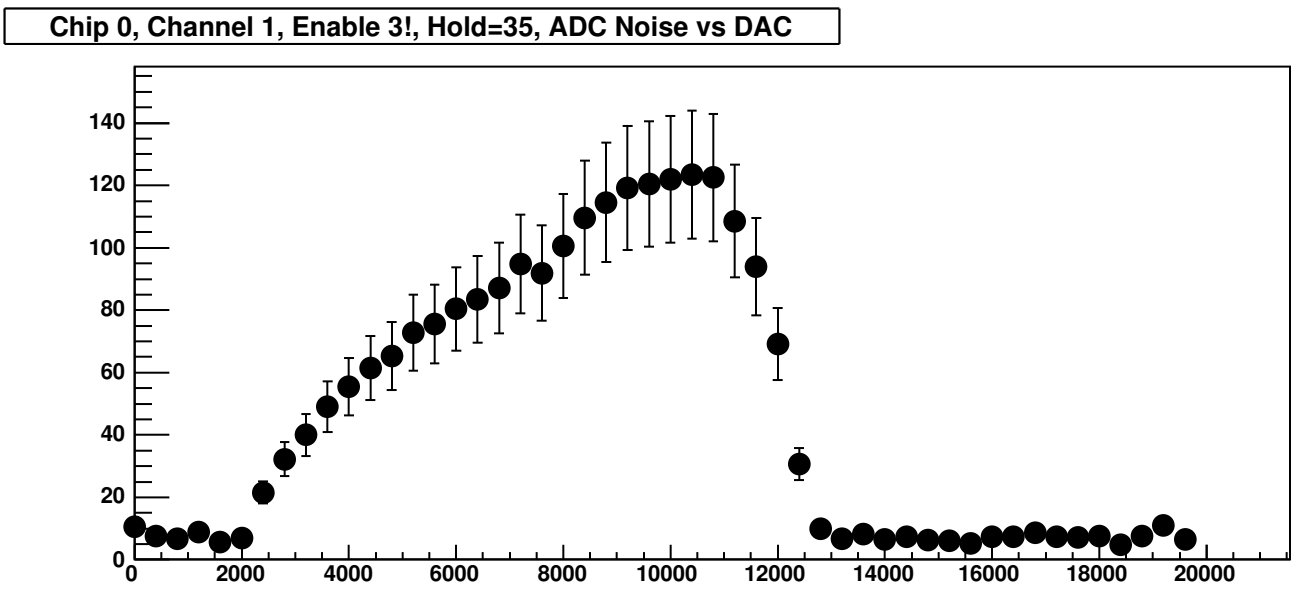

Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

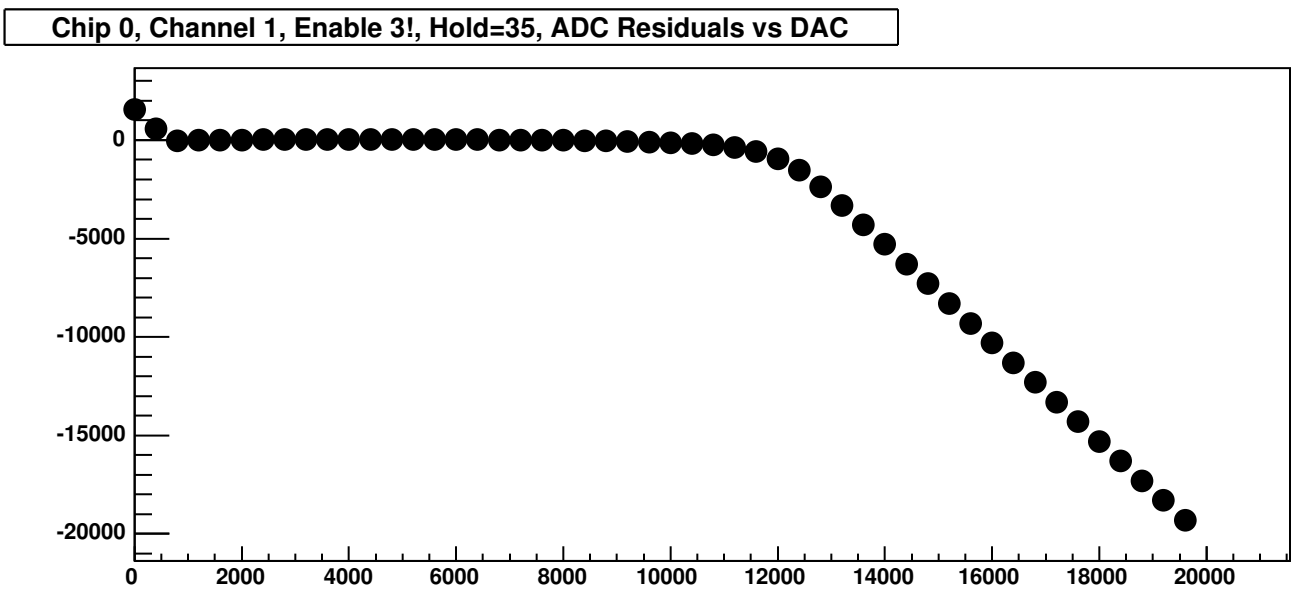

Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 36.15 / 23
p0 526.7 ± 0.7452
p1 0.01657 ± 0.0001097
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

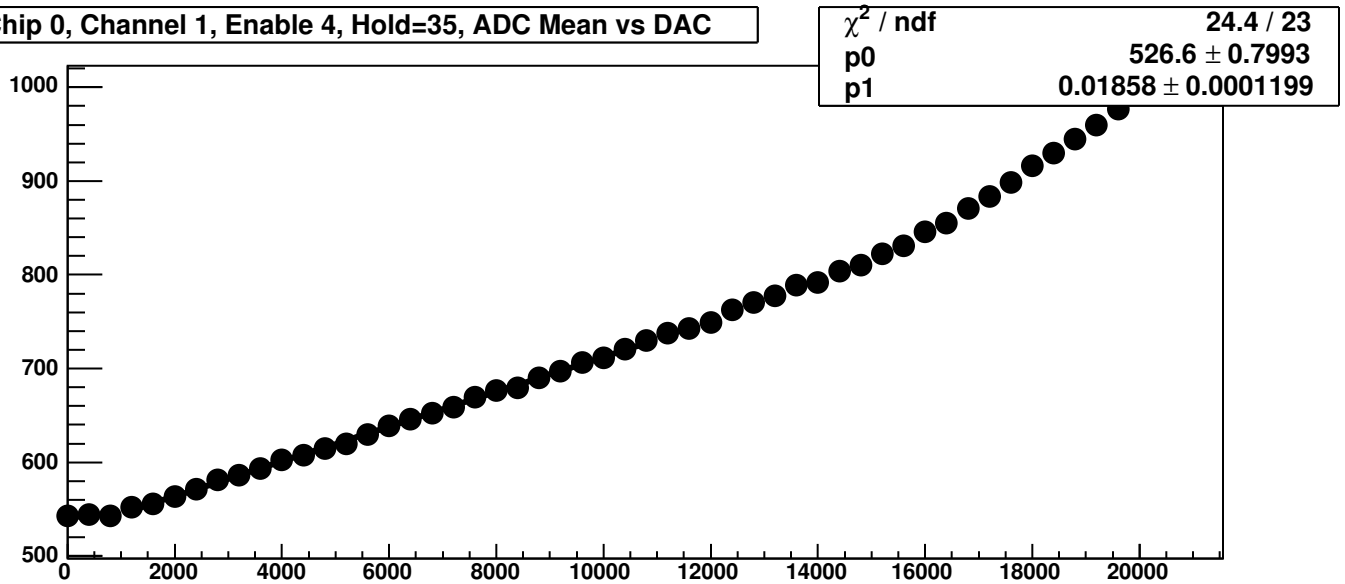

Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 381.7 / 23
p0 632.3 ± 1.839
p1 2.499 ± 0.0008622
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

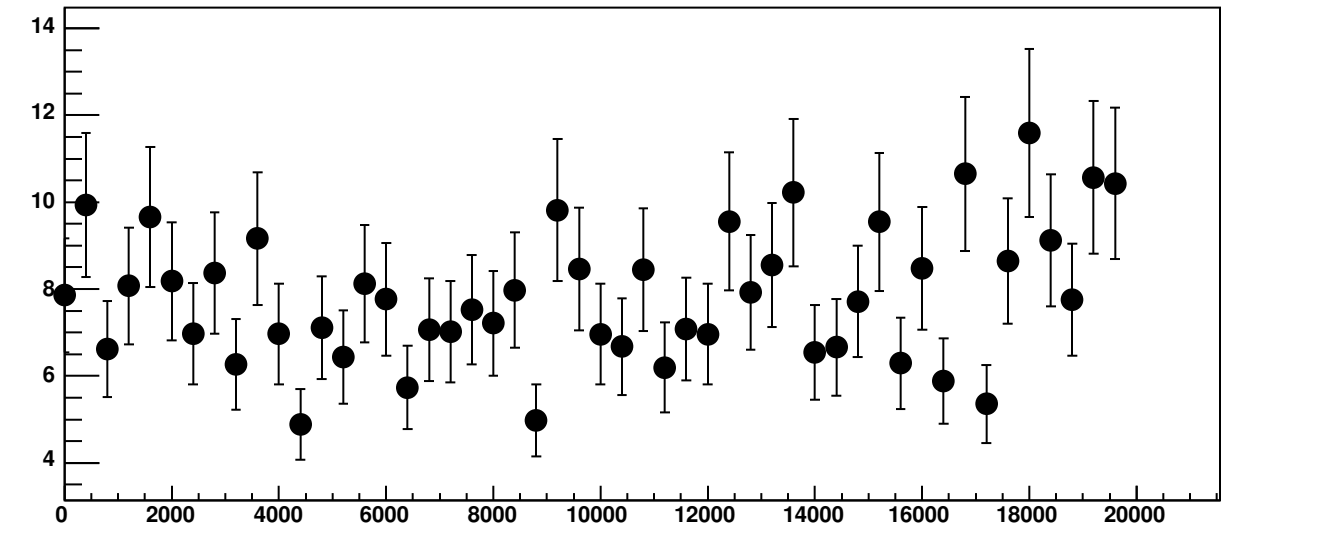

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

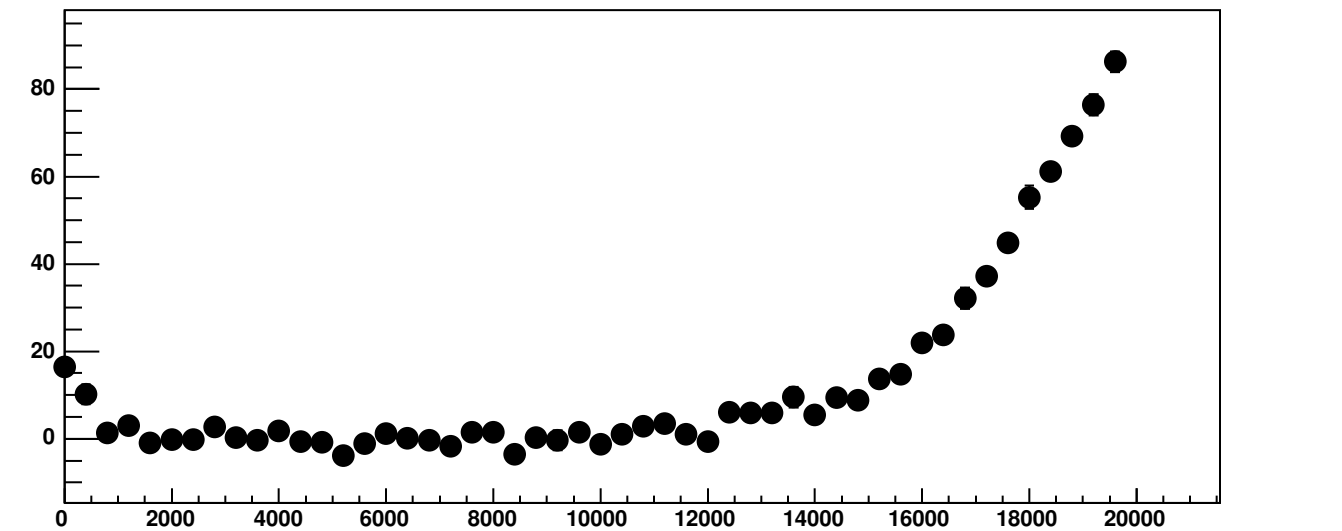
Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

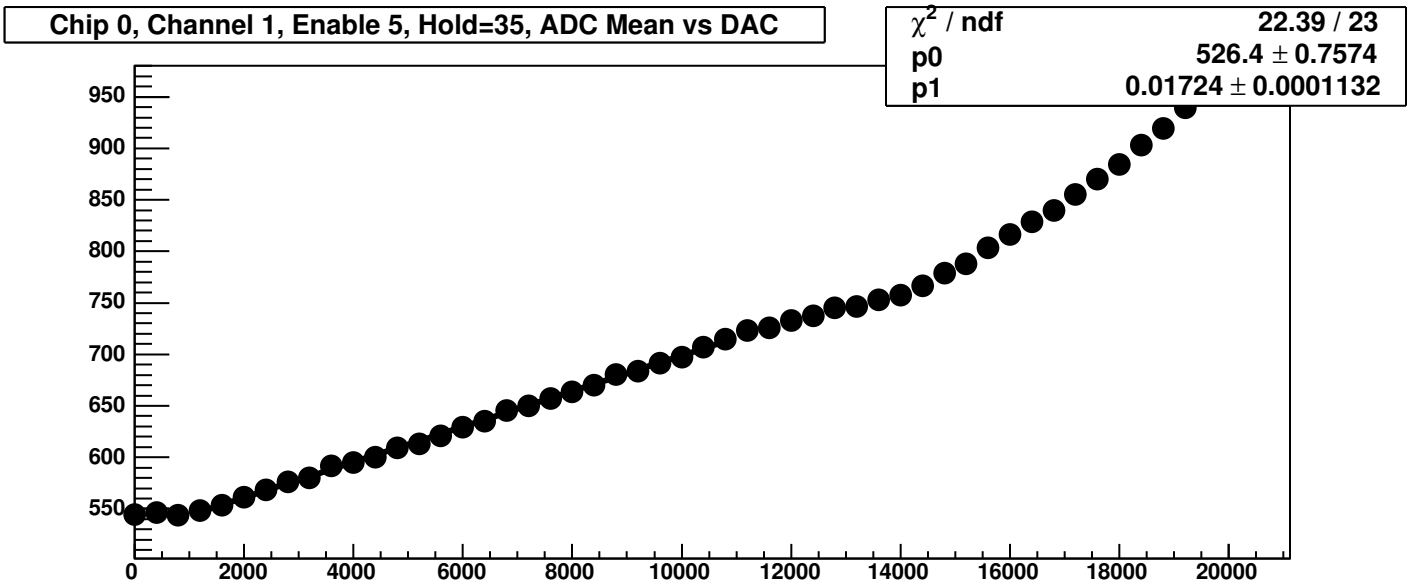


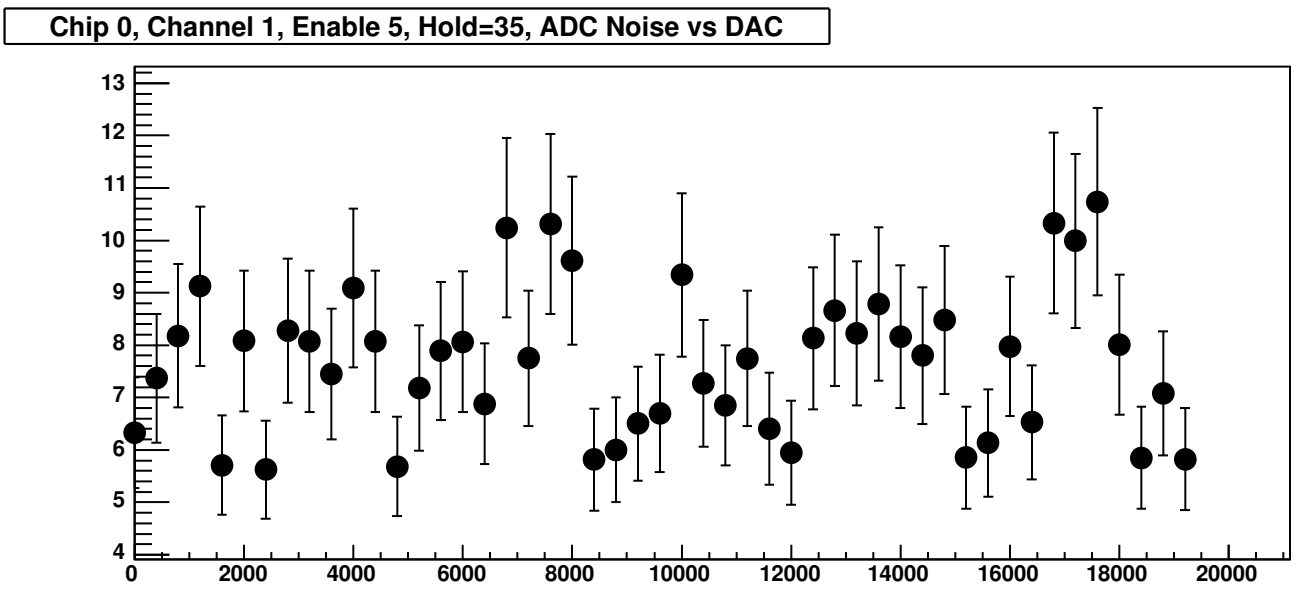
Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

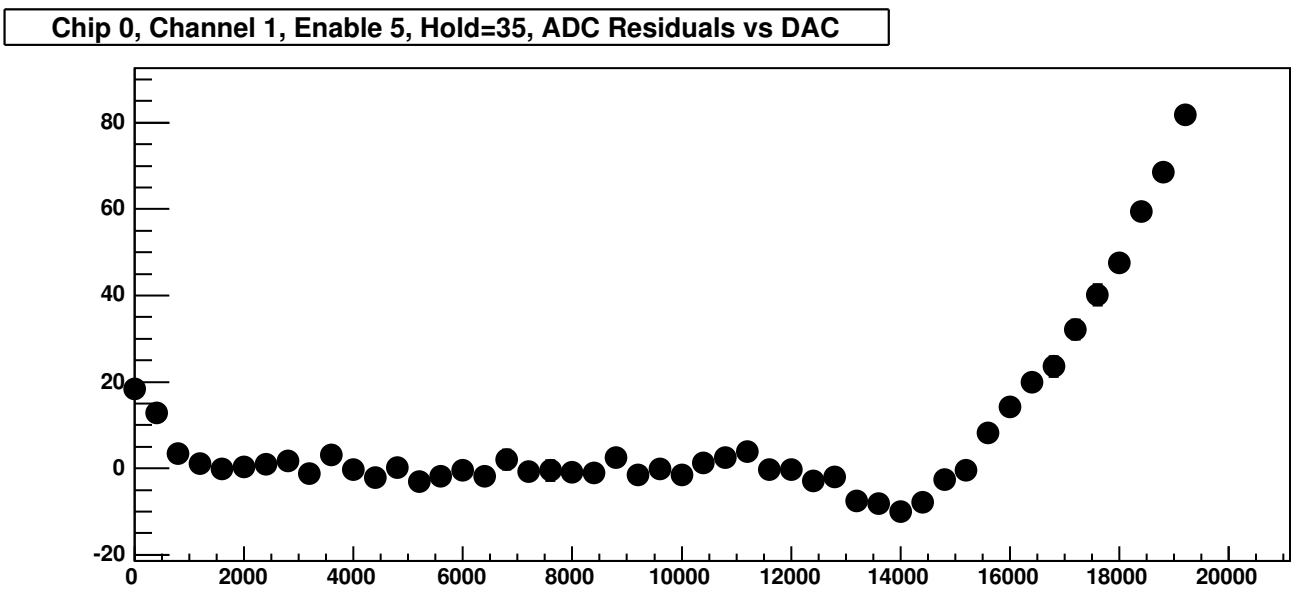


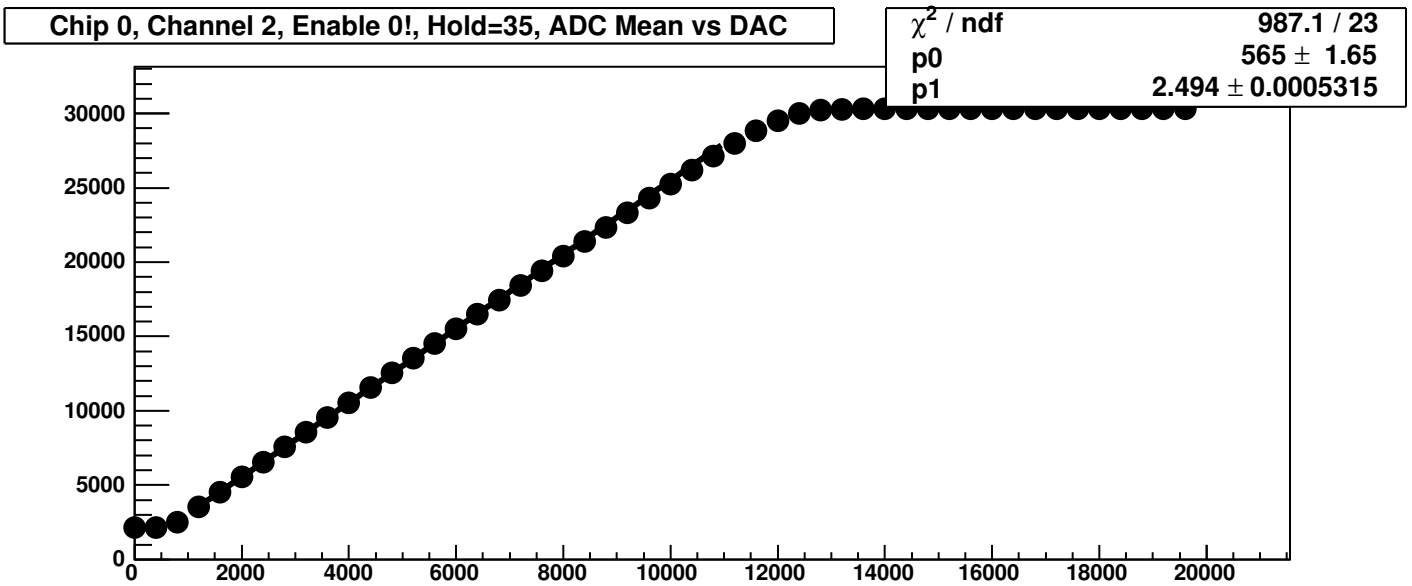
Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

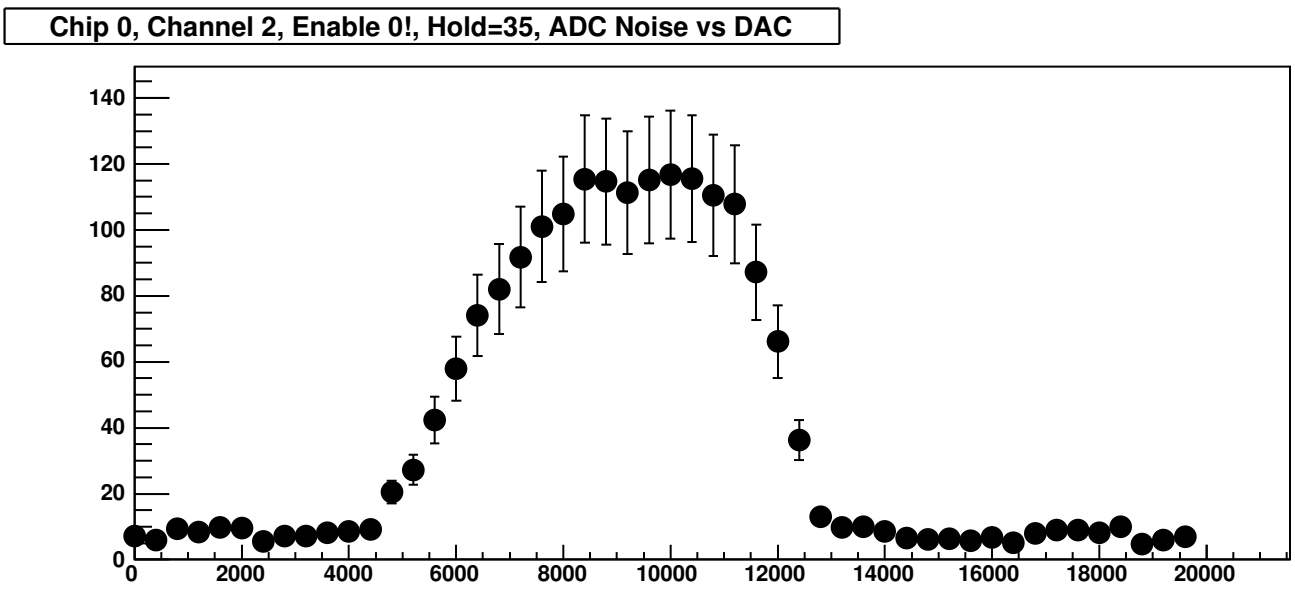


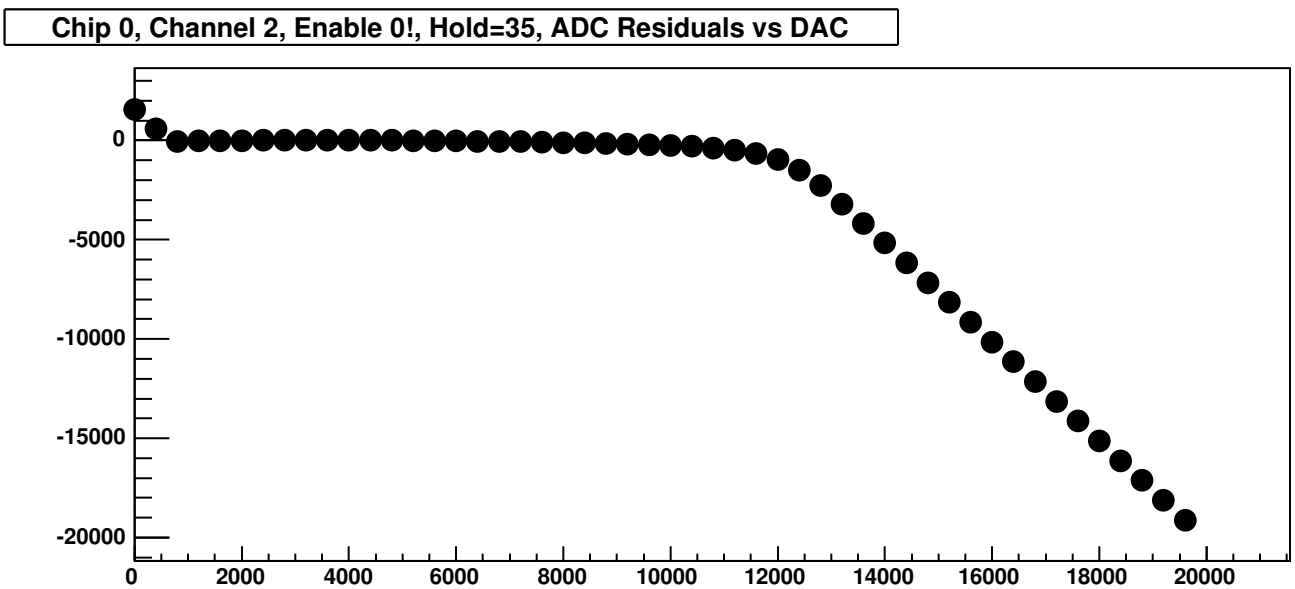

Chip 0, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 22.39 / 23
p0 526.4 ± 0.7574
p1 0.01724 ± 0.0001132
950
900
850
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

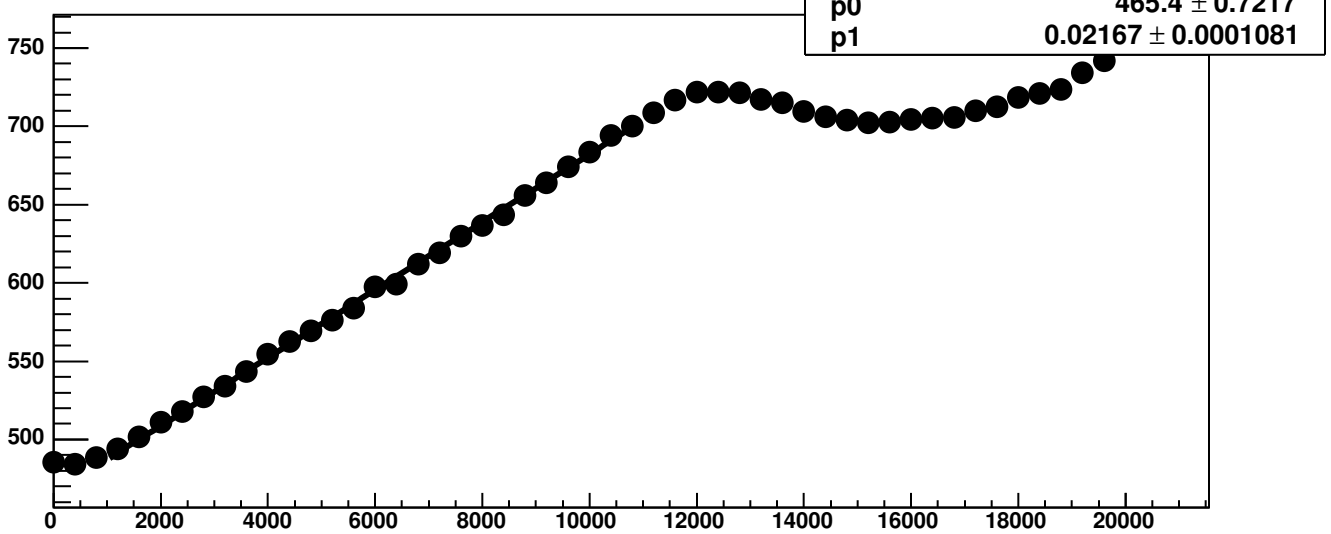

Chip 0, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 987.1 / 23
p0 565 ± 1.65
p1 2.494 ± 0.0005315
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

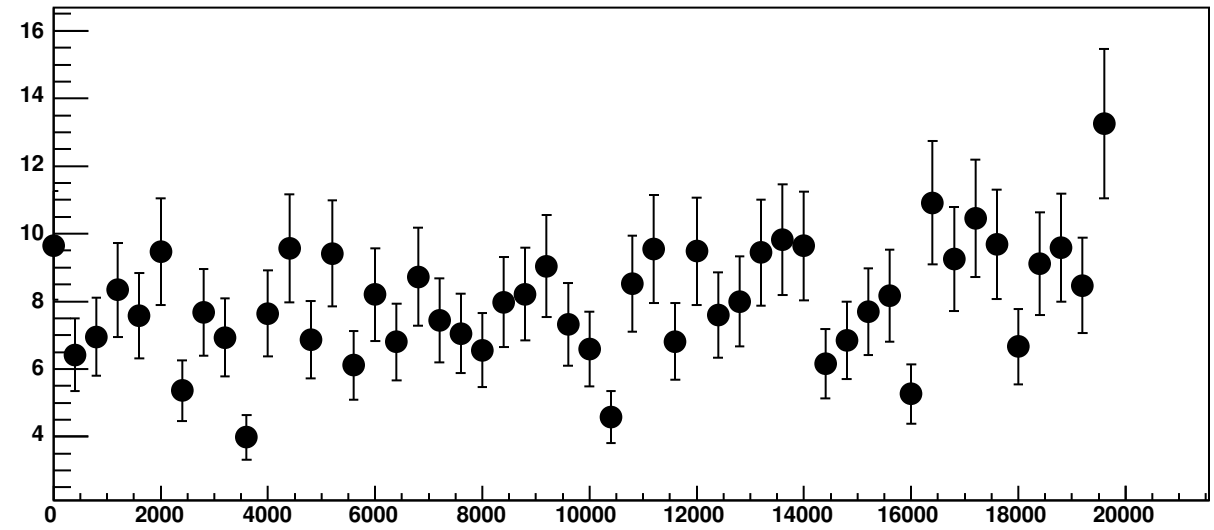

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

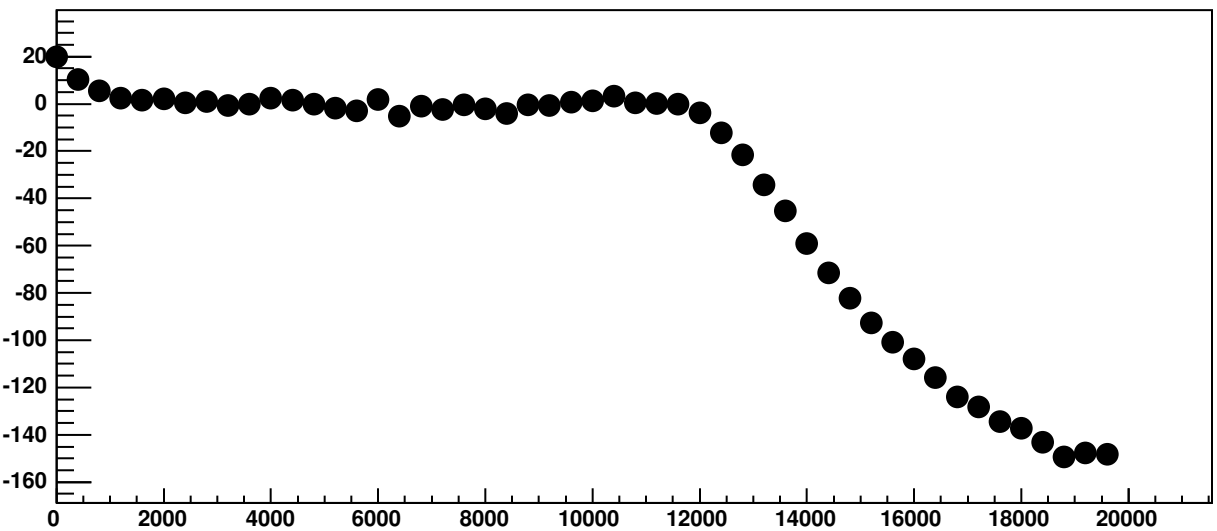
Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

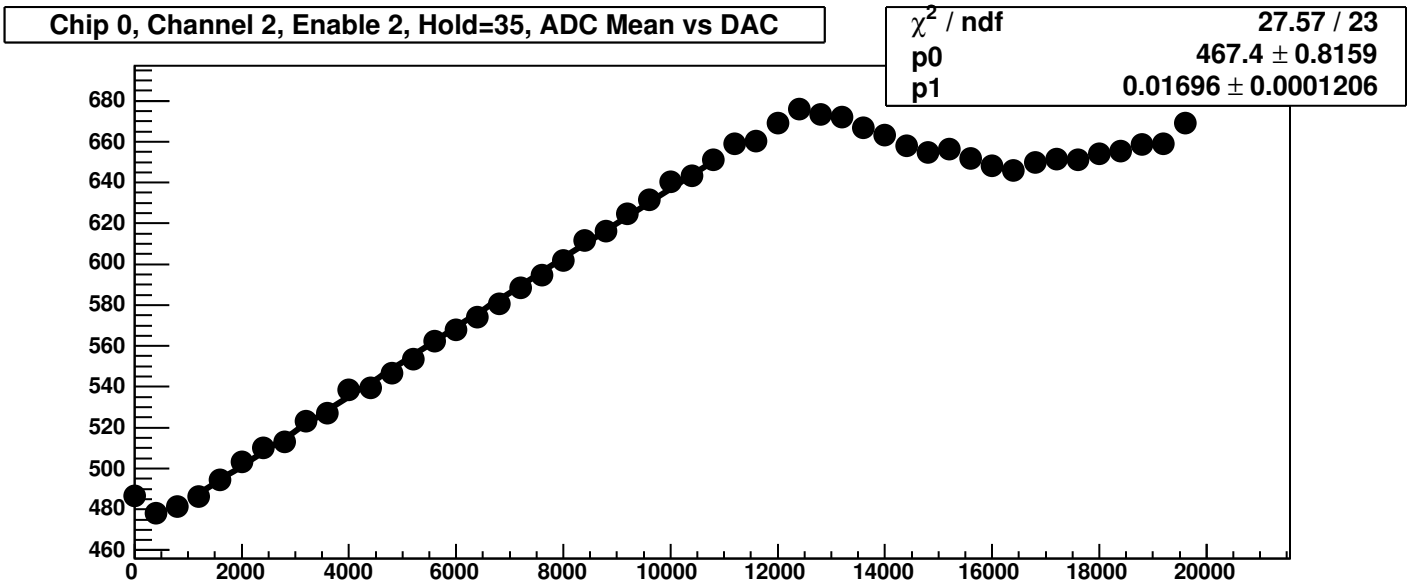


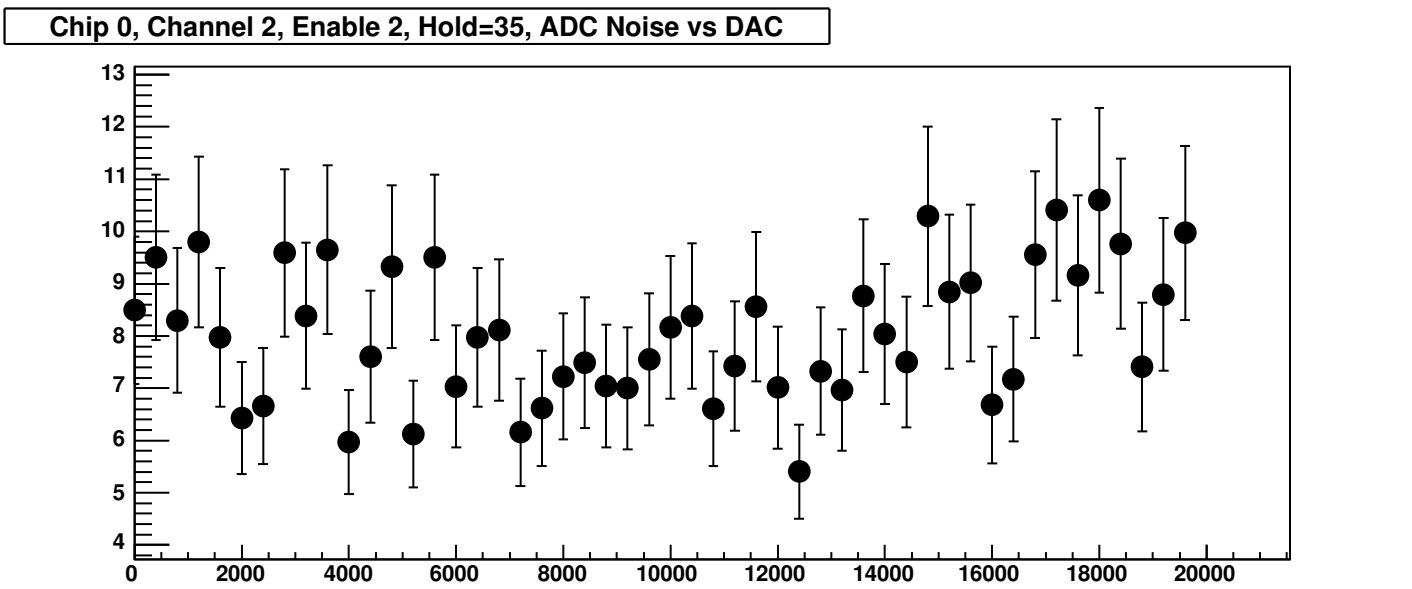
Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

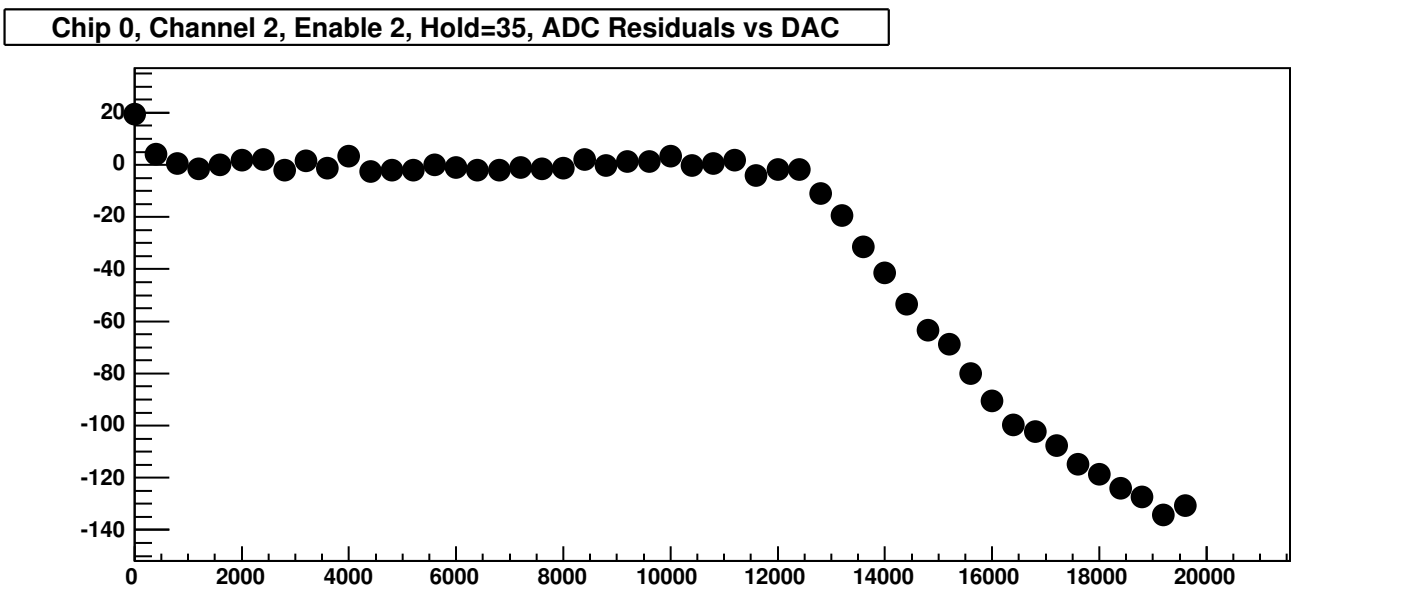


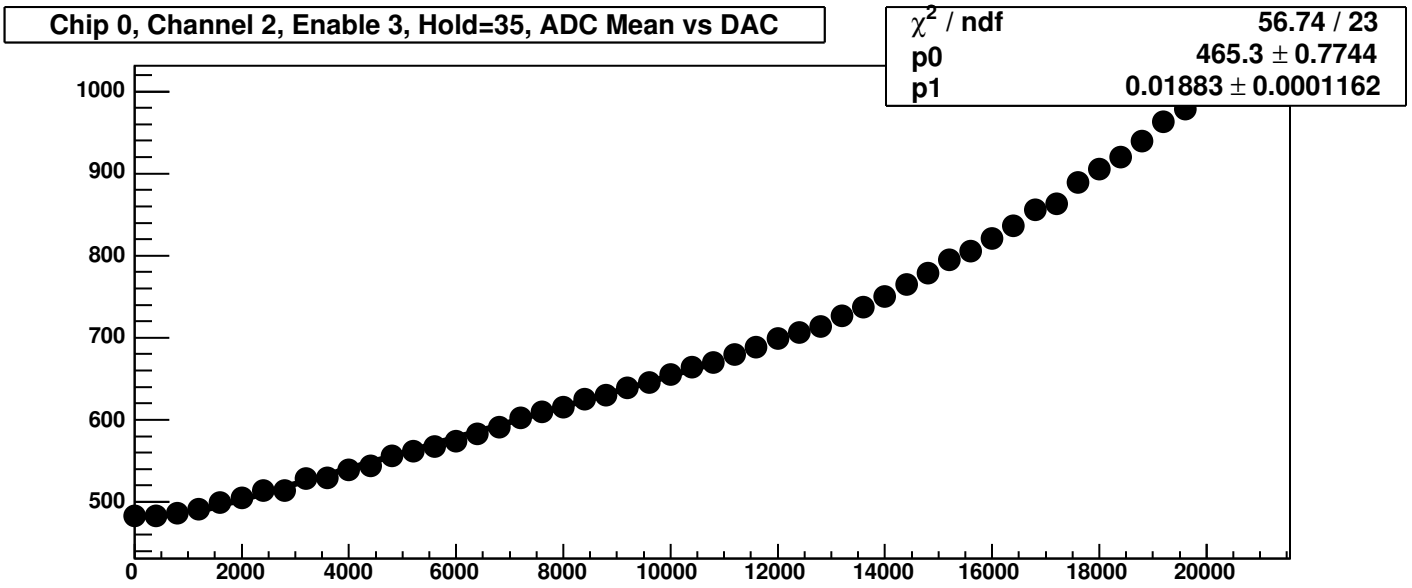
Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

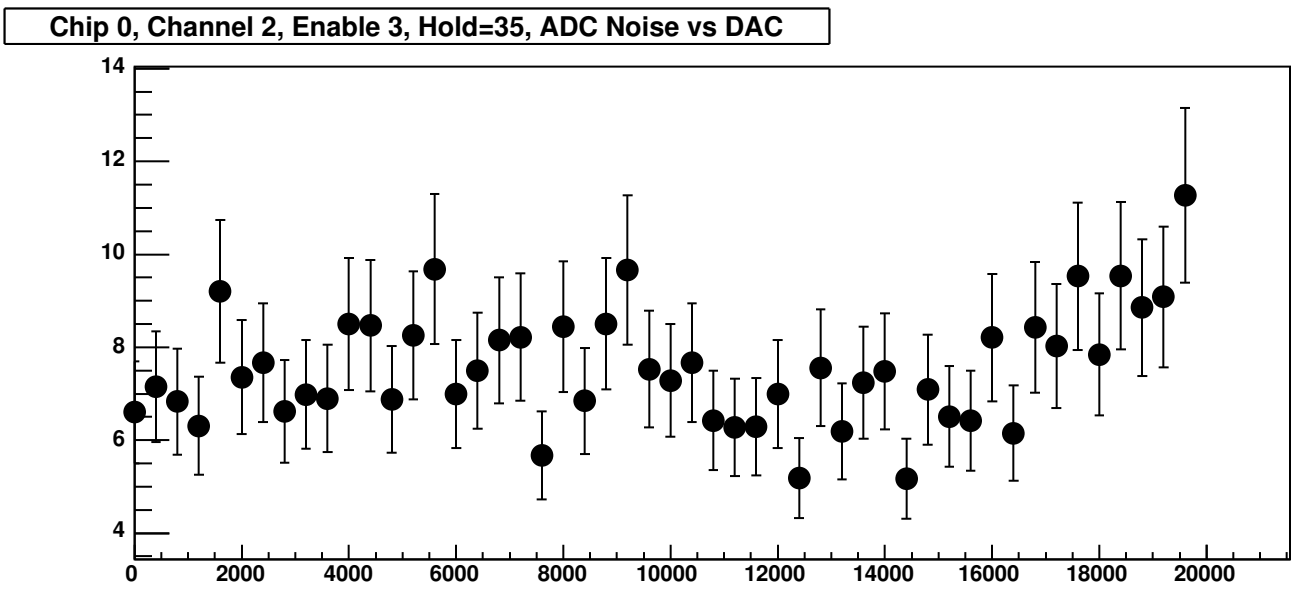


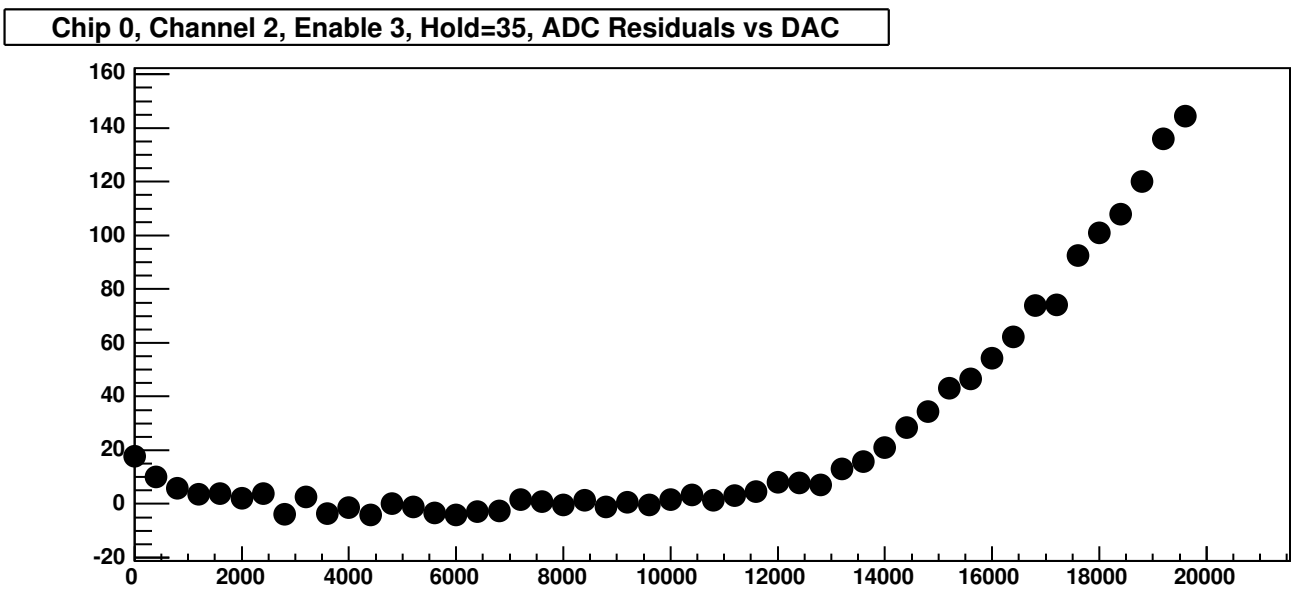

Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.57 / 23
p0 467.4 ± 0.8159
p1 0.01696 ± 0.0001206
680
660
640
620
600
580
560
540
520
500
480
460
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

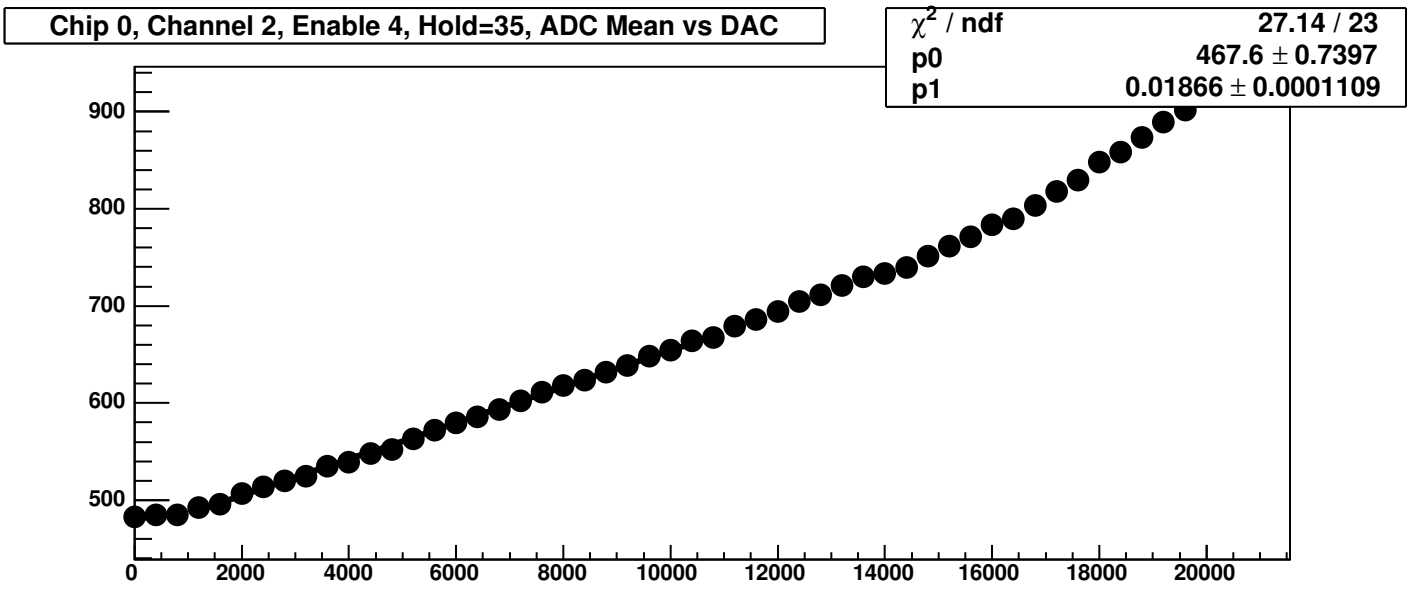

Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

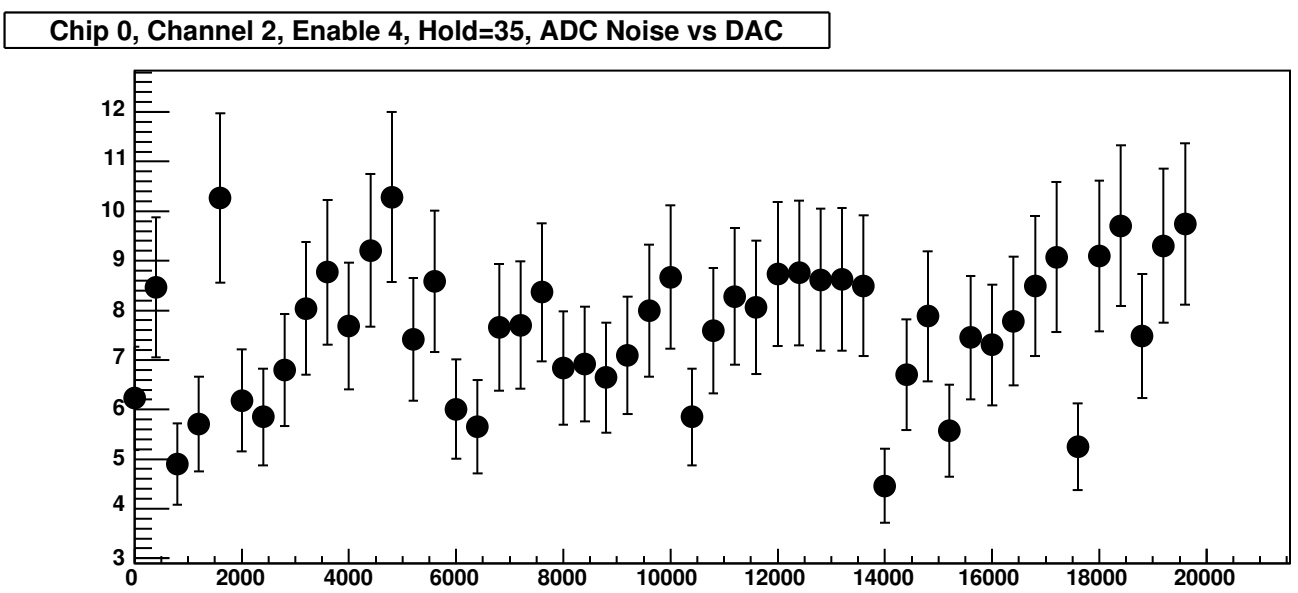

Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

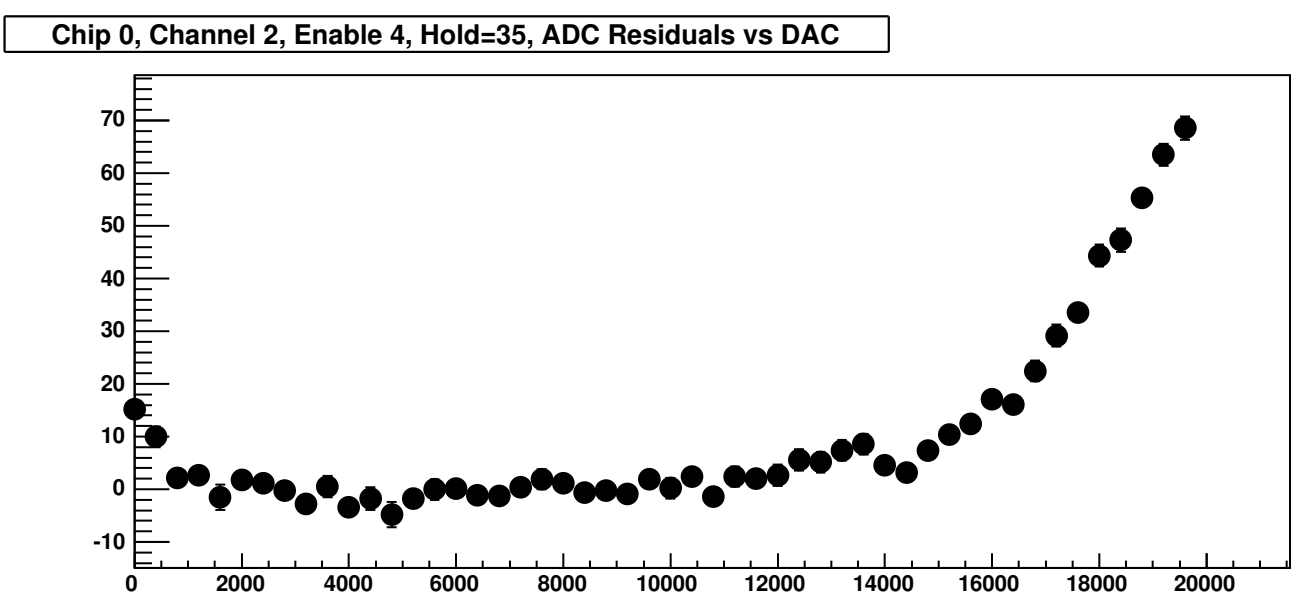

Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 56.74 / 23
p0 465.3 ± 0.7744
p1 0.01883 ± 0.0001162
1000
900
800
700
600
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

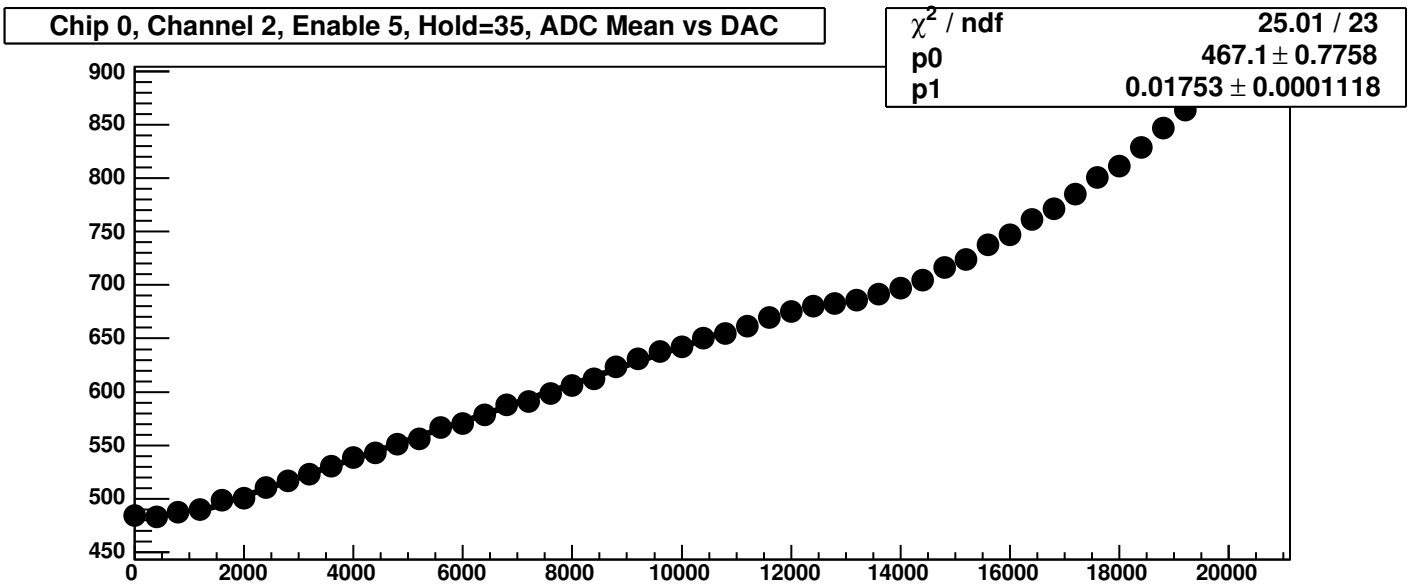

Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

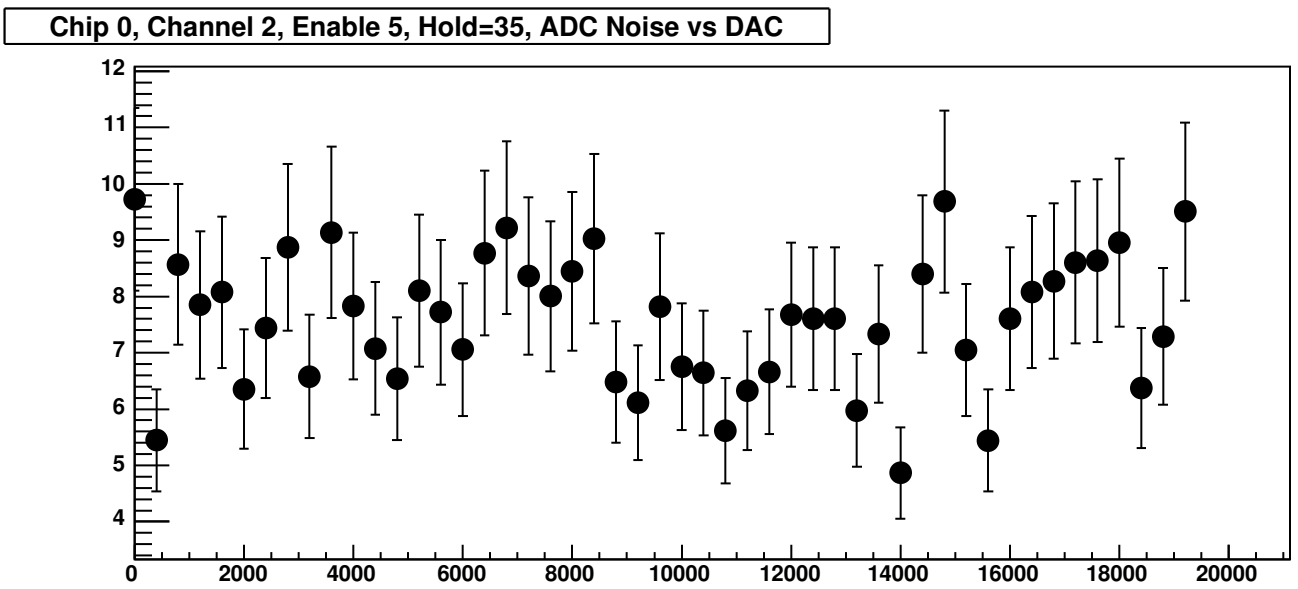

Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
160
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

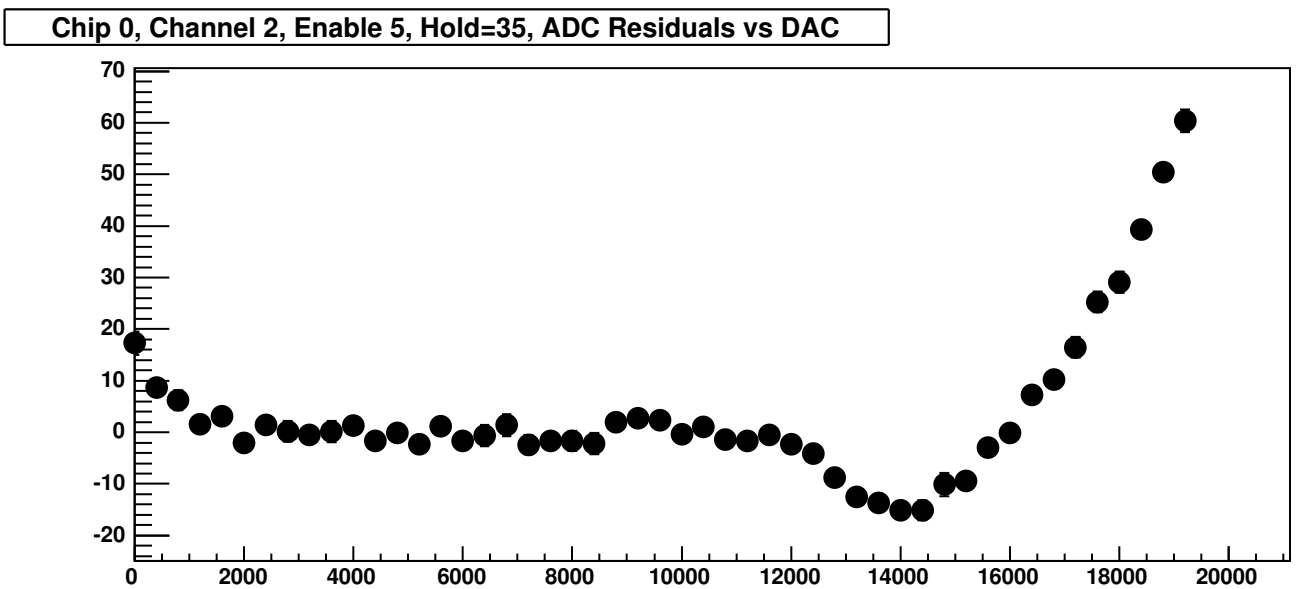

Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.14 / 23
p0 467.6 ± 0.7397
p1 0.01866 ± 0.0001109
900
800
700
600
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

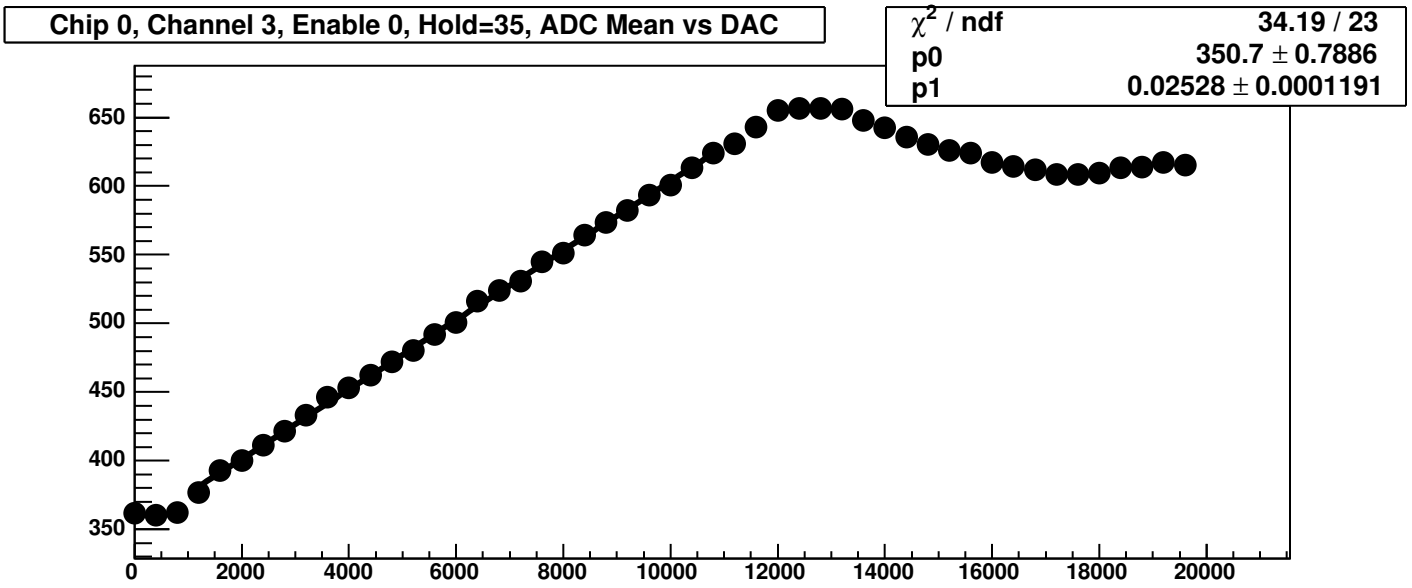

Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

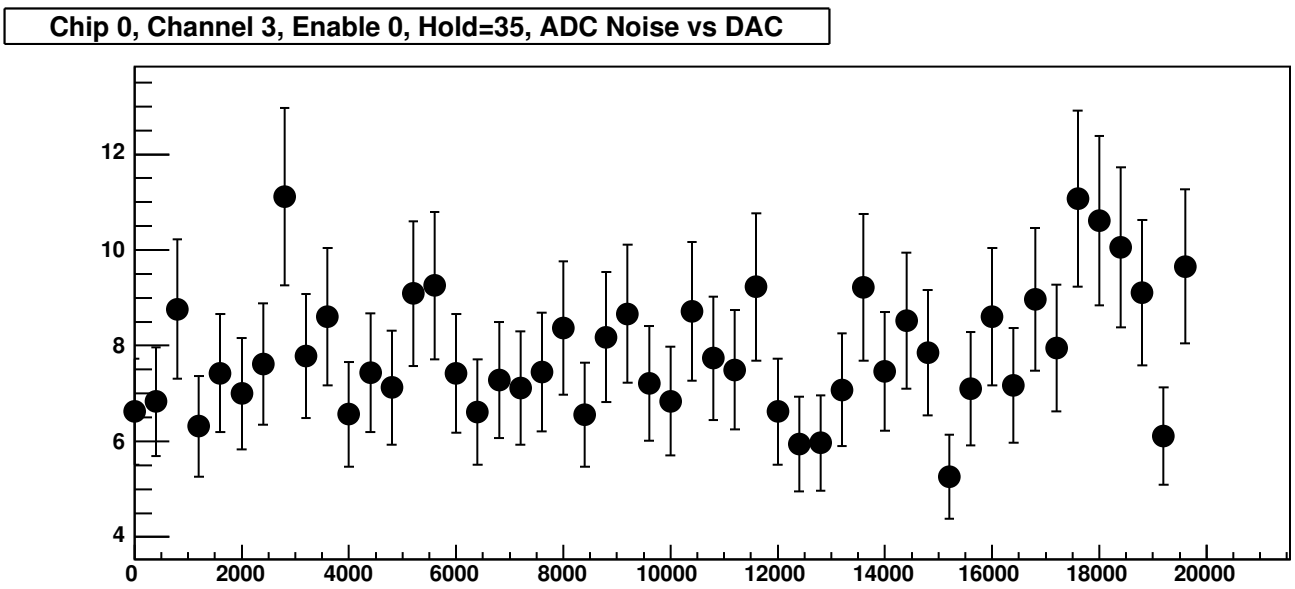

Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
70
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

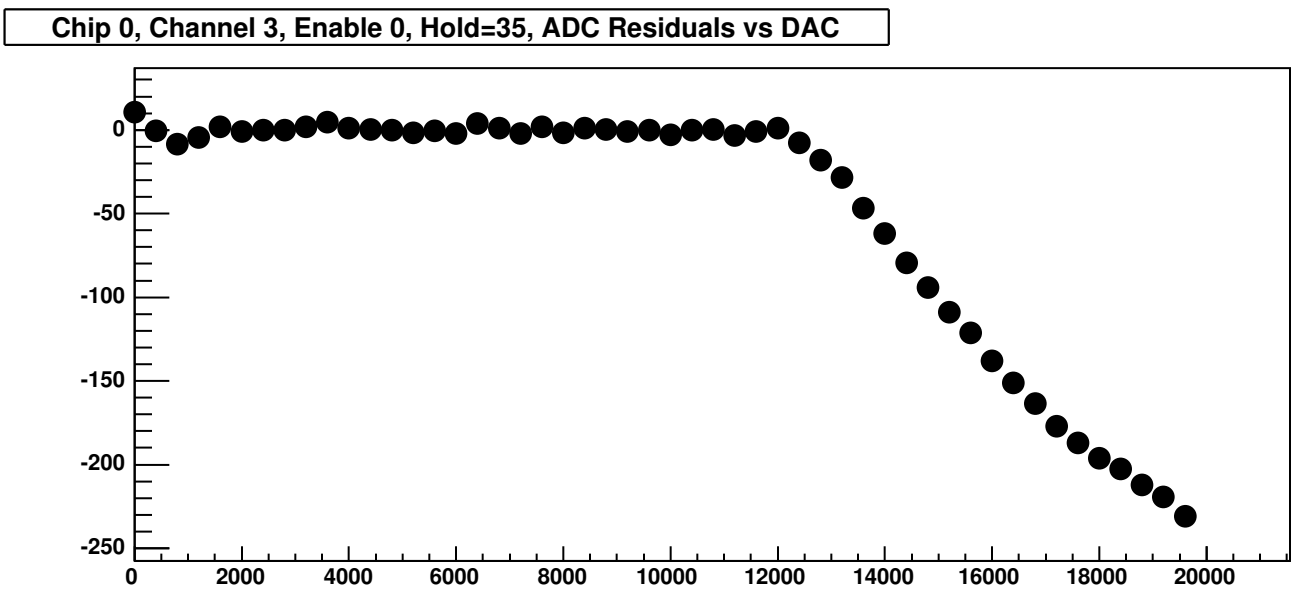

Chip 0, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 25.01 / 23
p0 467.1 ± 0.7758
p1 0.01753 ± 0.0001118
900
850
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

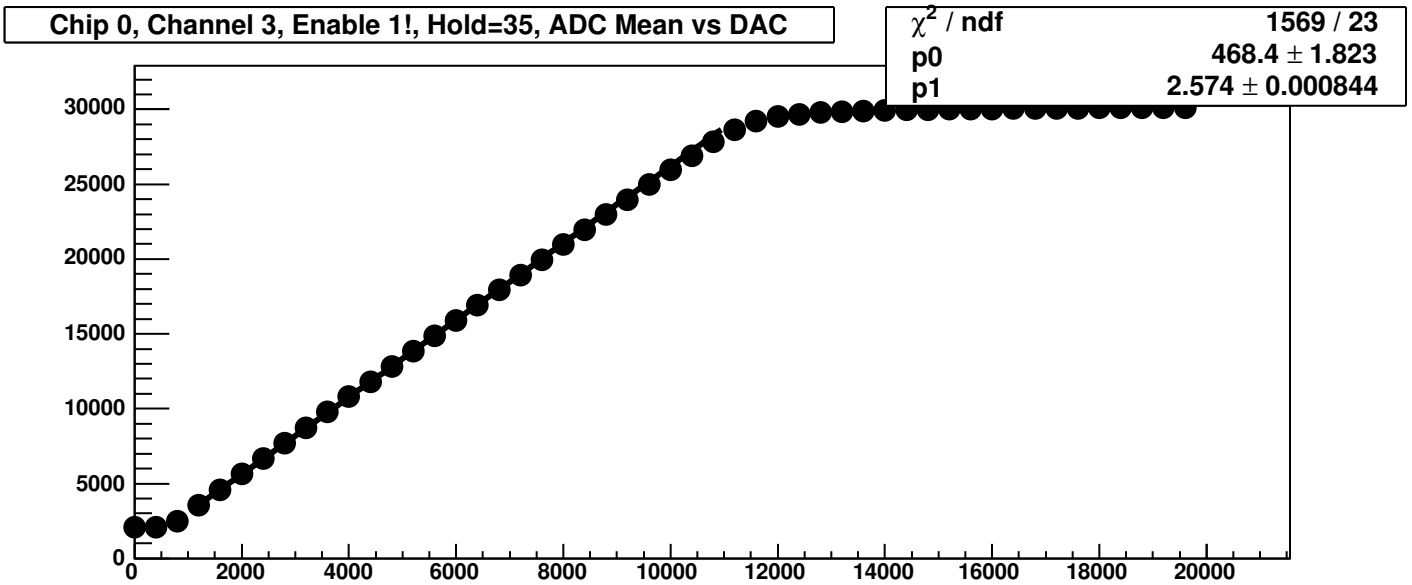

Chip 0, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

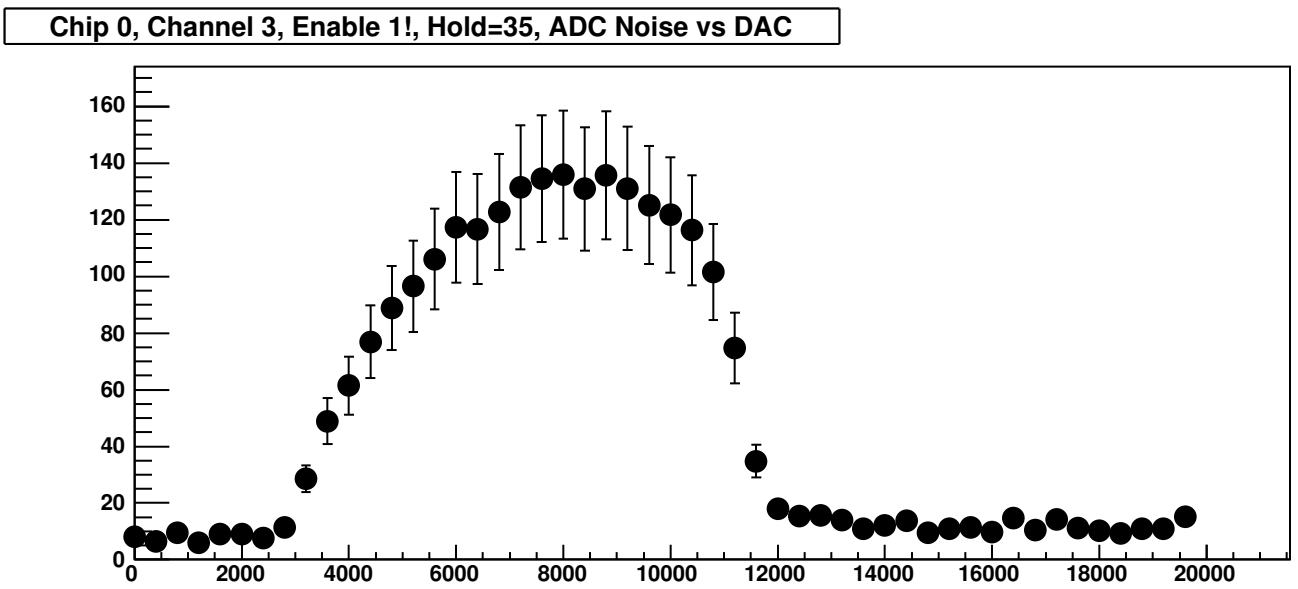

Chip 0, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
70
60
50
40
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

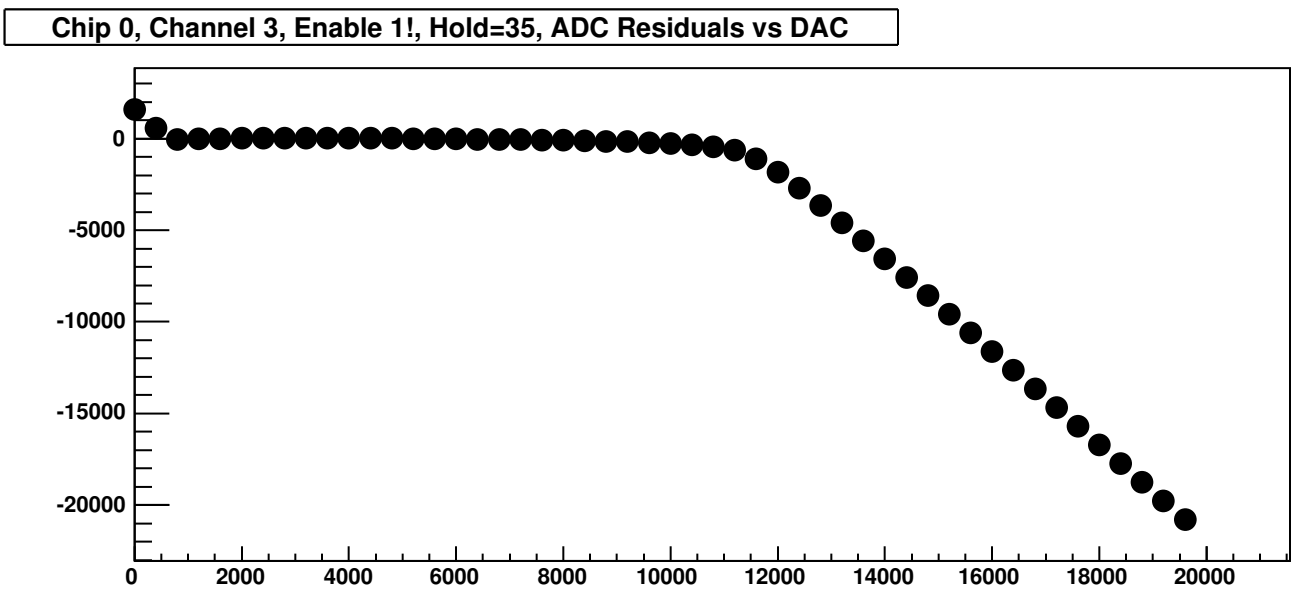

Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 34.19 / 23
p0 350.7 ± 0.7886
p1 0.02528 ± 0.0001191
650
600
550
500
450
400
350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

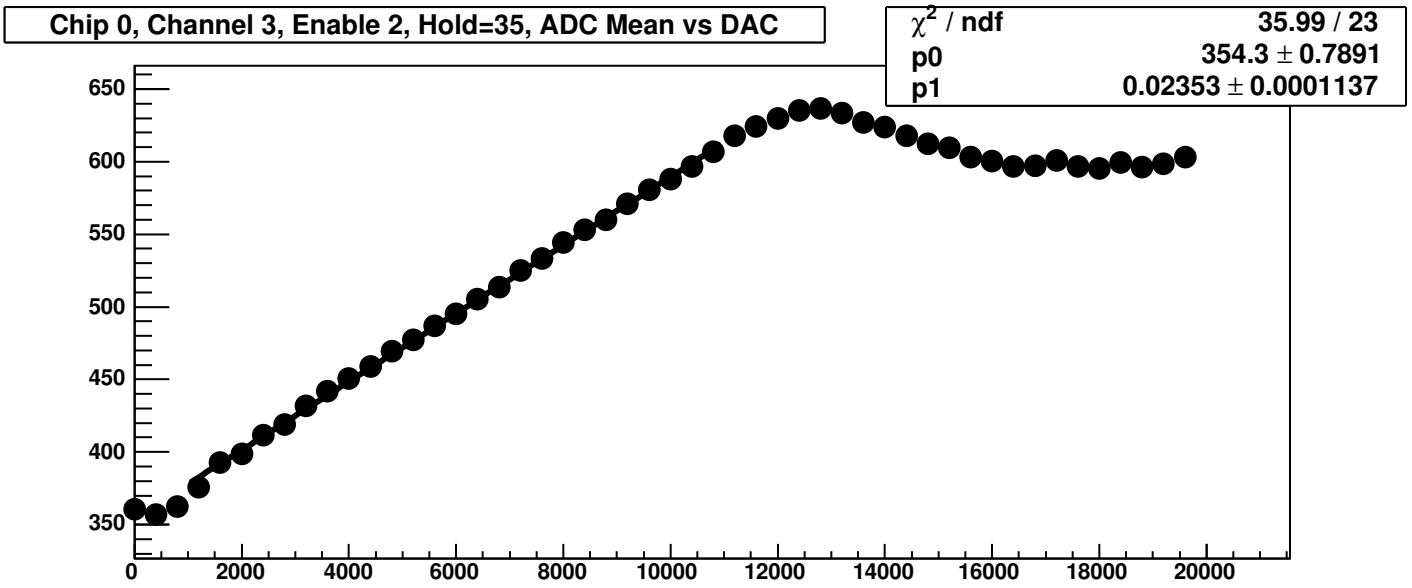

Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

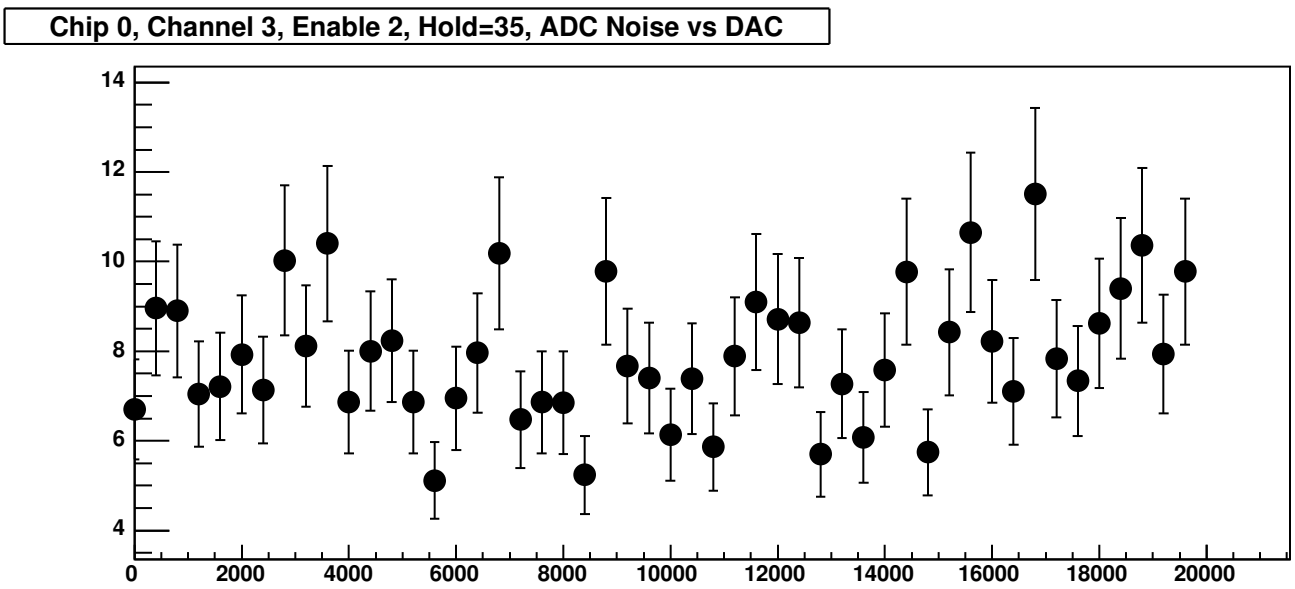

Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

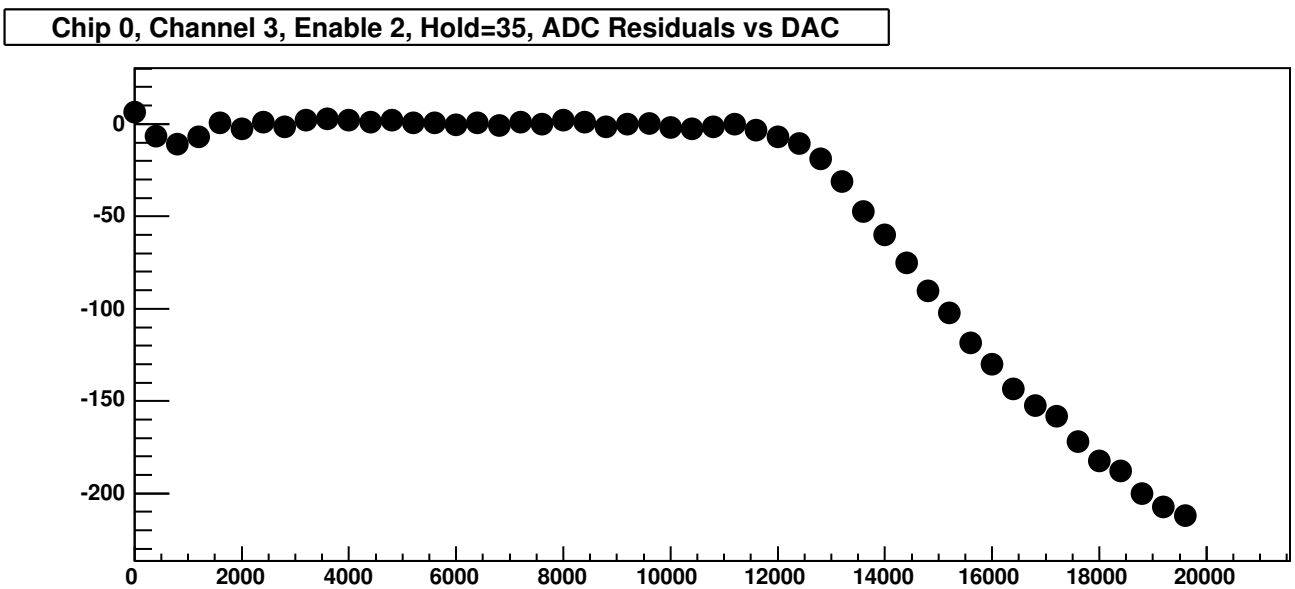

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1569 / 23
p0 468.4 ± 1.823
p1 2.574 ± 0.000844
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

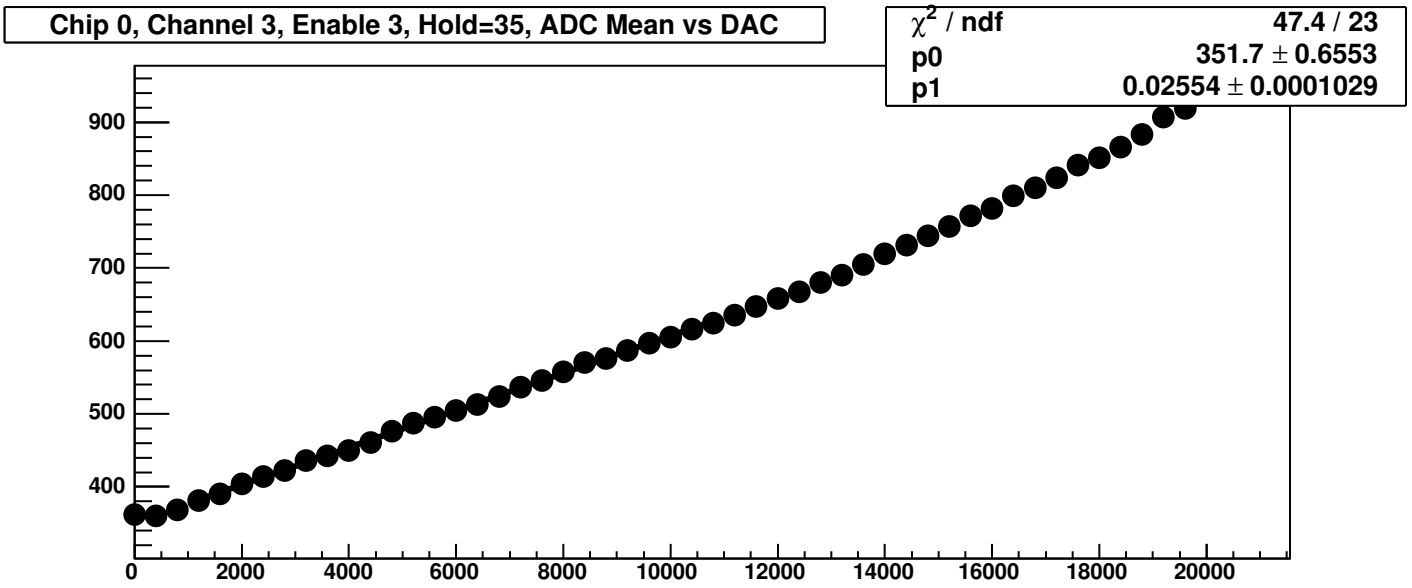

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

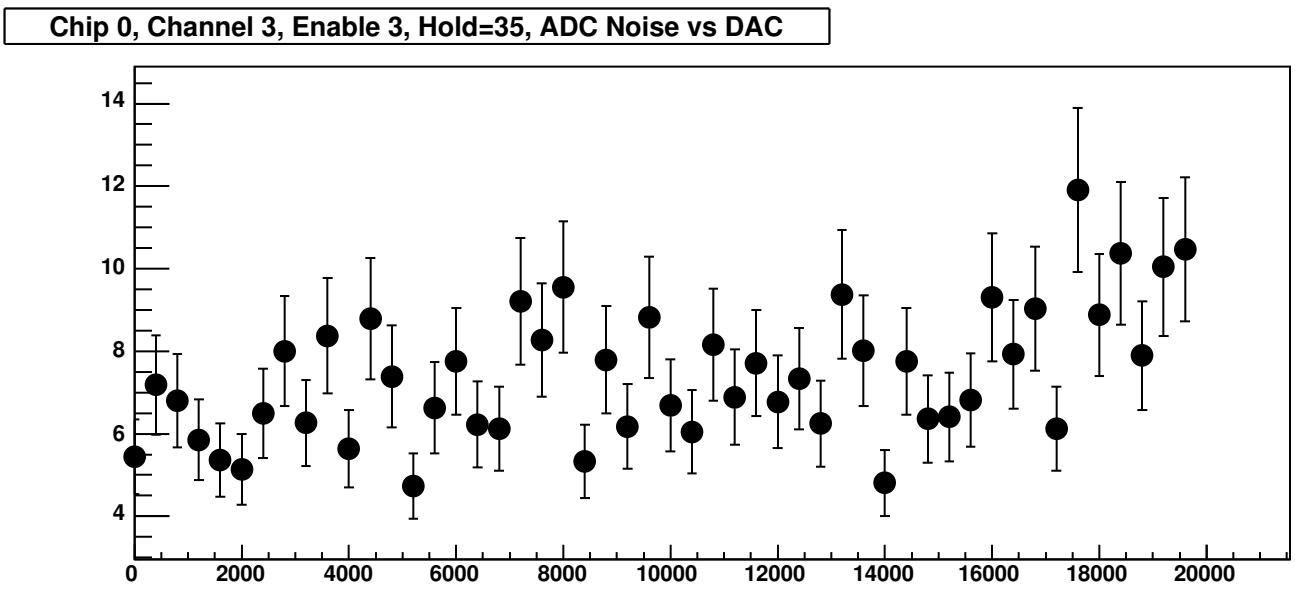

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

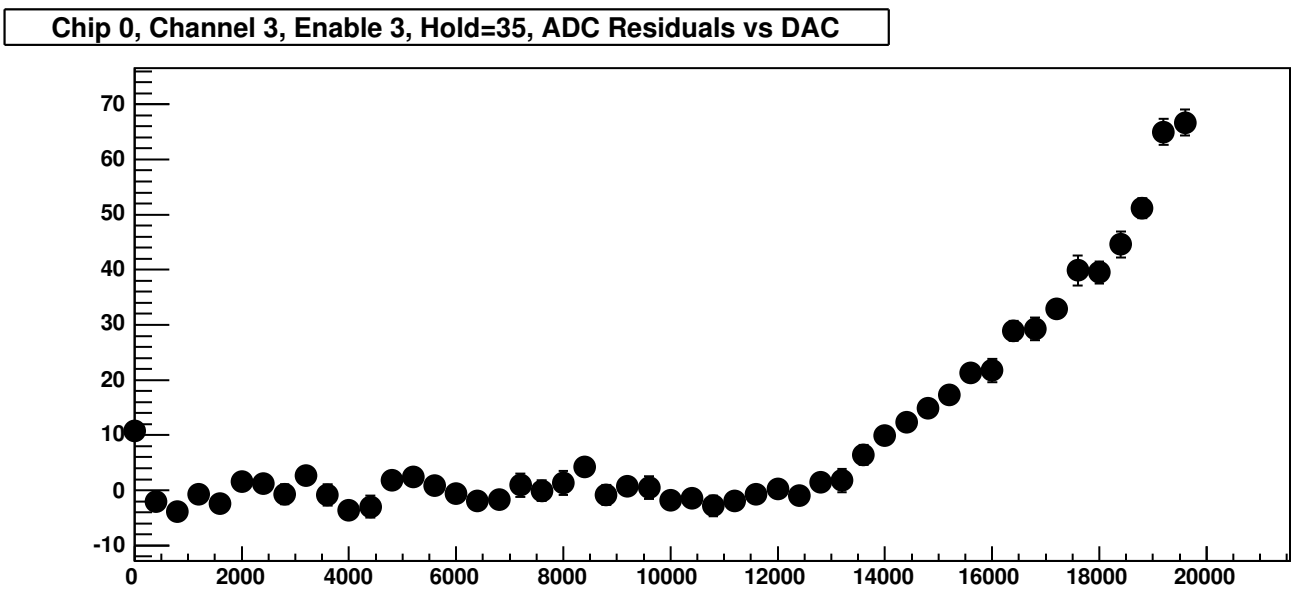

Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.99 / 23
p0 354.3 ± 0.7891
p1 0.02353 ± 0.0001137
650
600
550
500
450
400
350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

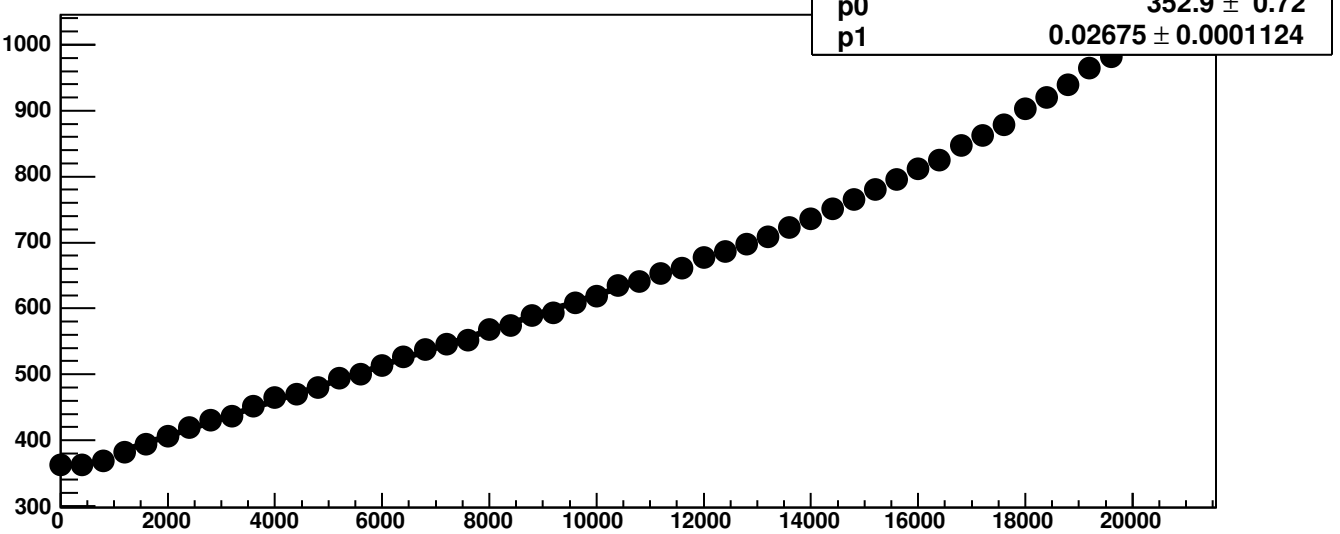

Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 47.4 / 23
p0 351.7 ± 0.6553
p1 0.02554 ± 0.0001029
900
800
700
600
500
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

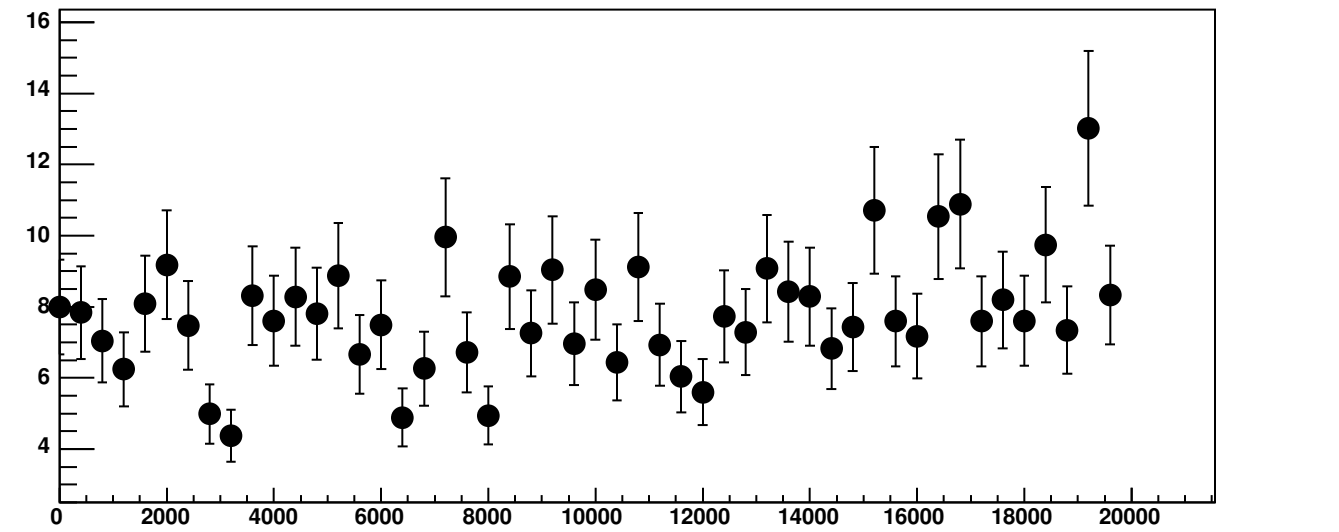

Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
70
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

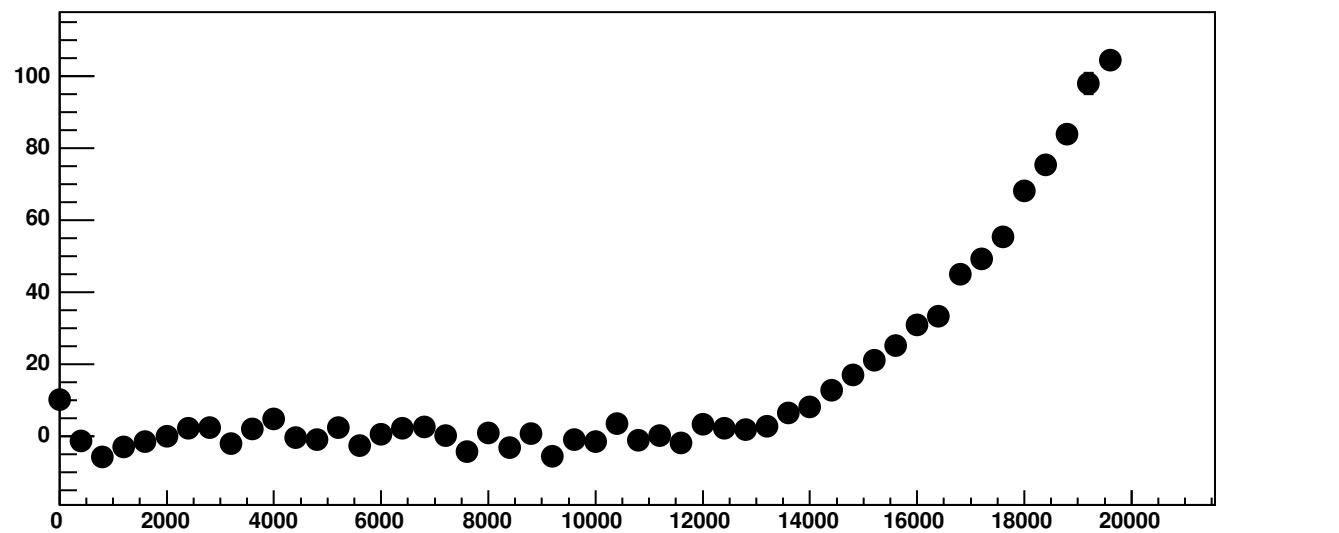
Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

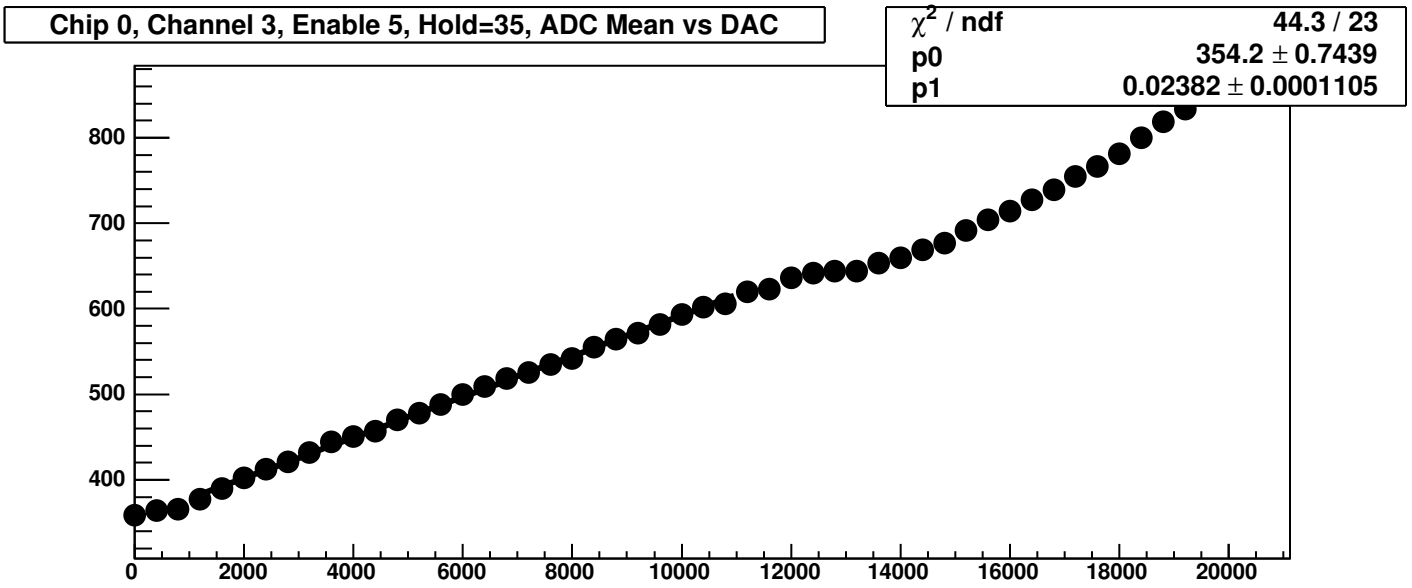


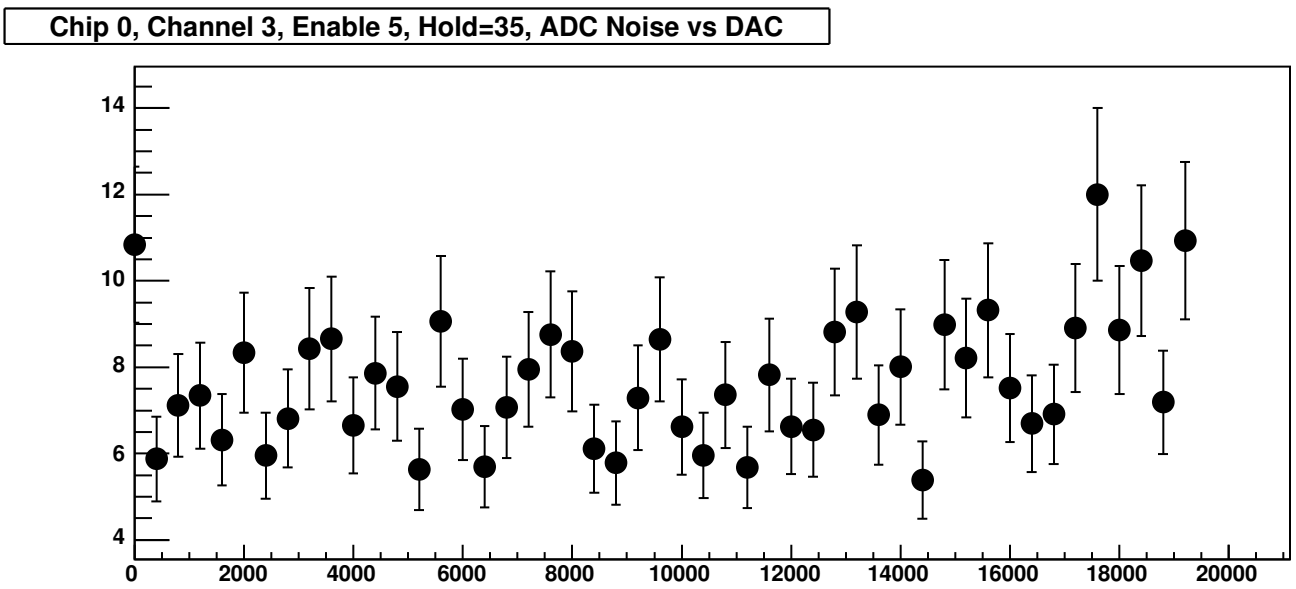
Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

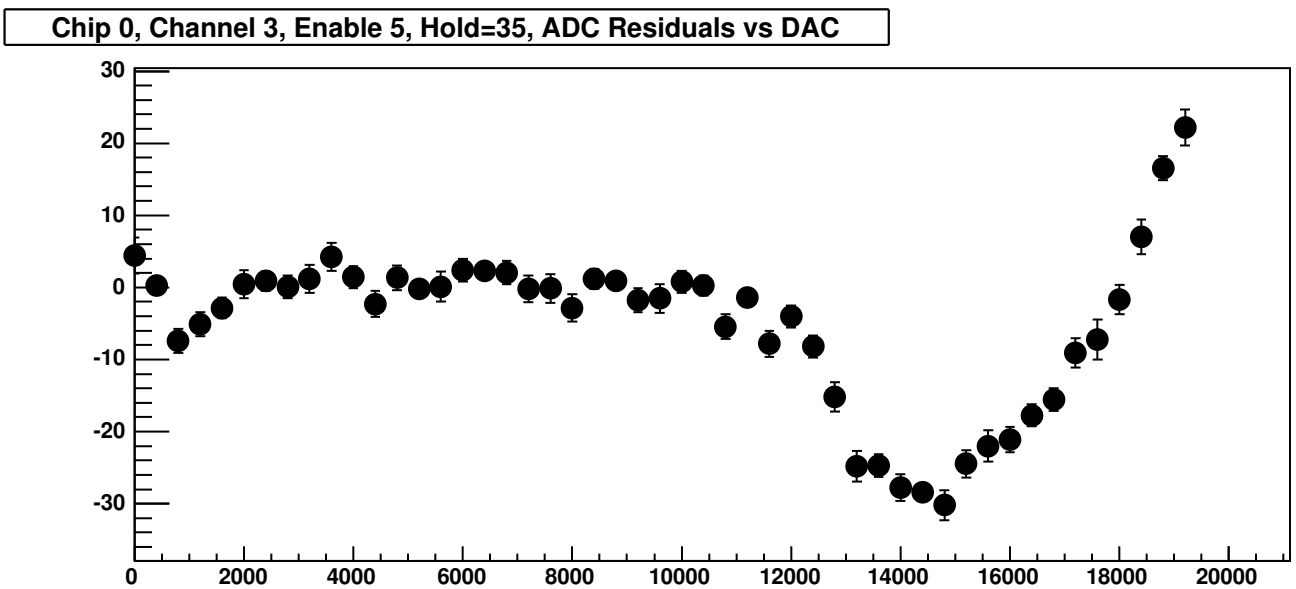


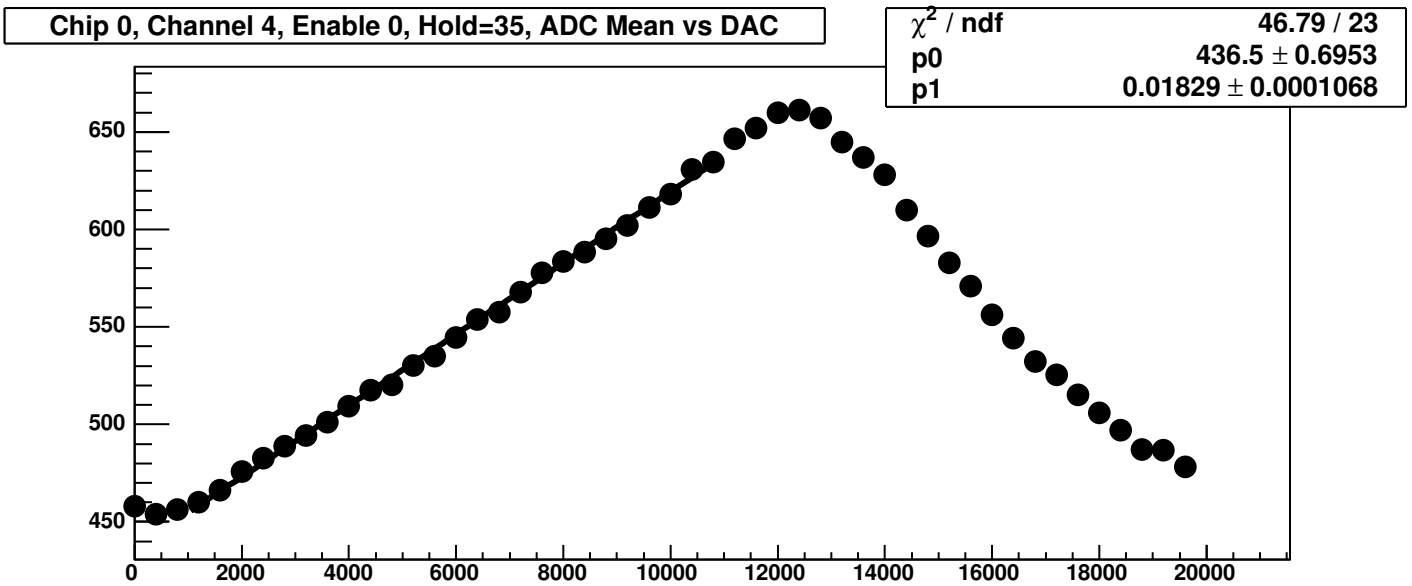
Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

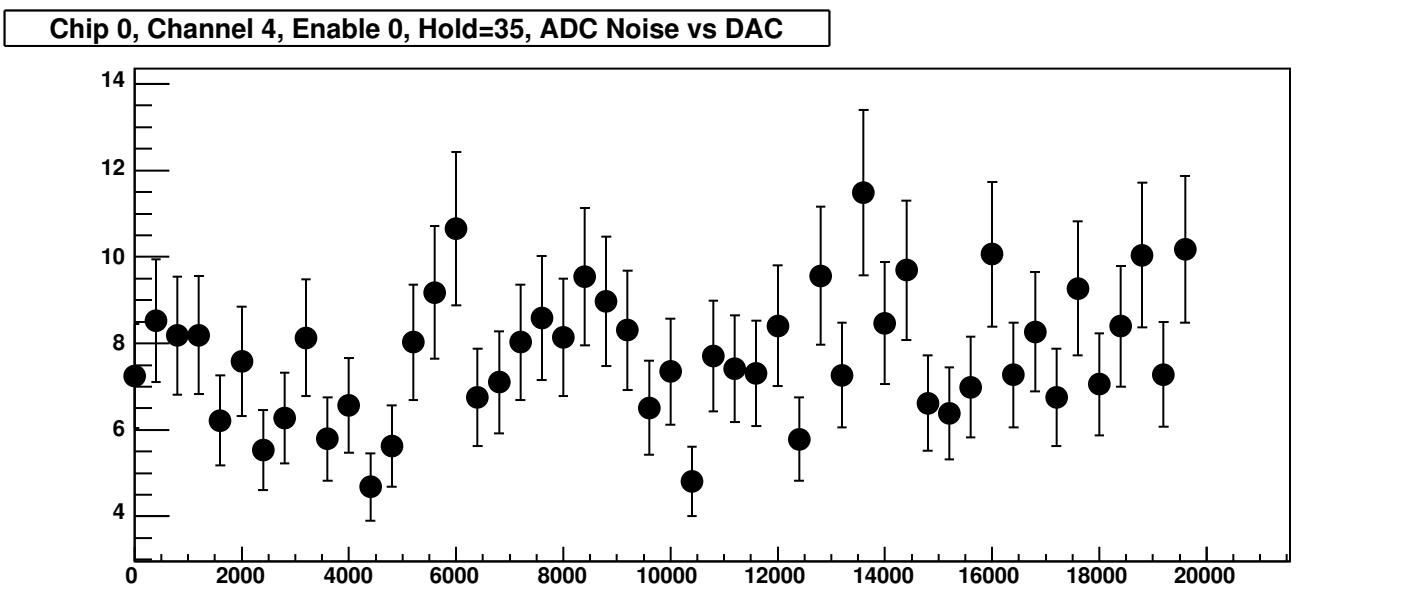


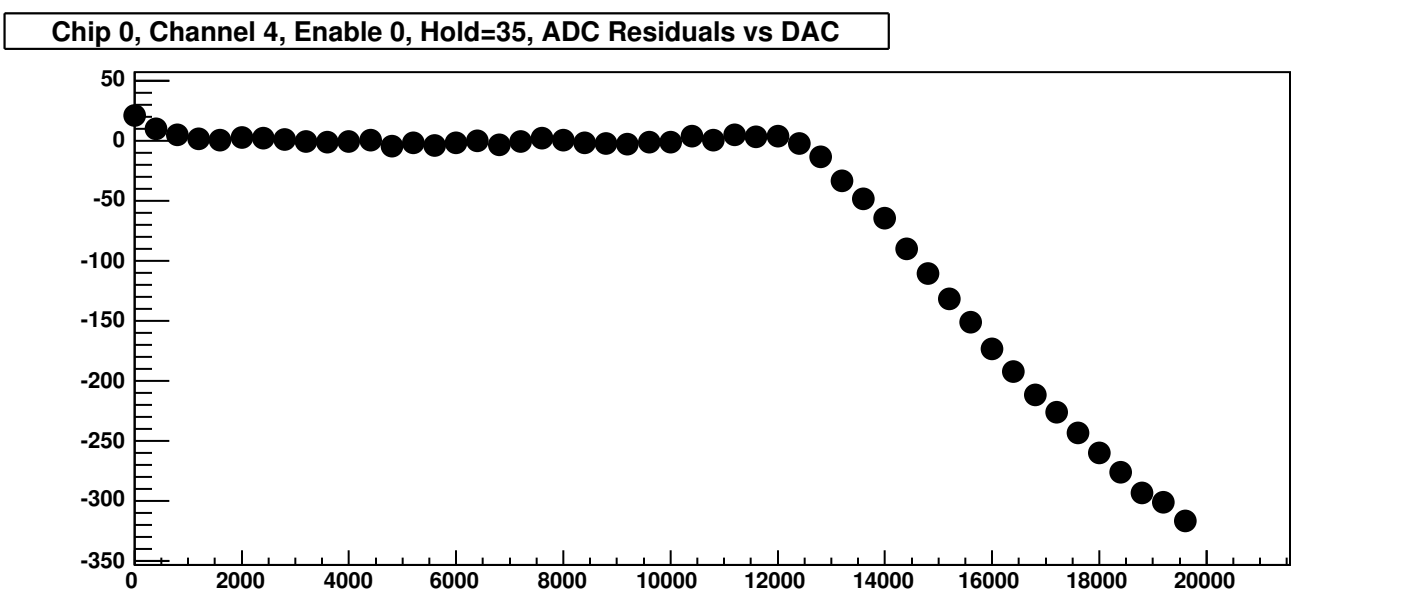

Chip 0, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 44.3 / 23
p0 354.2 ± 0.7439
p1 0.02382 ± 0.0001105
800
700
600
500
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

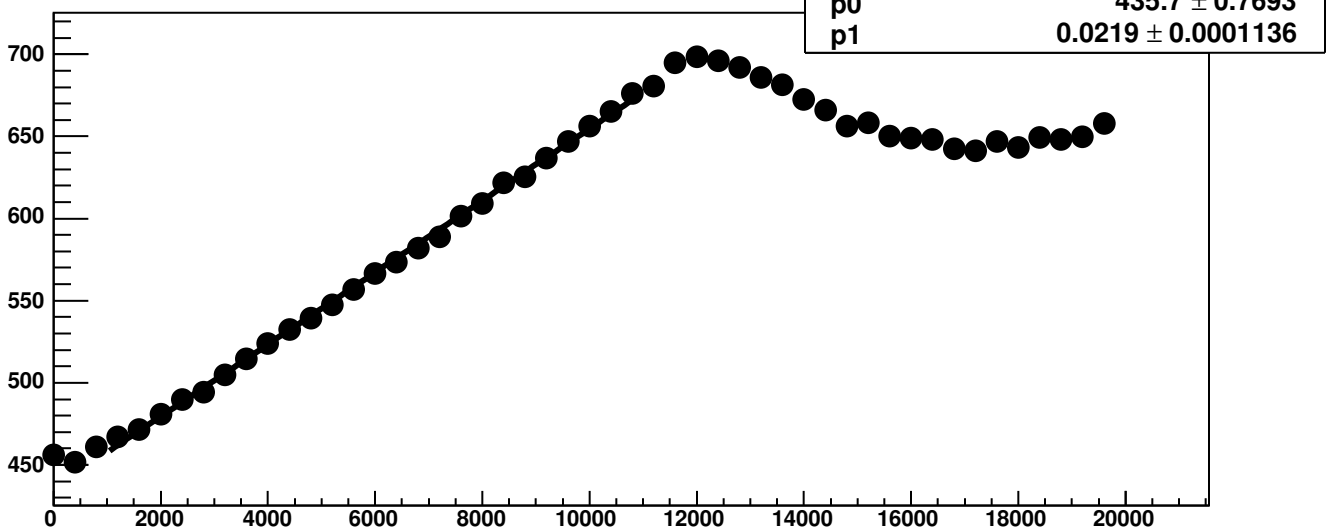

Chip 0, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 46.79 / 23
p0 436.5 ± 0.6953
p1 0.01829 ± 0.0001068
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

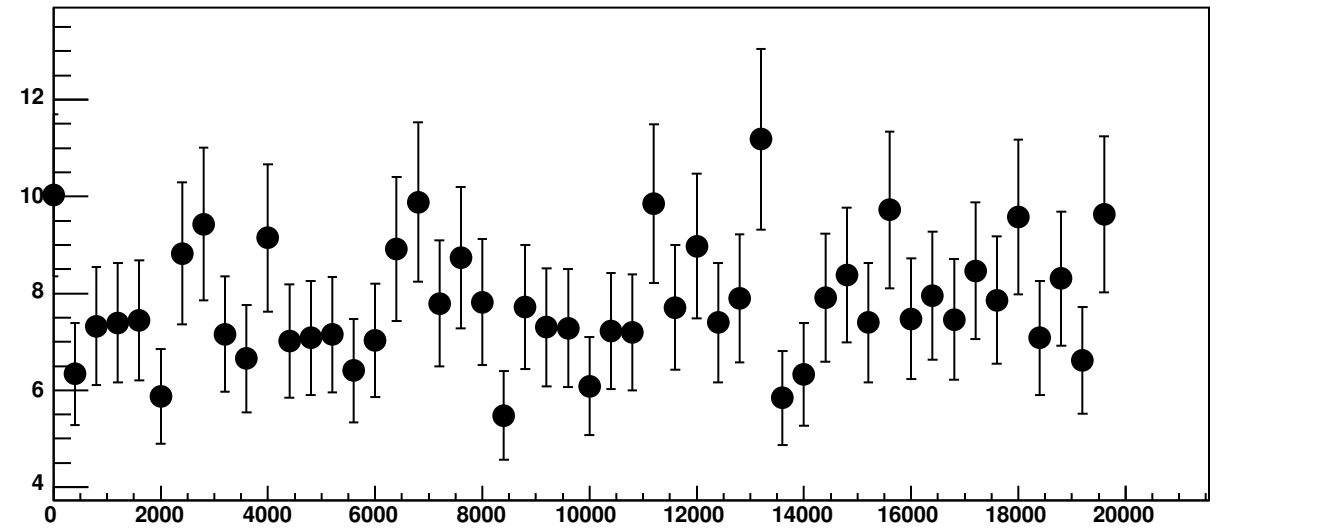

Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

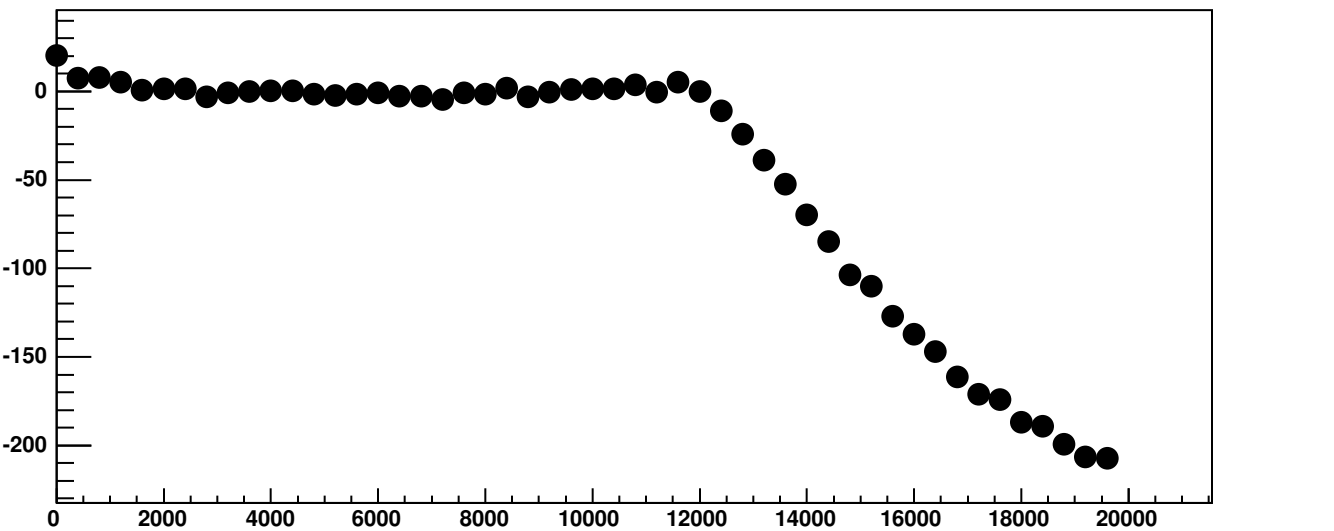
Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC

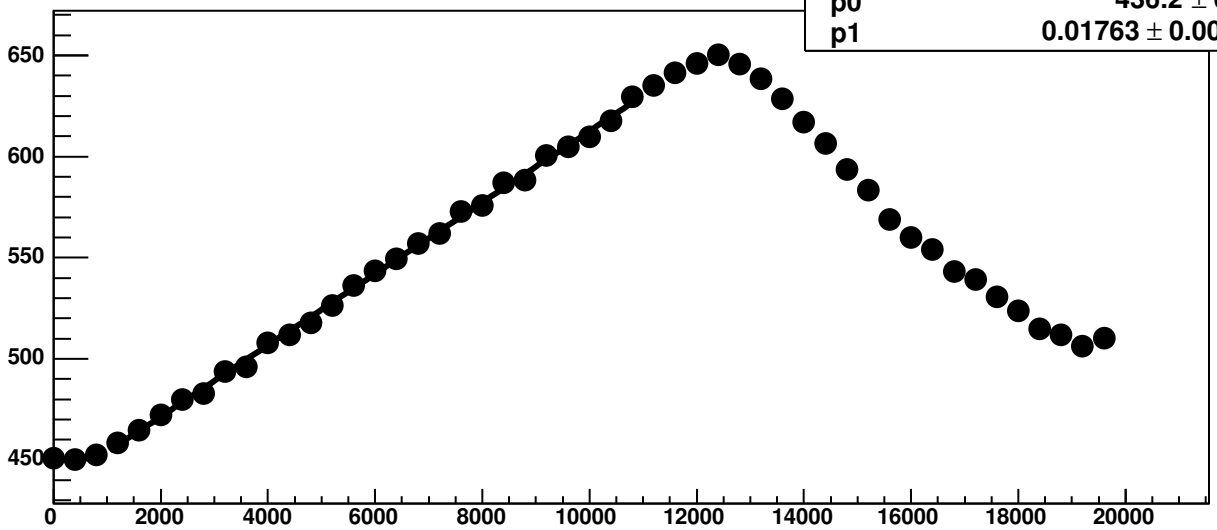


Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

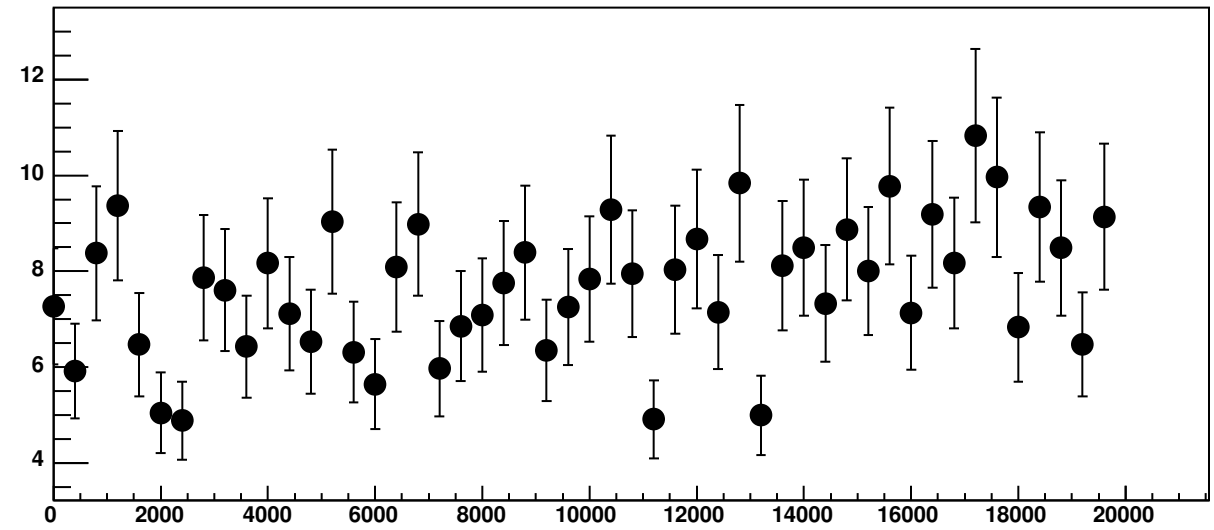


Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

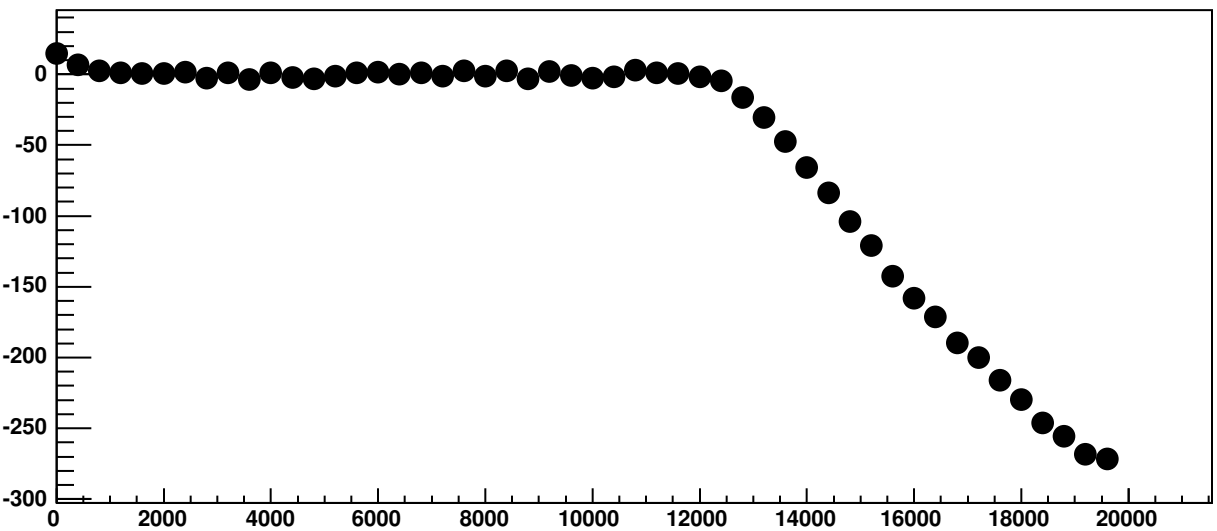
χ^2 / ndf	36.63 / 23
p0	436.2 $\pm$ 0.7085
p1	0.01763 $\pm$ 0.0001124



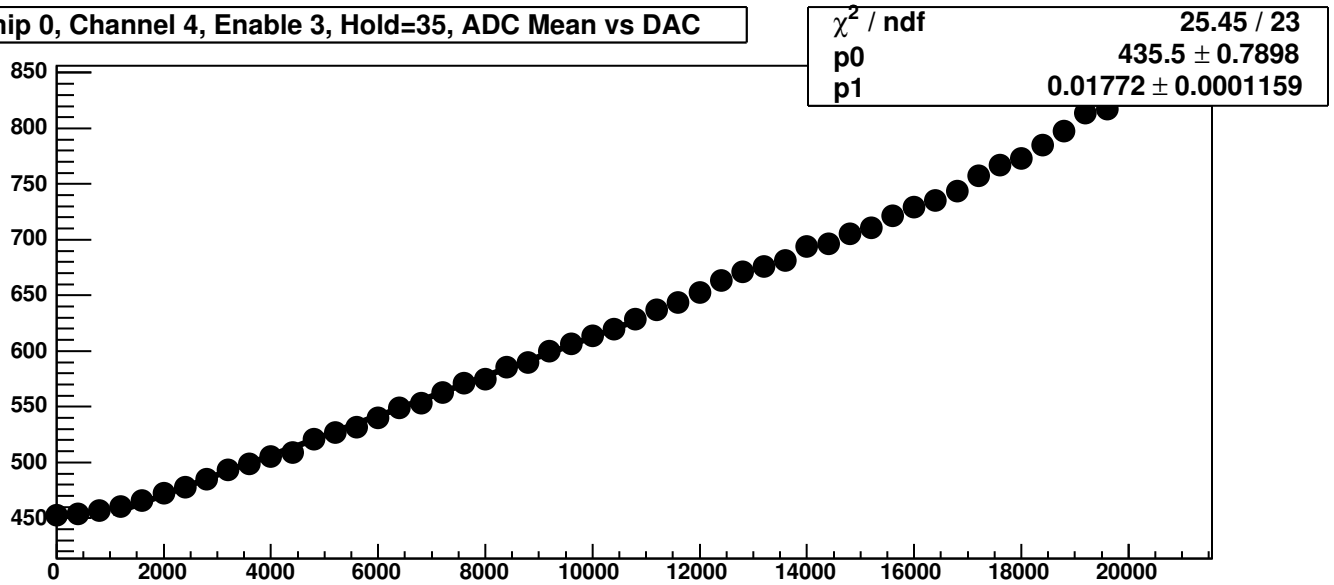
Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



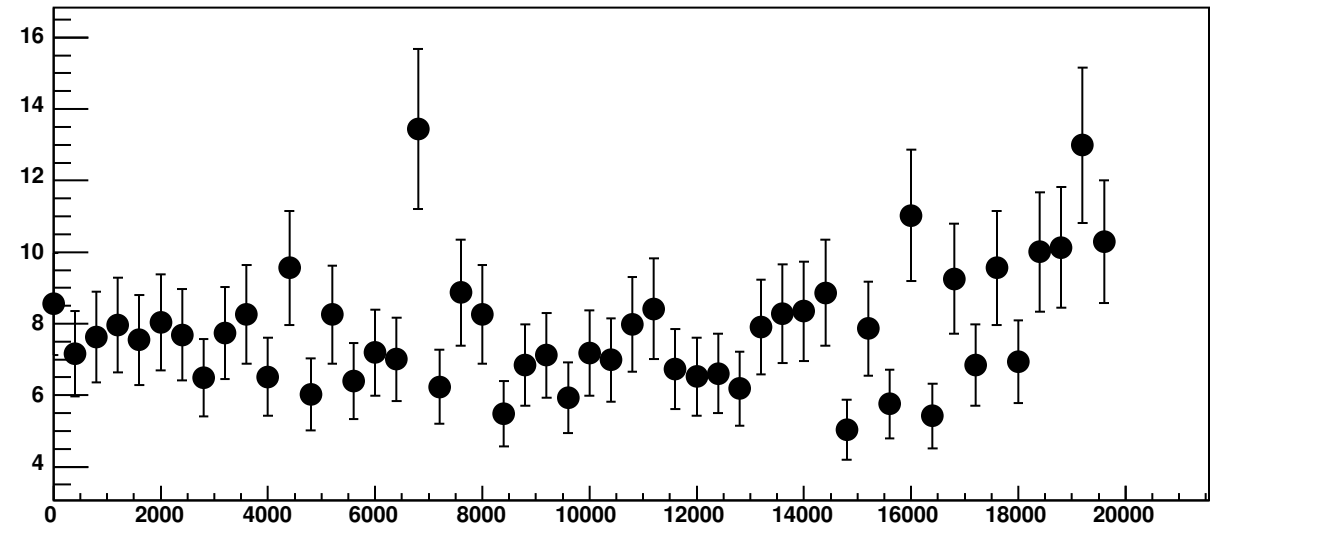
Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



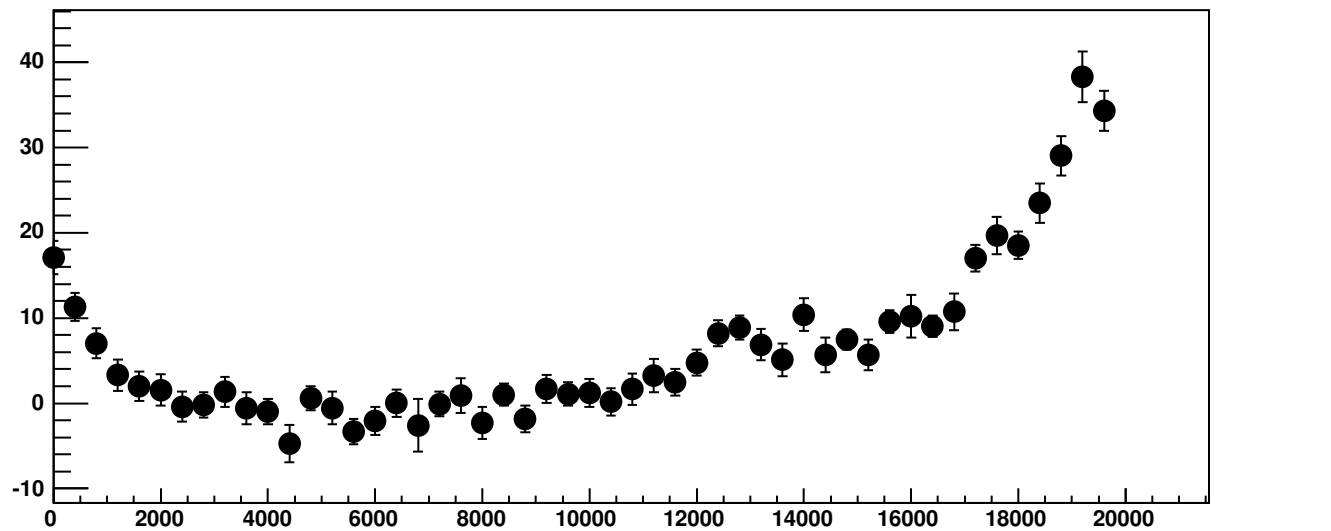
Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

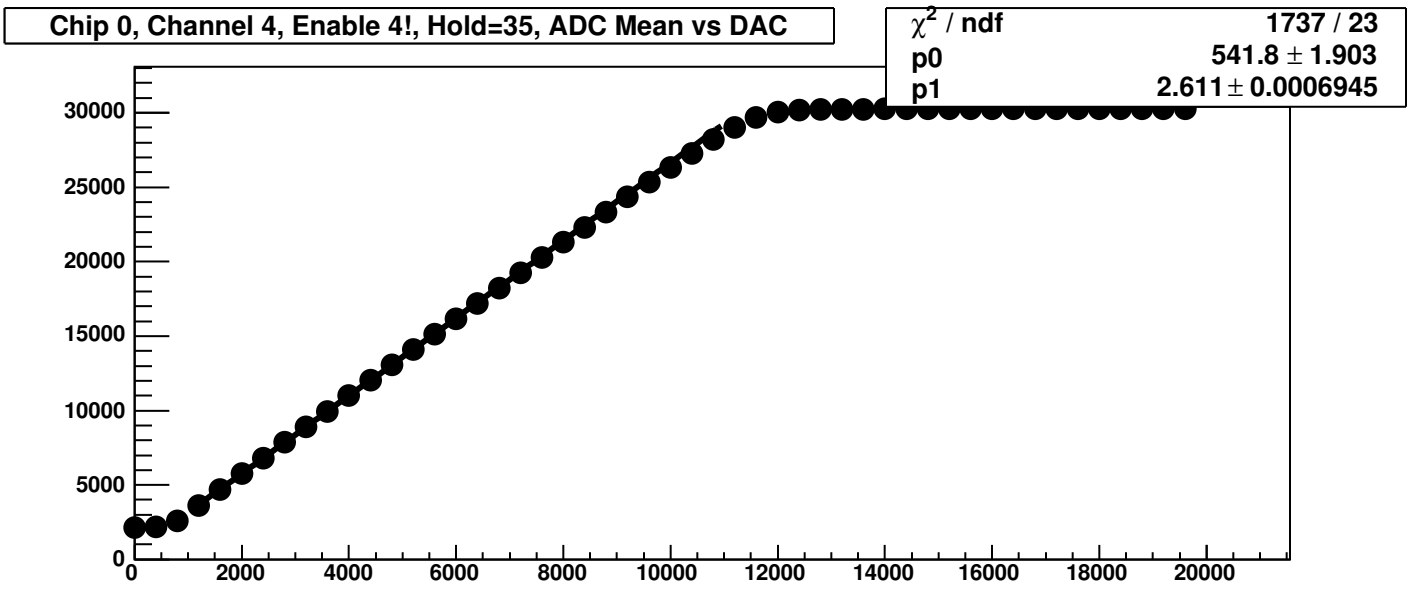


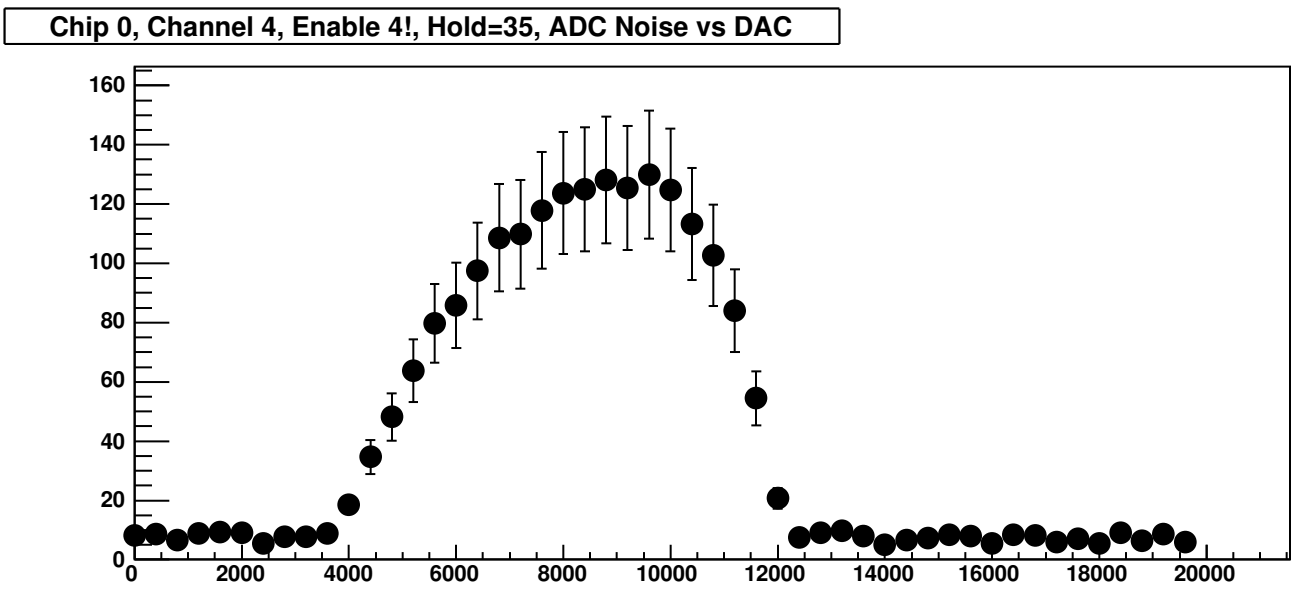
Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

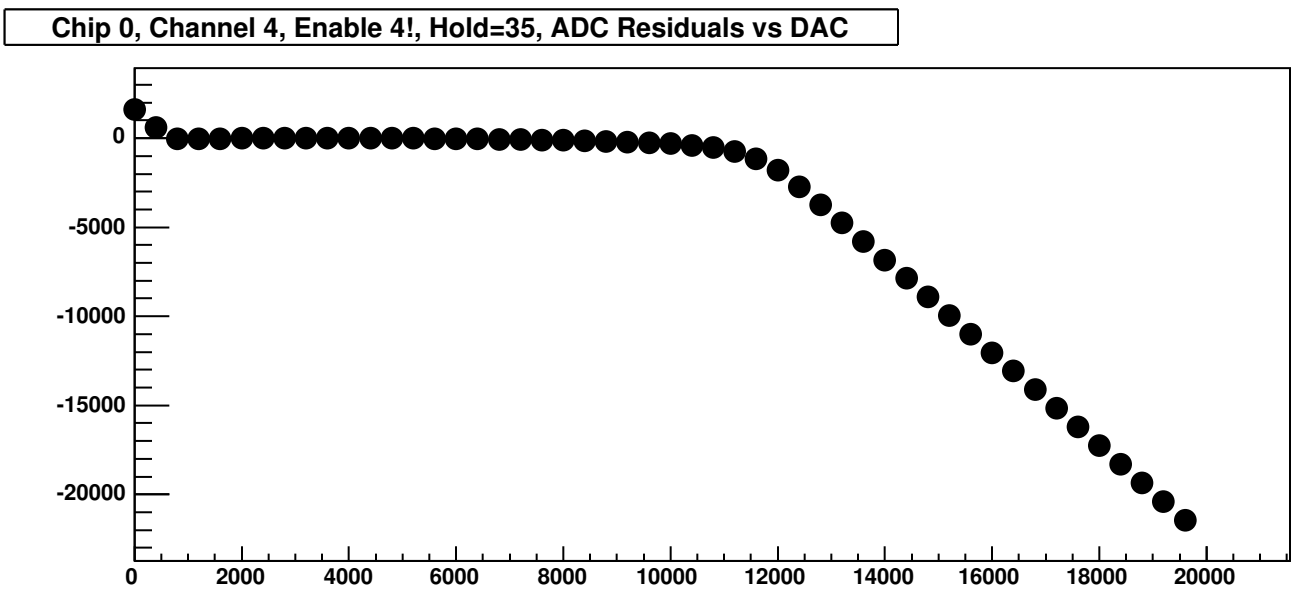


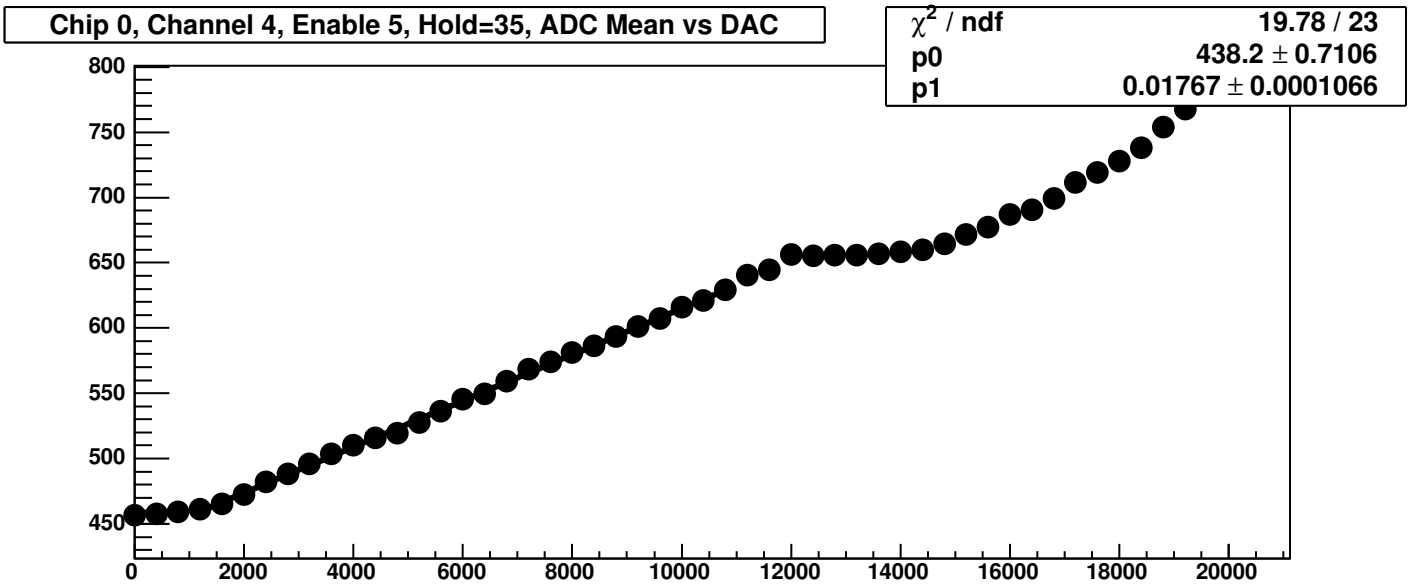
Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

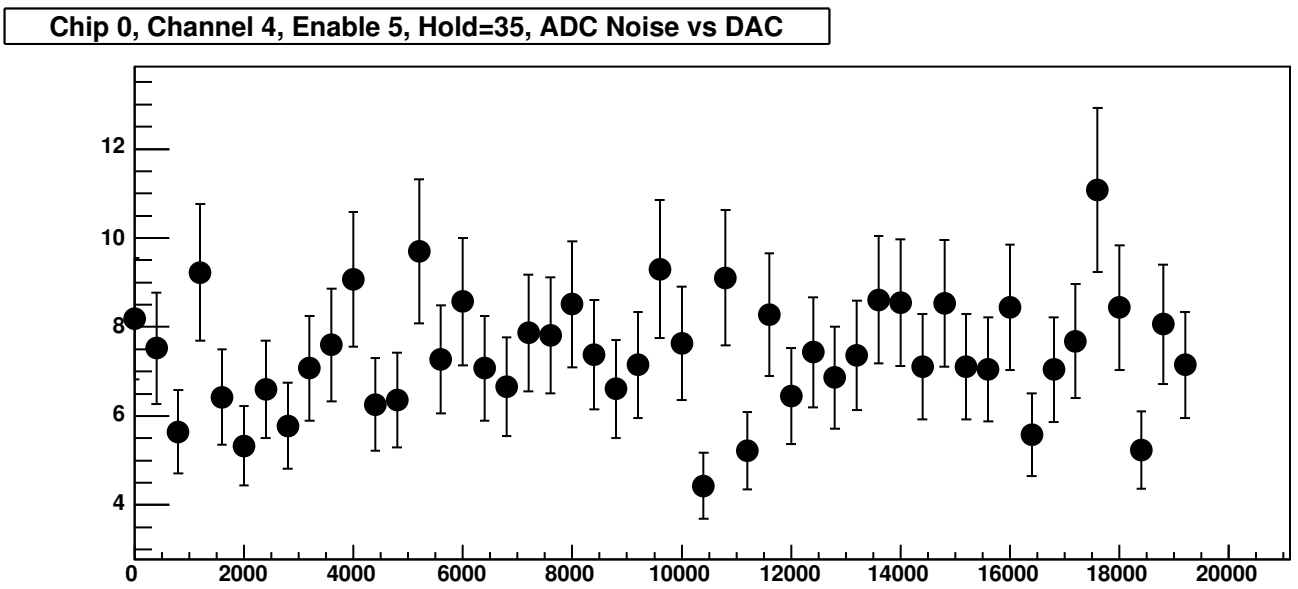


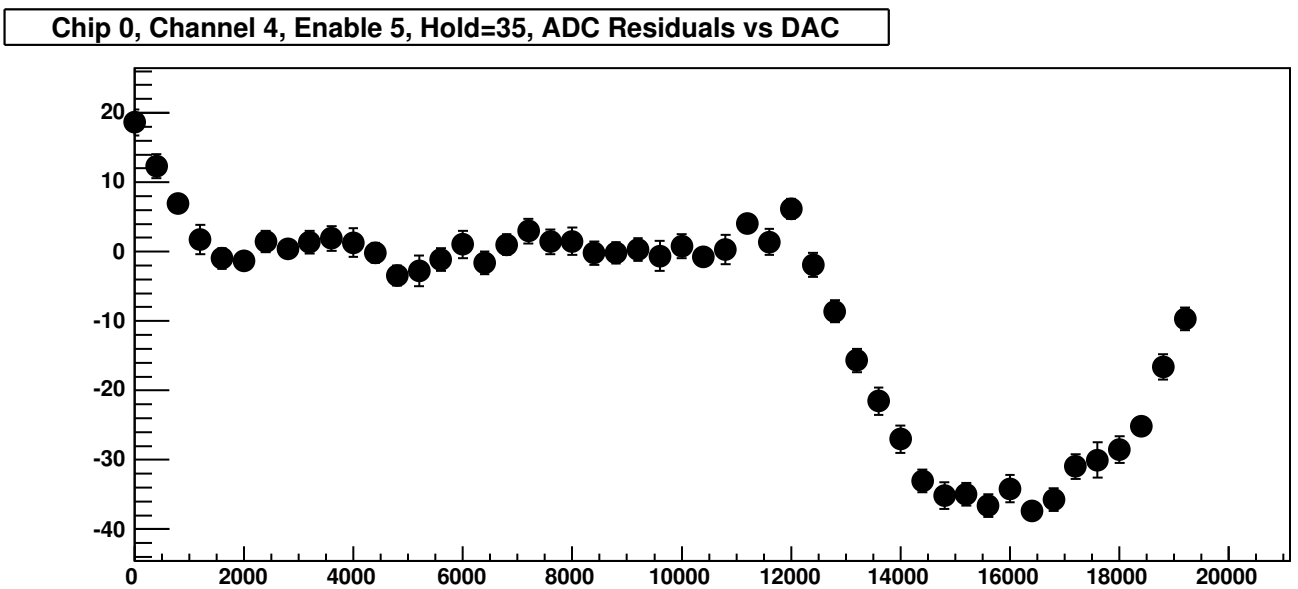

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1737 / 23
p0 541.8 ± 1.903
p1 2.611 ± 0.0006945
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

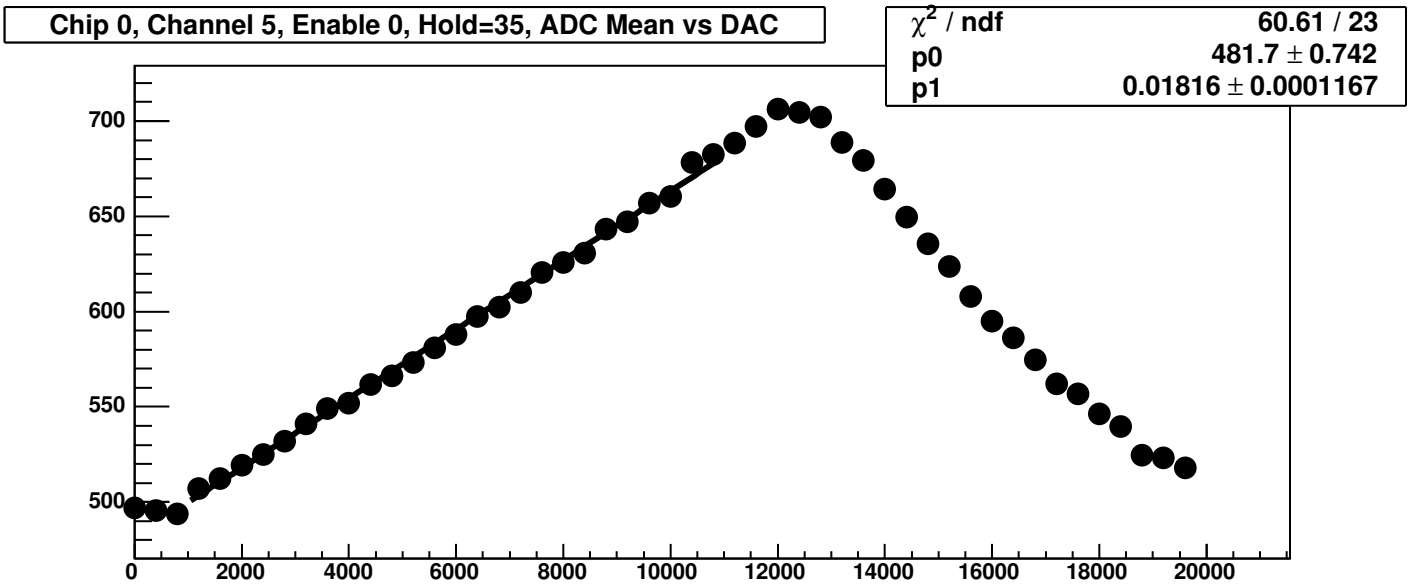

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

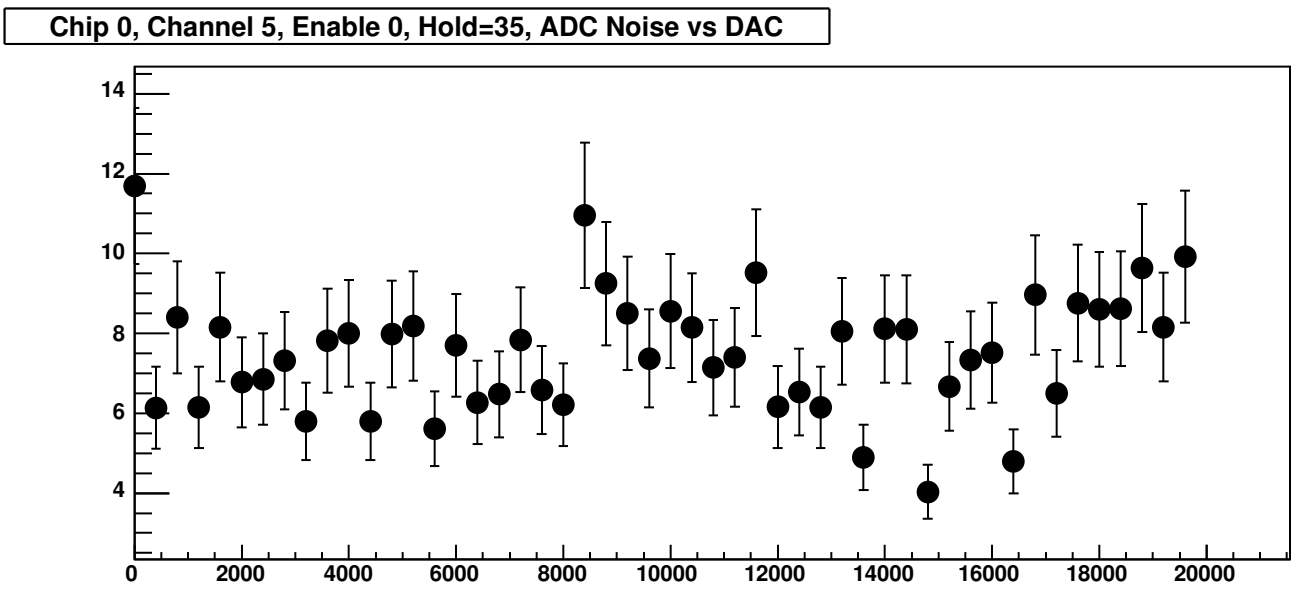

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

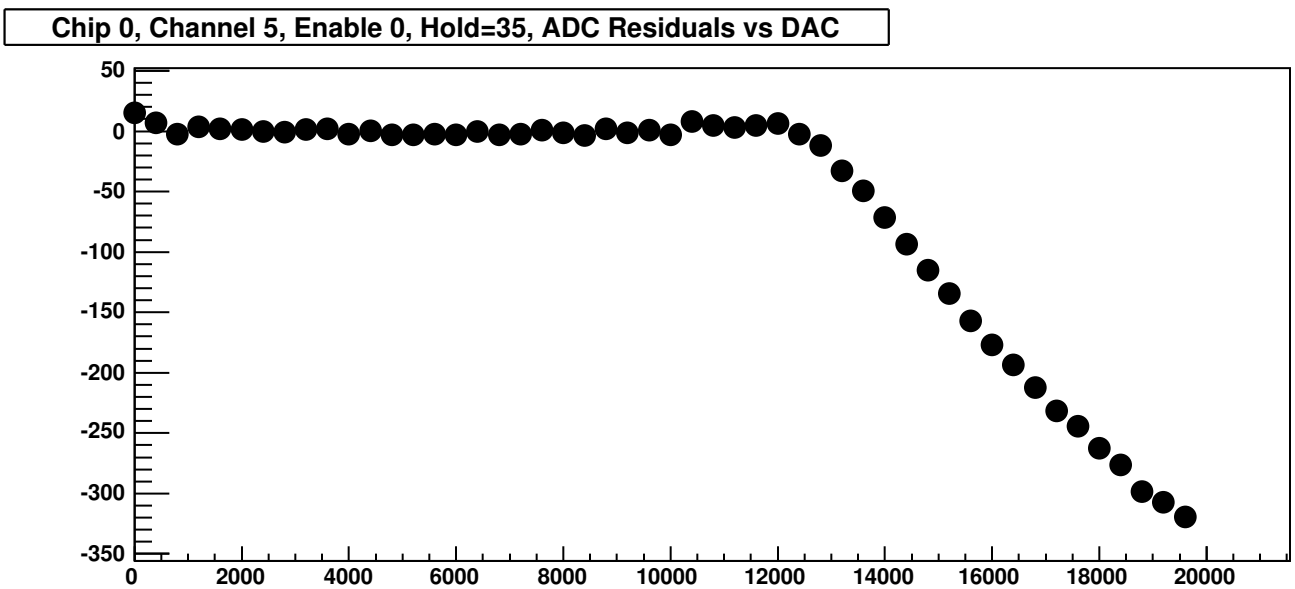

Chip 0, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 19.78 / 23
p0 438.2 ± 0.7106
p1 0.01767 ± 0.0001066
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

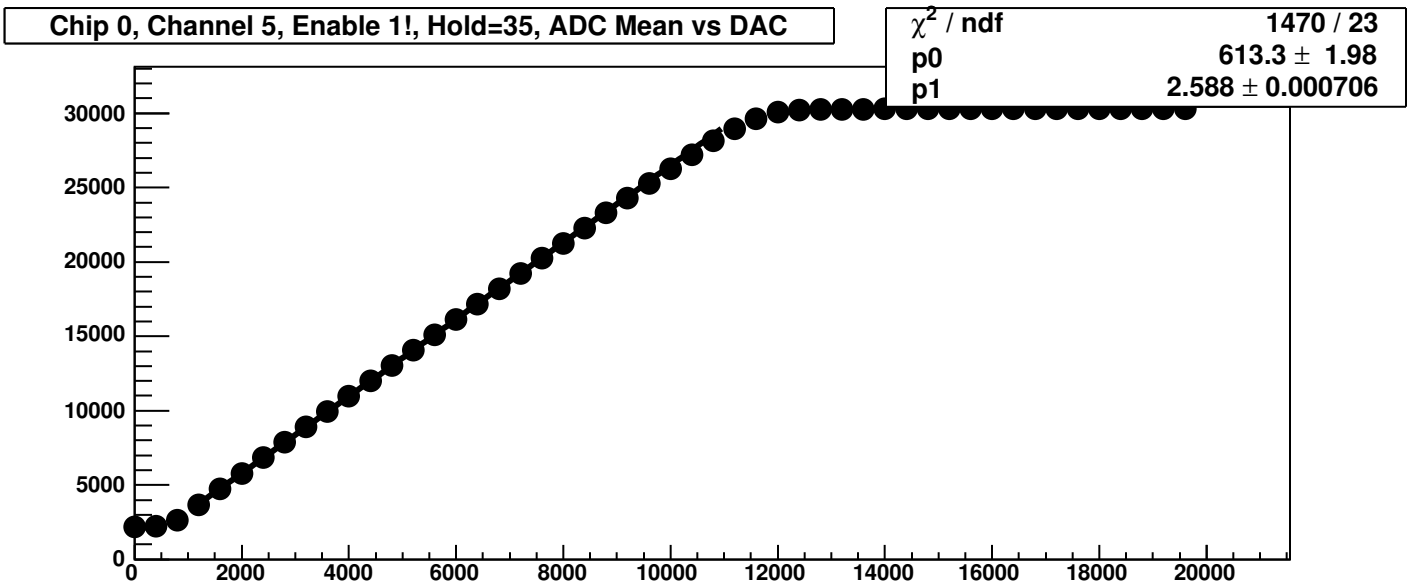

Chip 0, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

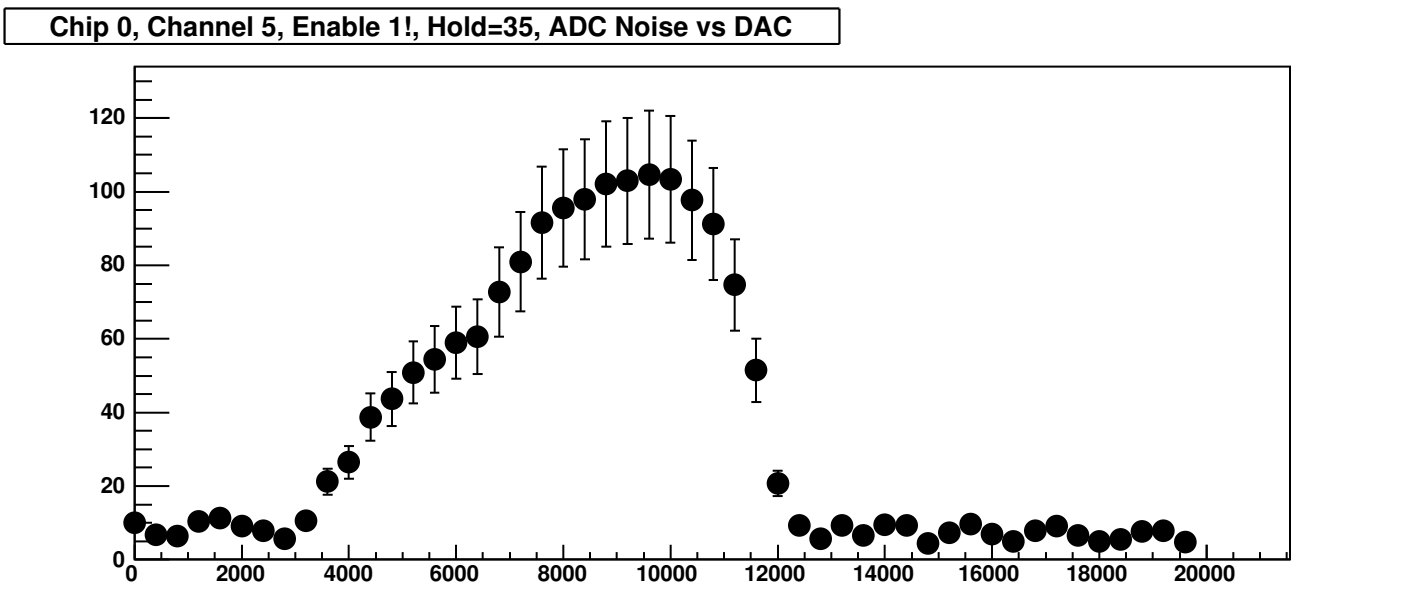

Chip 0, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

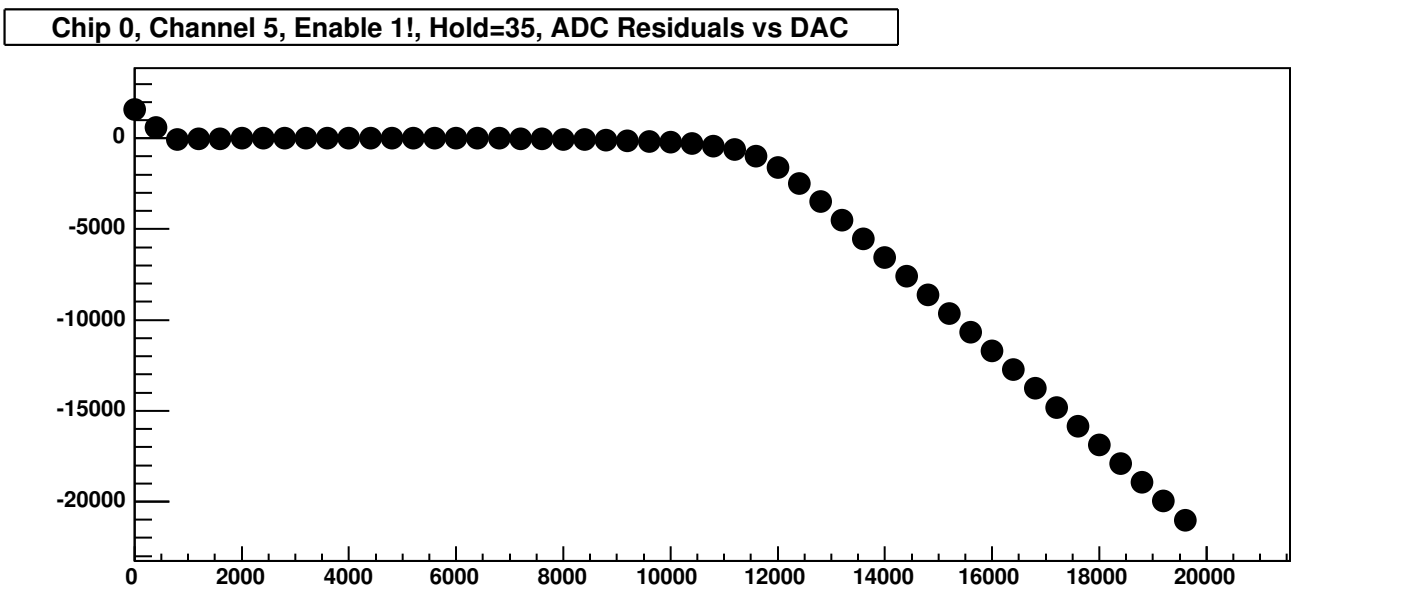

Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 60.61 / 23
p0 481.7 ± 0.742
p1 0.01816 ± 0.0001167
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

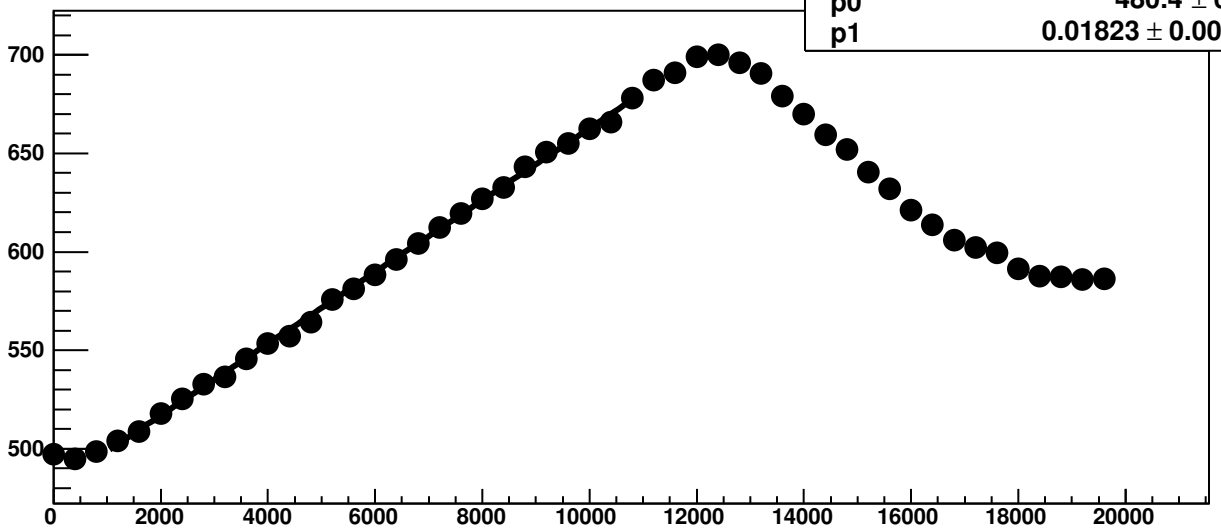

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1470 / 23
p0 613.3 ± 1.98
p1 2.588 ± 0.000706
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

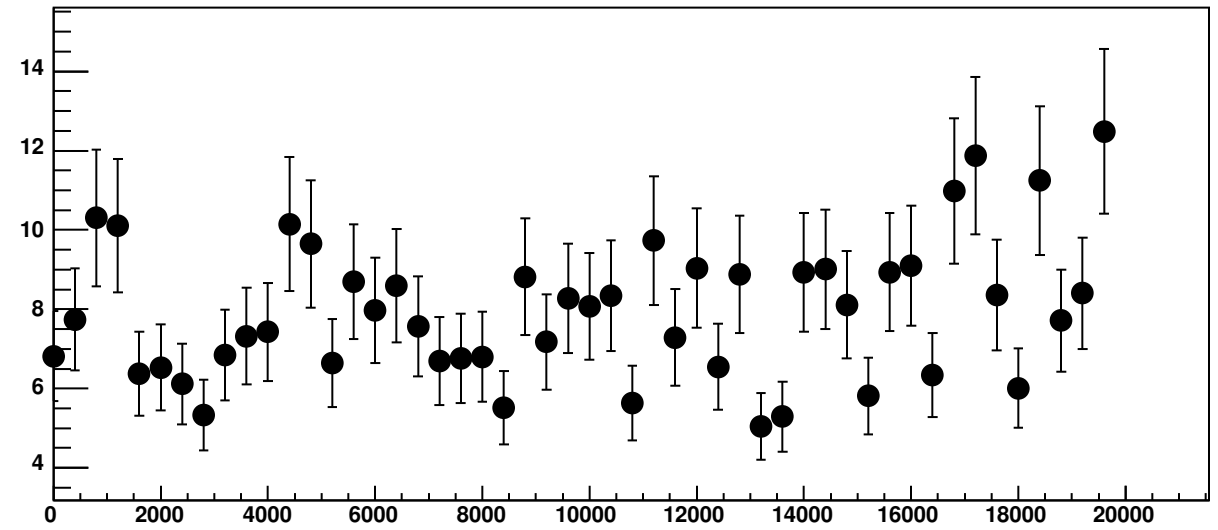

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

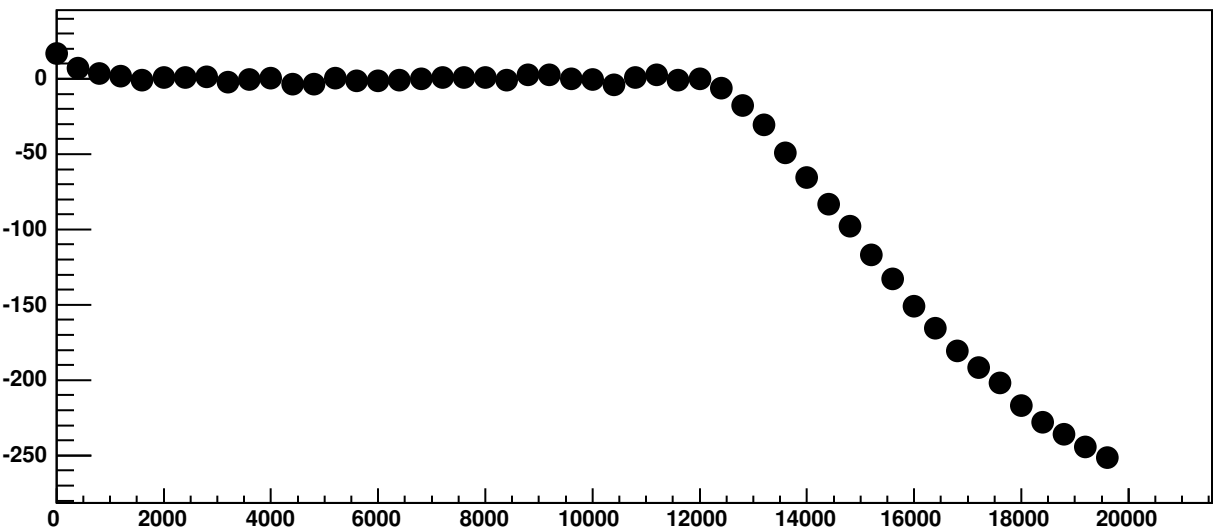
χ^2 / ndf	20.87 / 23
p0	480.4 ± 0.7363
p1	0.01823 ± 0.0001106

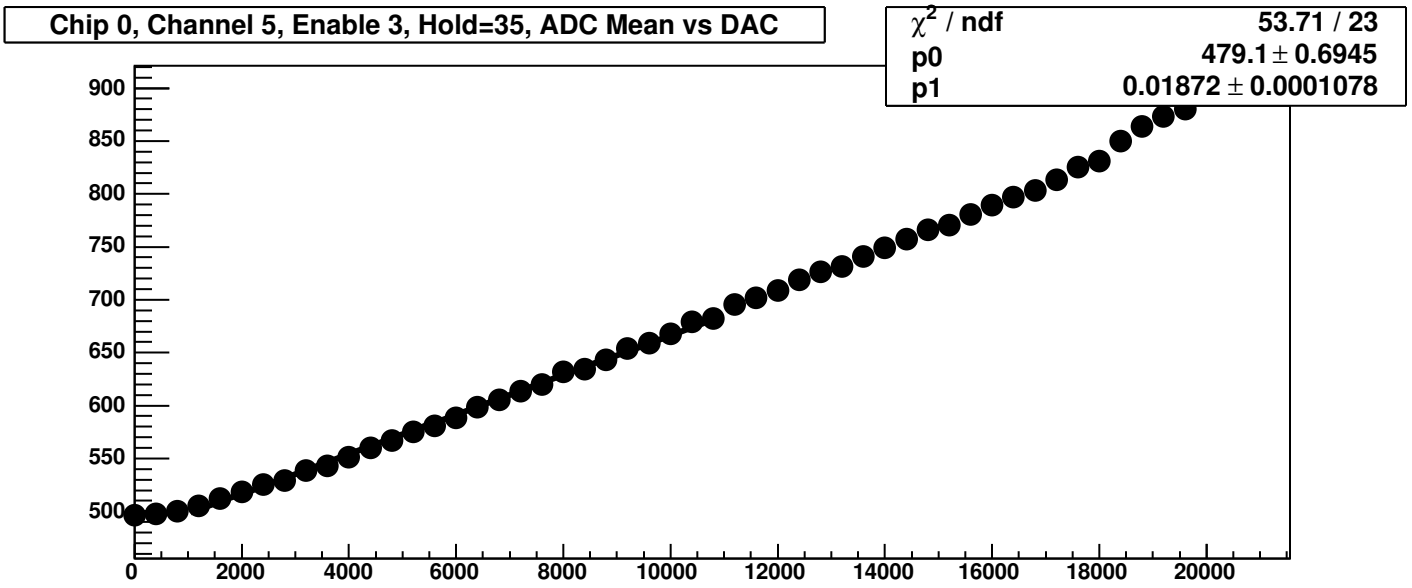


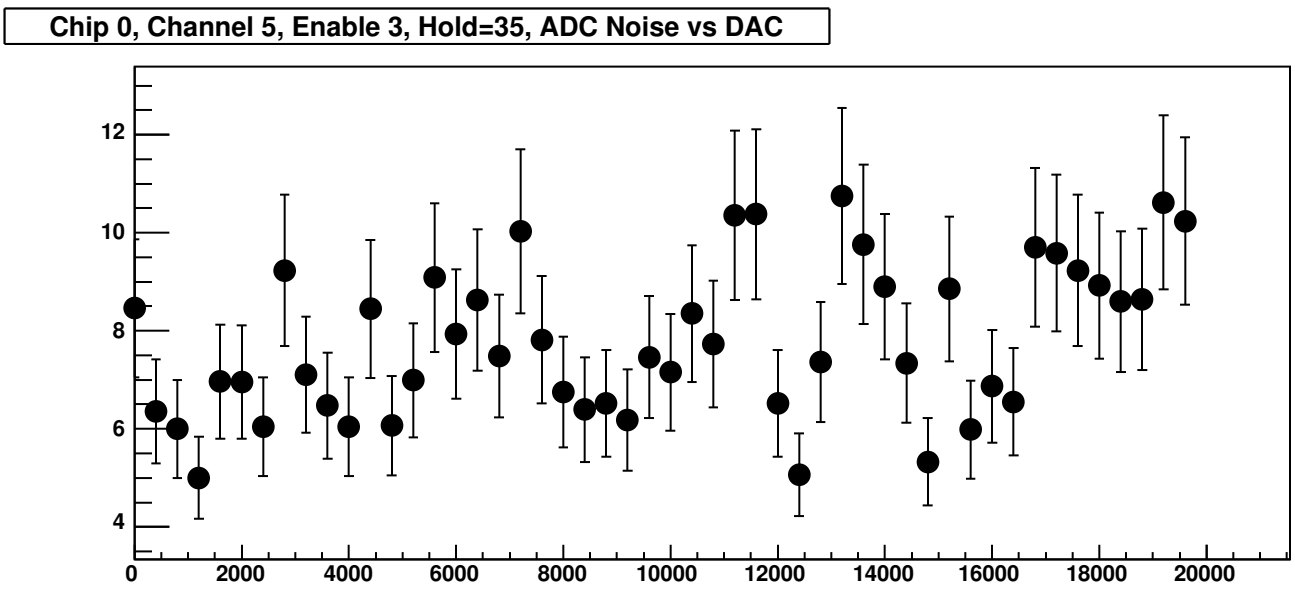
Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

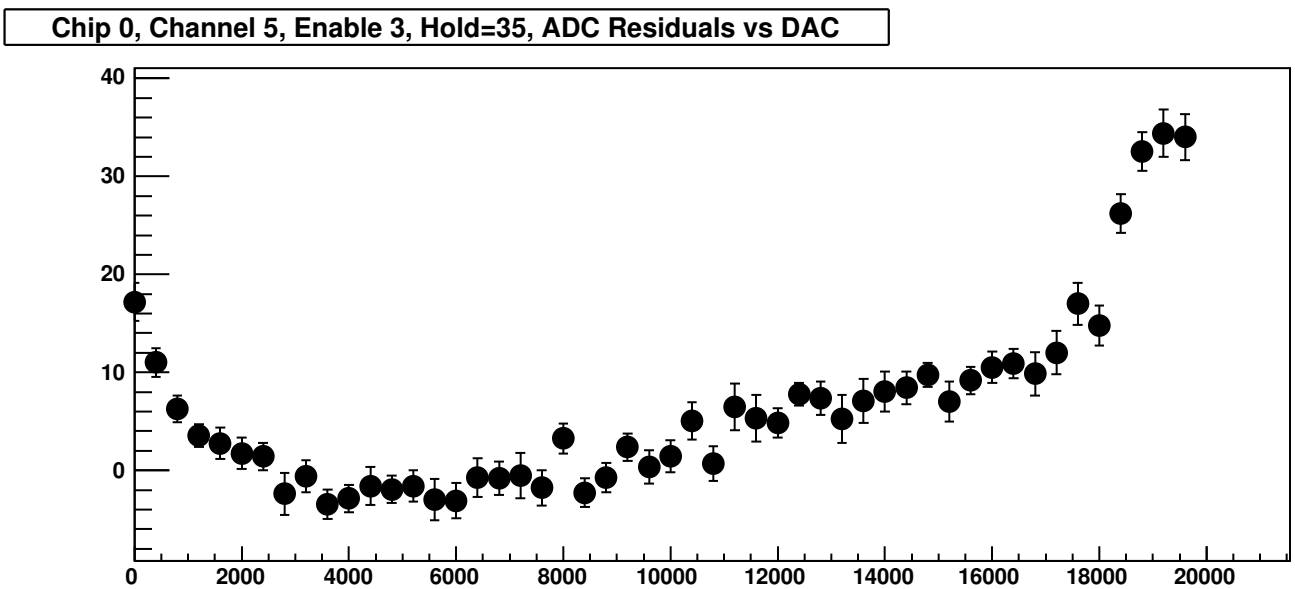


Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

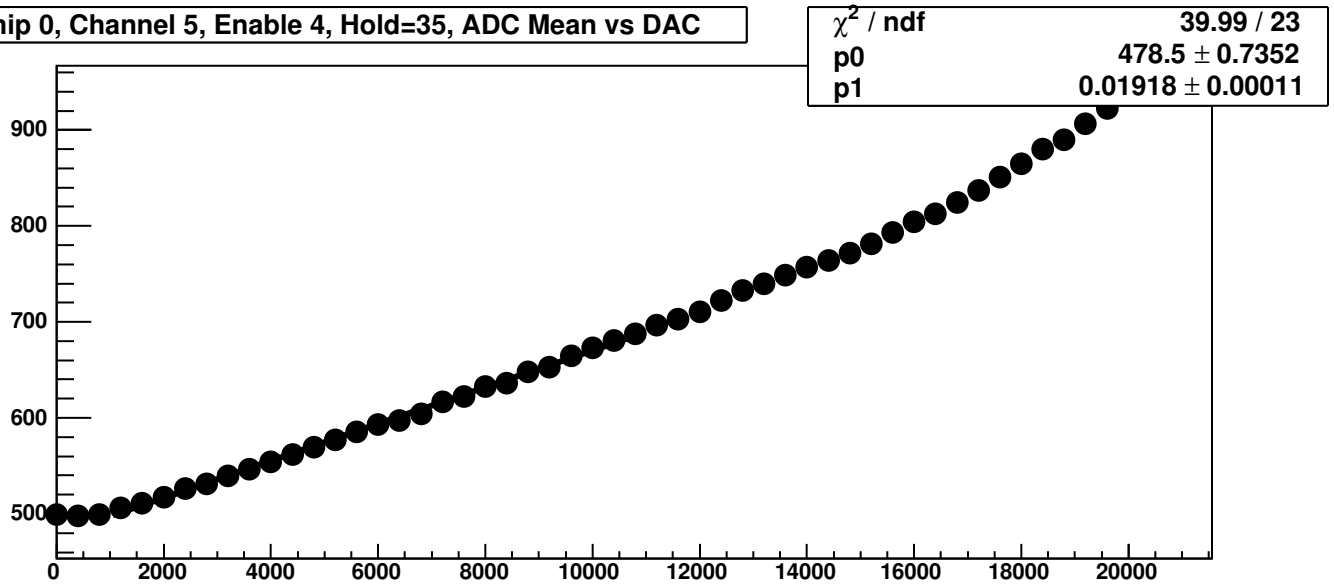



Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 53.71 / 23
p0 479.1 ± 0.6945
p1 0.01872 ± 0.0001078
900
850
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

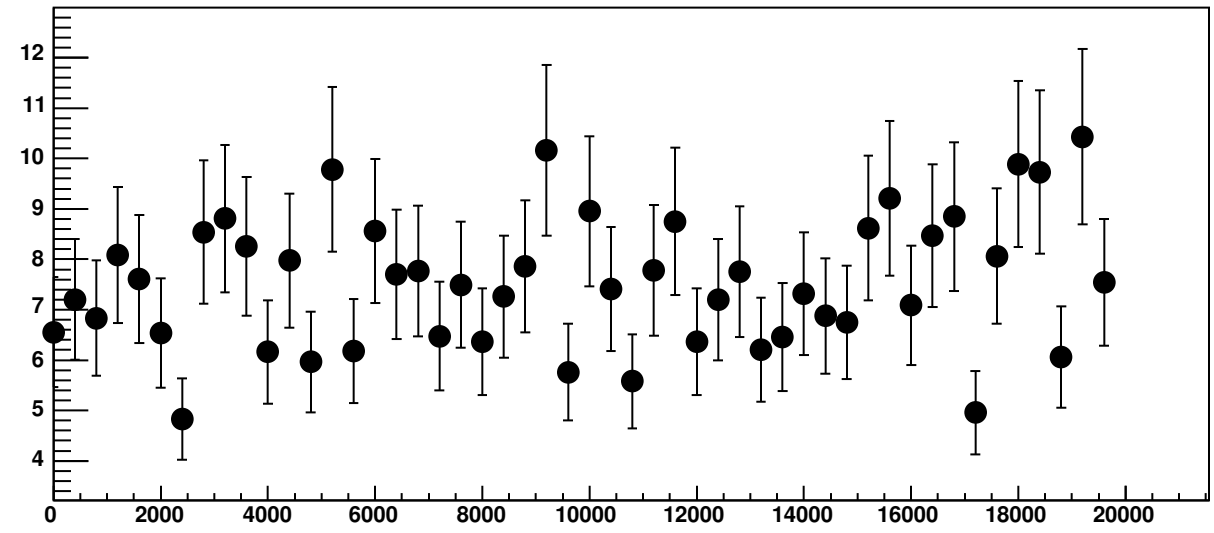

Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

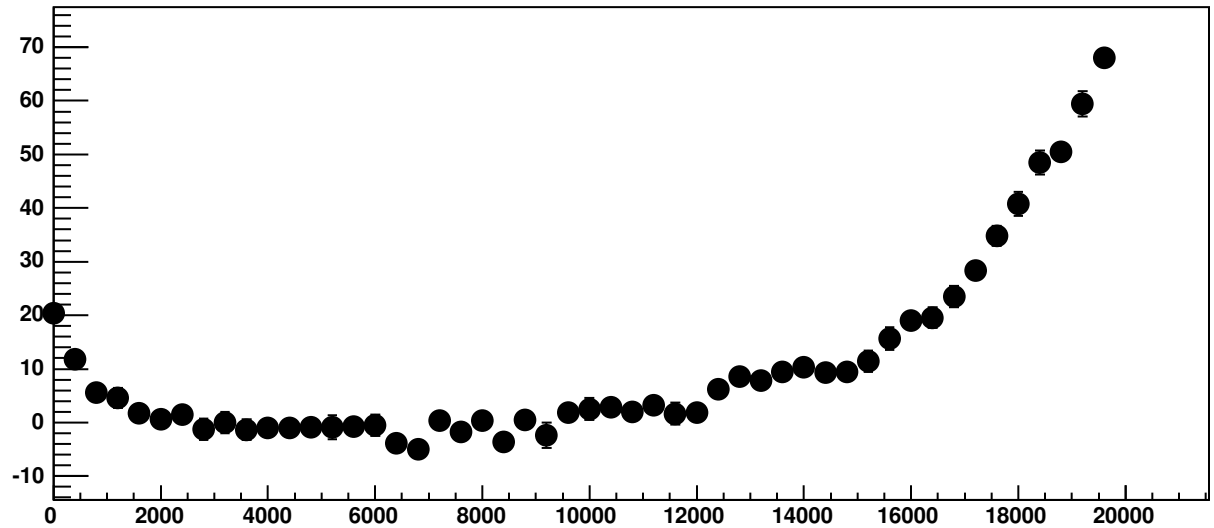
Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



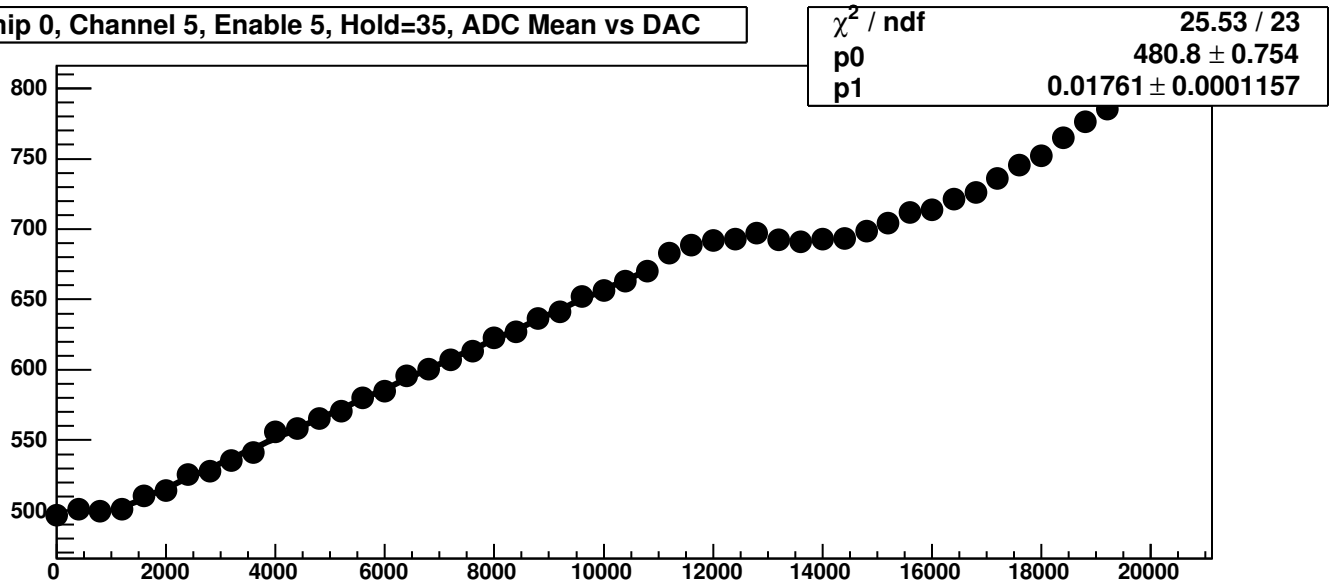
Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



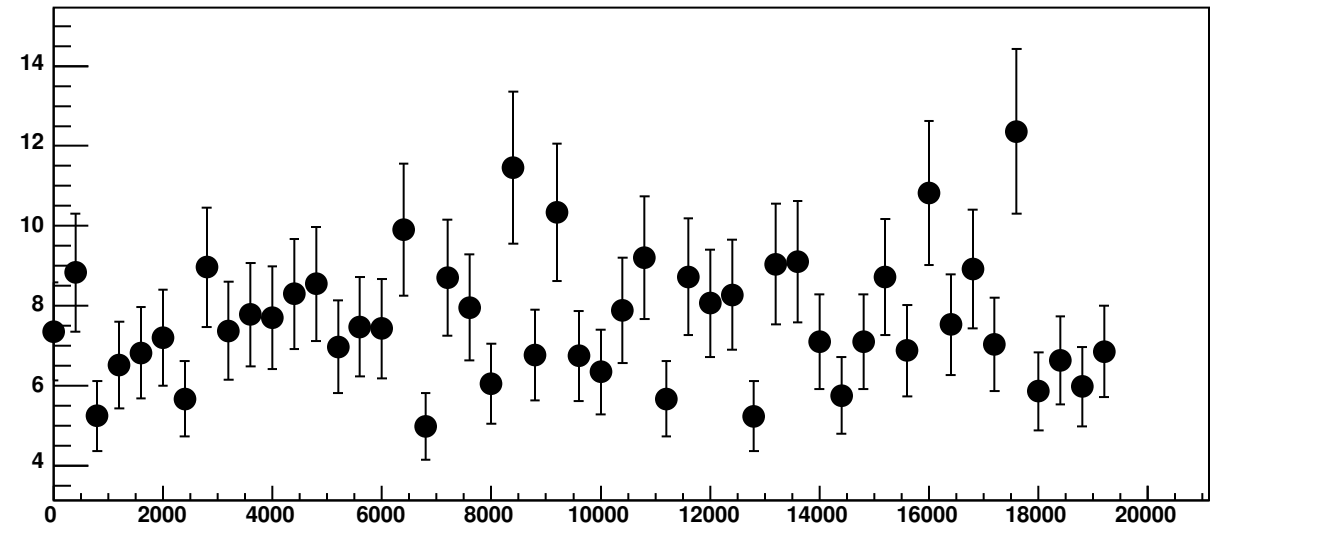
Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



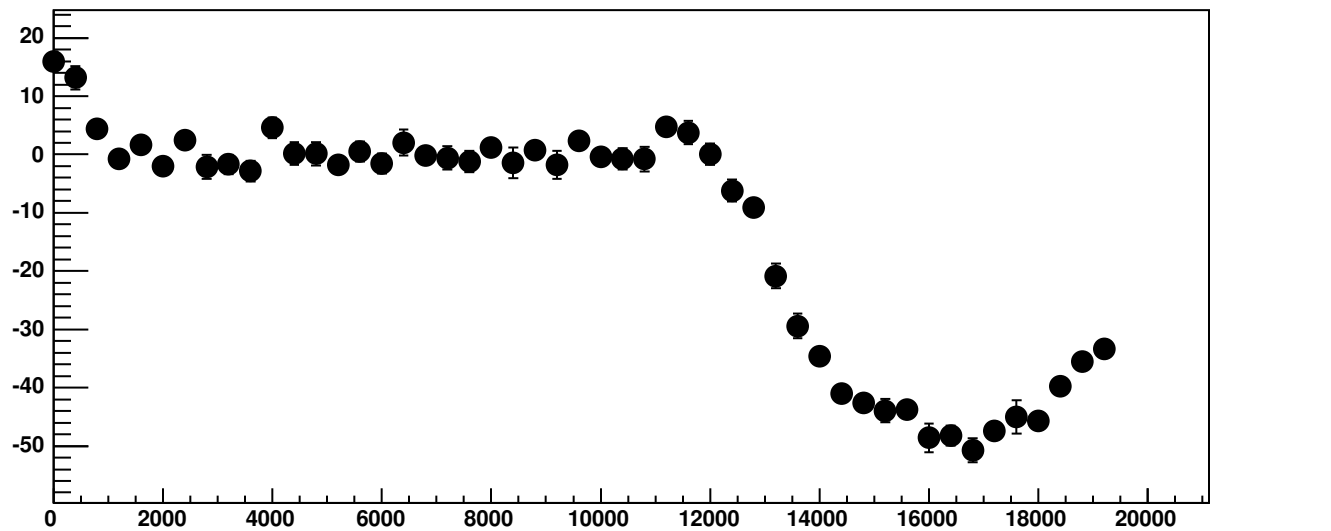
Chip 0, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

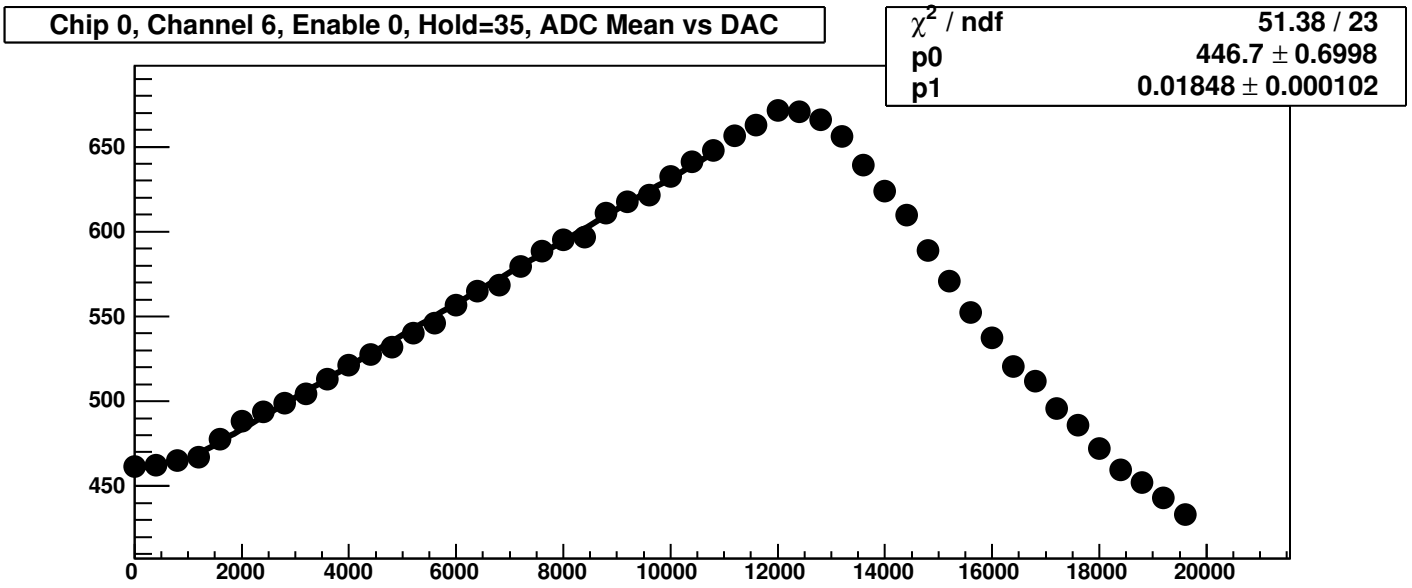


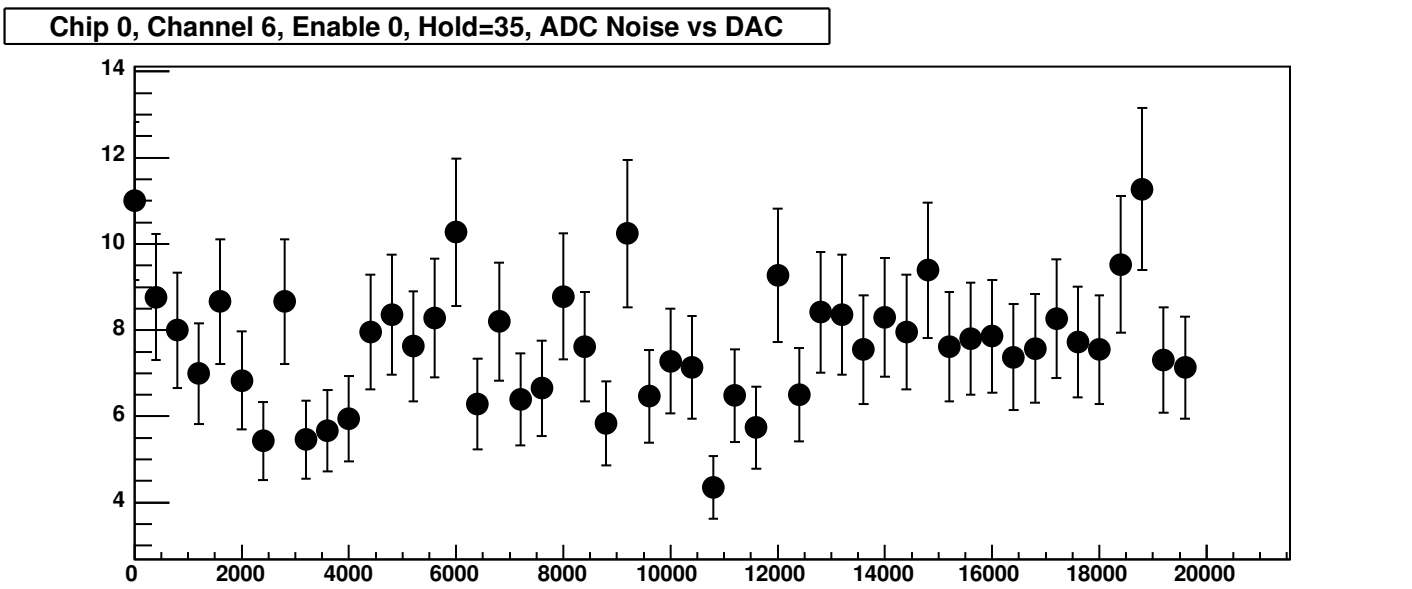
Chip 0, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

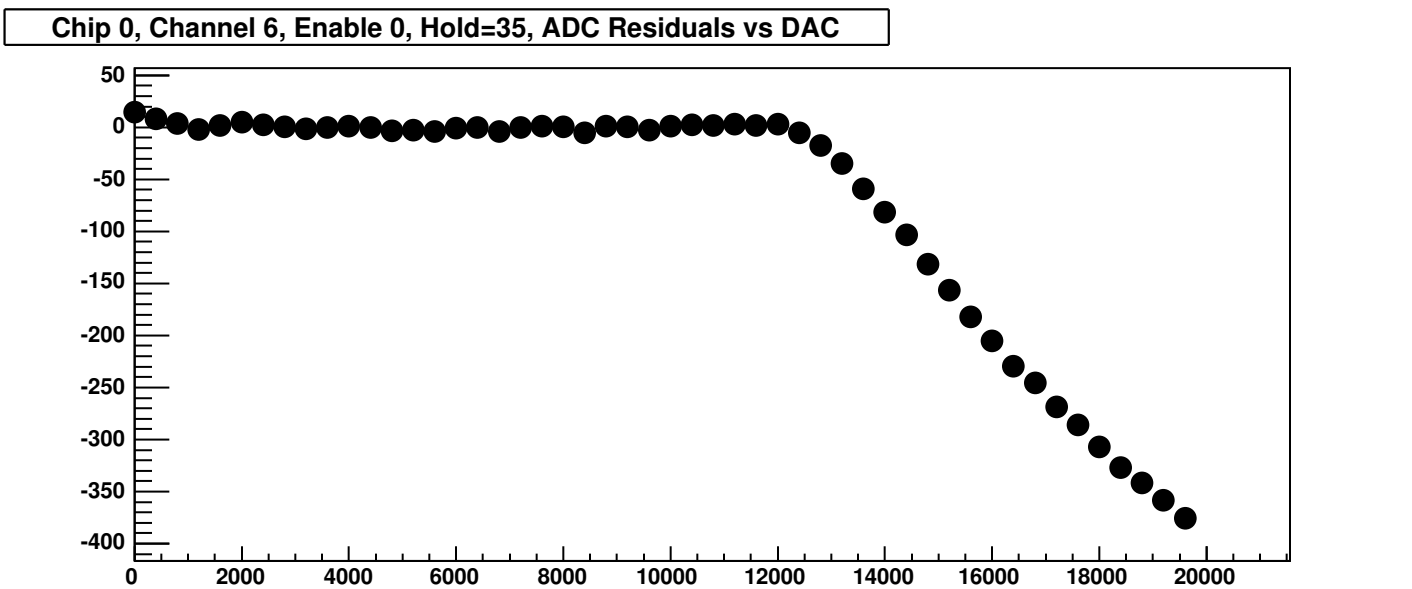


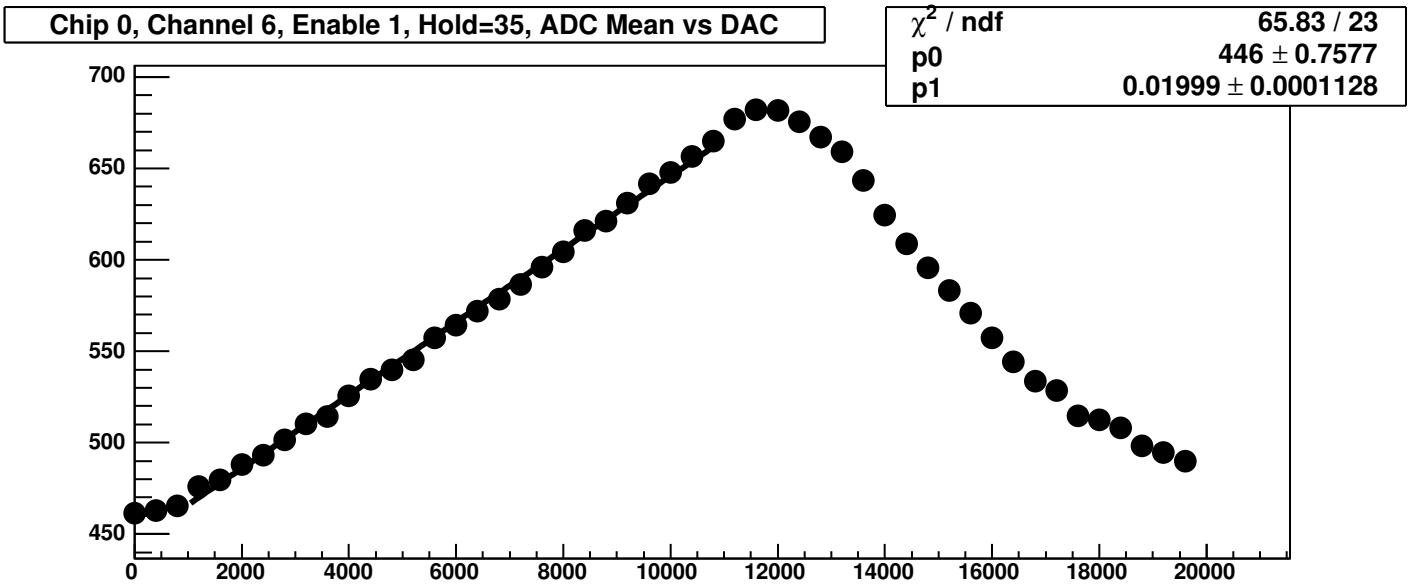
Chip 0, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

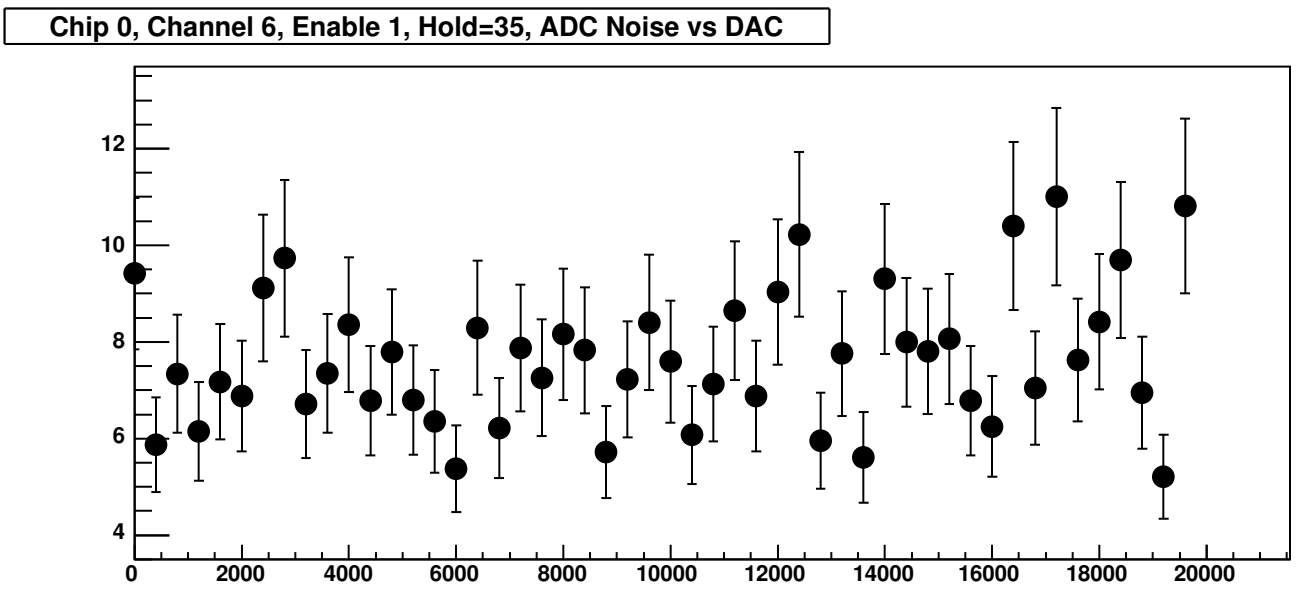


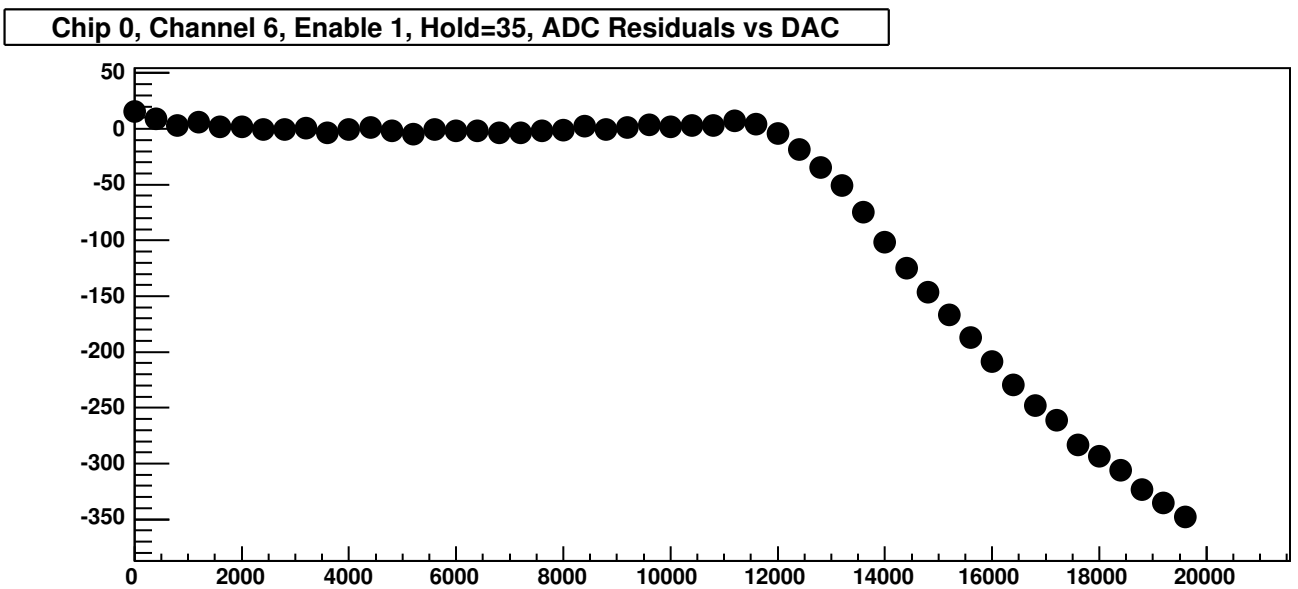

Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 51.38 / 23
p0 446.7 ± 0.6998
p1 0.01848 ± 0.000102
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

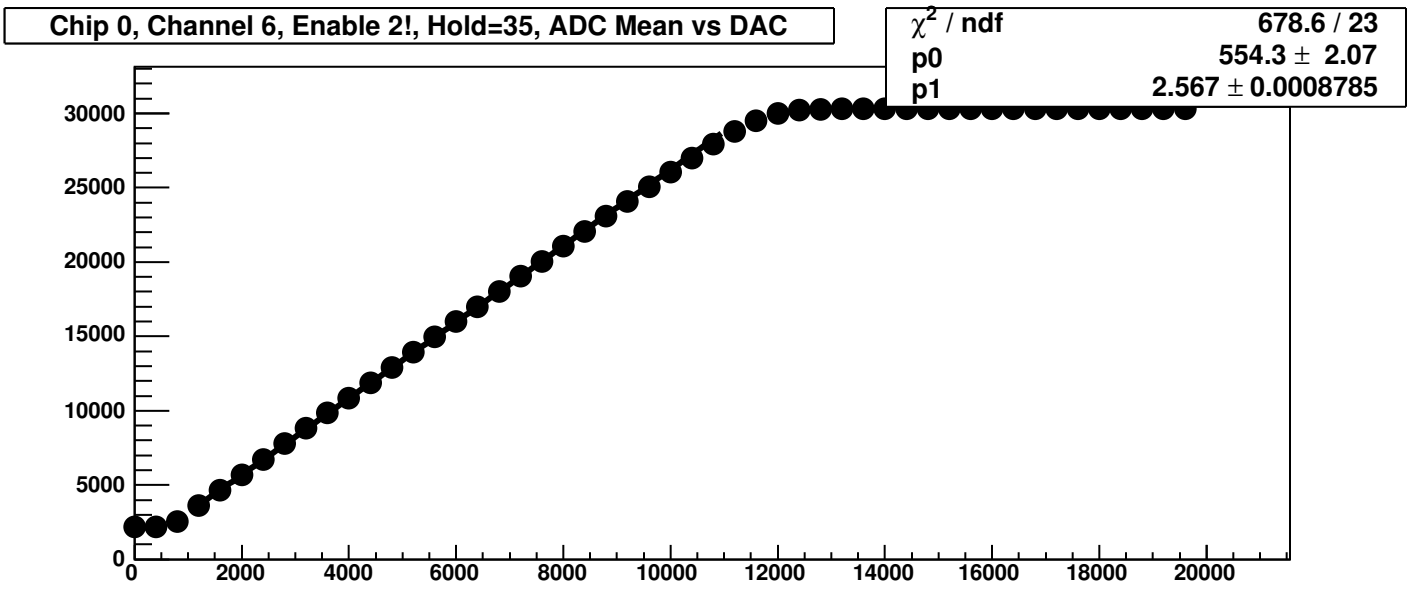

Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

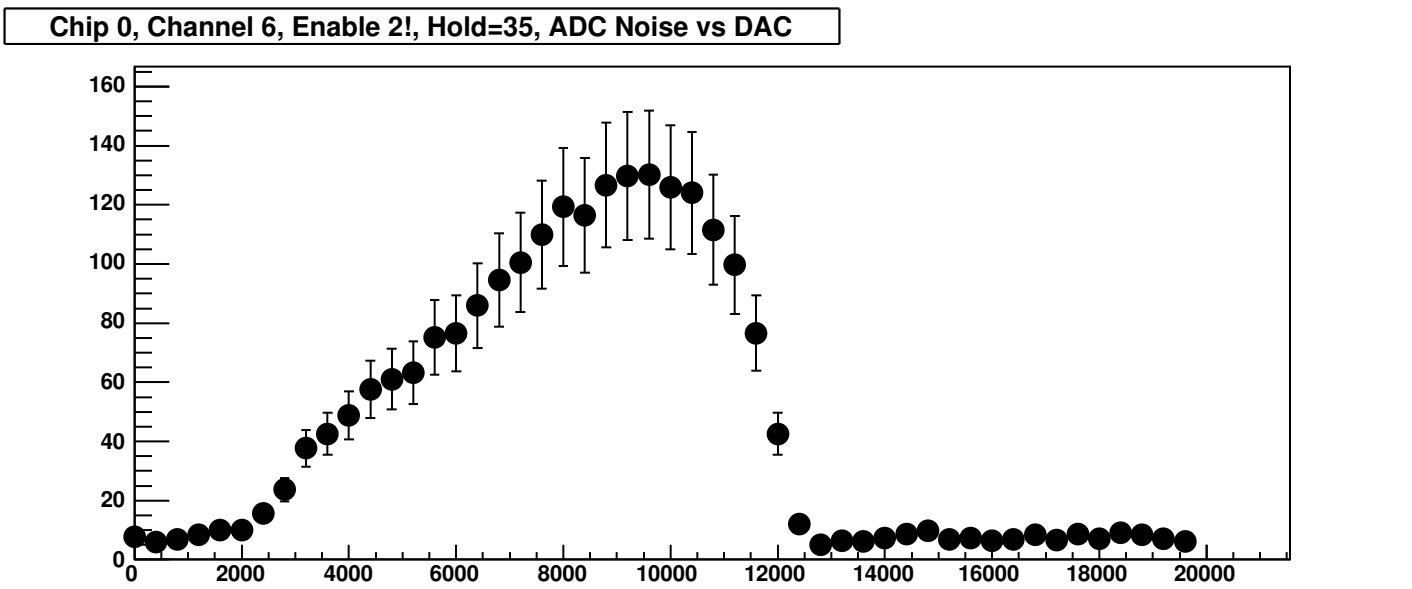

Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

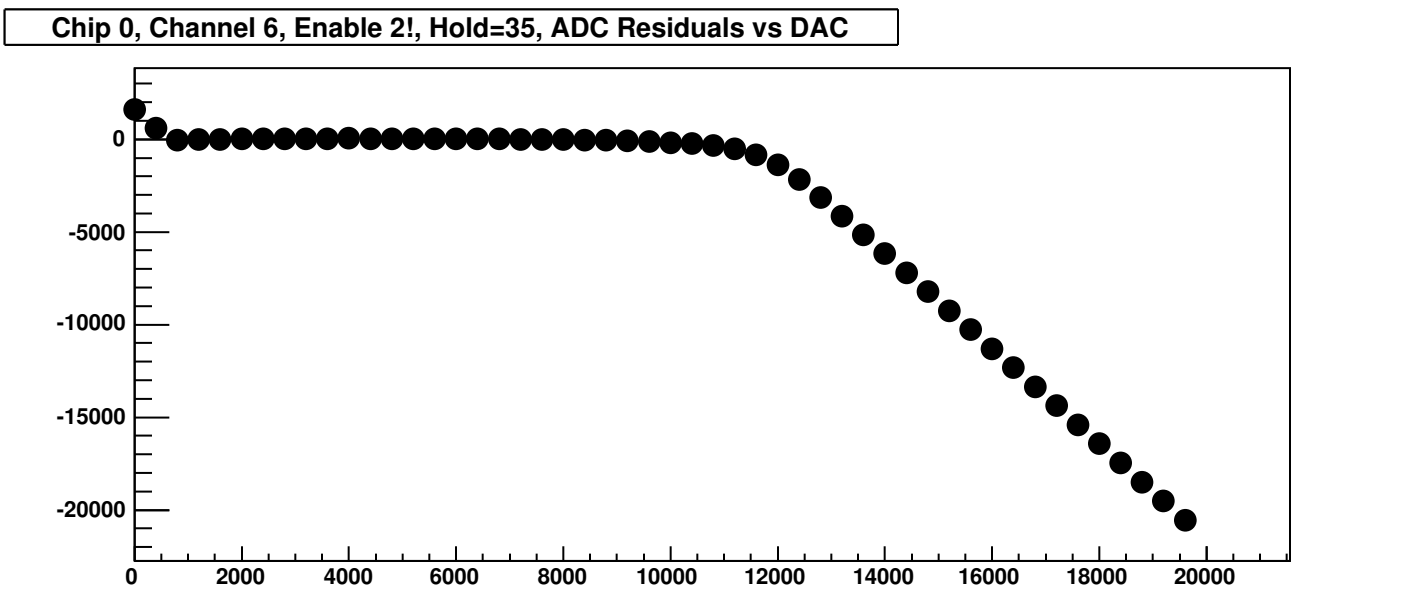

Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 65.83 / 23
p0 446 ± 0.7577
p1 0.01999 ± 0.0001128
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

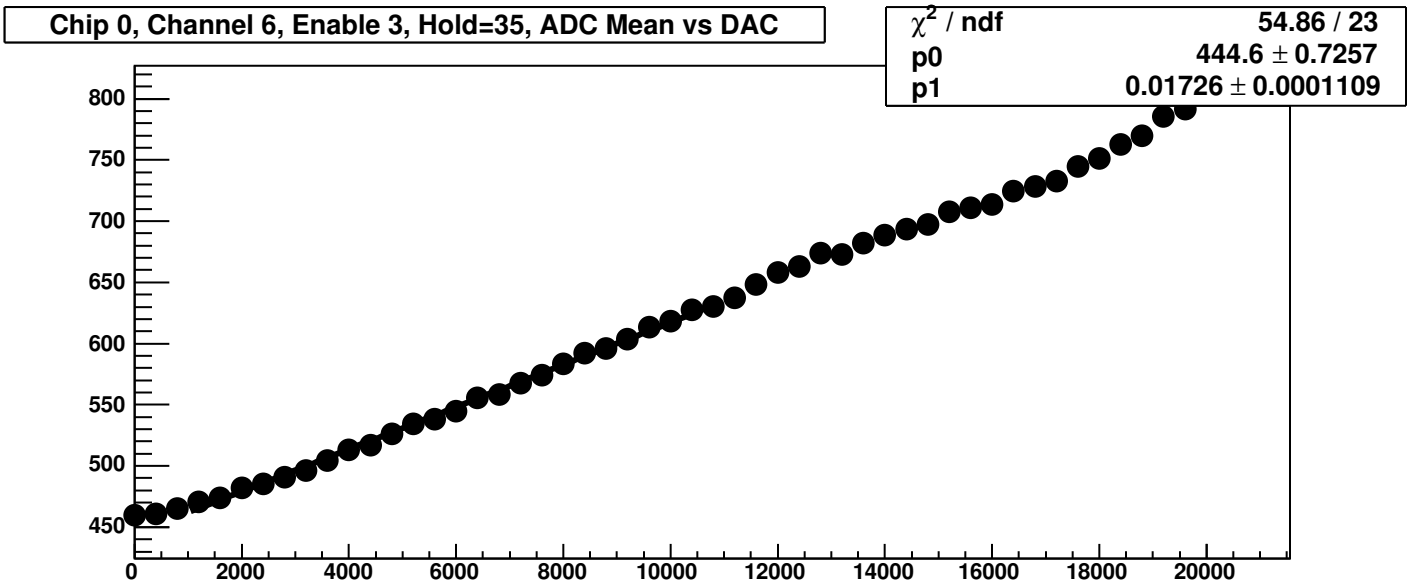

Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

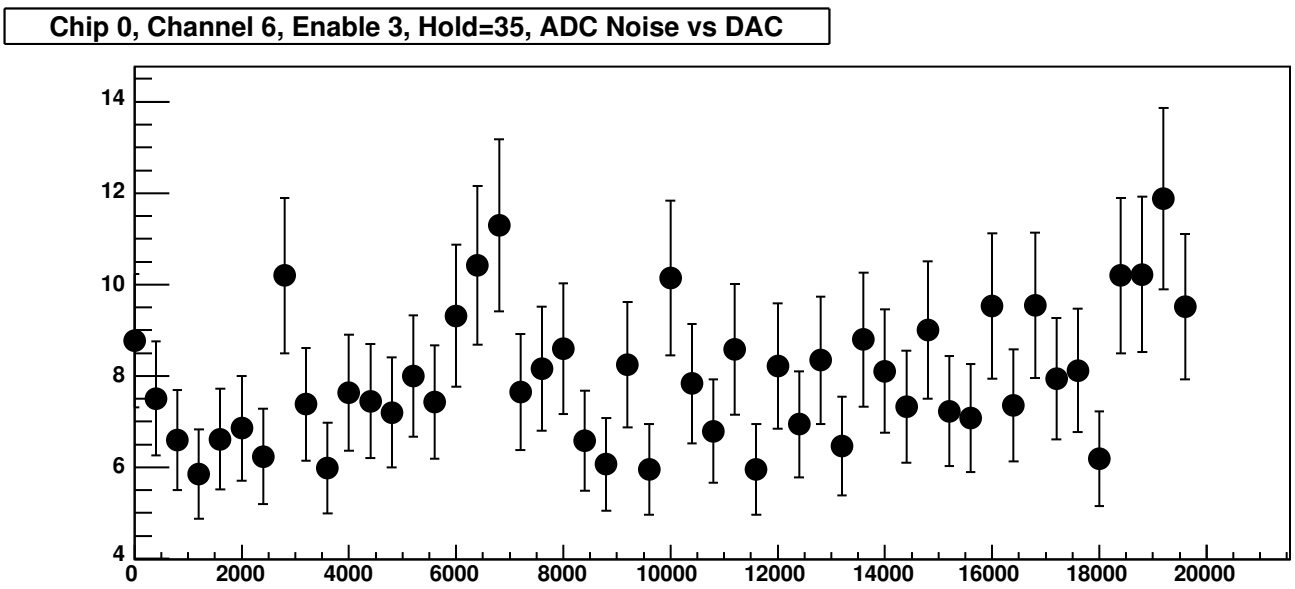

Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

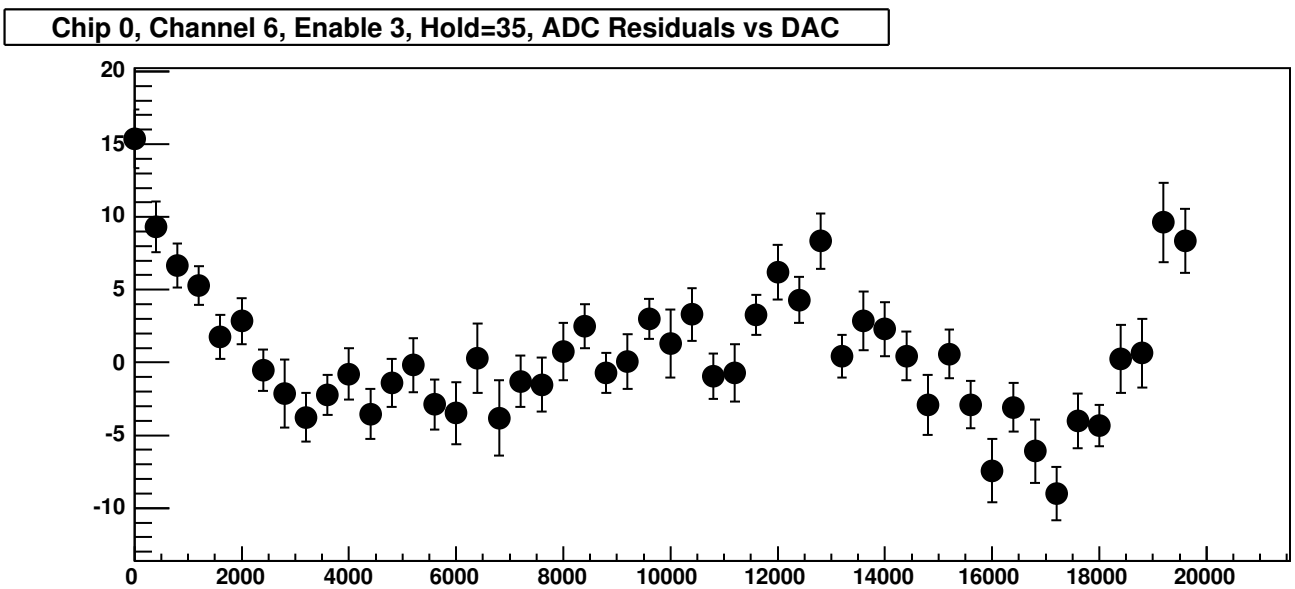

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 678.6 / 23
p0 554.3 ± 2.07
p1 2.567 ± 0.0008785
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

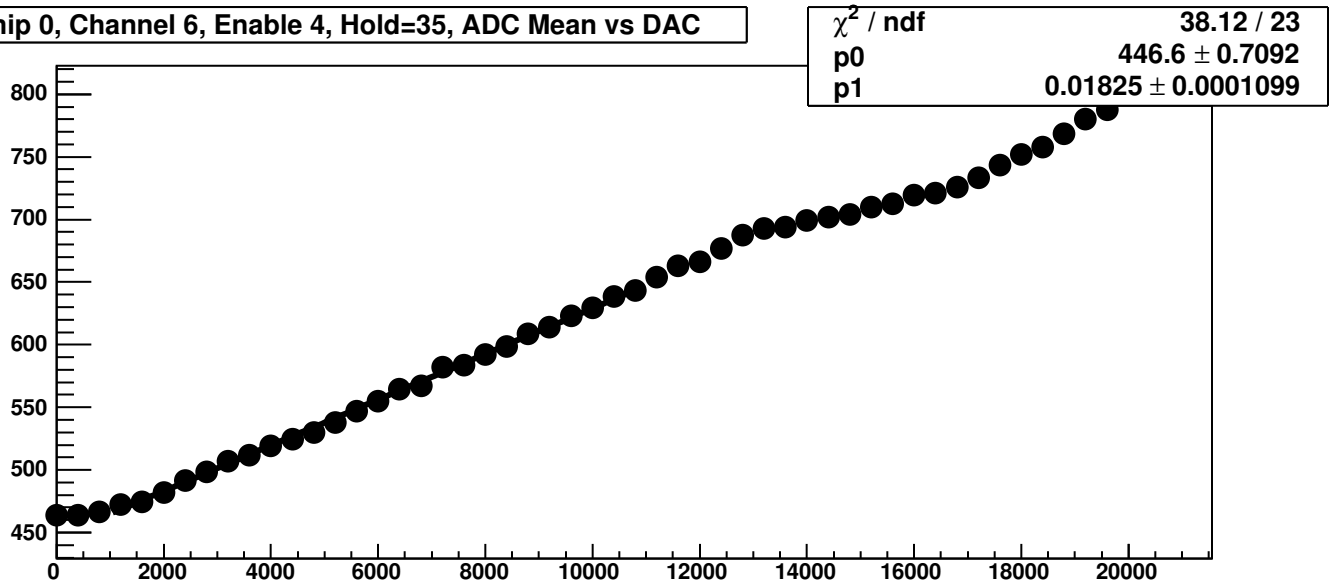

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 54.86 / 23
p0 444.6 ± 0.7257
p1 0.01726 ± 0.0001109
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

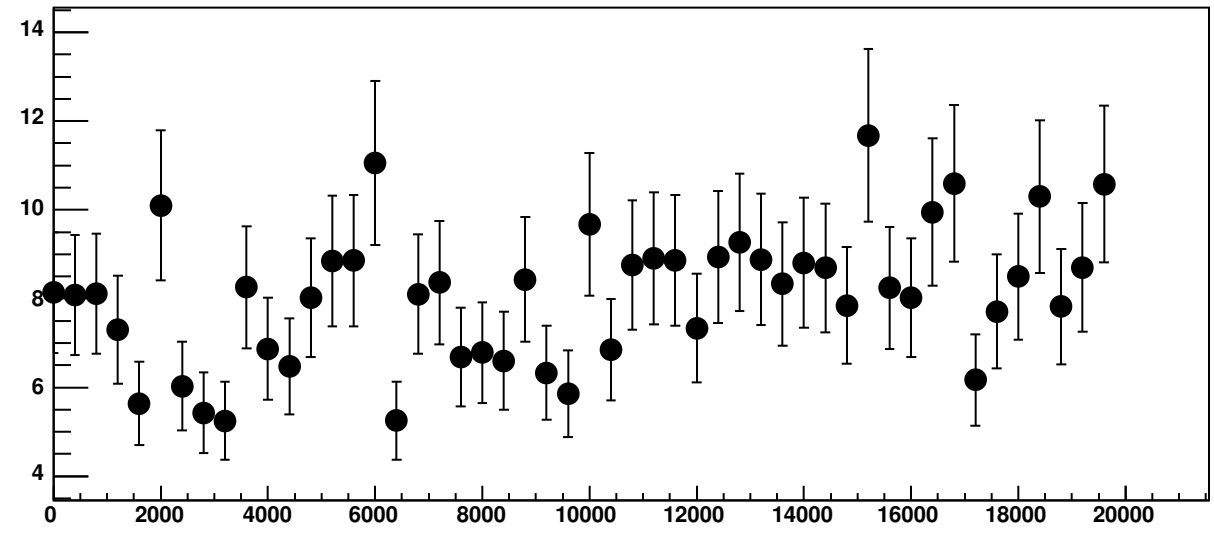

Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

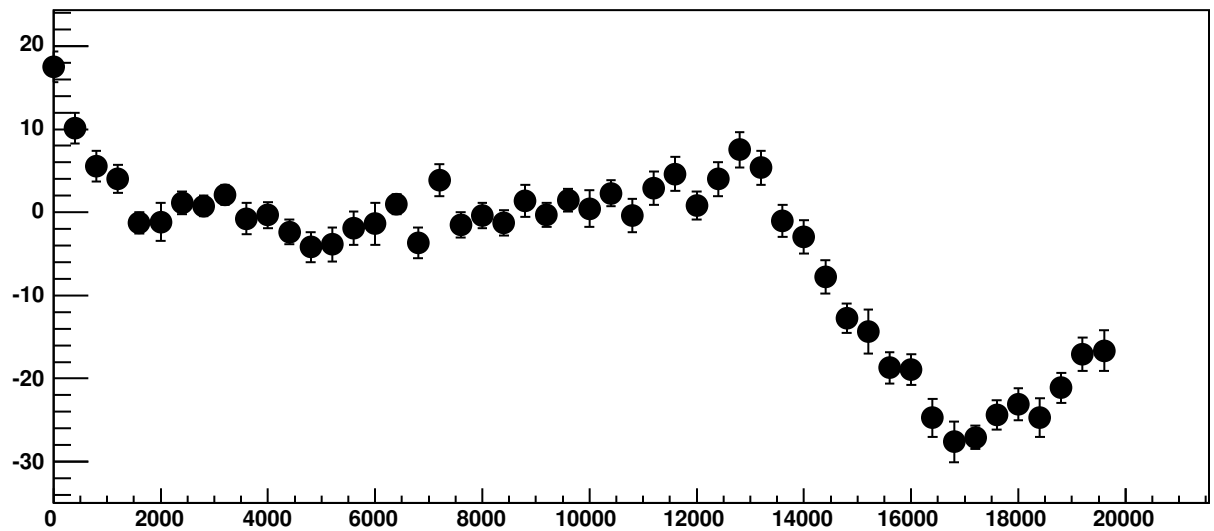
Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

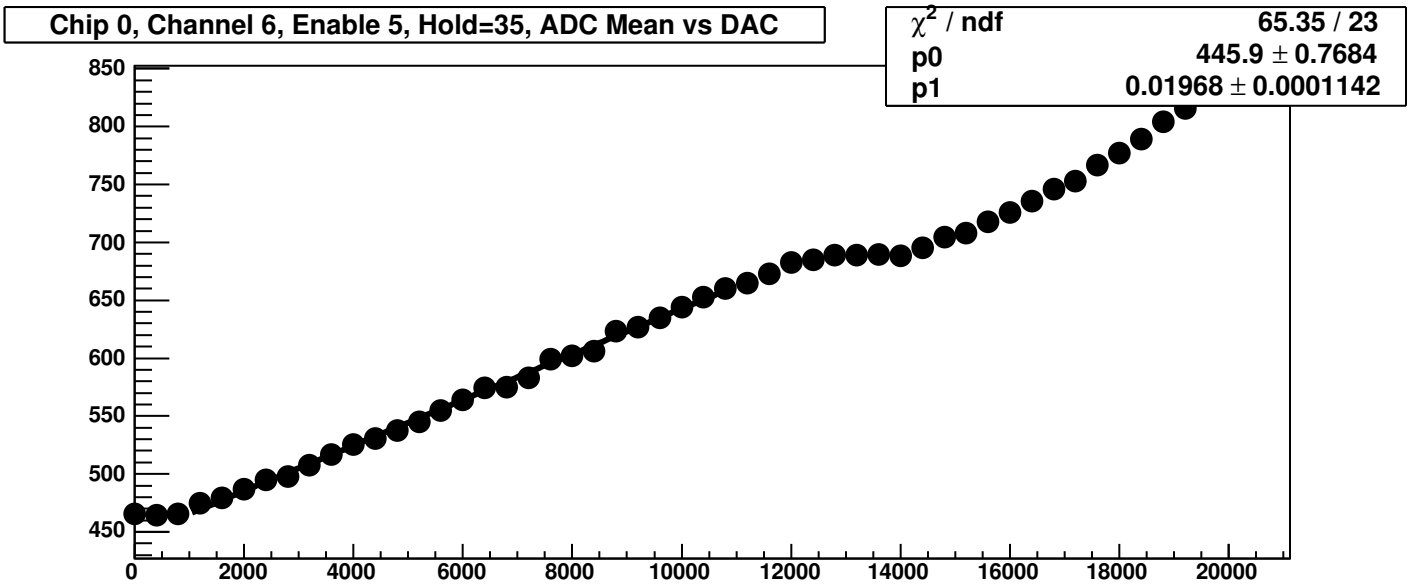


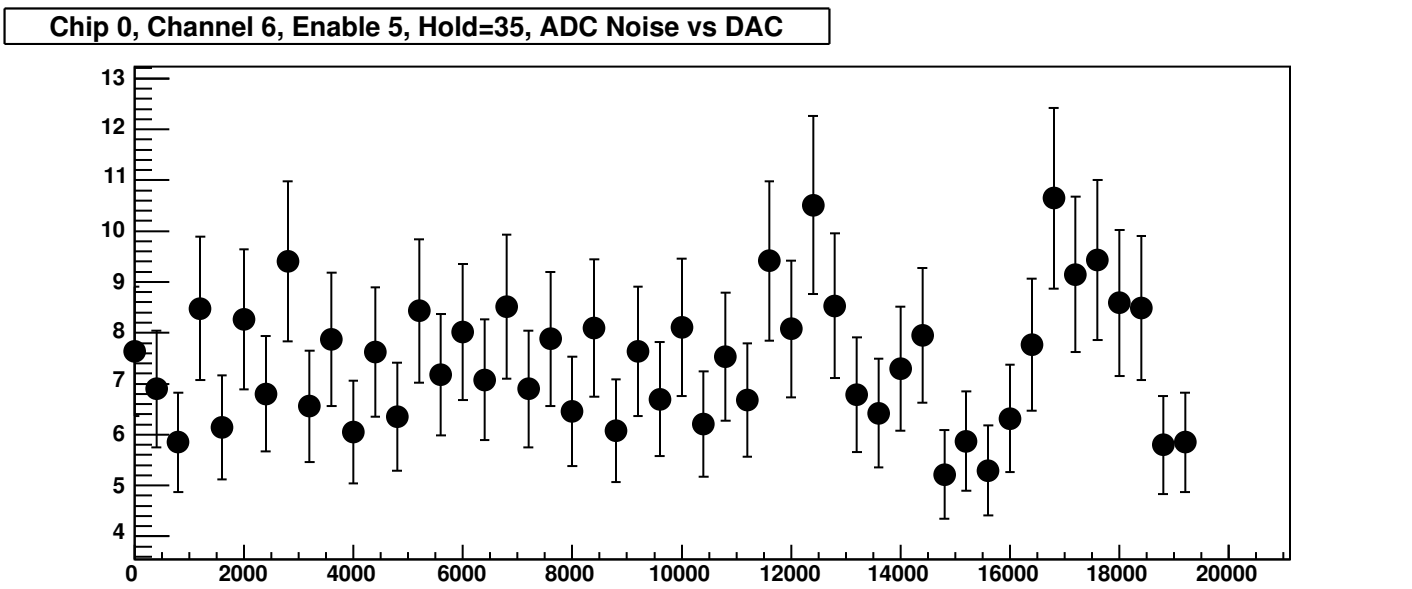
Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

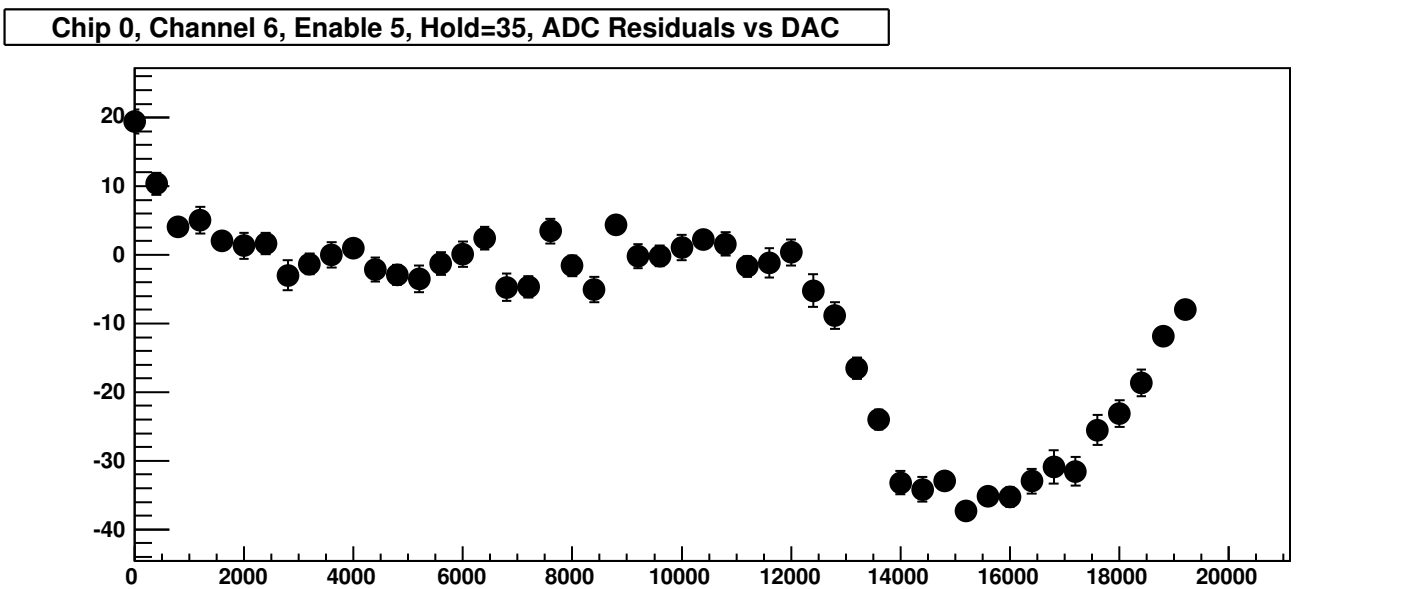


Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

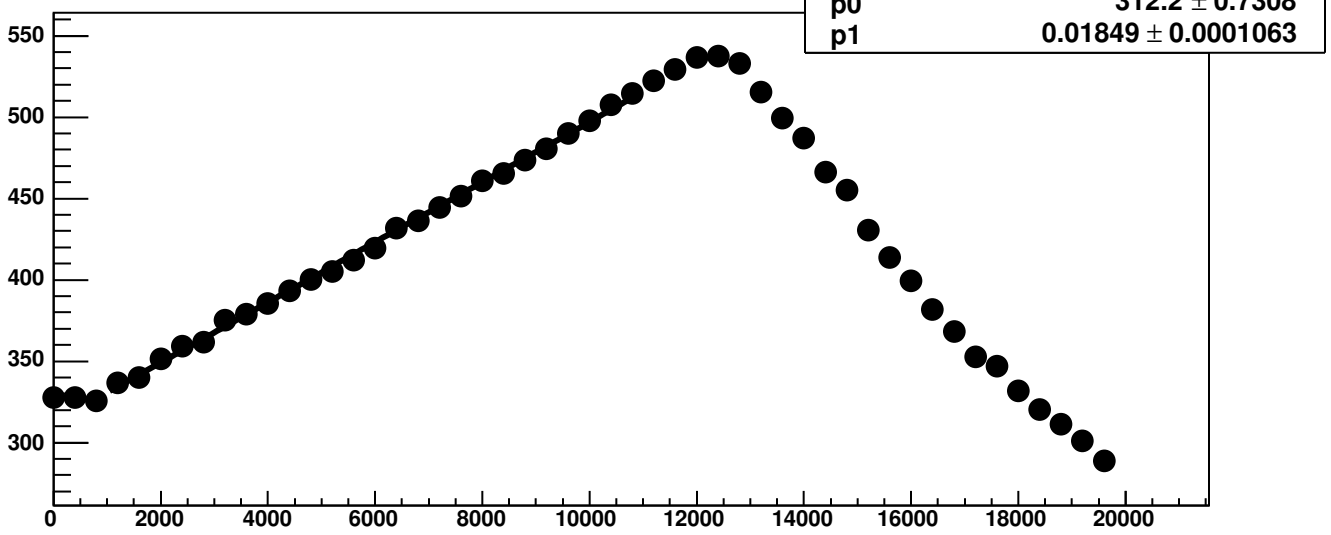



Chip 0, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 65.35 / 23
p0 445.9 ± 0.7684
p1 0.01968 ± 0.0001142
850
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

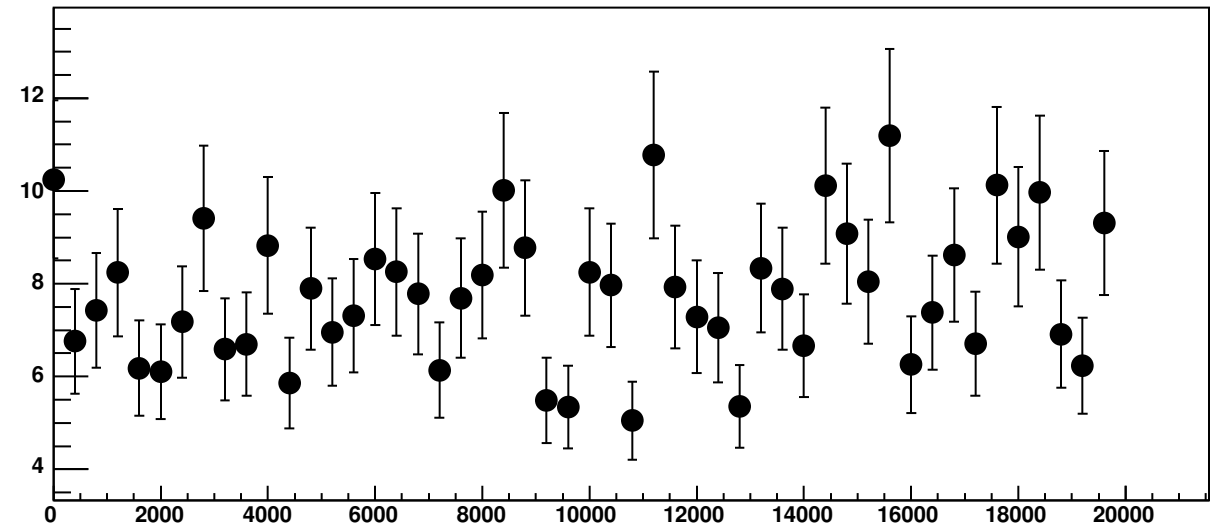

Chip 0, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

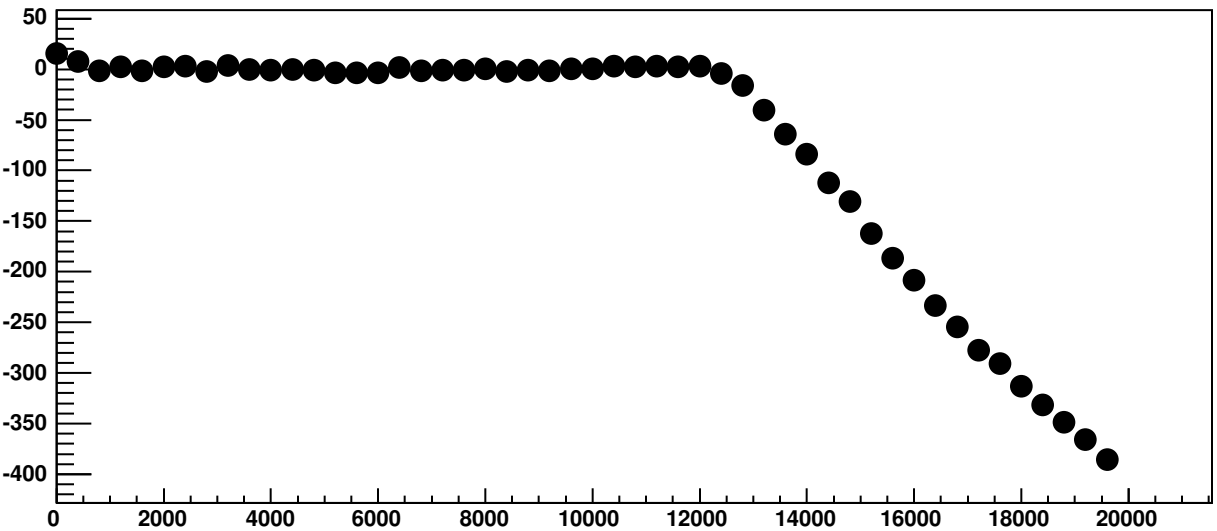
Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

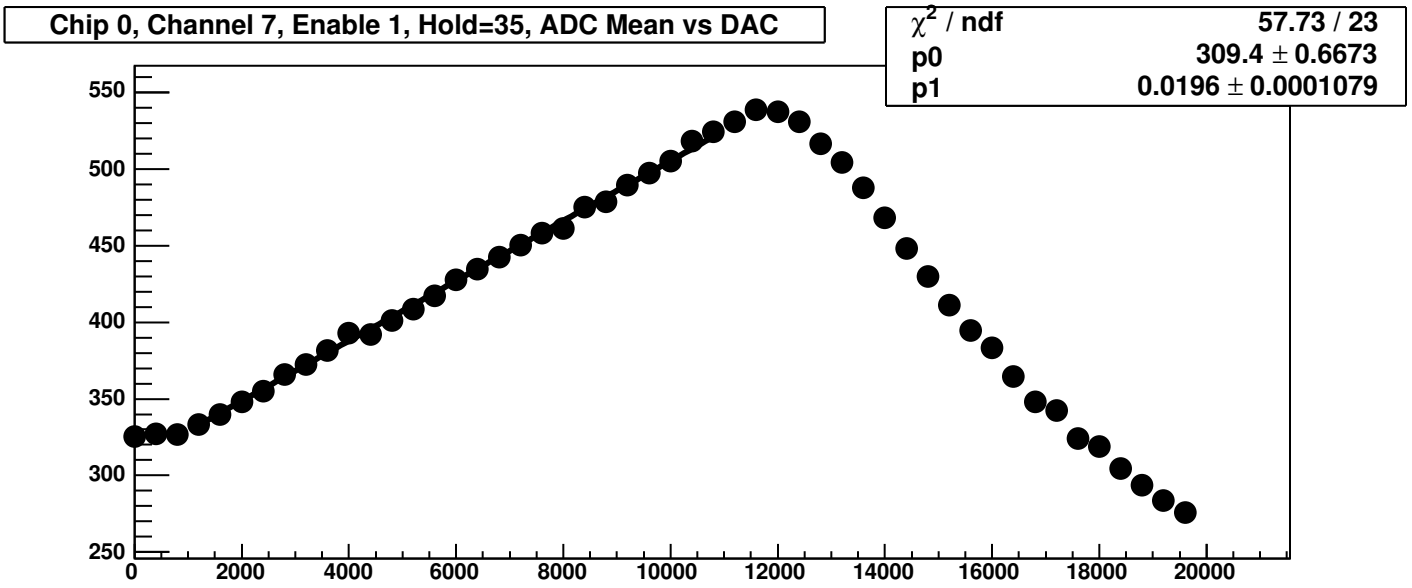


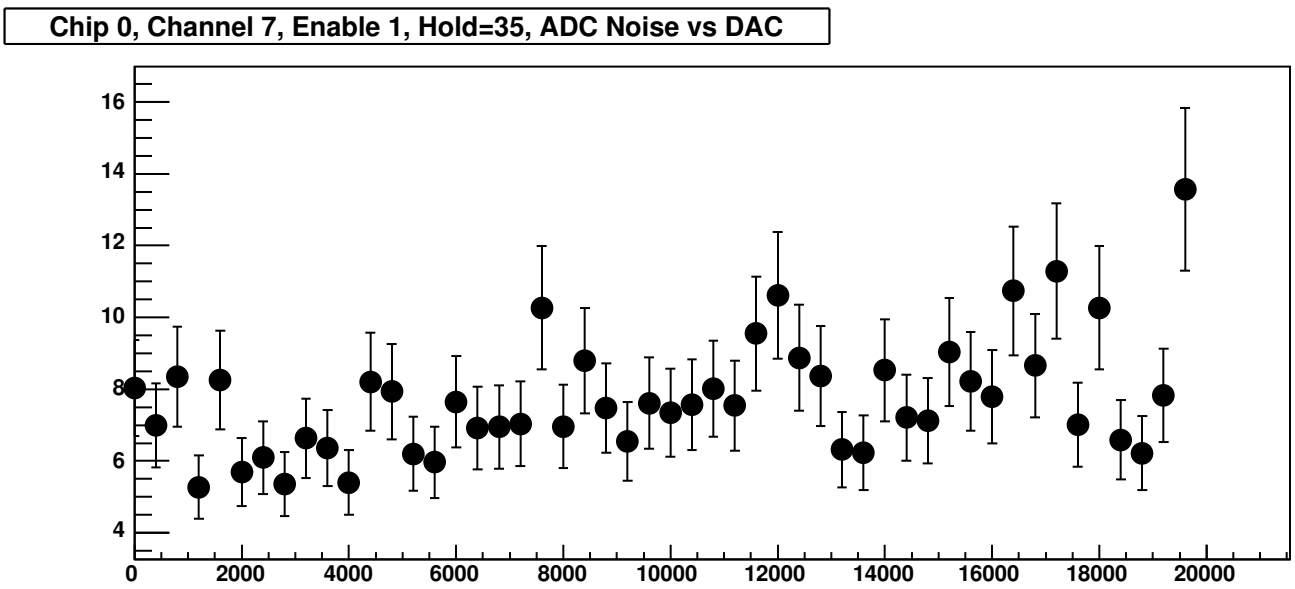
Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

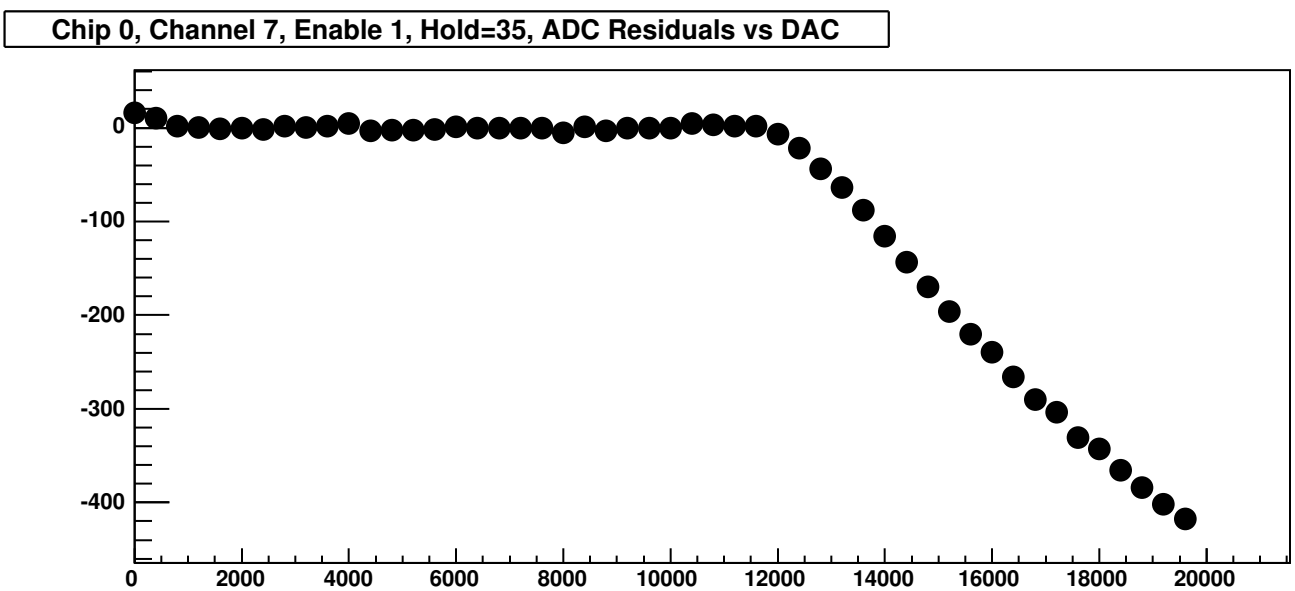


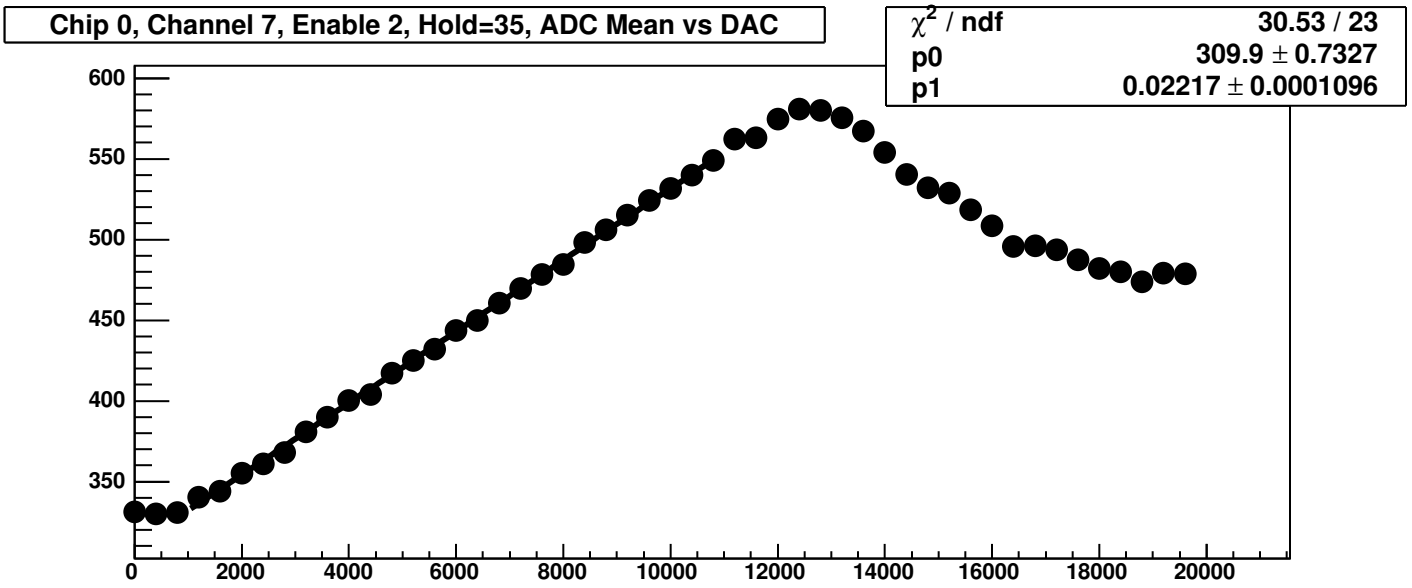
Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

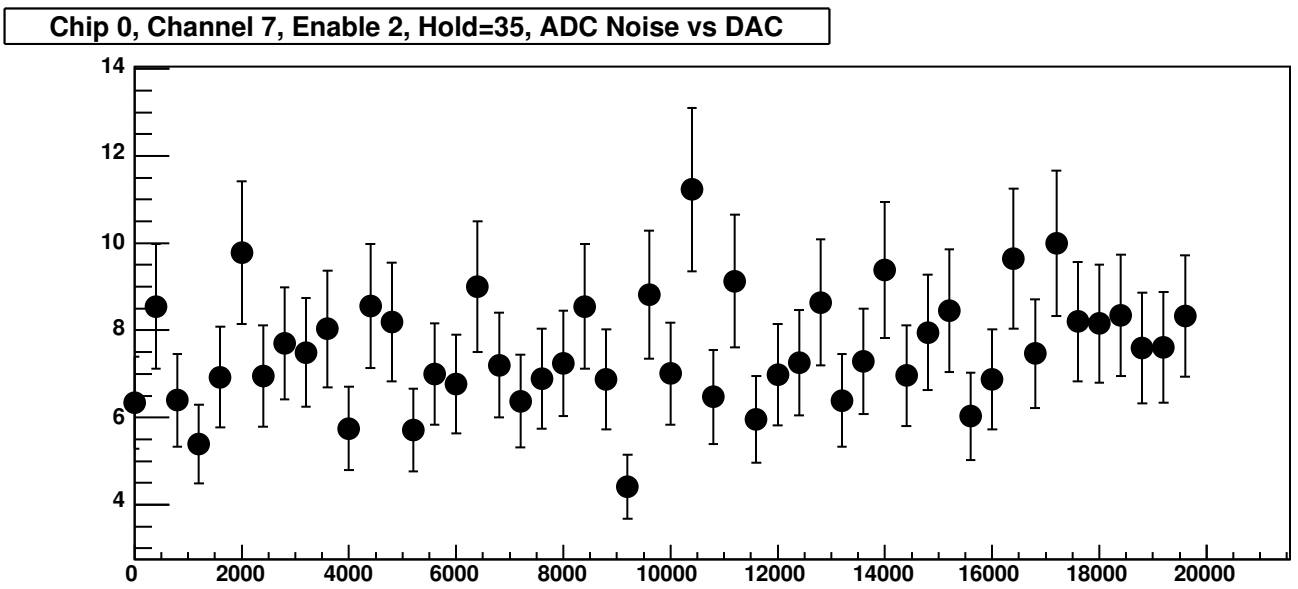


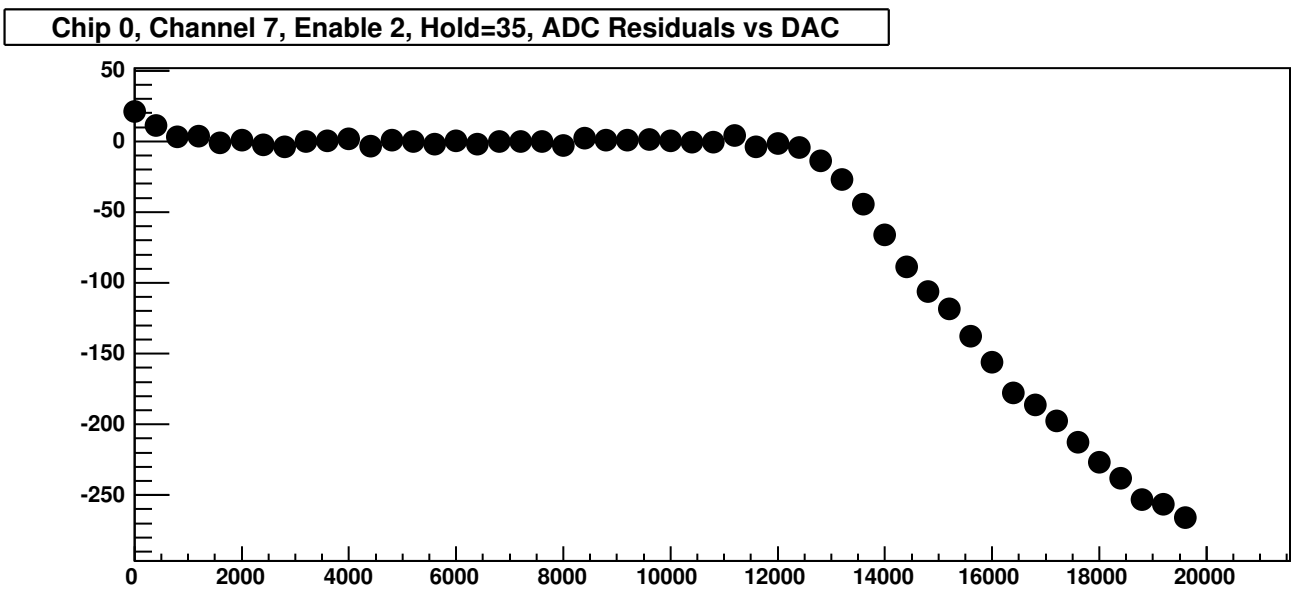

Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 57.73 / 23
p0 309.4 ± 0.6673
p1 0.0196 ± 0.0001079
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

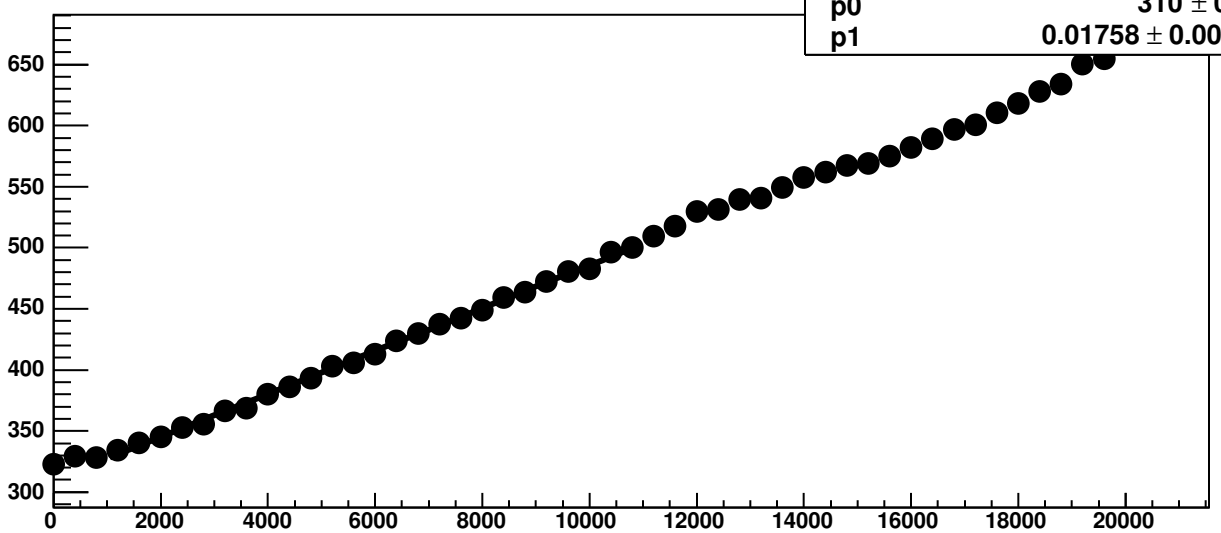

Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.53 / 23
p0 309.9 ± 0.7327
p1 0.02217 ± 0.0001096
600
550
500
450
400
350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

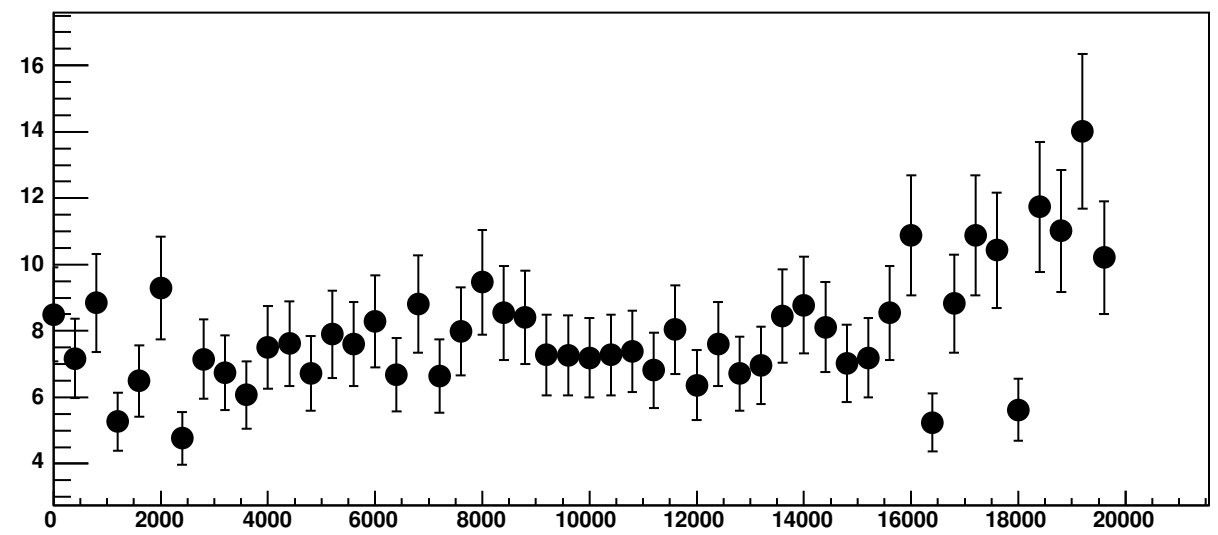

Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

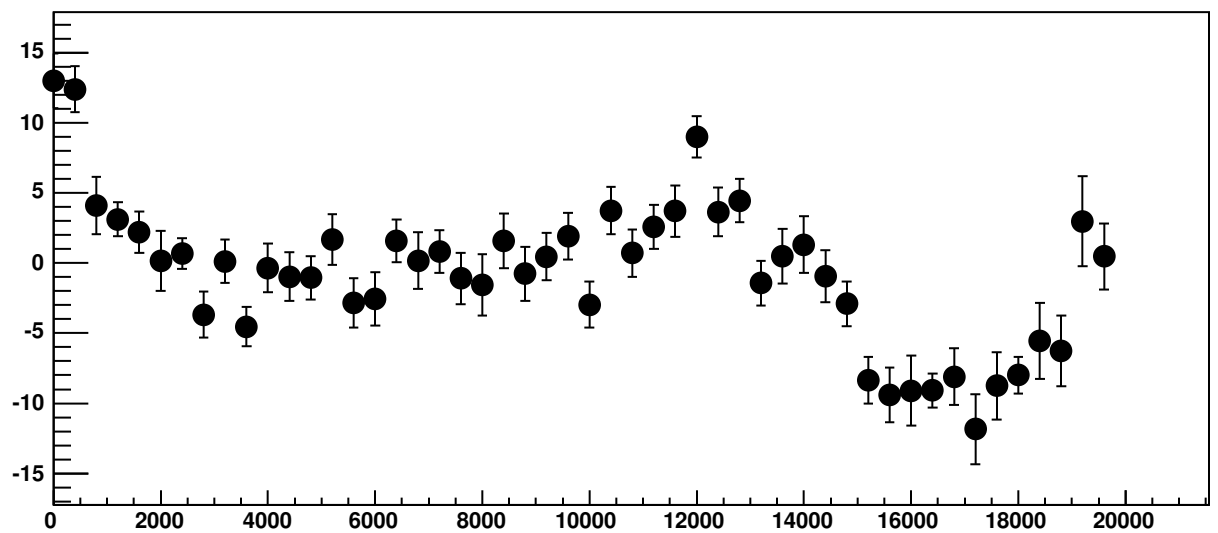
χ^2 / ndf	43.83 / 23
p0	310 ± 0.6813
p1	0.01758 ± 0.0001084

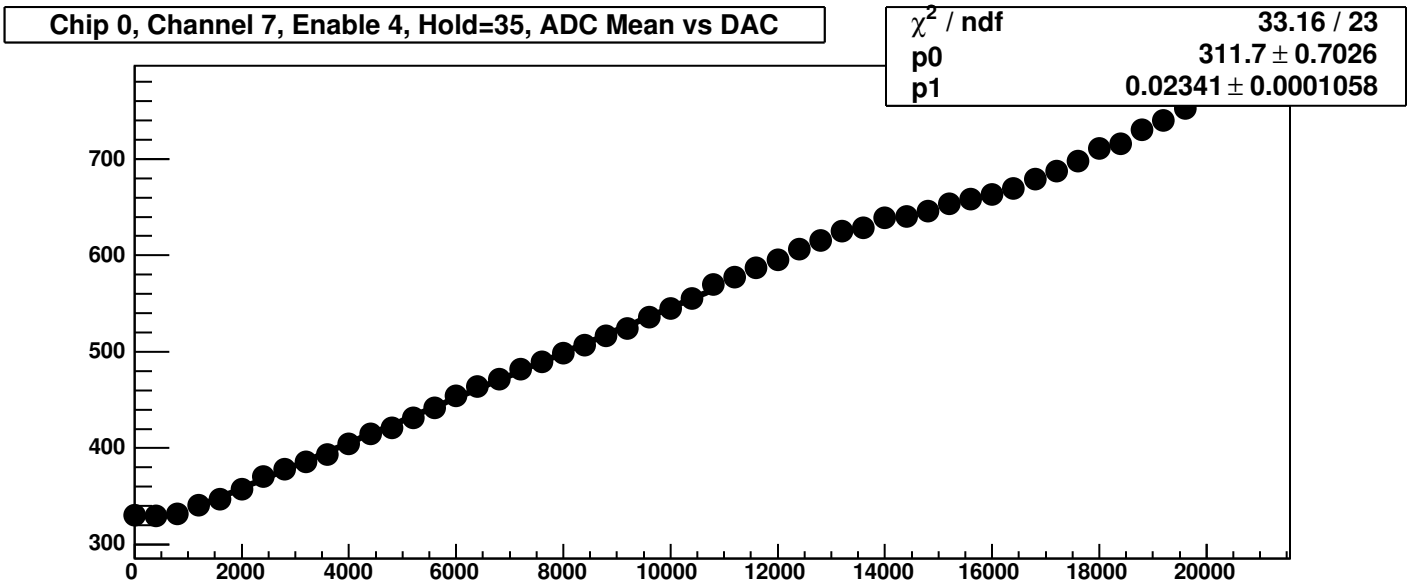


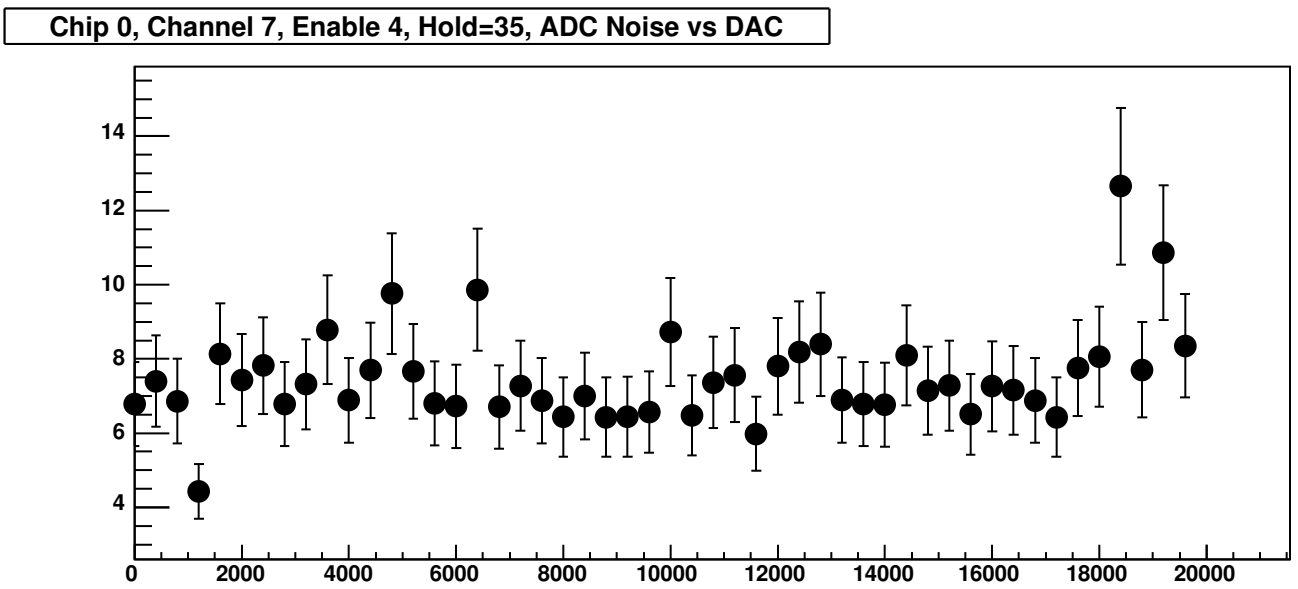
Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC

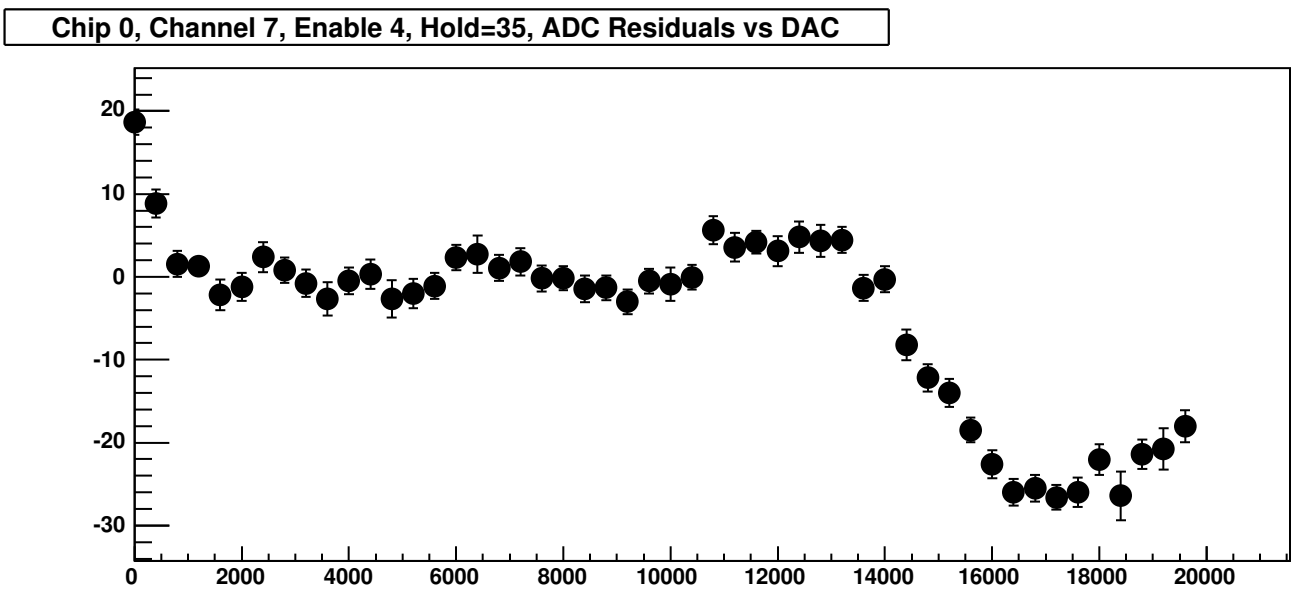


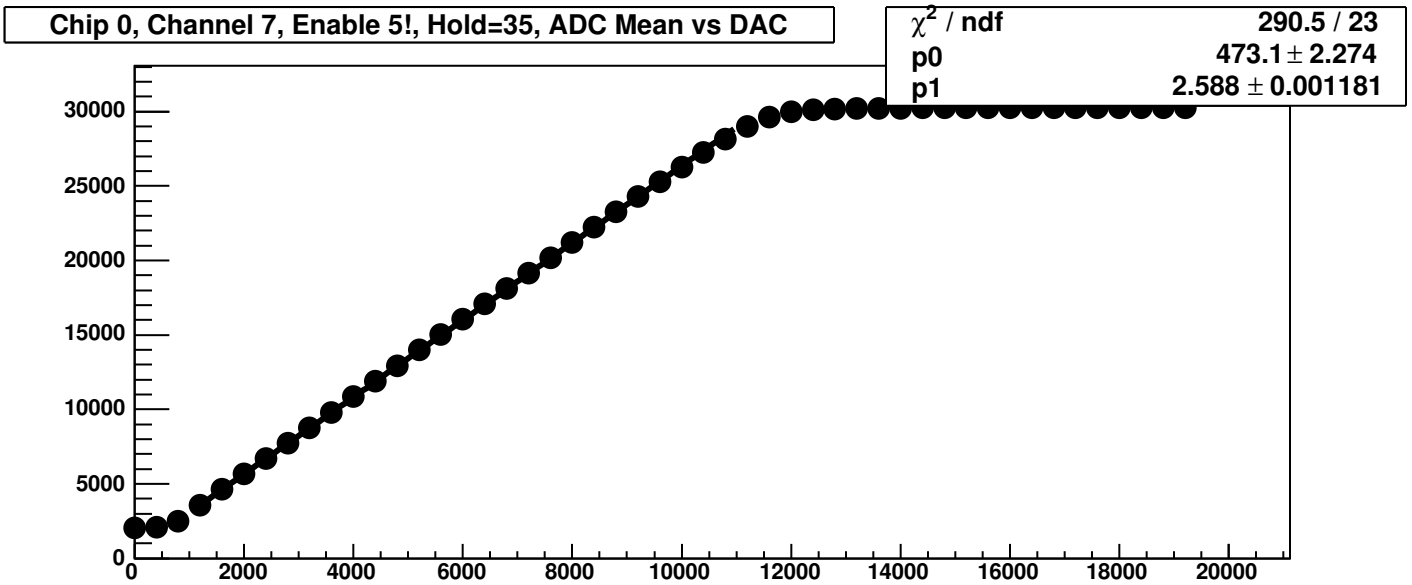
Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

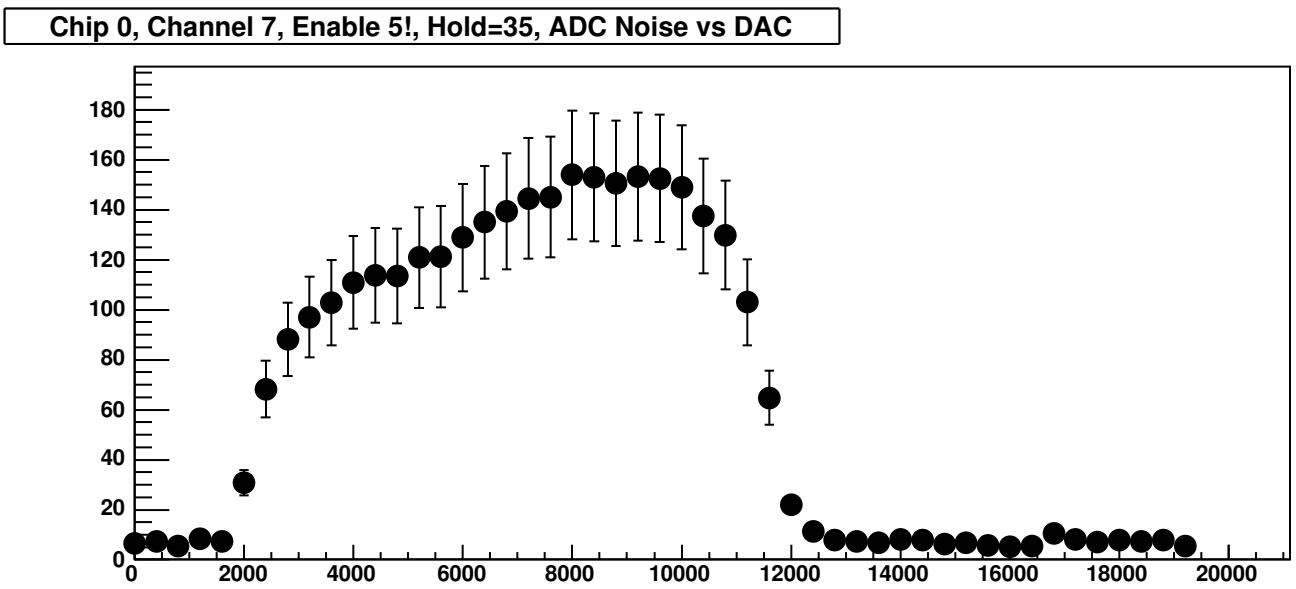


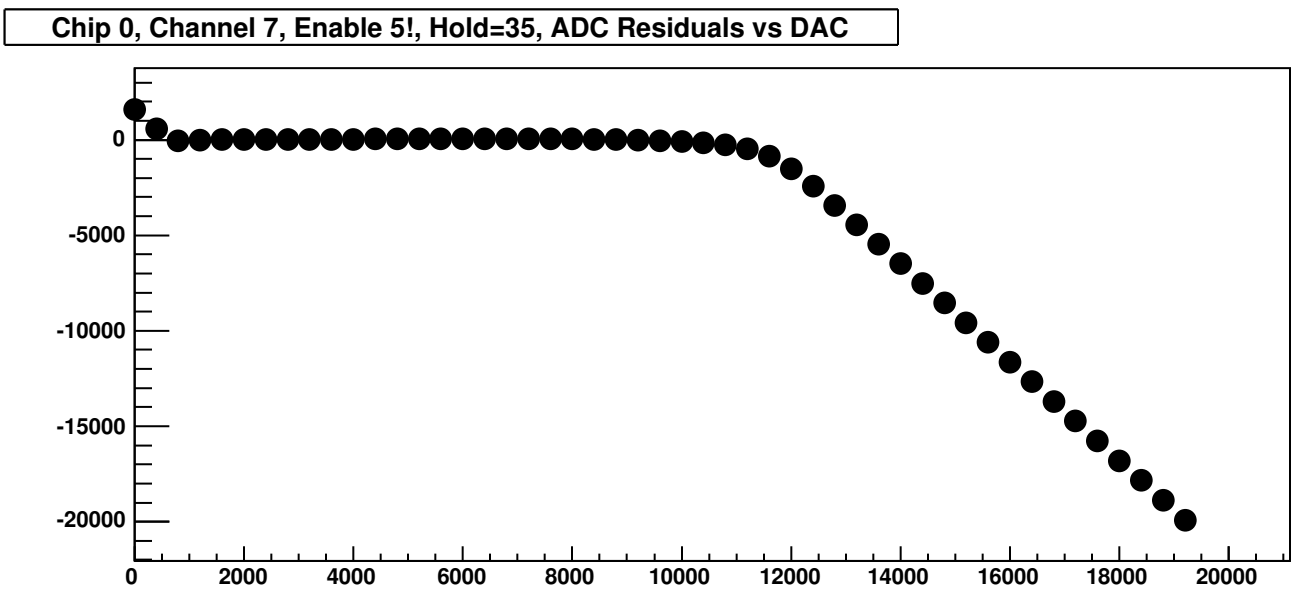

Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.16 / 23
p0 311.7 ± 0.7026
p1 0.02341 ± 0.0001058
700
600
500
400
300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

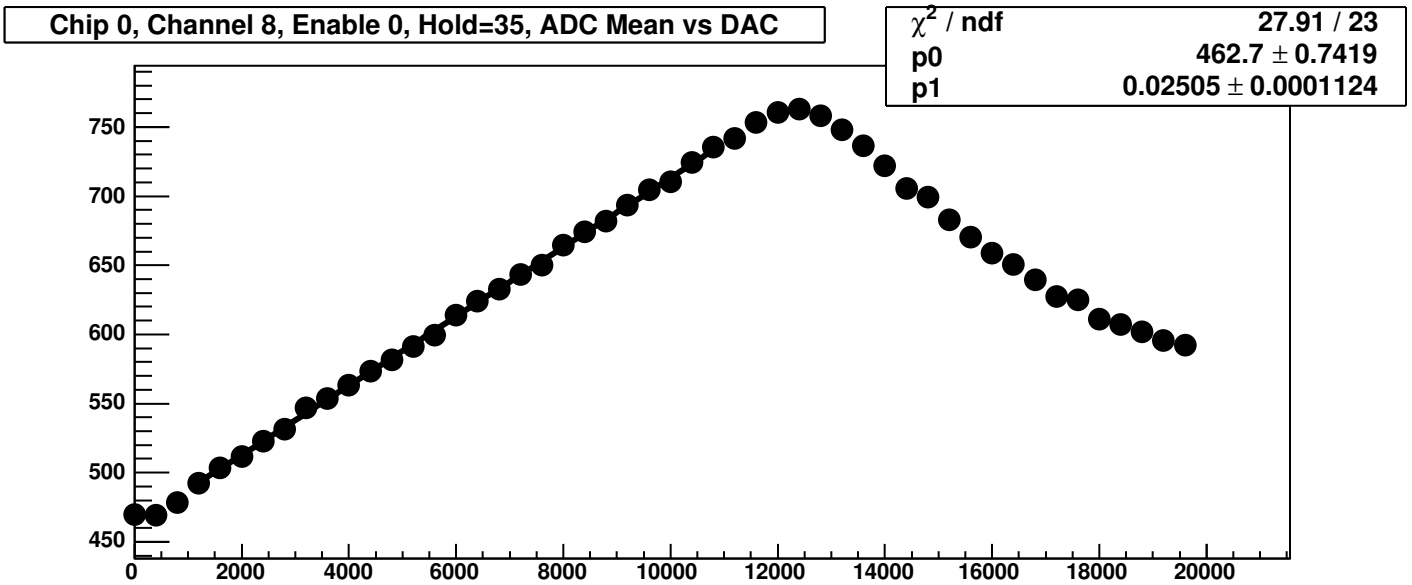

Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

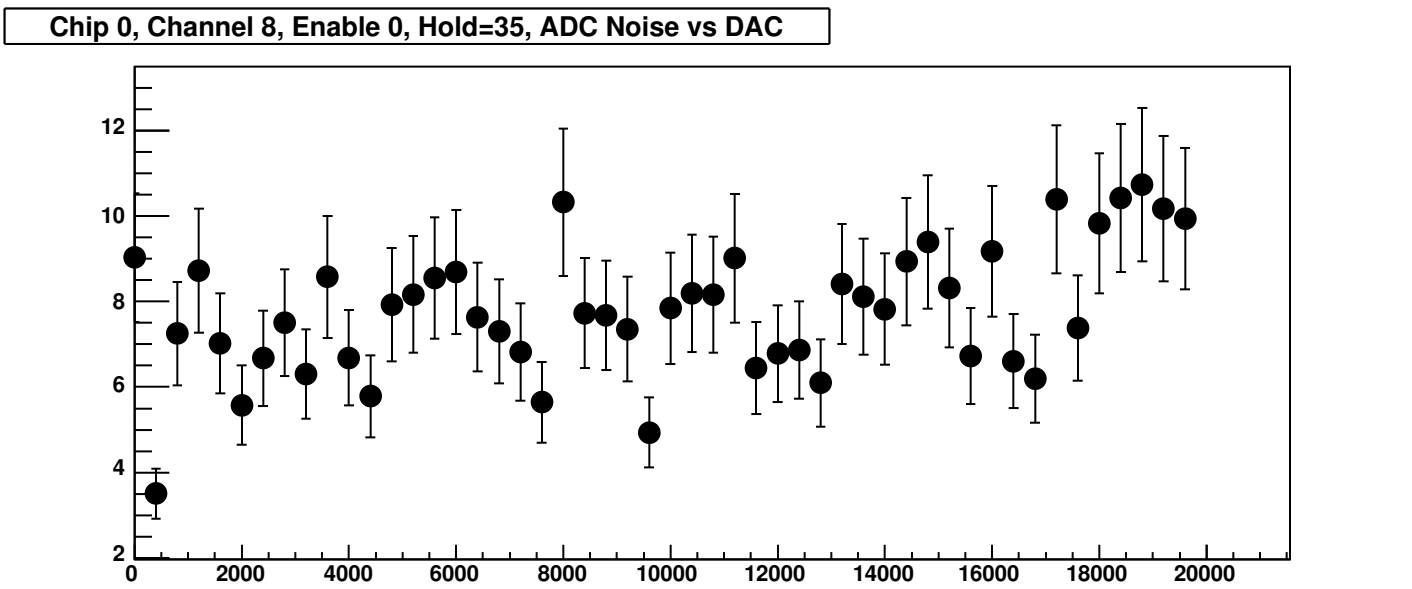

Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

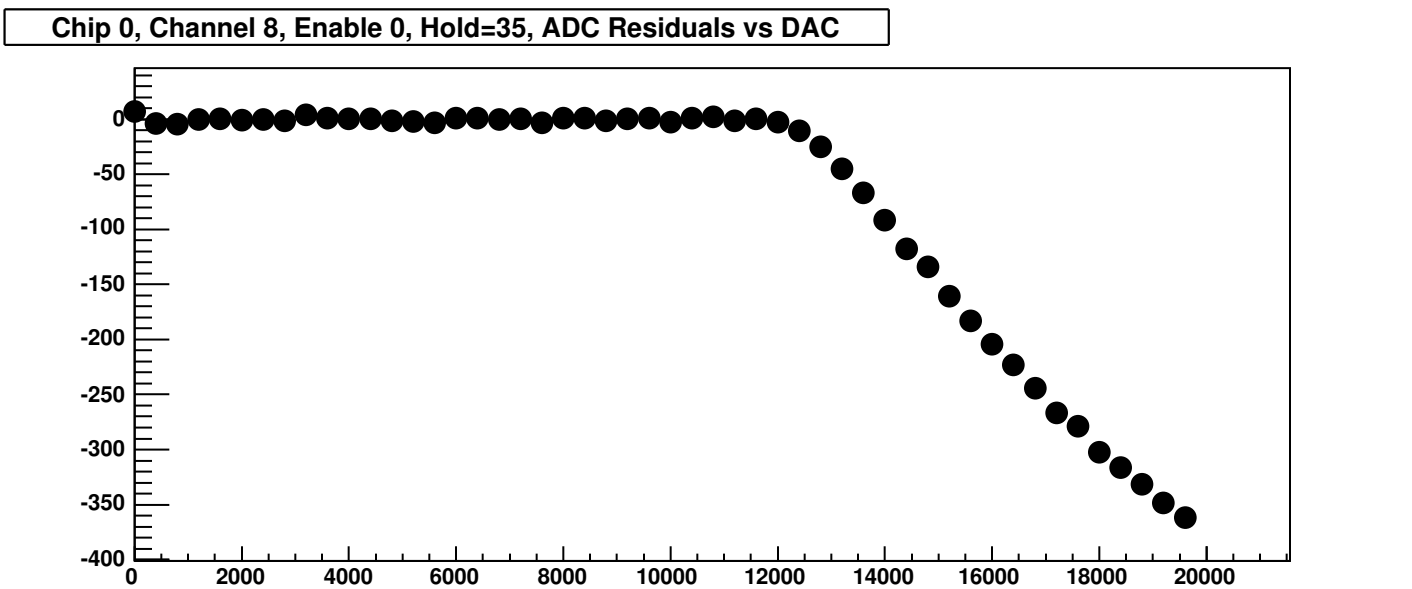

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 290.5 / 23
p0 473.1 ± 2.274
p1 2.588 ± 0.001181
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

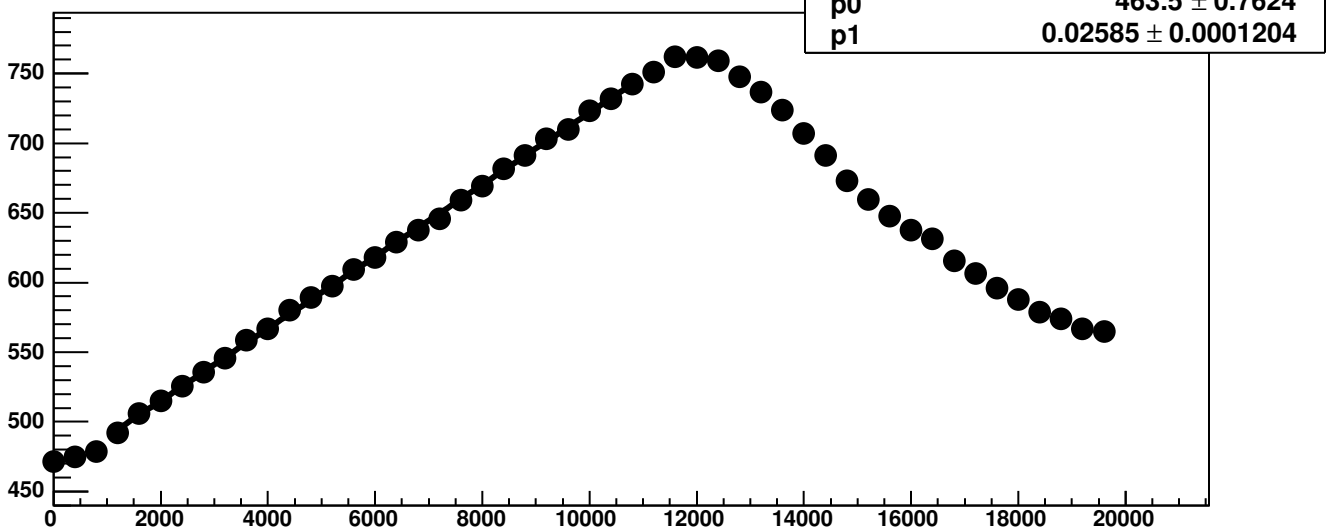

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 27.91 / 23
p0 462.7 ± 0.7419
p1 0.02505 ± 0.0001124
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

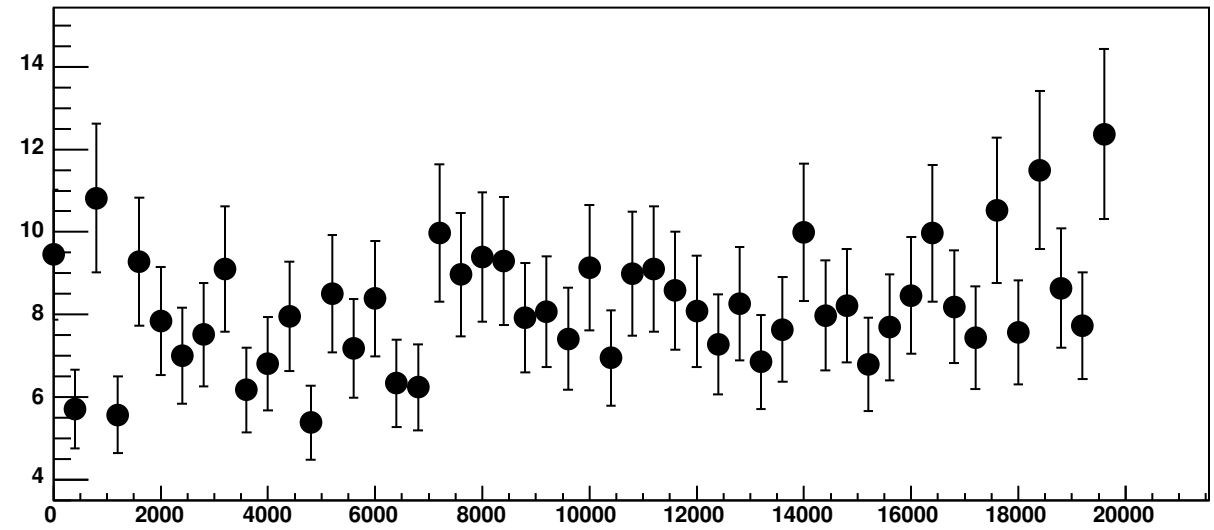

Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

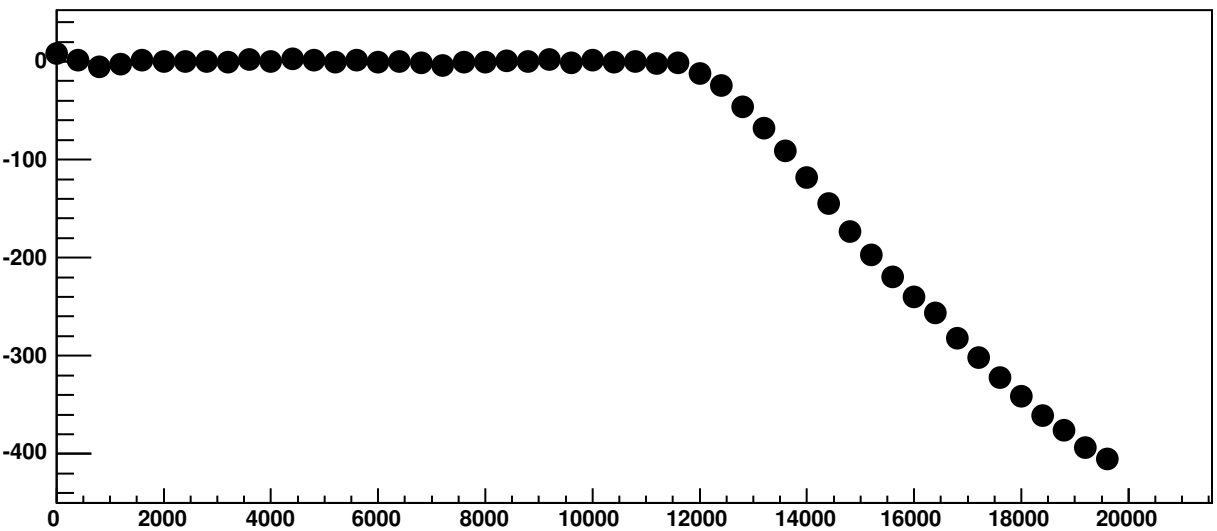
Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

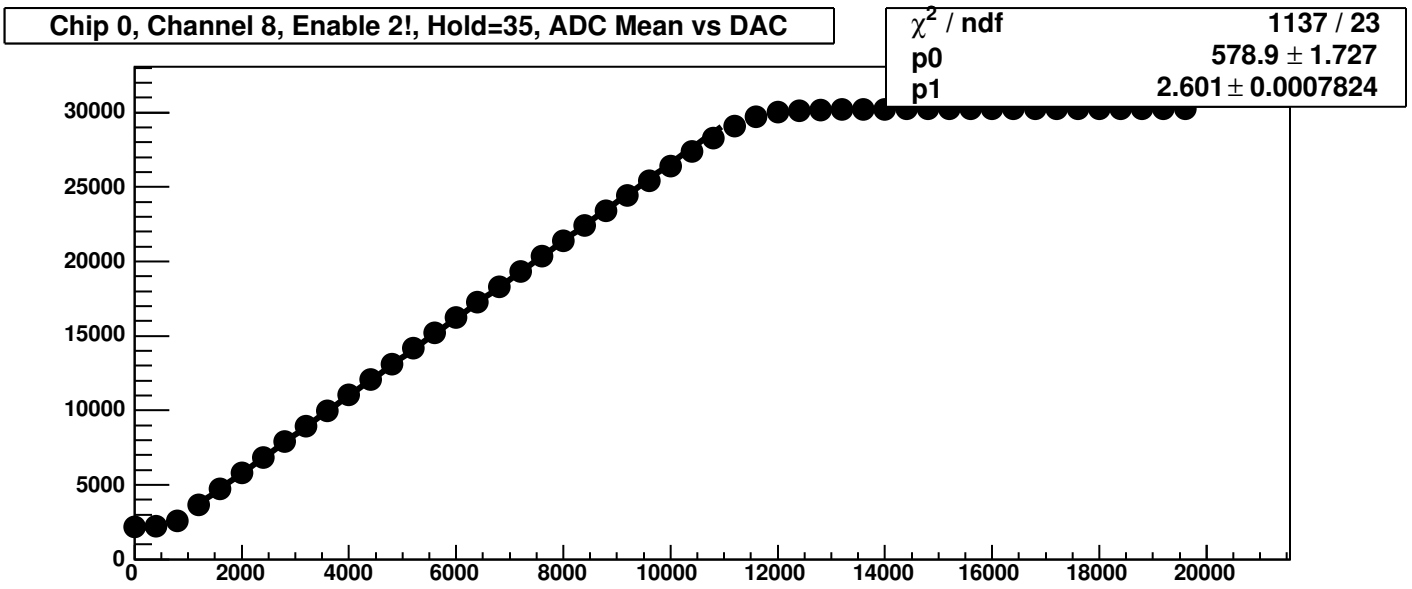


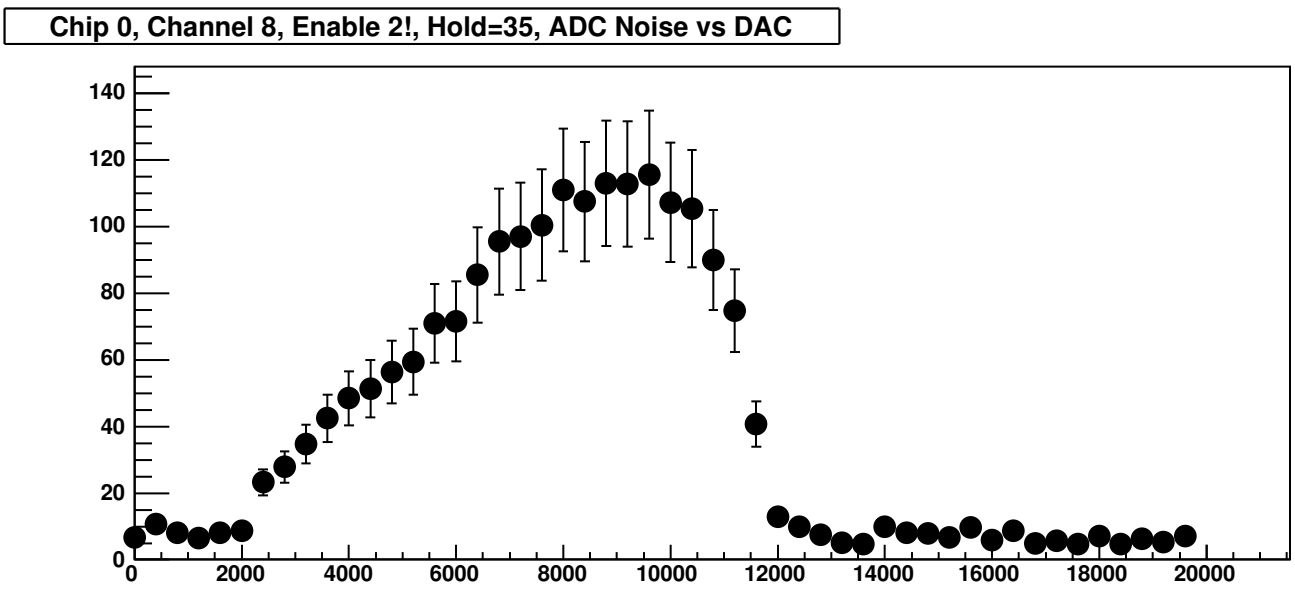
Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

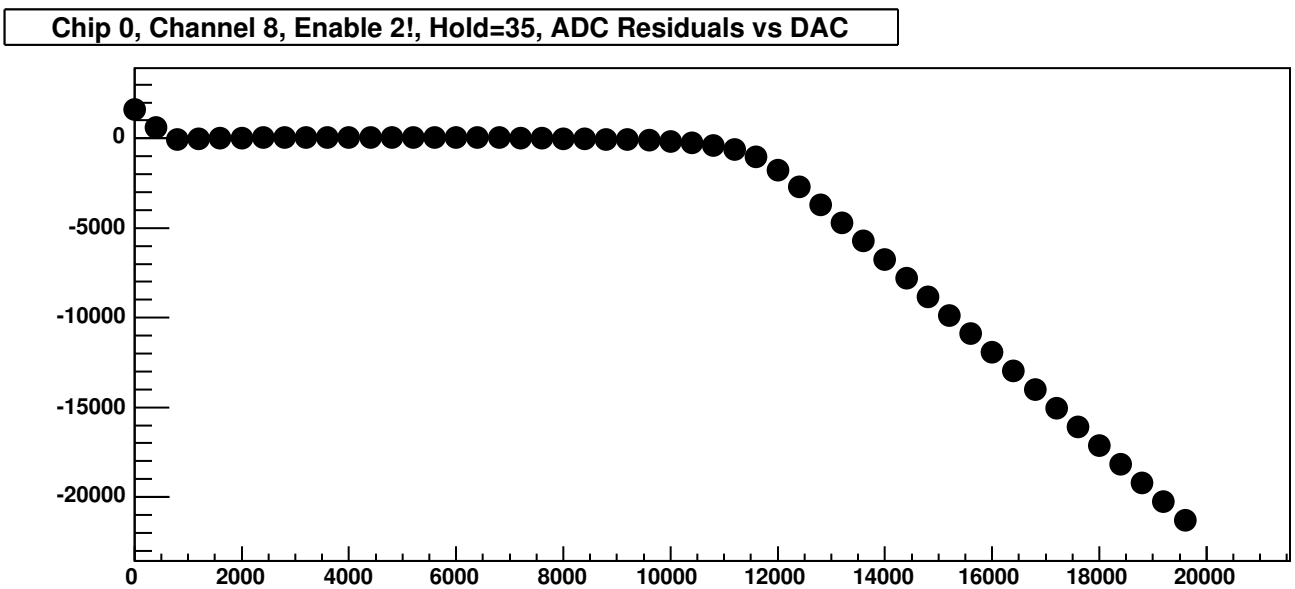


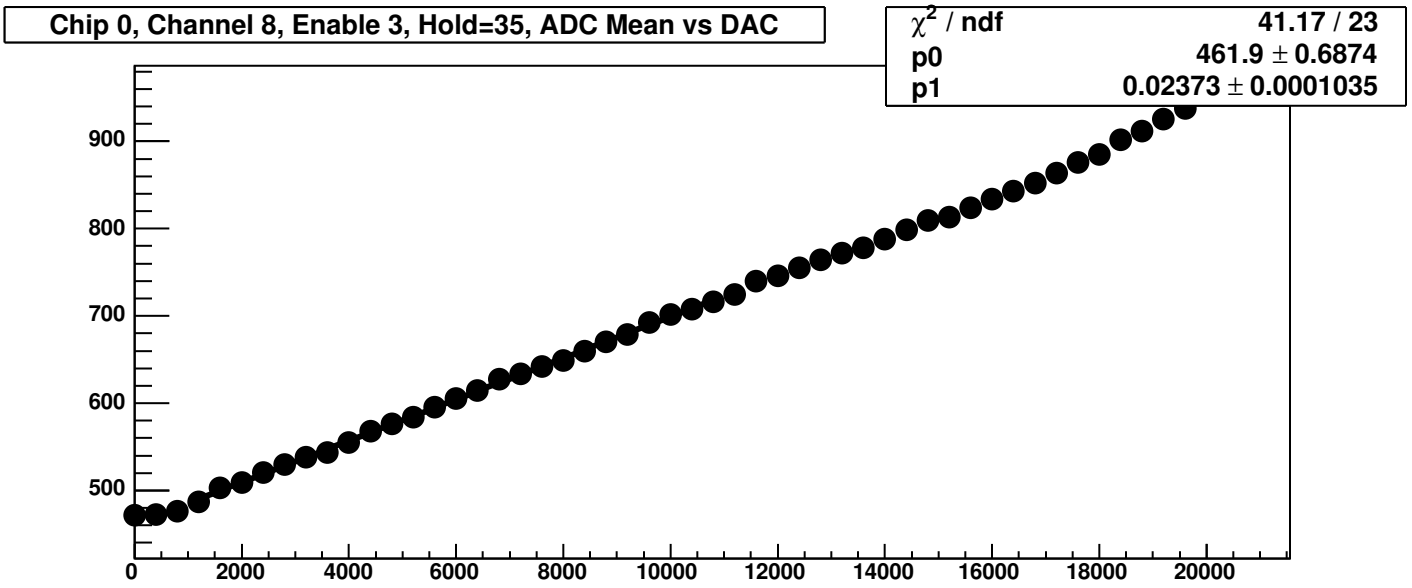
Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

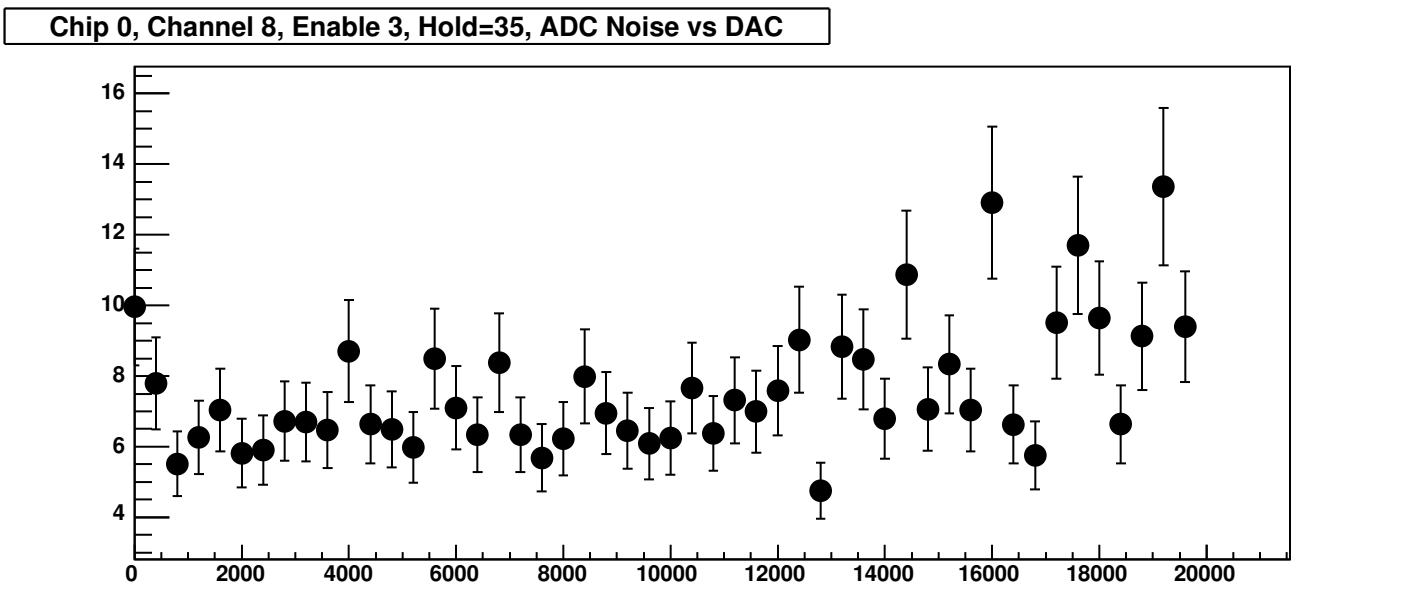


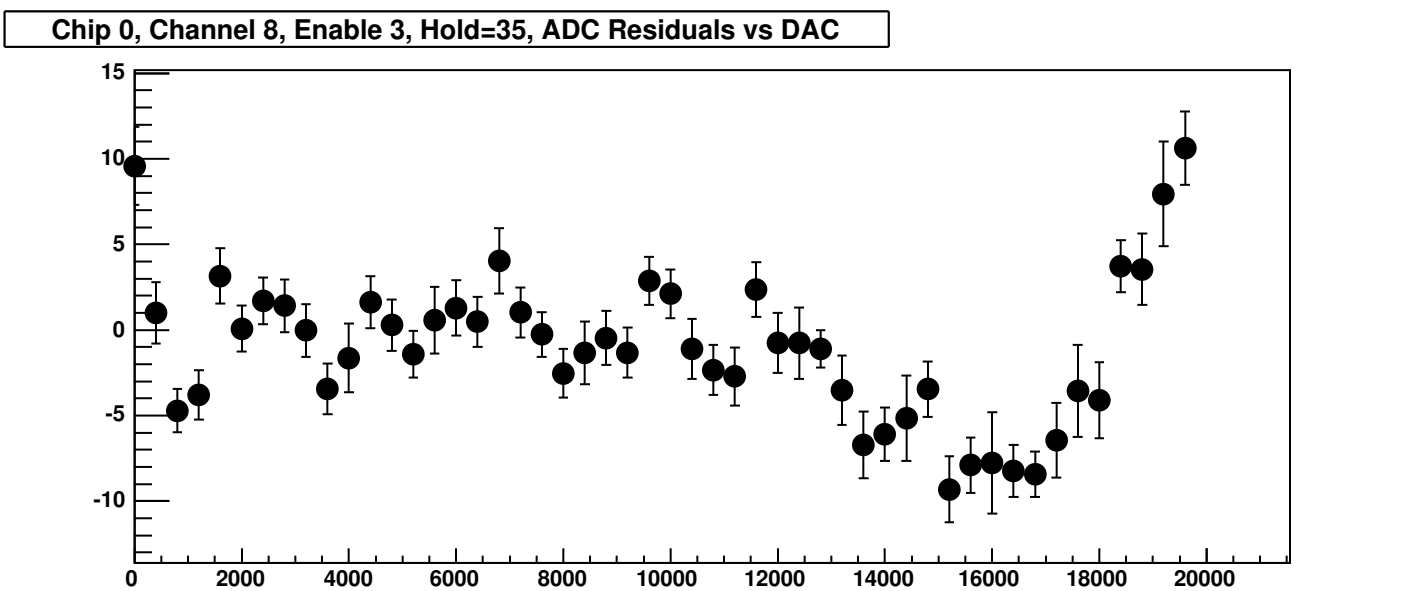

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1137 / 23
p0 578.9 ± 1.727
p1 2.601 ± 0.0007824
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

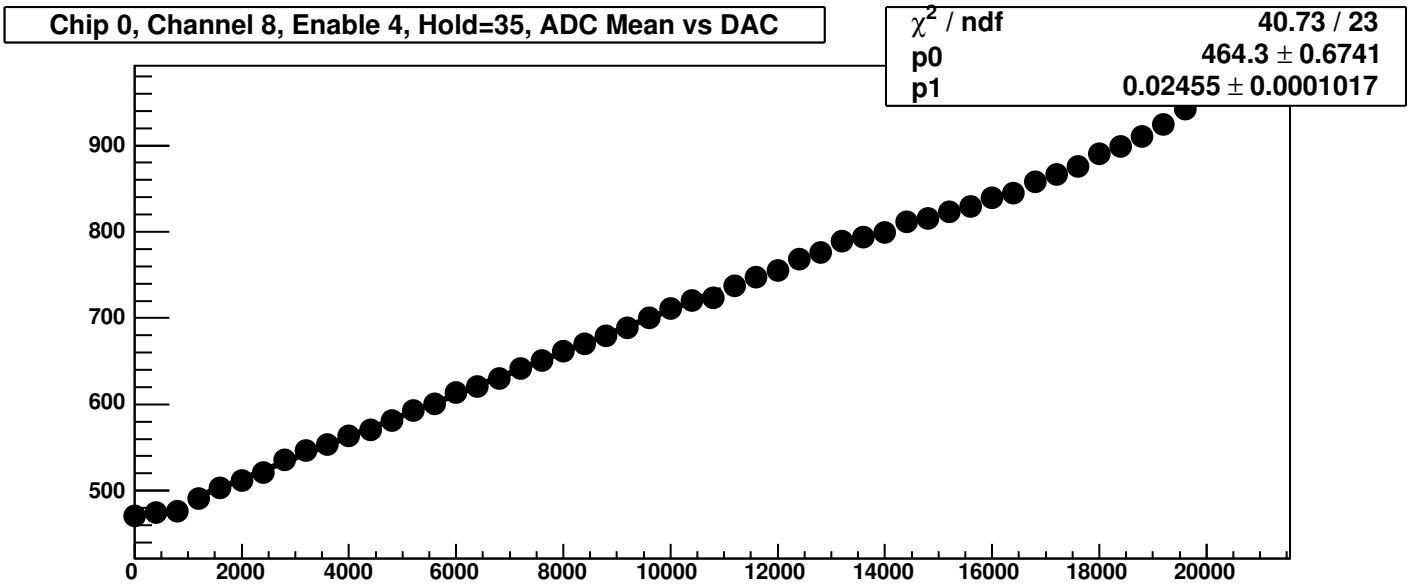

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

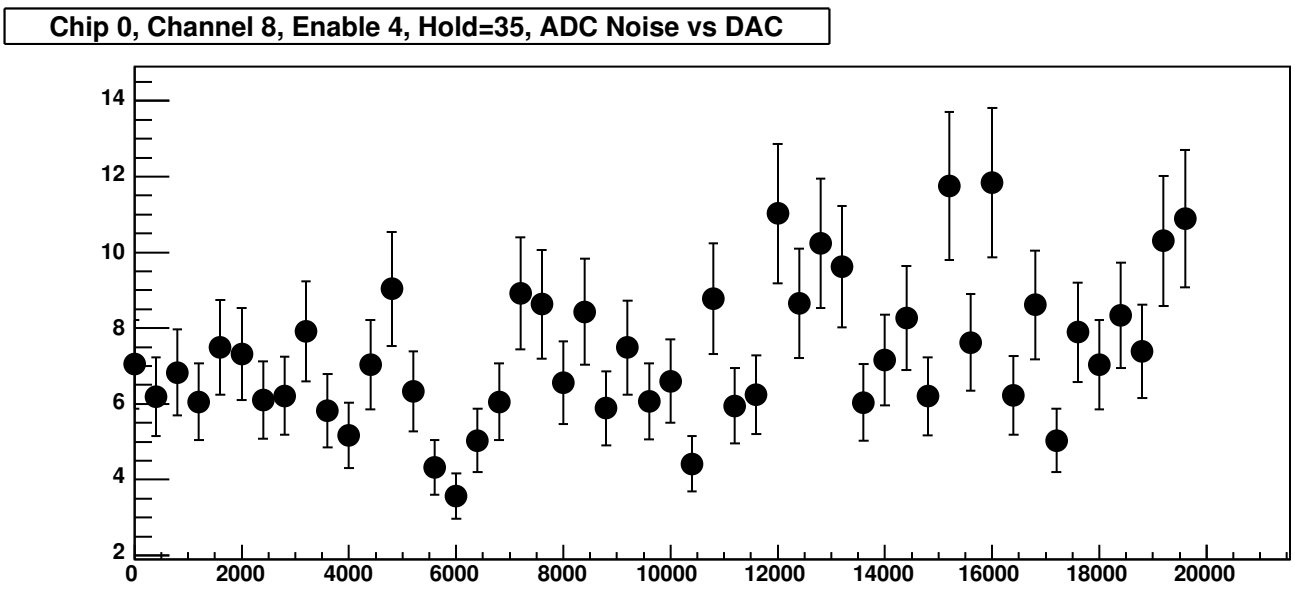

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

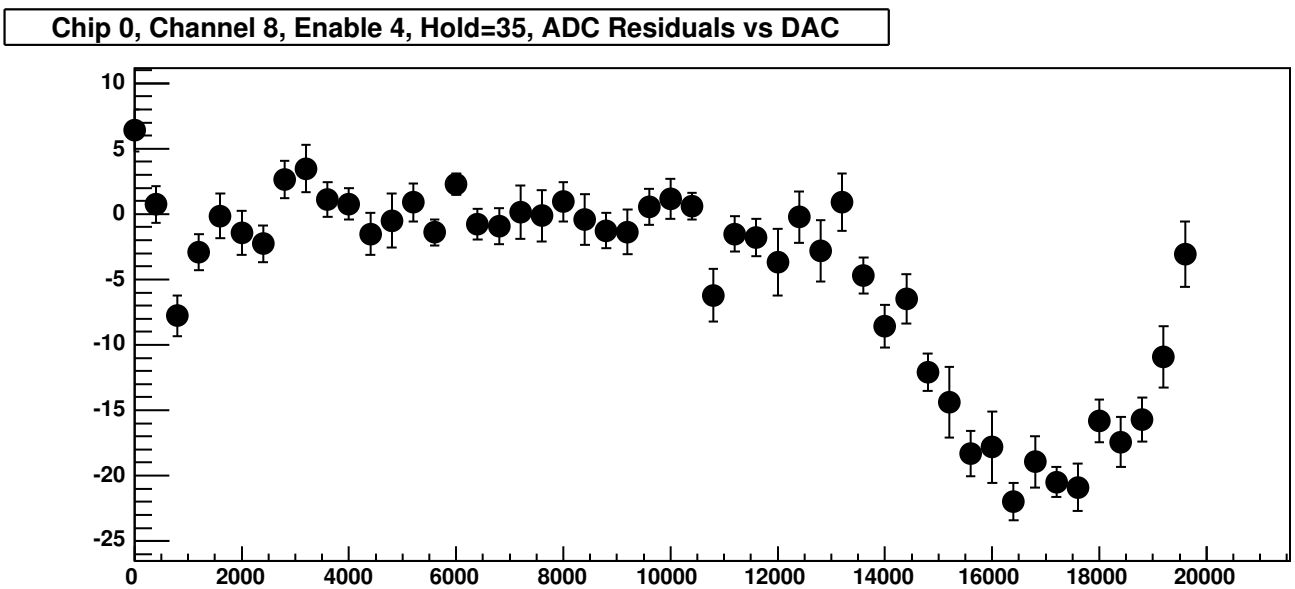

Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 41.17 / 23
p0 461.9 ± 0.6874
p1 0.02373 ± 0.0001035
900
800
700
600
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

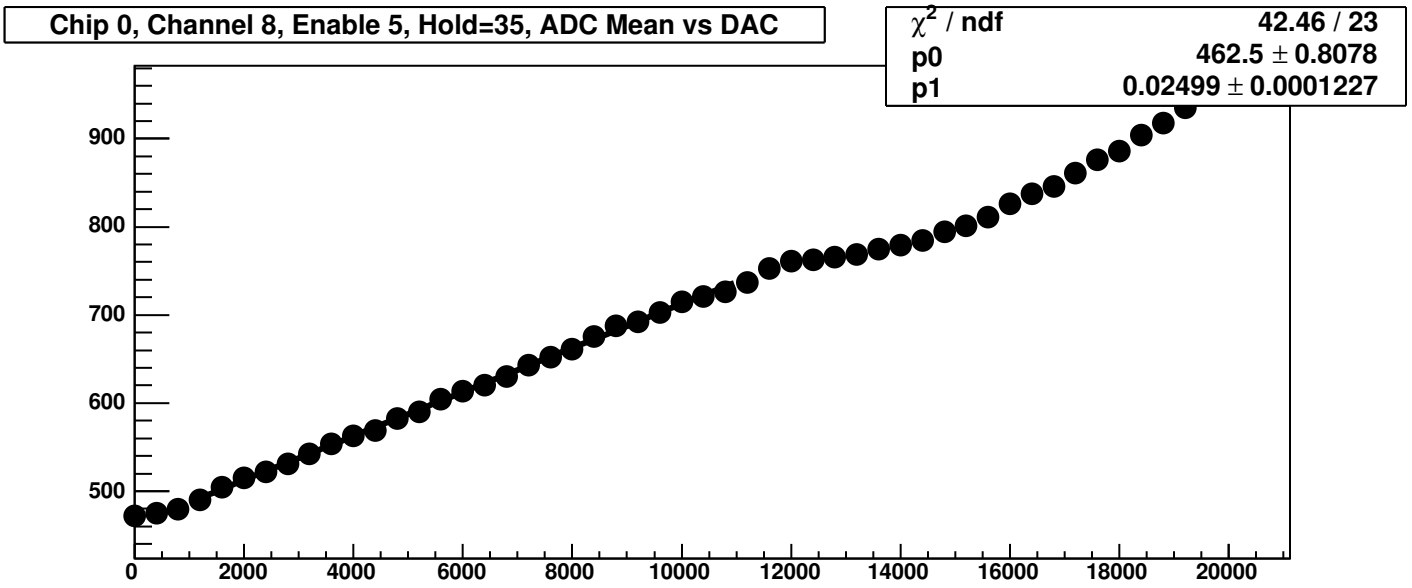

Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

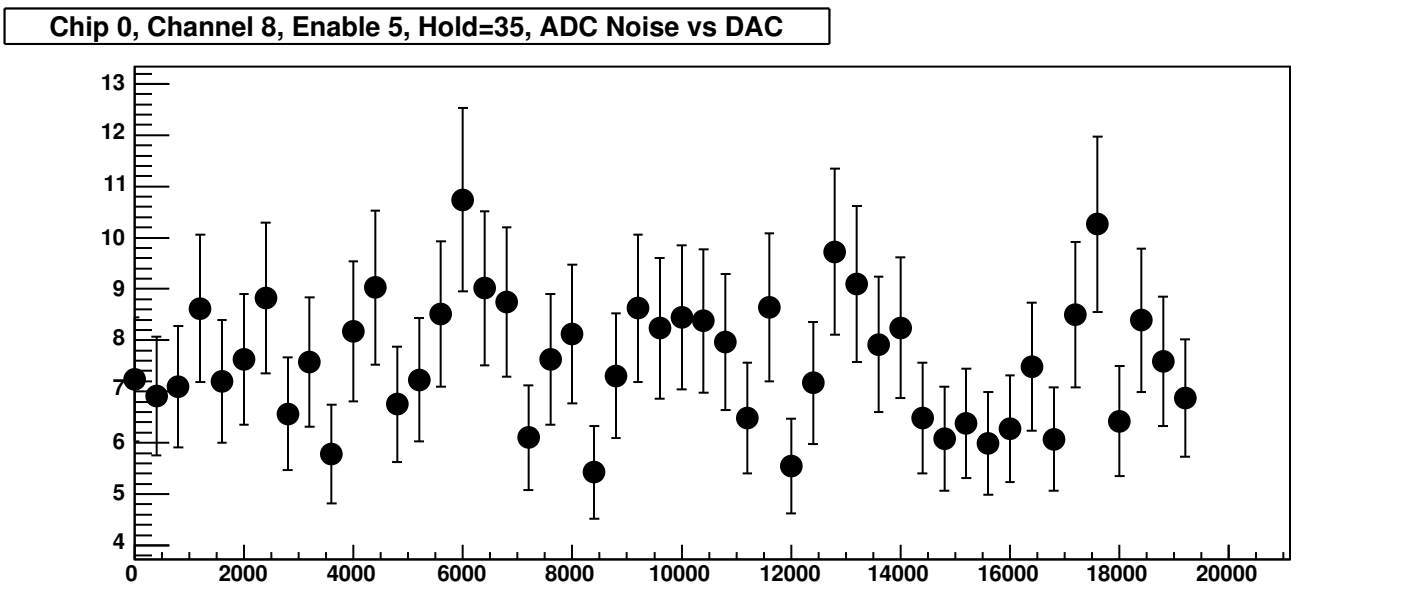

Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

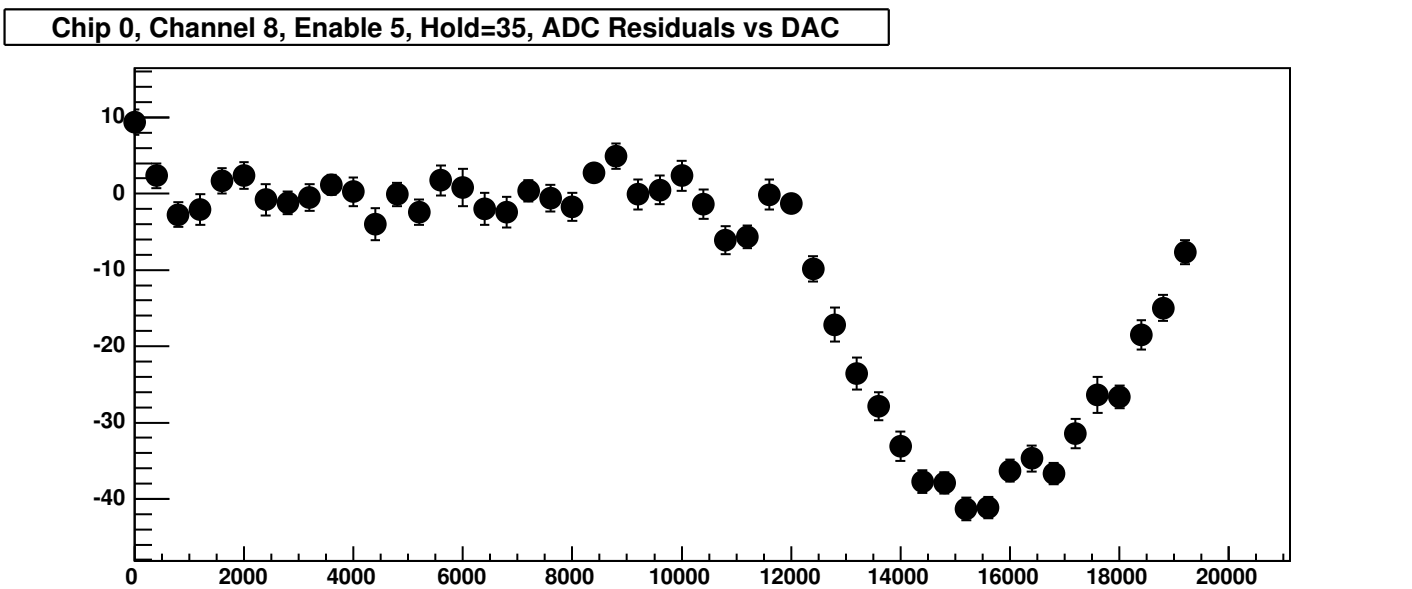

Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 40.73 / 23
p0 464.3 ± 0.6741
p1 0.02455 ± 0.0001017
900
800
700
600
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

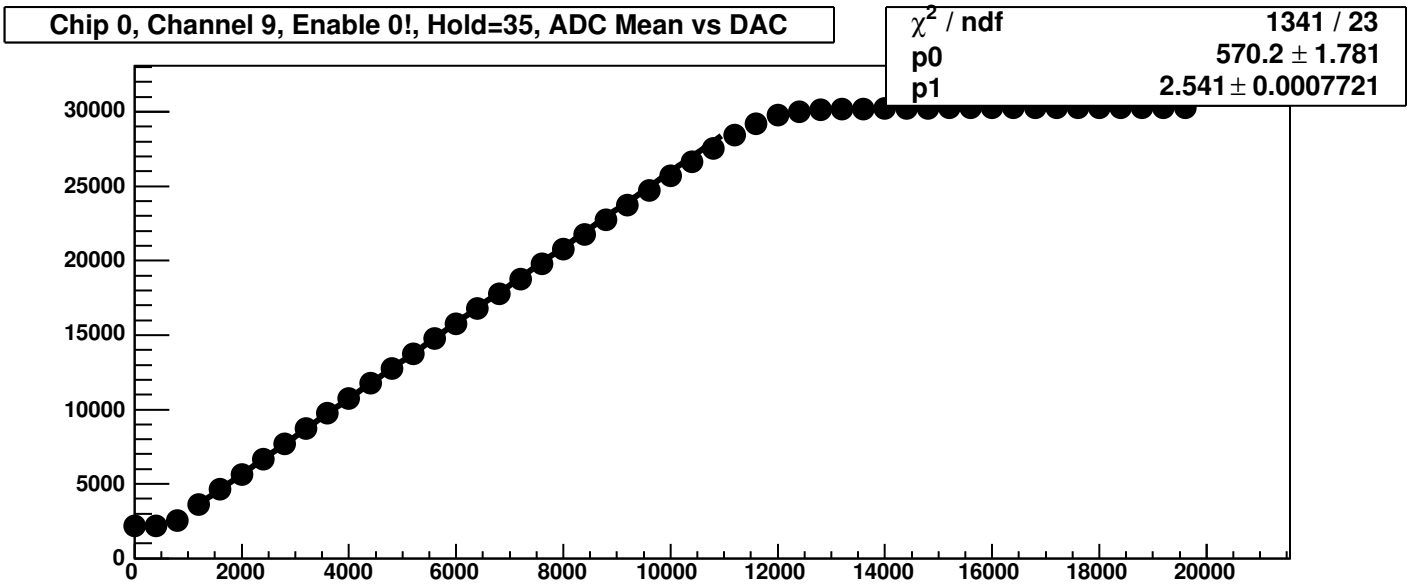

Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

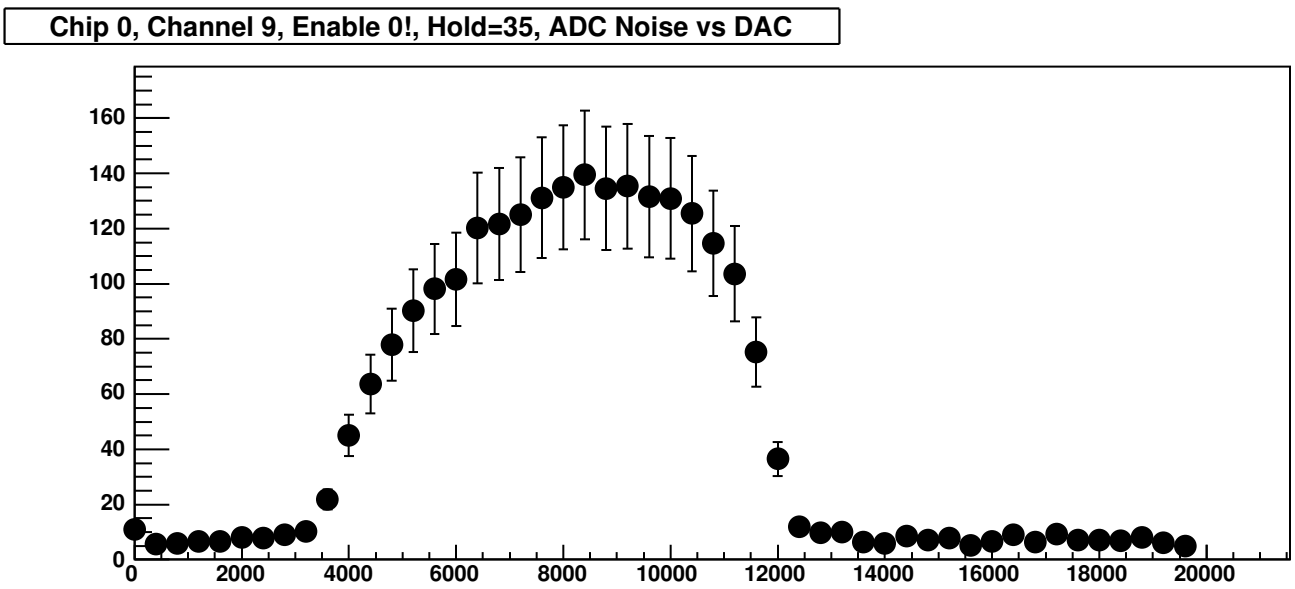

Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

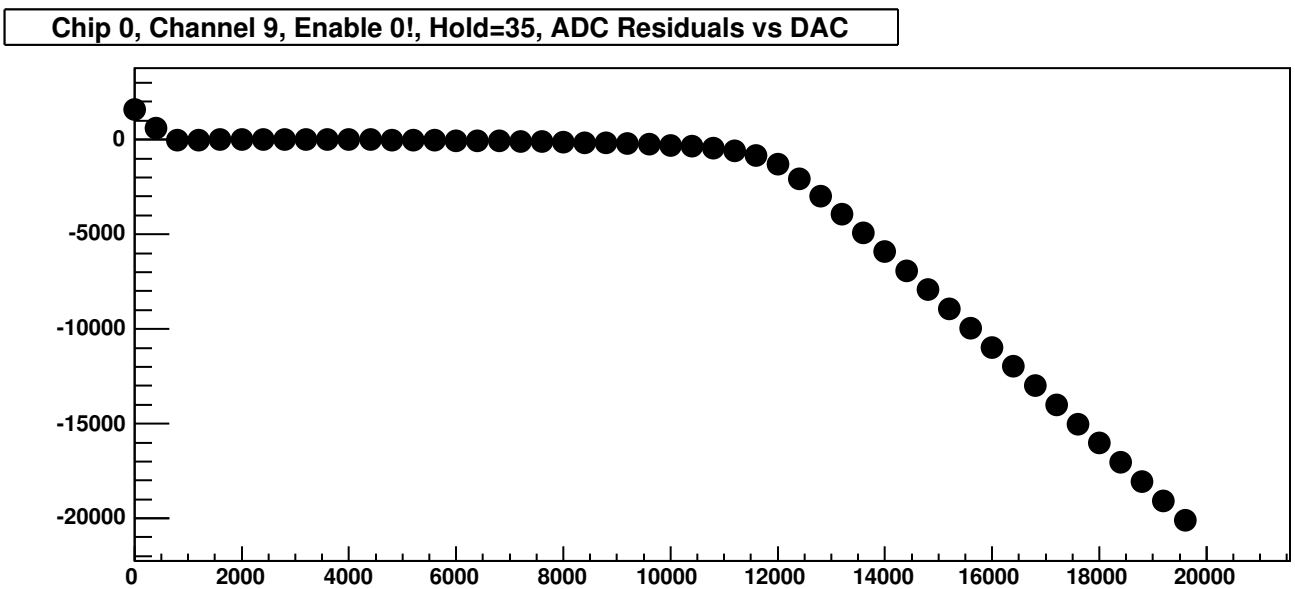

Chip 0, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 42.46 / 23
p0 462.5 ± 0.8078
p1 0.02499 ± 0.0001227
900
800
700
600
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

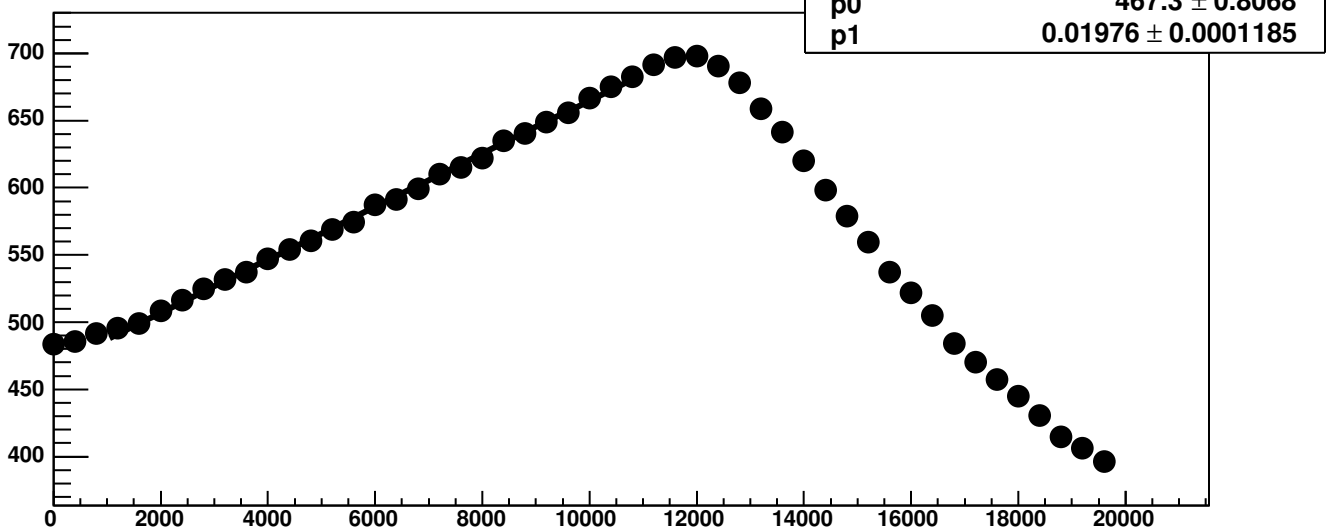

Chip 0, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1341 / 23
p0 570.2 ± 1.781
p1 2.541 ± 0.0007721
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

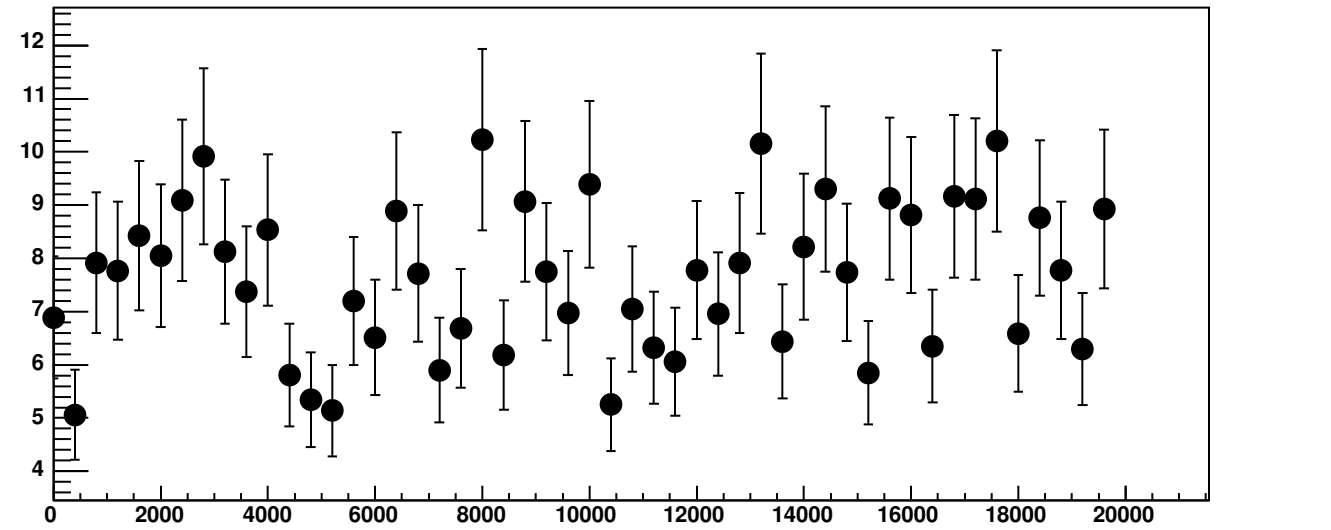

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

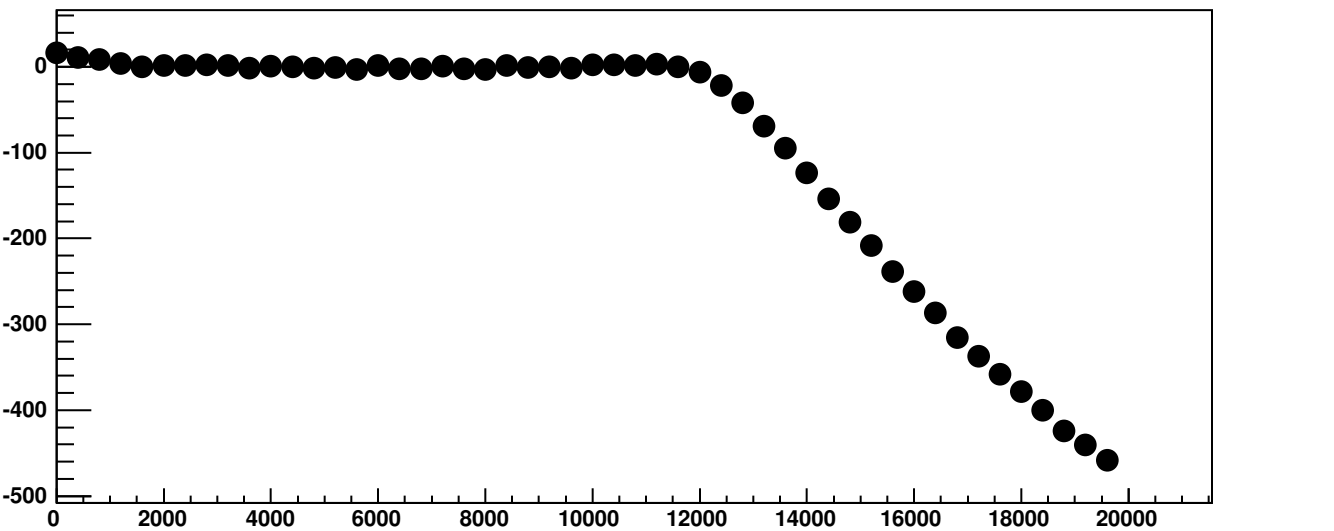
Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

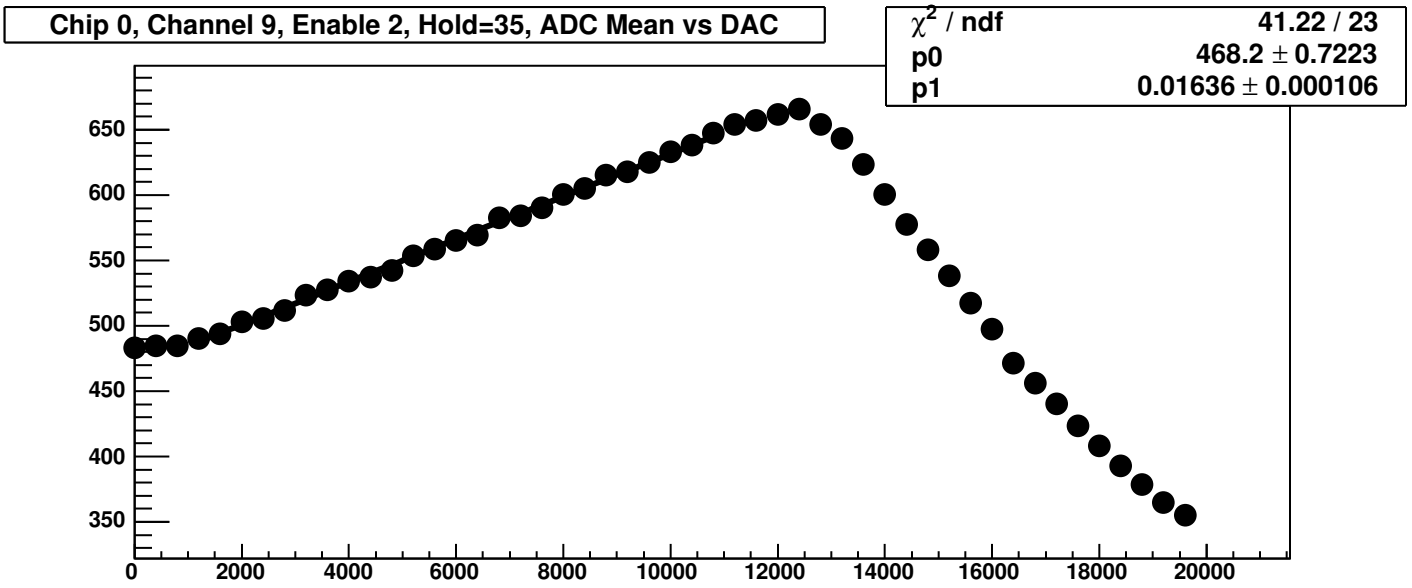


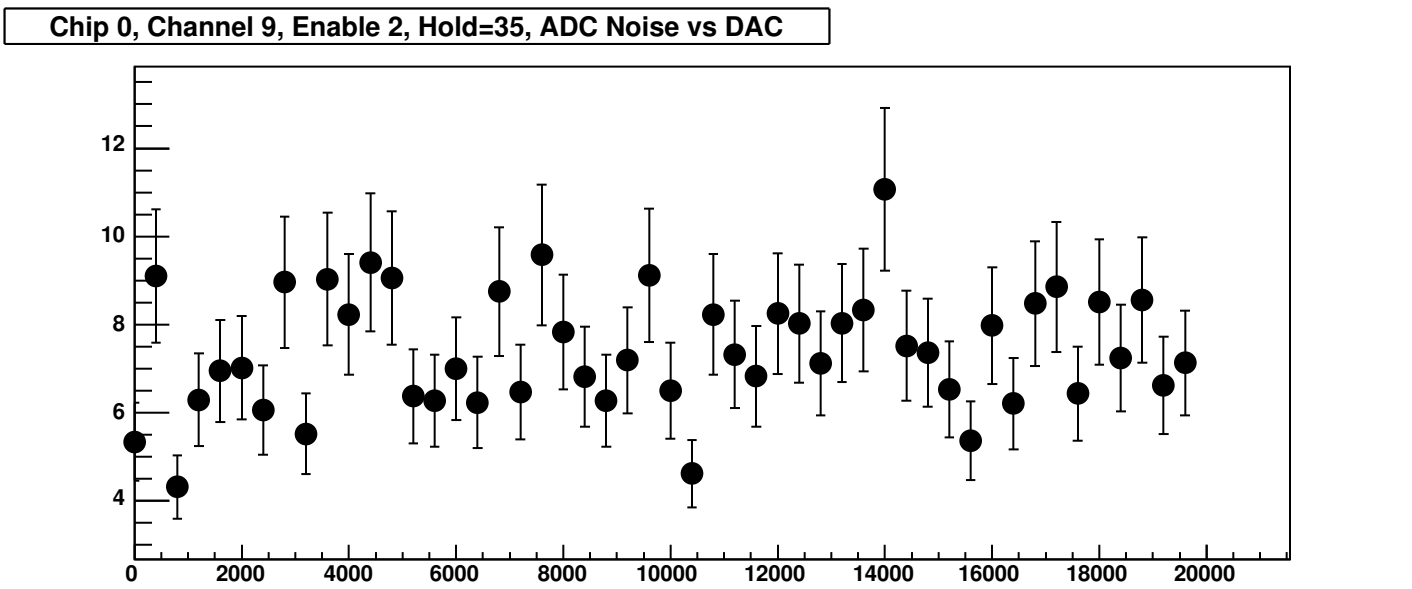
Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

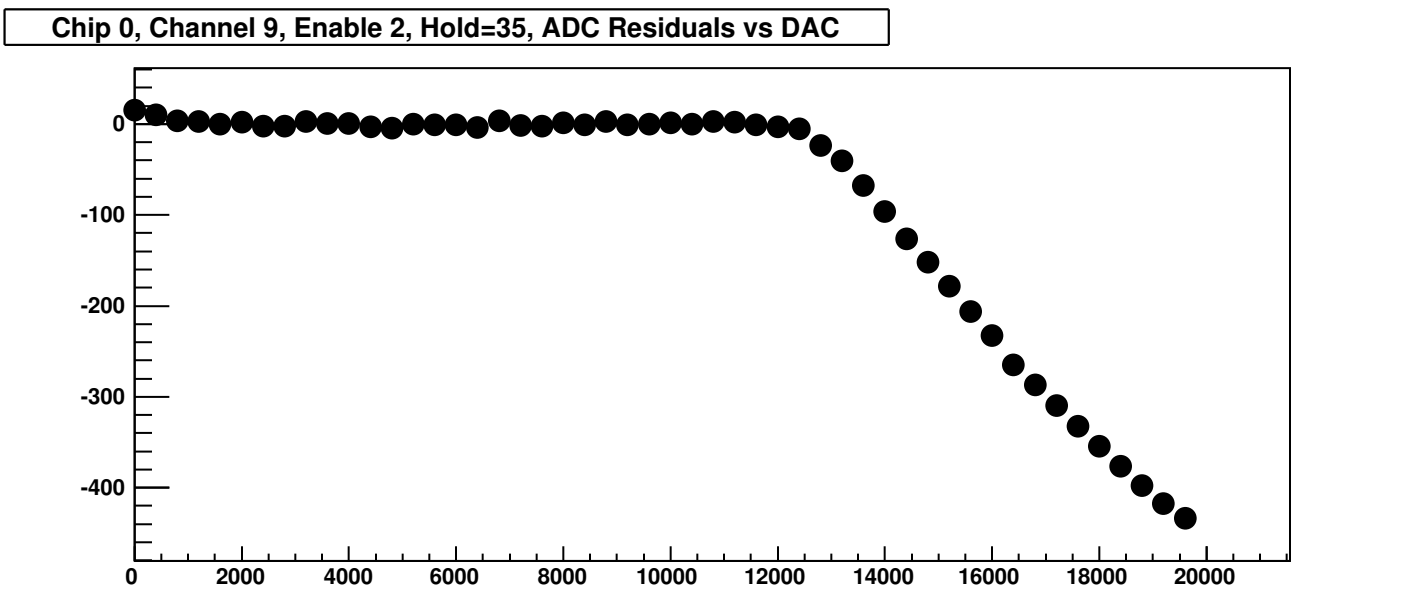


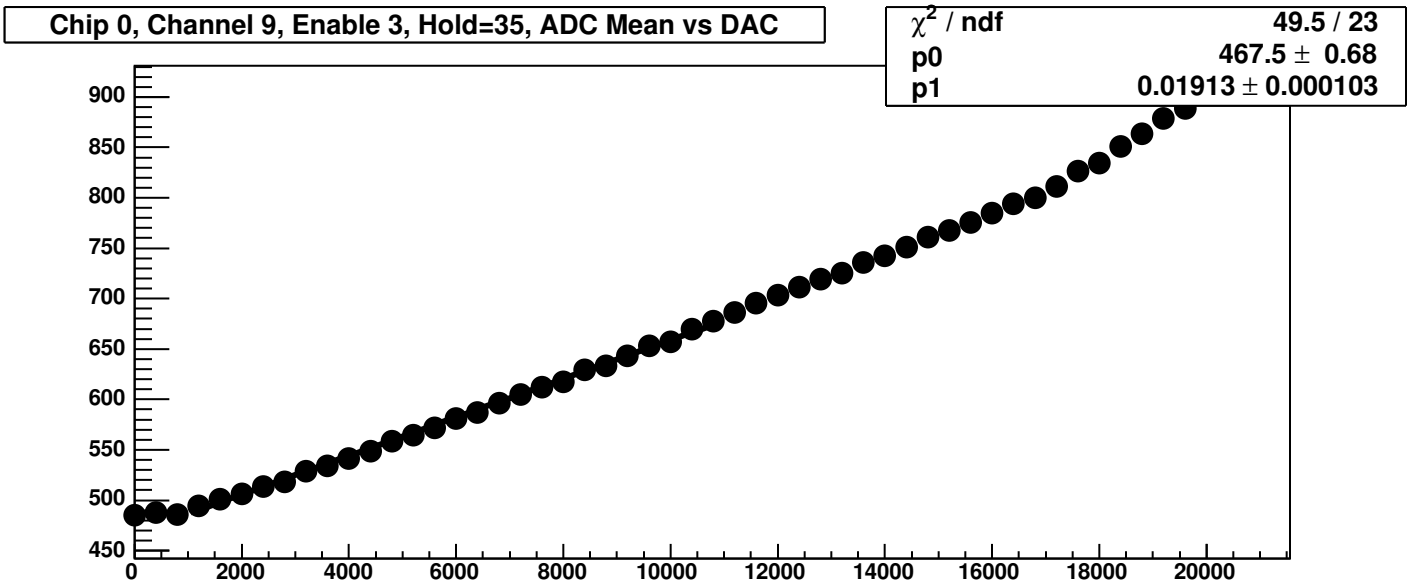
Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

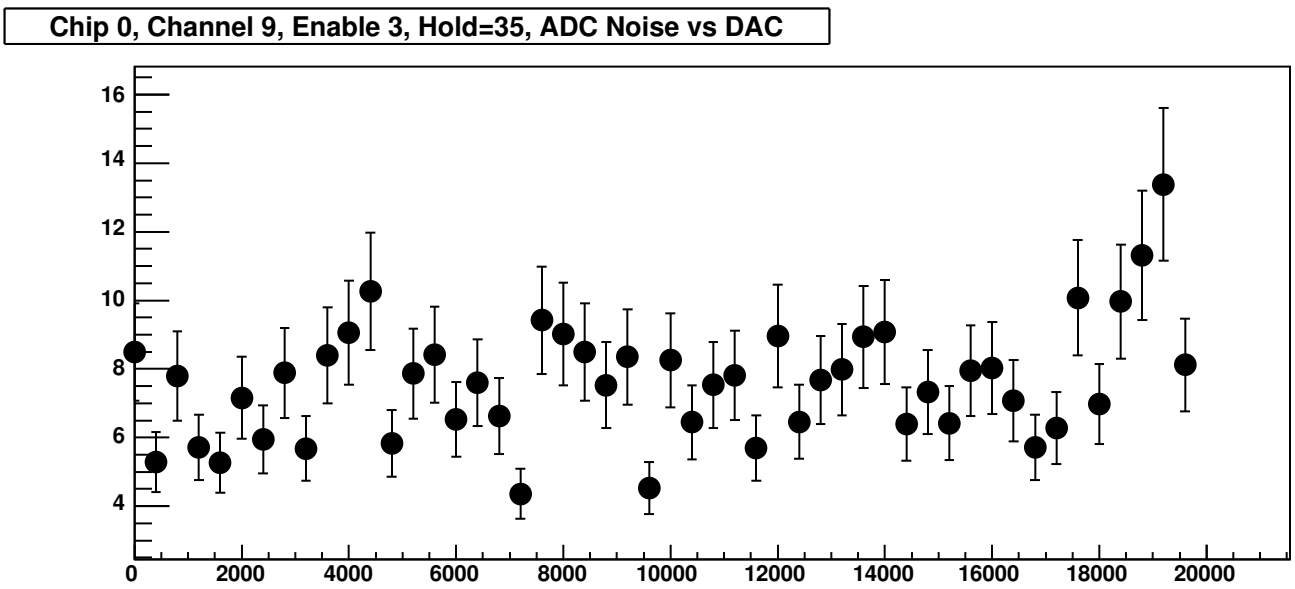


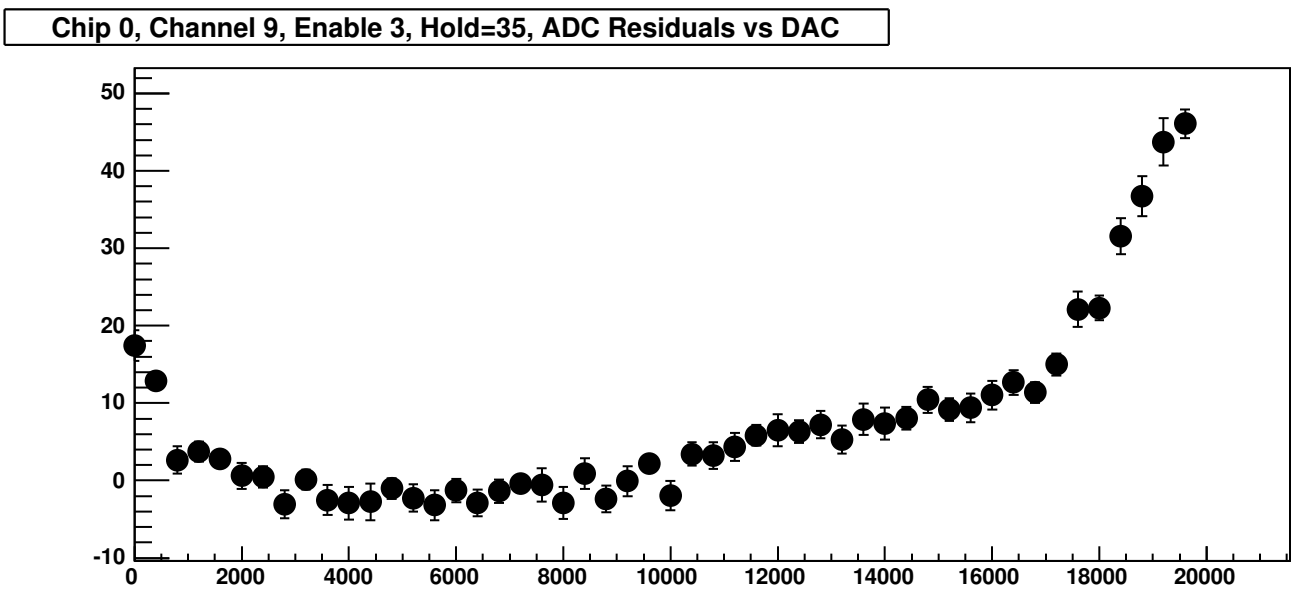

Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 41.22 / 23
p0 468.2 ± 0.7223
p1 0.01636 ± 0.000106
650
600
550
500
450
400
350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

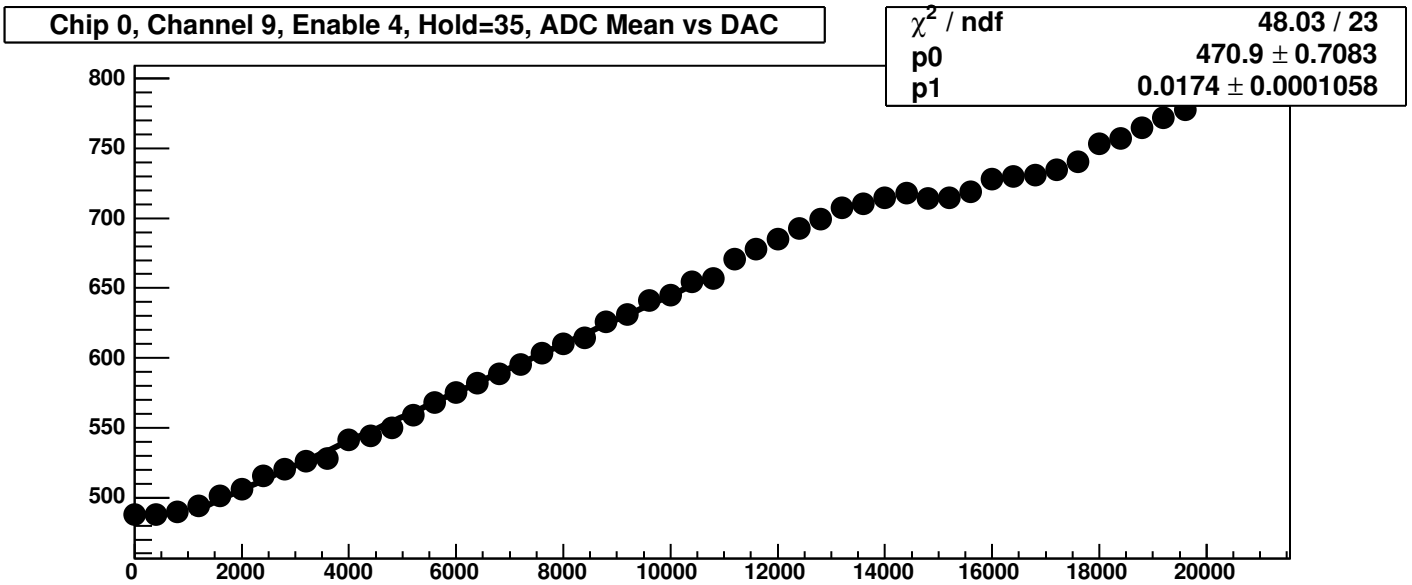

Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

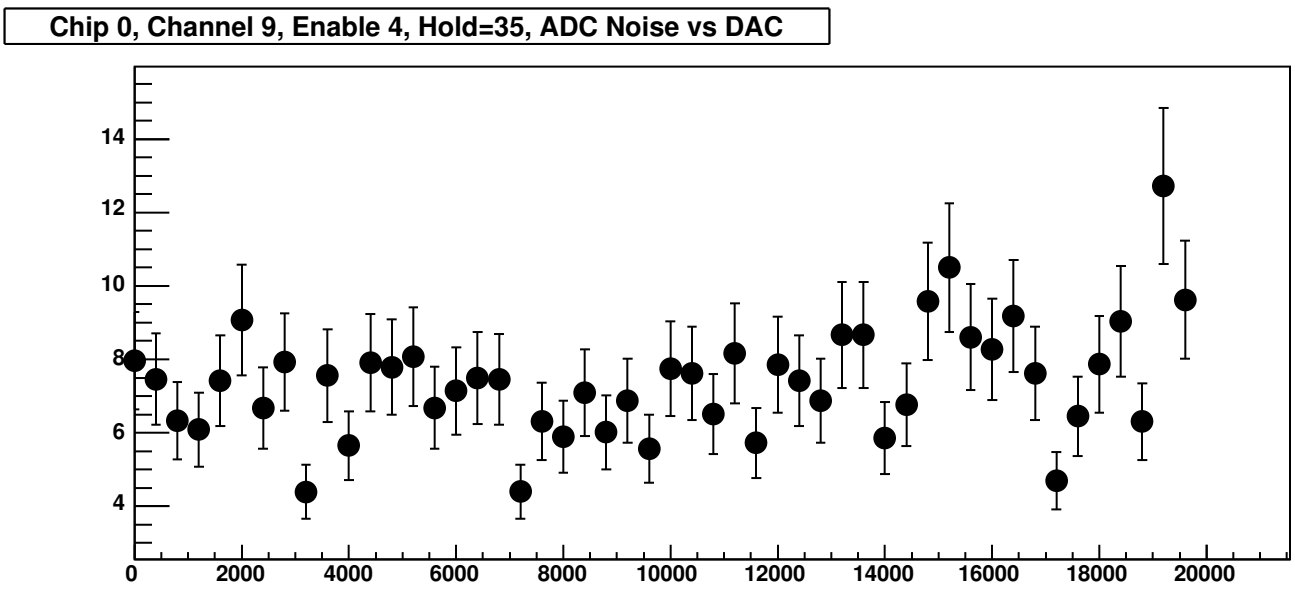

Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

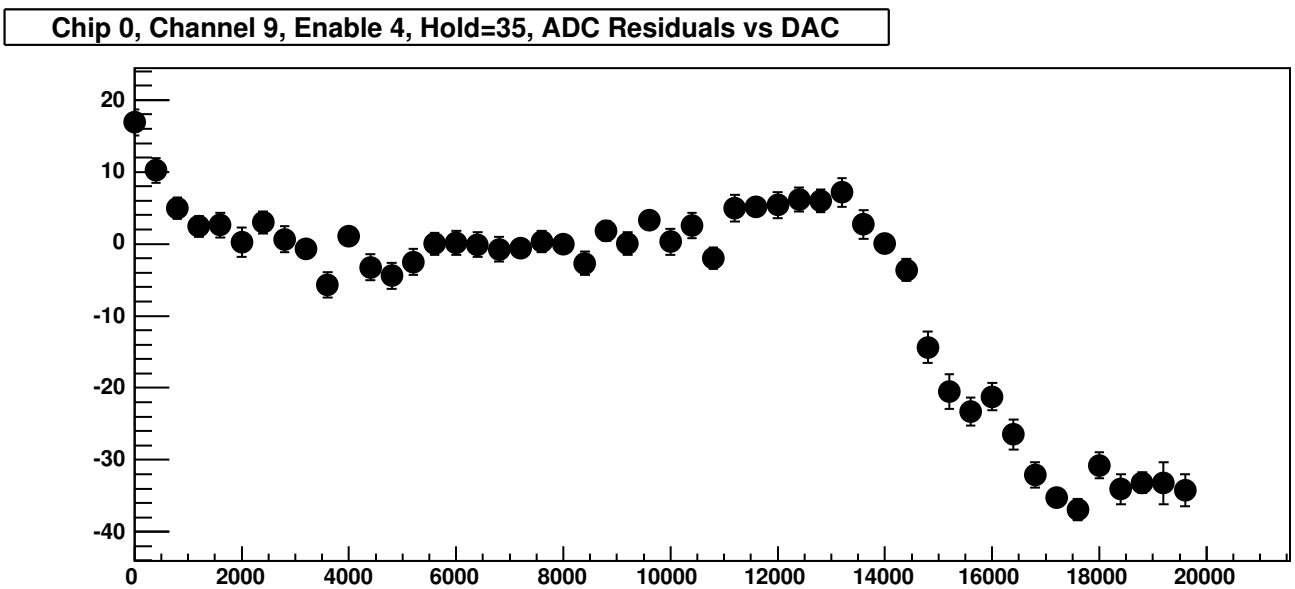

Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 49.5 / 23
p0 467.5 ± 0.68
p1 0.01913 ± 0.000103
900
850
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

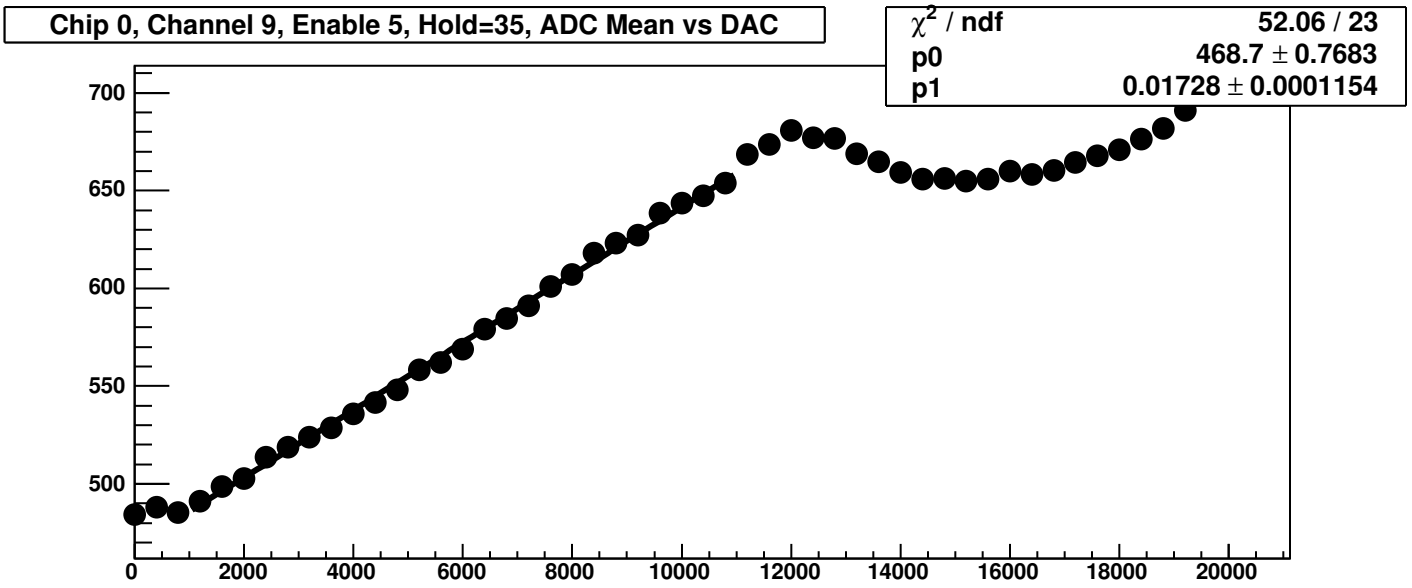

Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

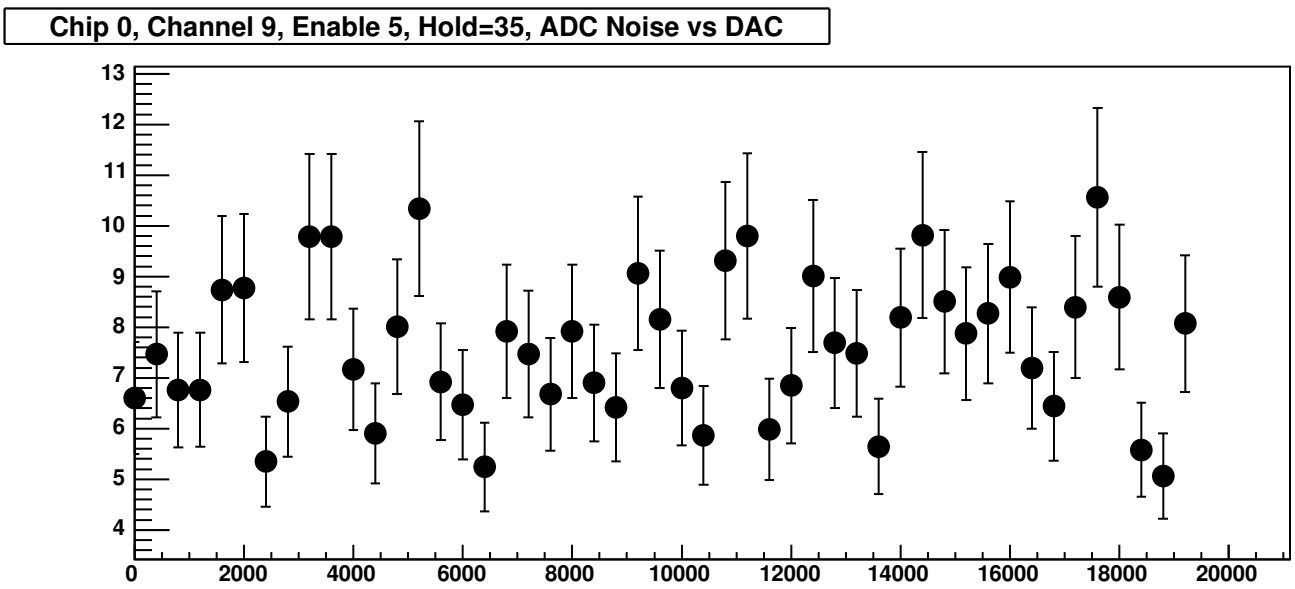

Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

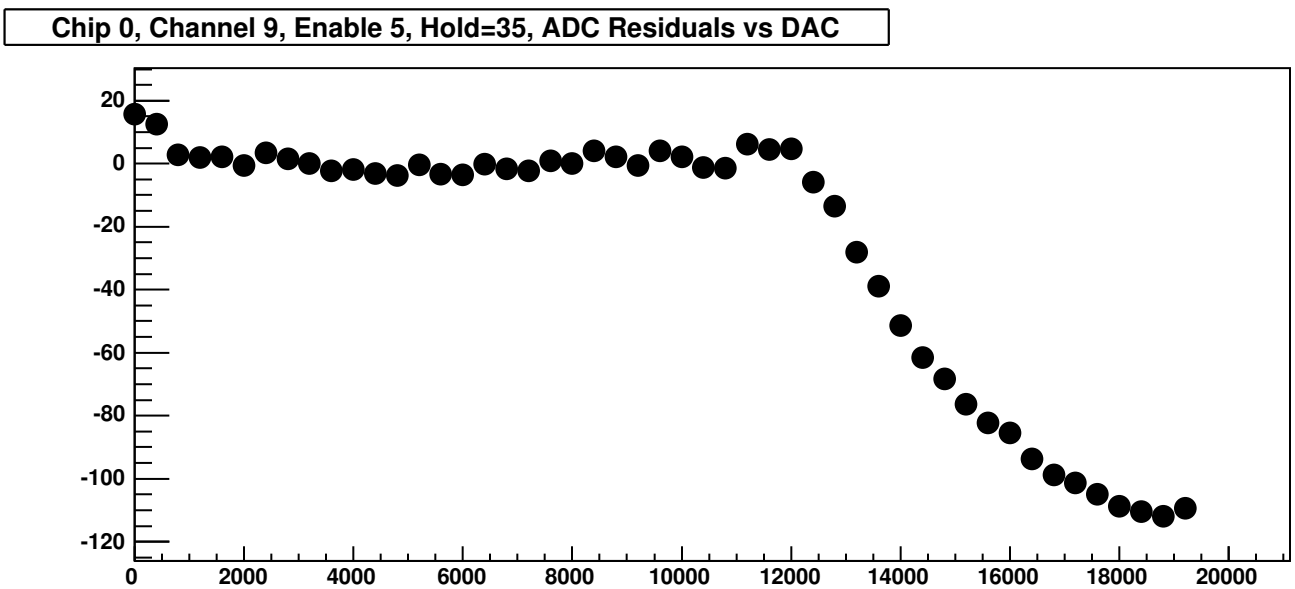

Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 48.03 / 23
p0 470.9 ± 0.7083
p1 0.0174 ± 0.0001058
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

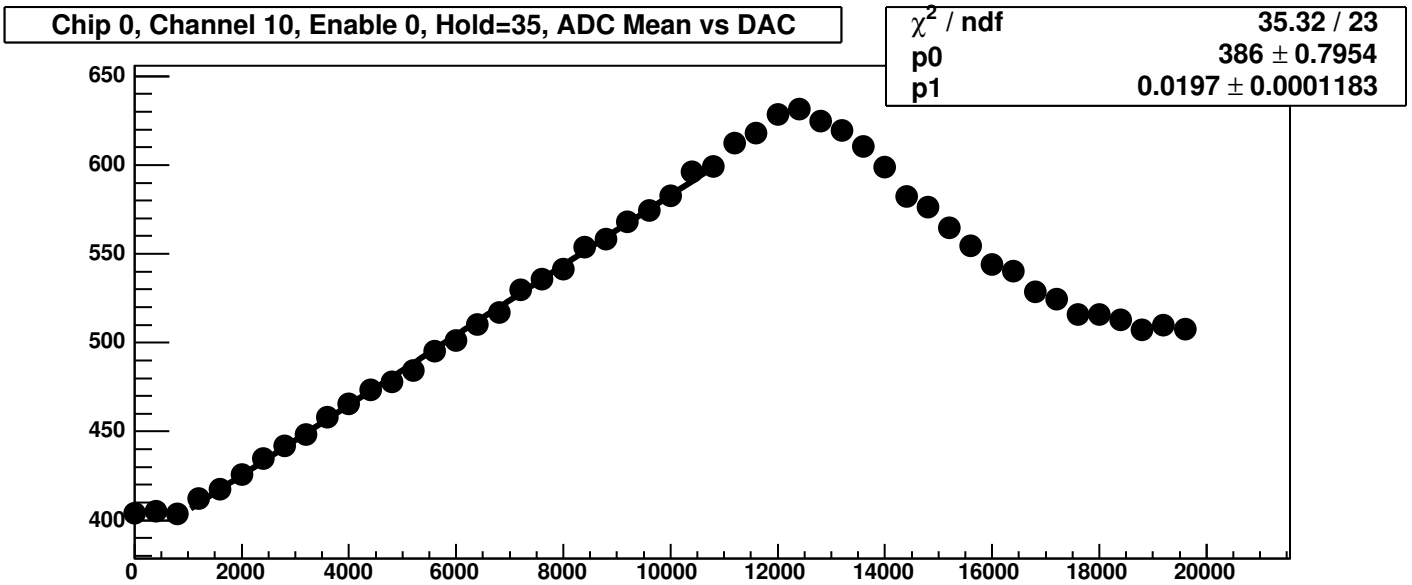

Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

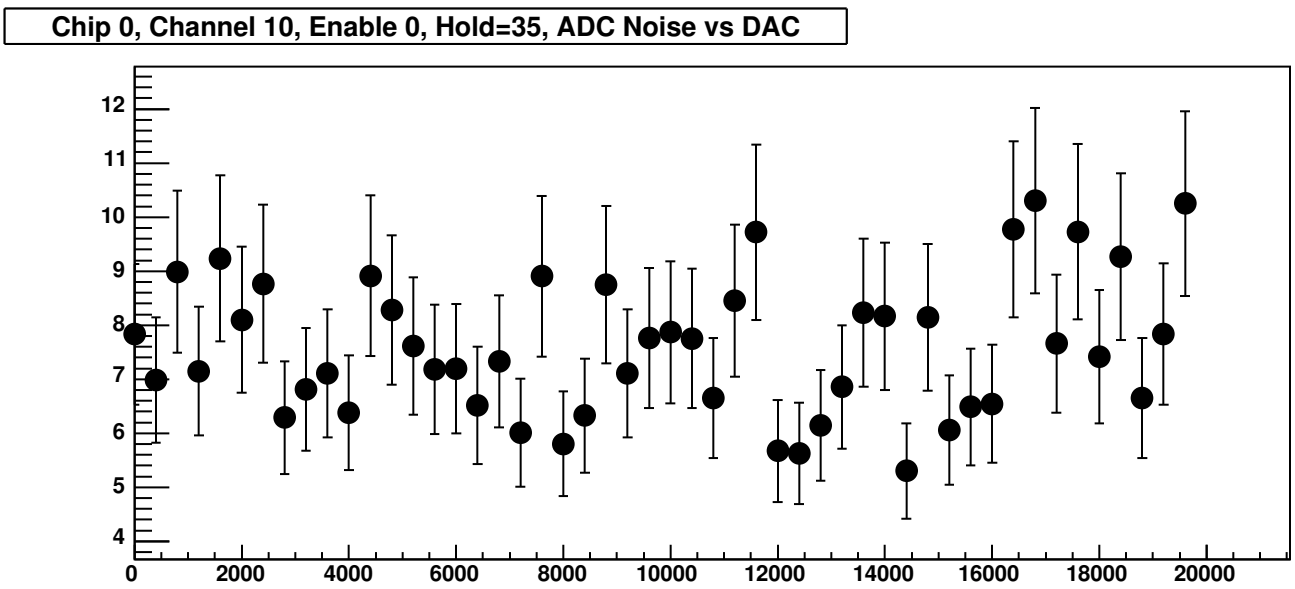

Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

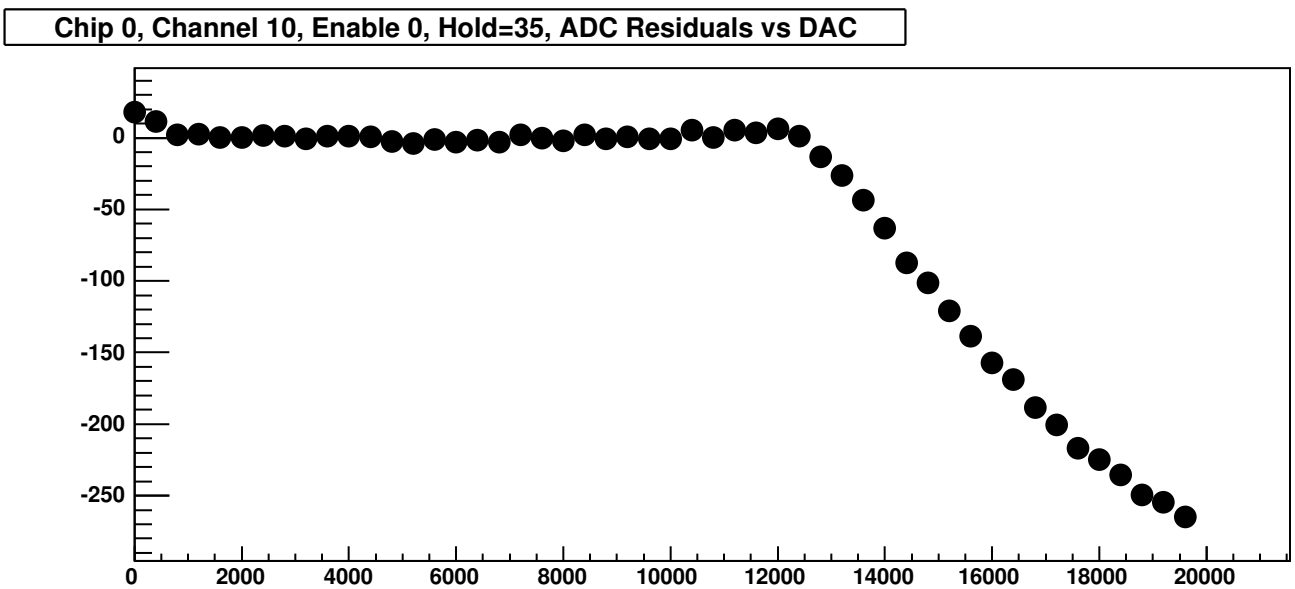

Chip 0, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 52.06 / 23
p0 468.7 ± 0.7683
p1 0.01728 ± 0.0001154
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

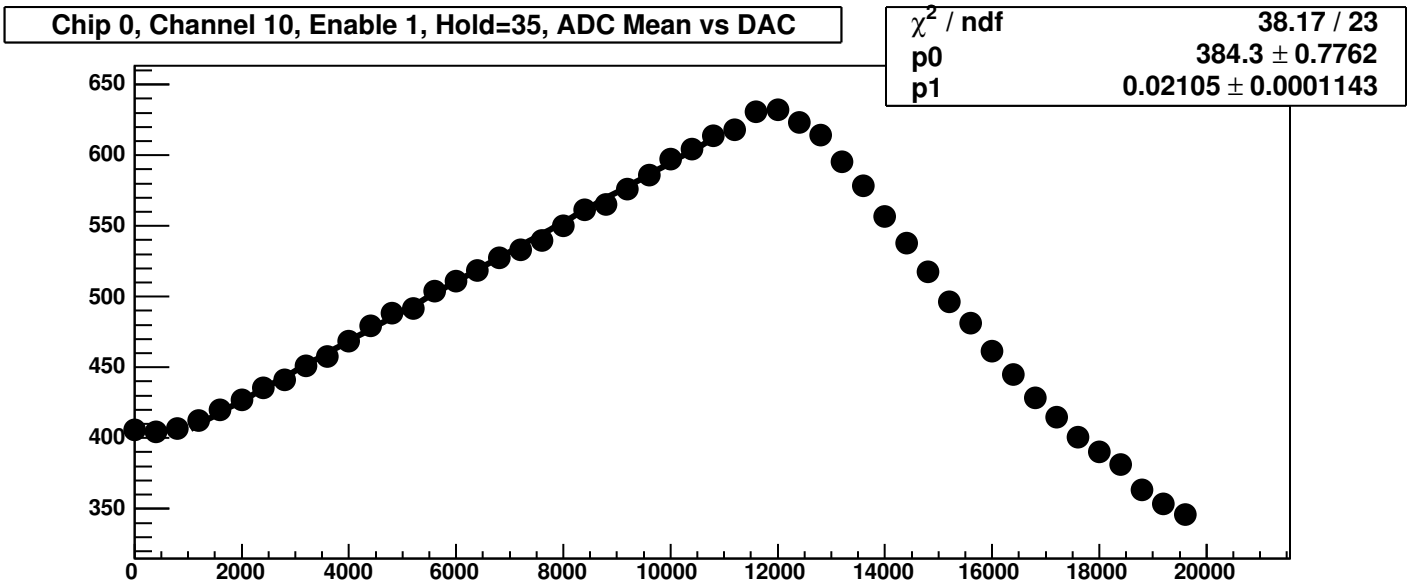

Chip 0, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

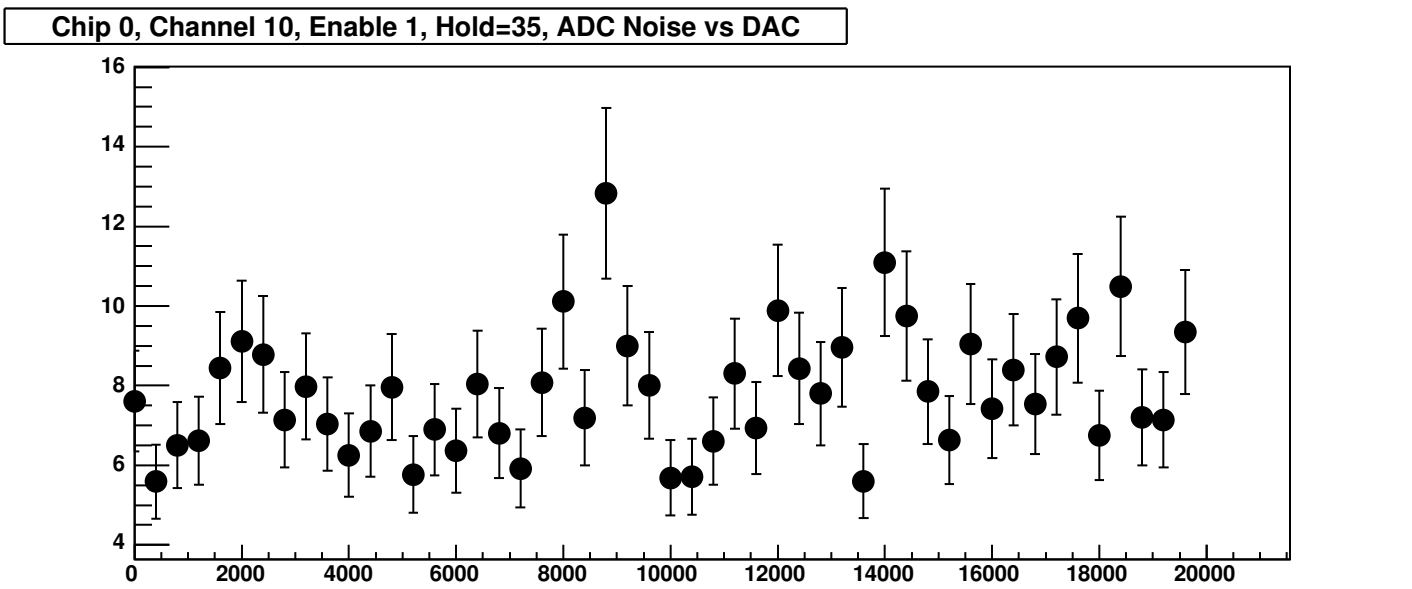

Chip 0, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

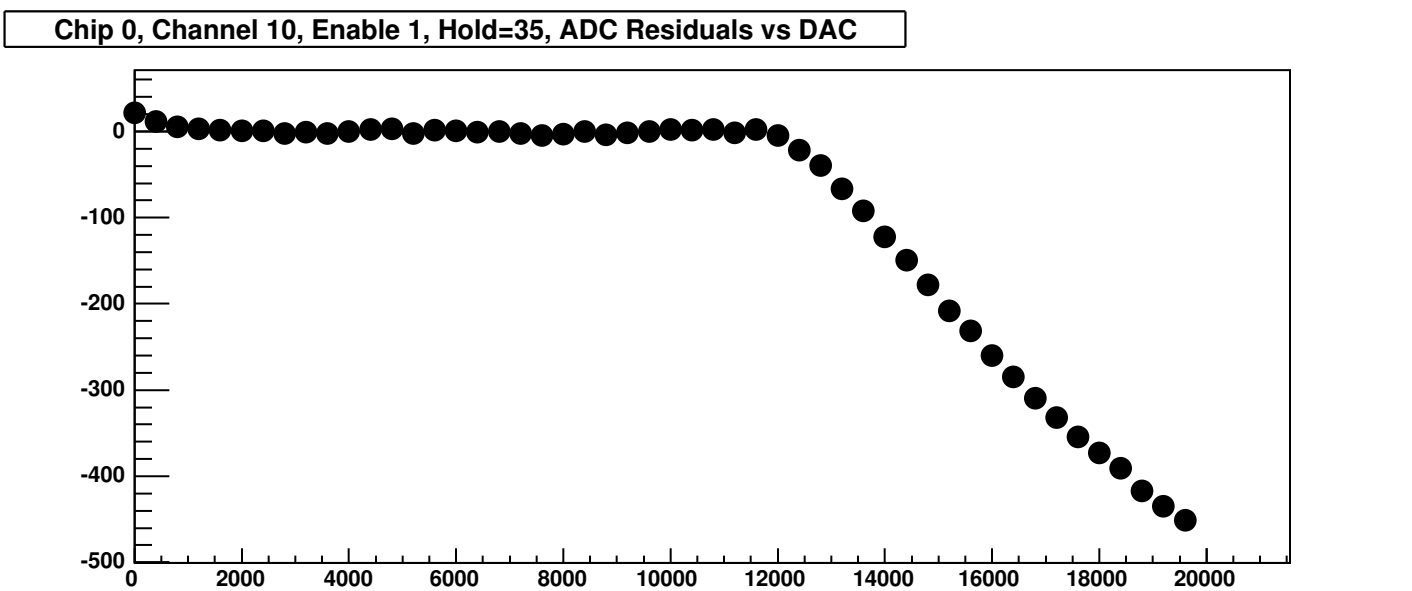

Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 35.32 / 23
p0 386 ± 0.7954
p1 0.0197 ± 0.0001183
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

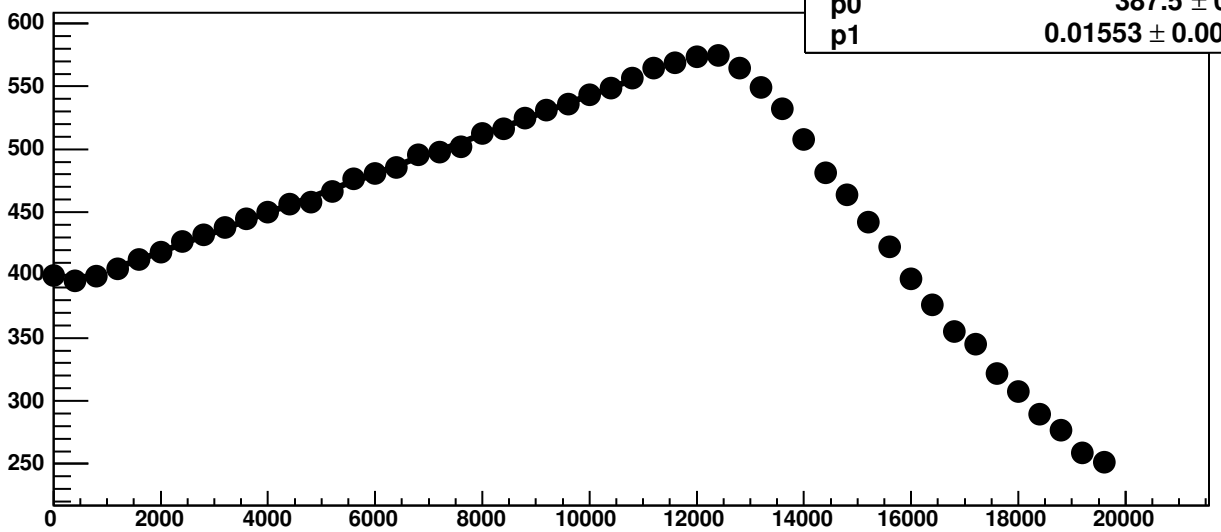

Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.17 / 23
p0 384.3 ± 0.7762
p1 0.02105 ± 0.0001143
650
600
550
500
450
400
350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

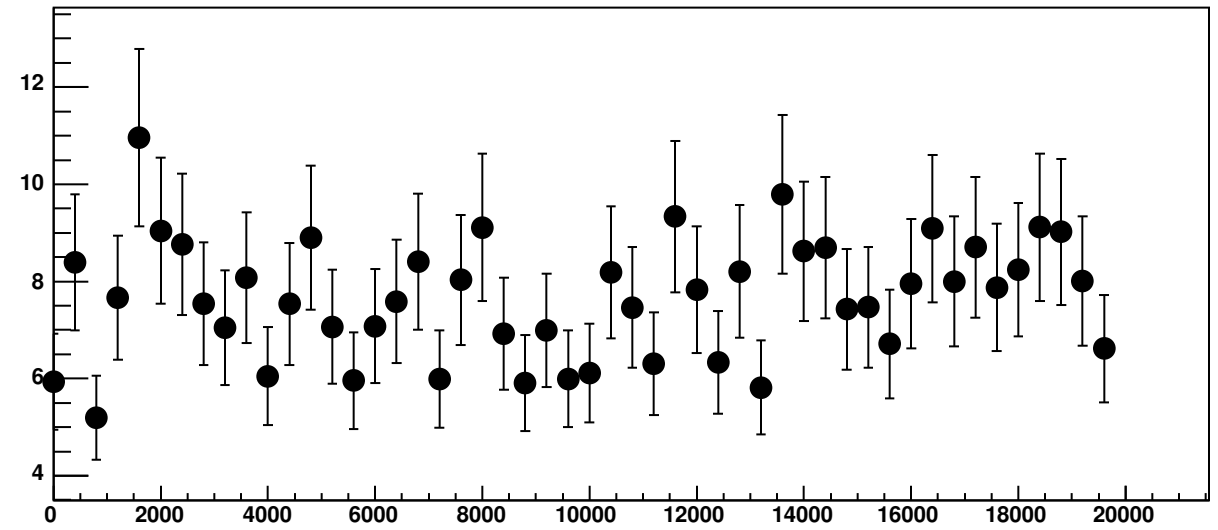

Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

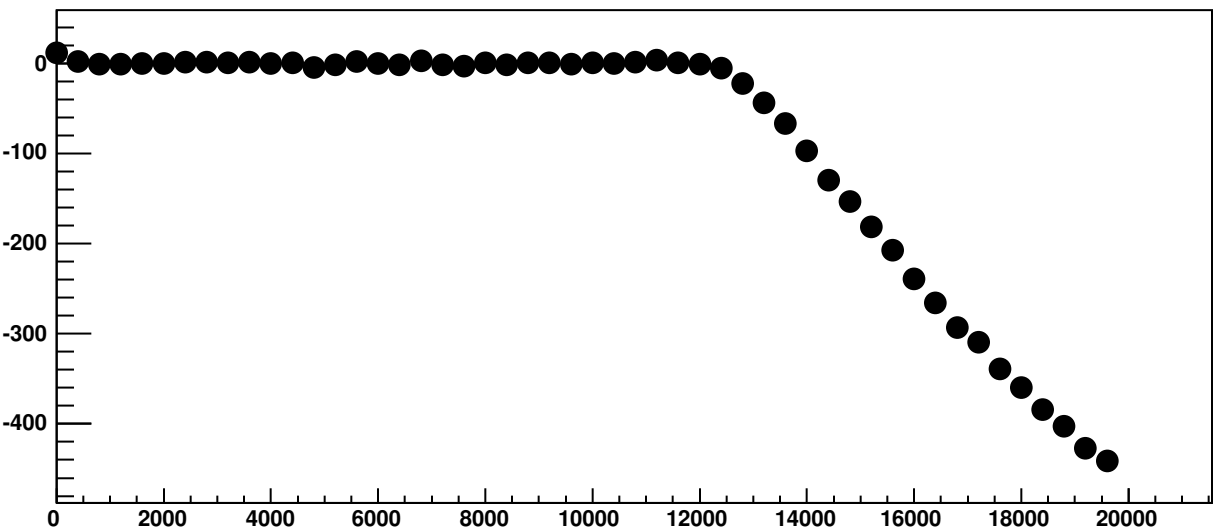
χ^2 / ndf	20.78 / 23
p0	387.5 ± 0.8314
p1	0.01553 ± 0.0001199

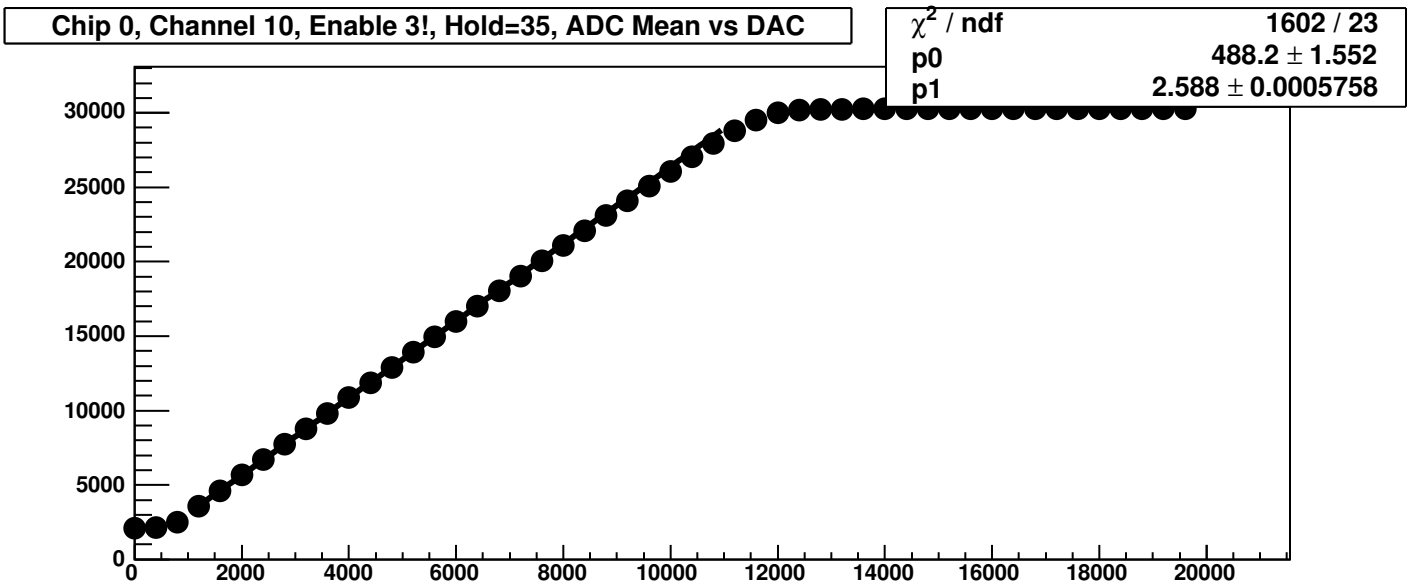


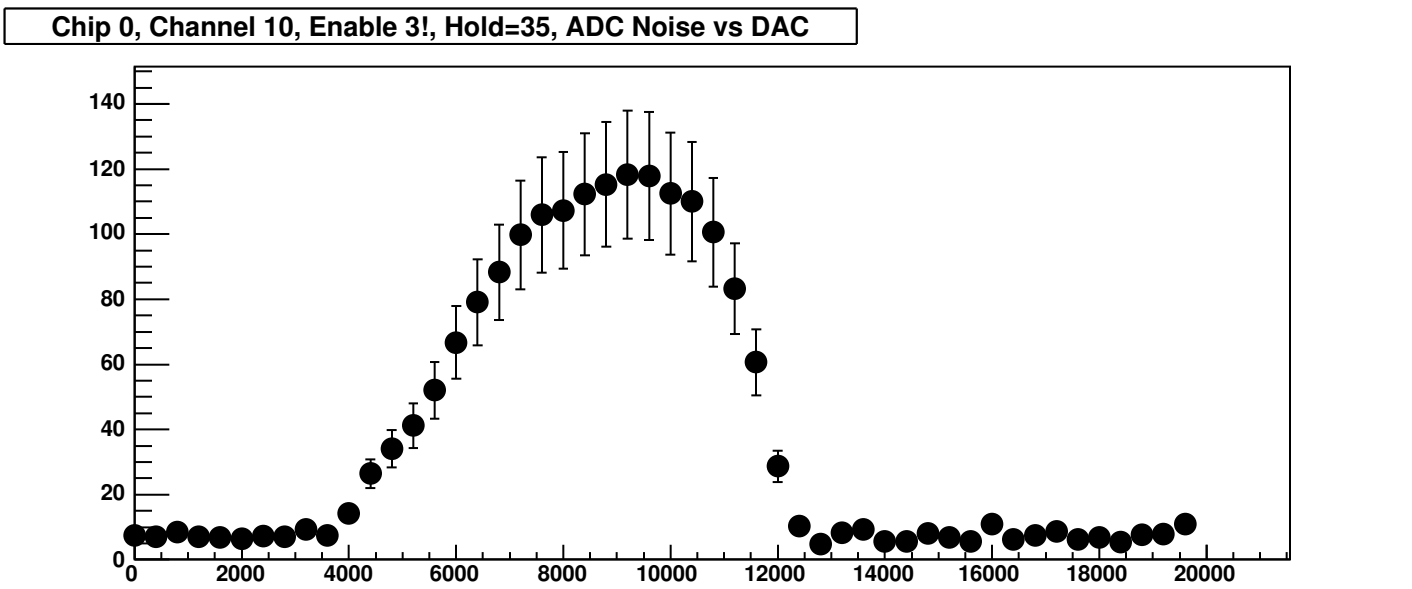
Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

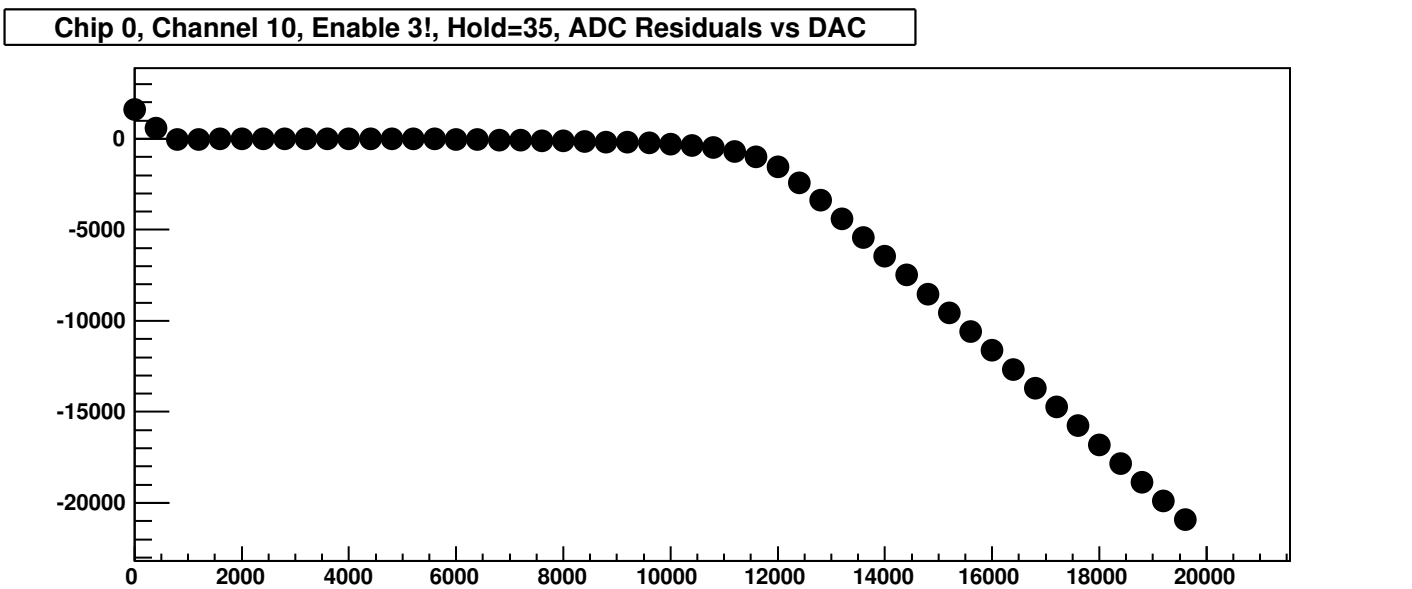


Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

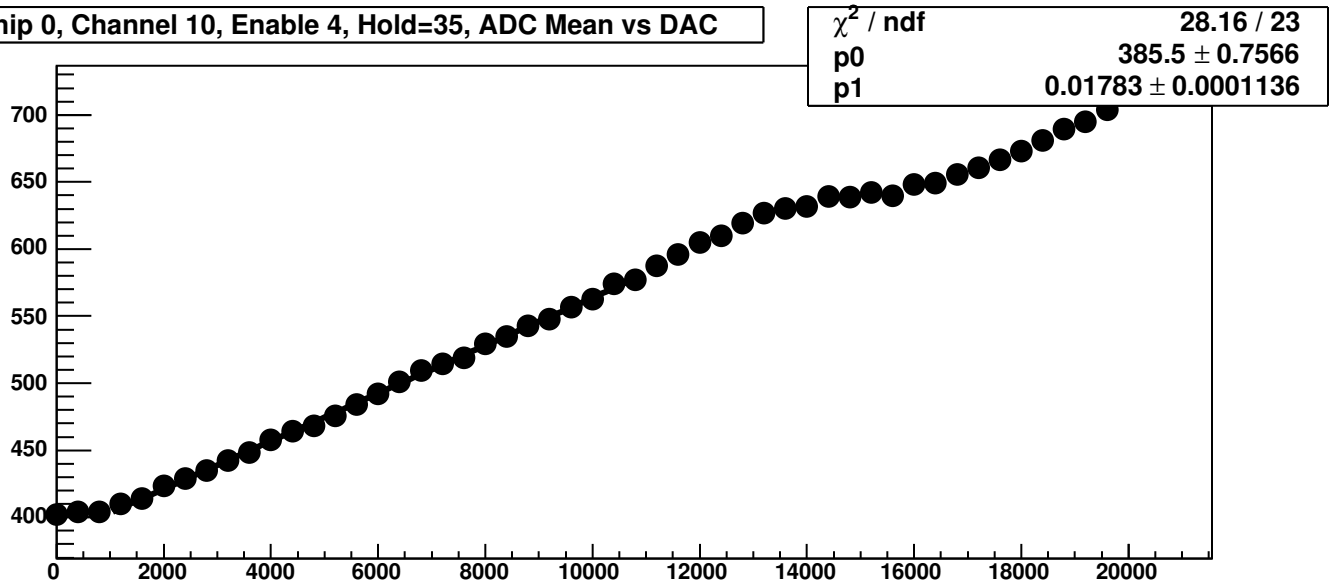



Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1602 / 23
p0 488.2 ± 1.552
p1 2.588 ± 0.0005758
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

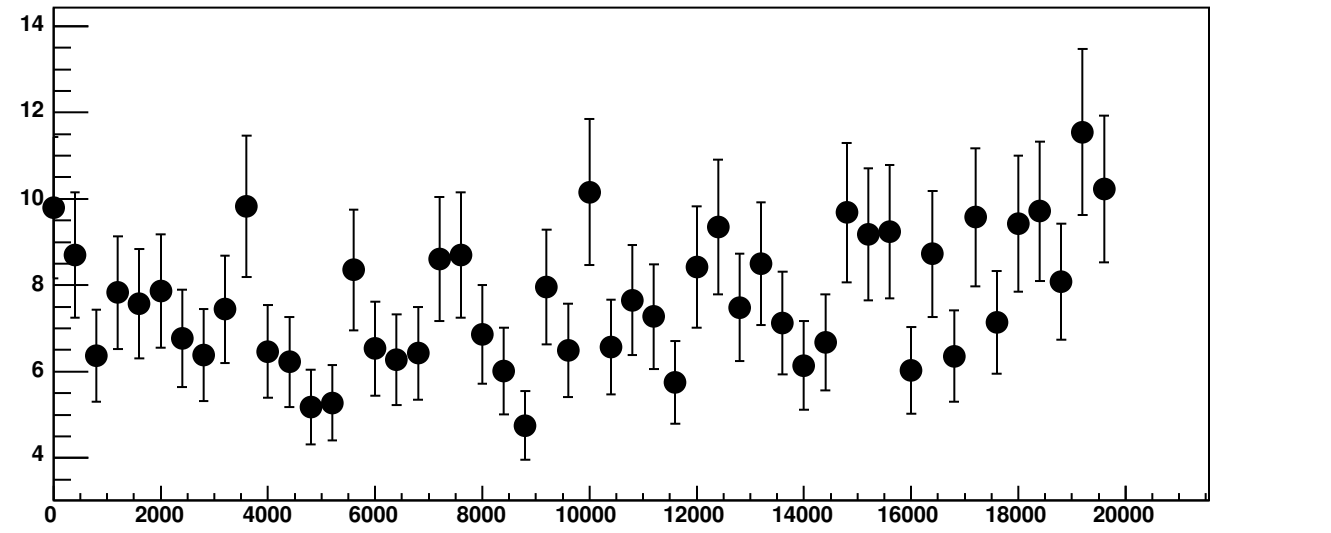

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

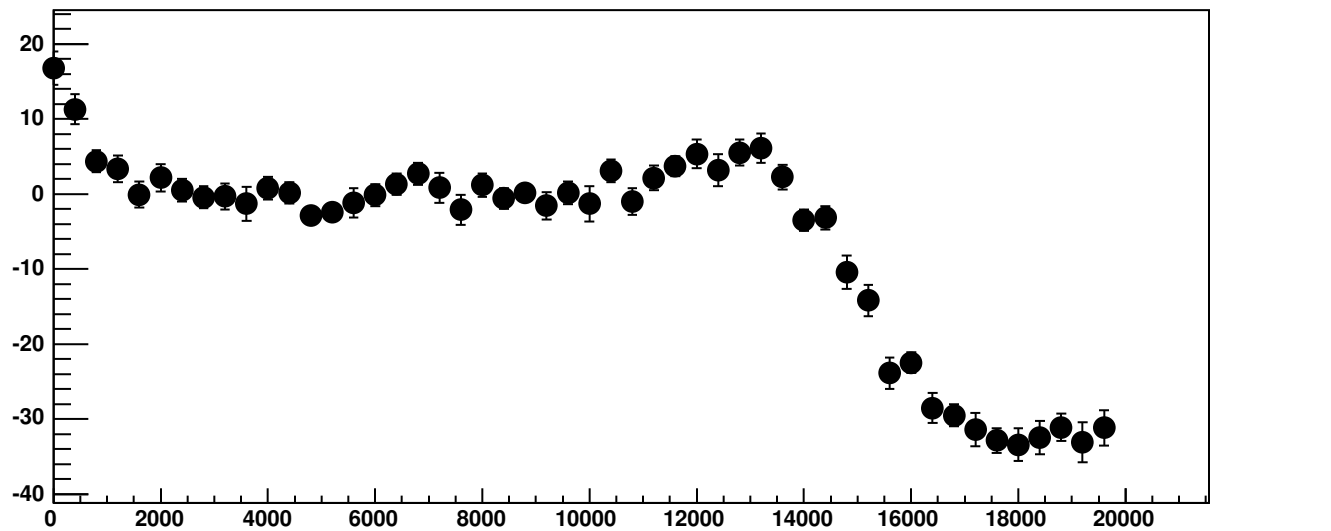
Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

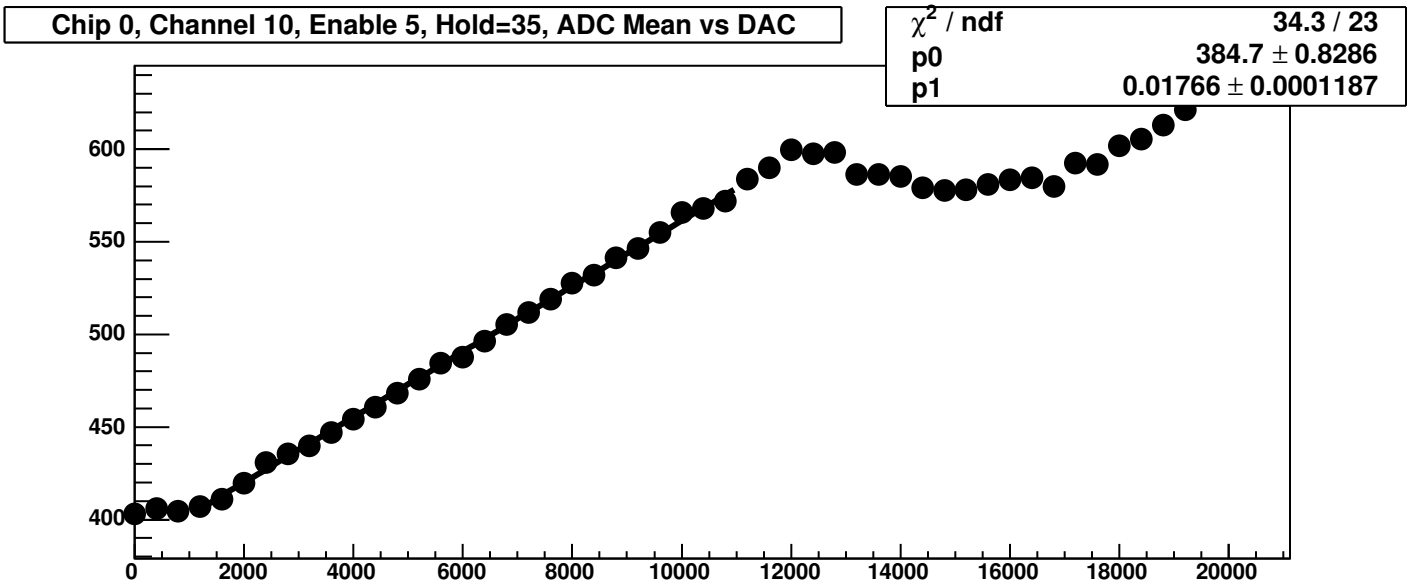


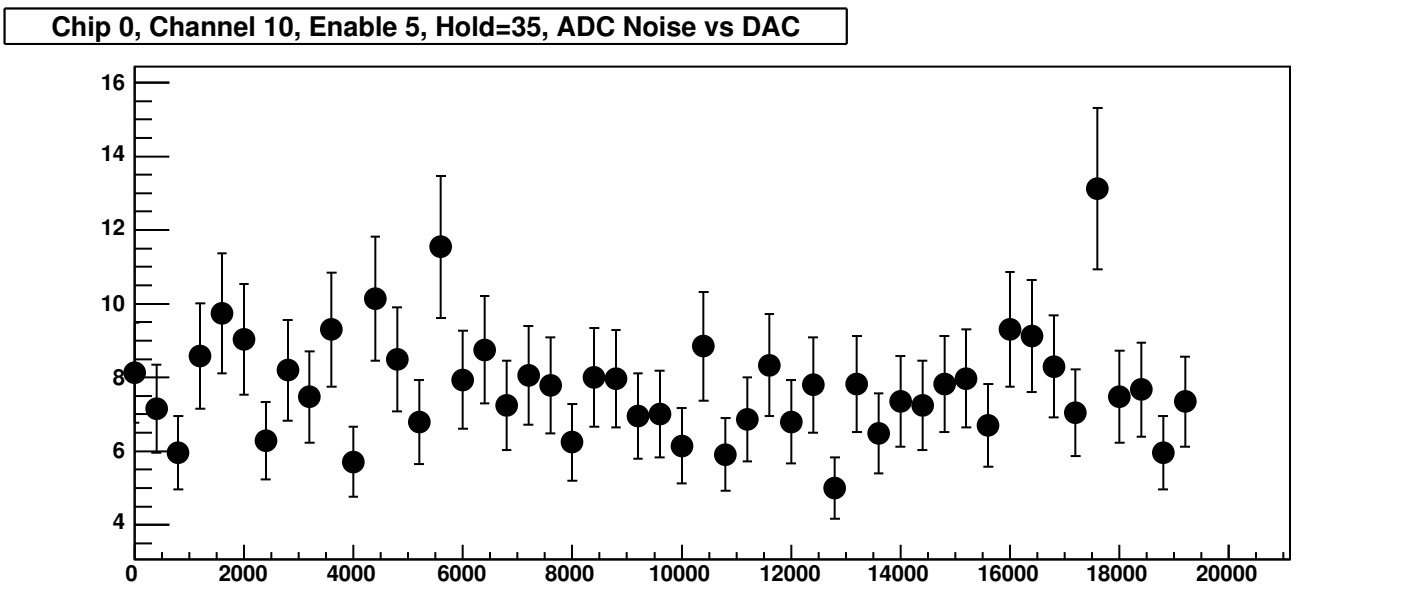
Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

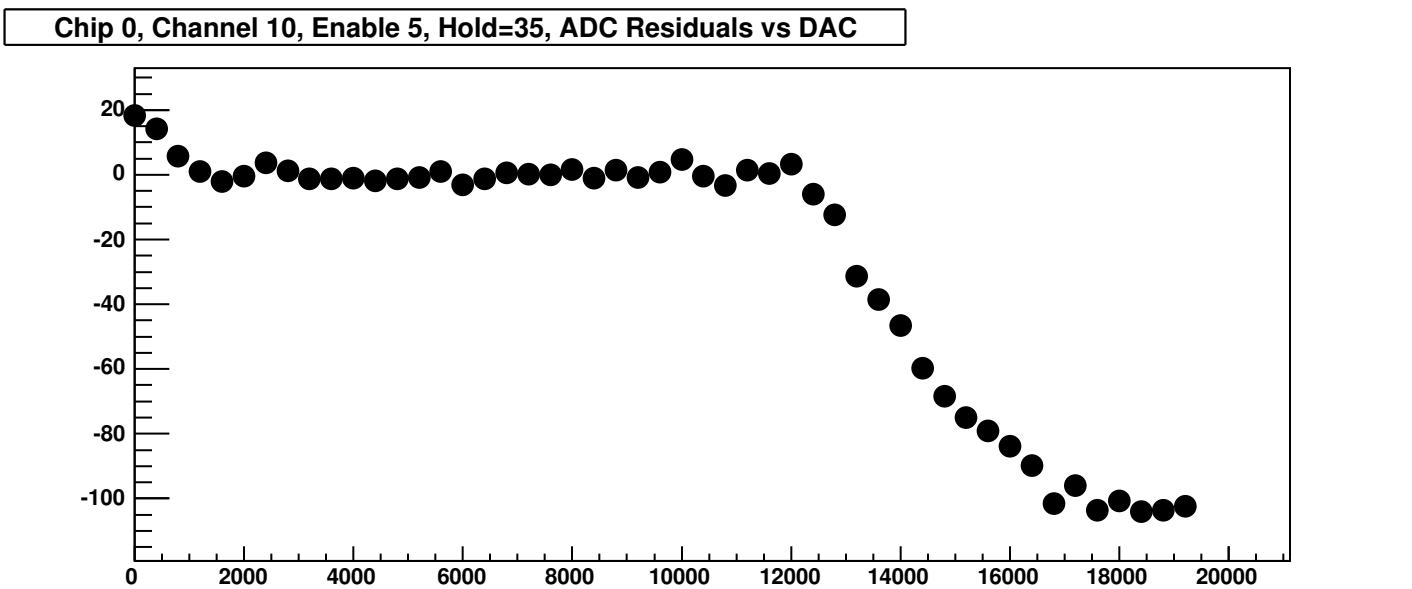


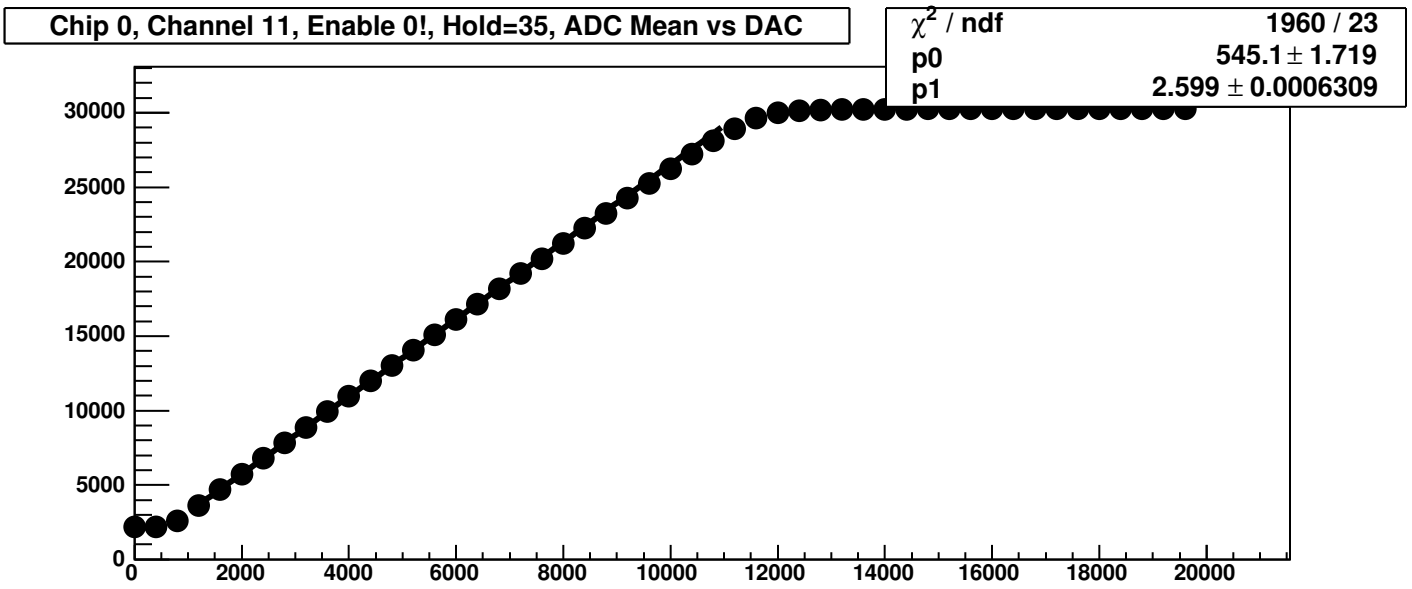
Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

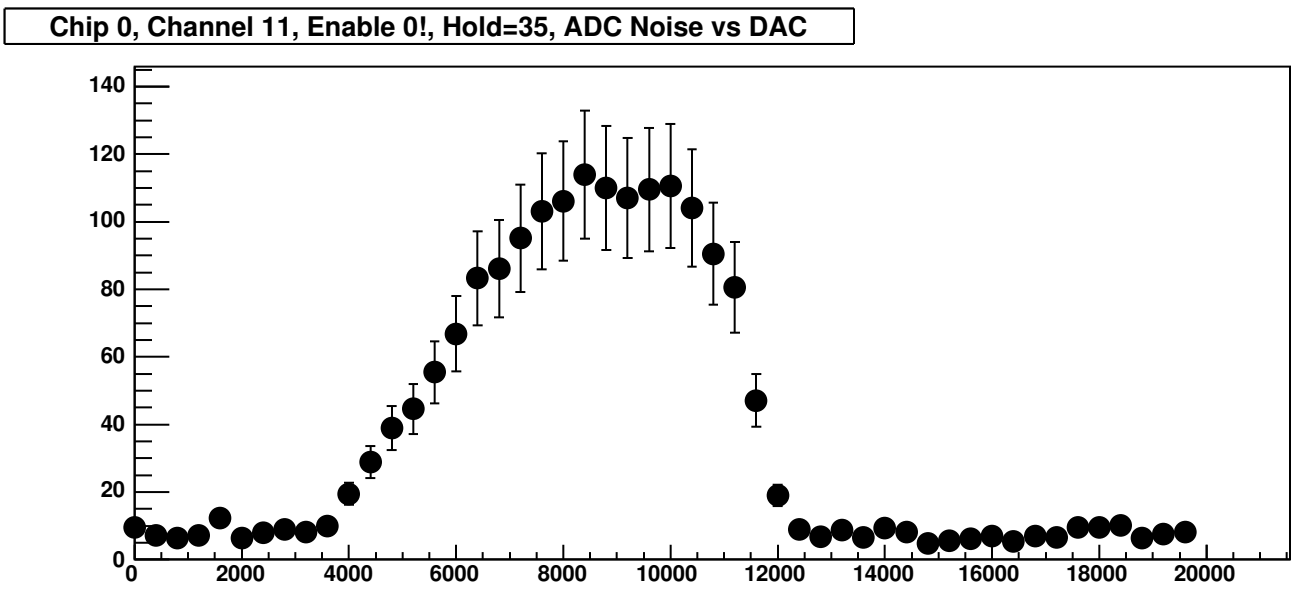


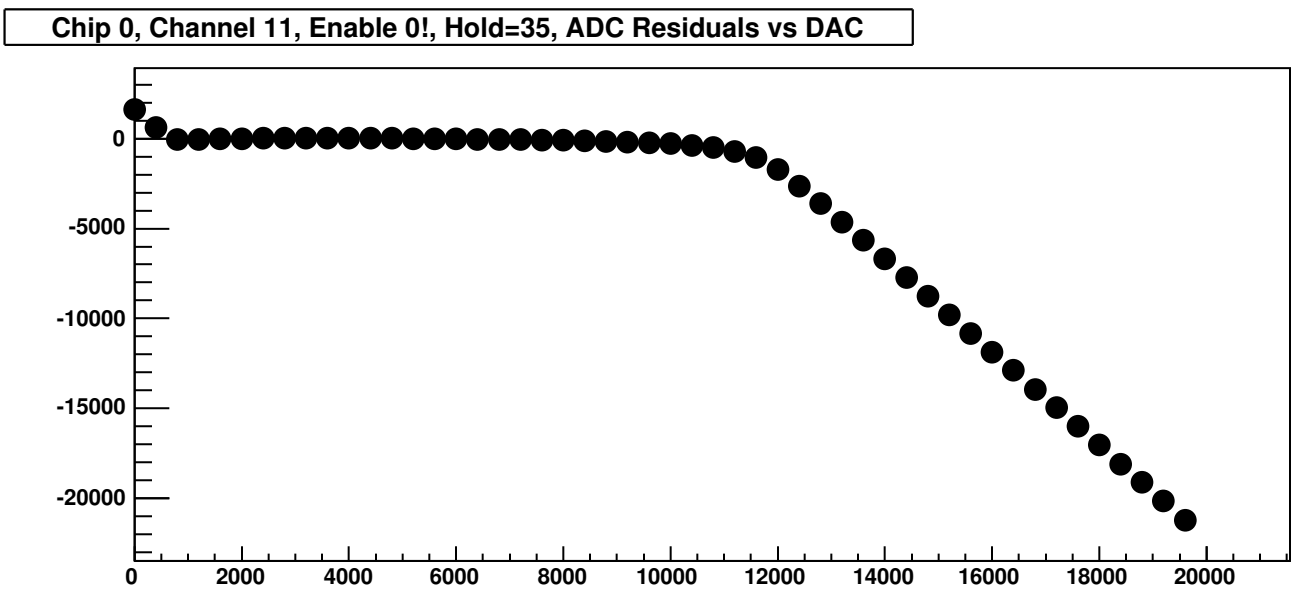

Chip 0, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 34.3 / 23
p0 384.7 ± 0.8286
p1 0.01766 ± 0.0001187
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

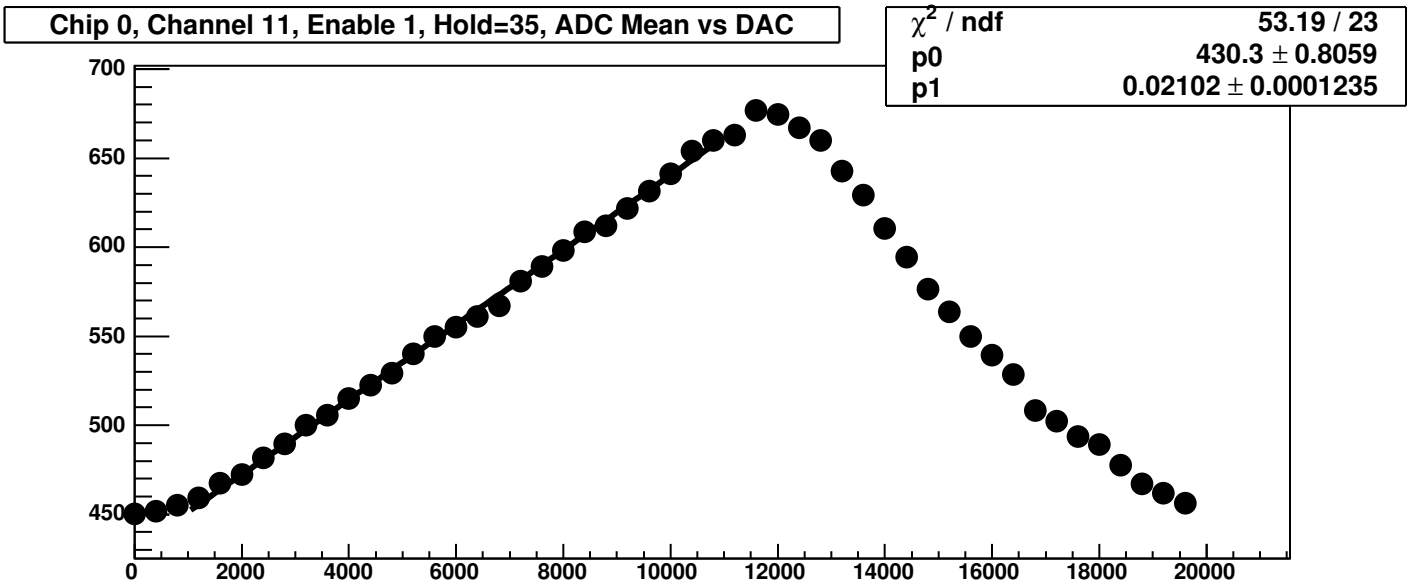

Chip 0, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

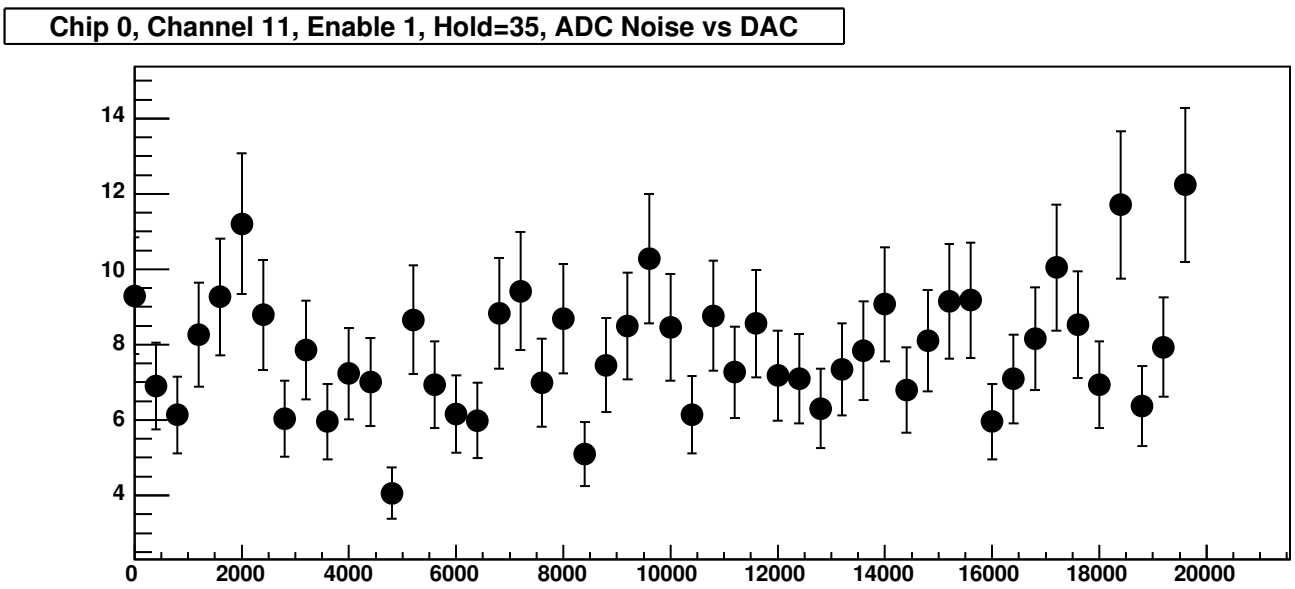

Chip 0, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

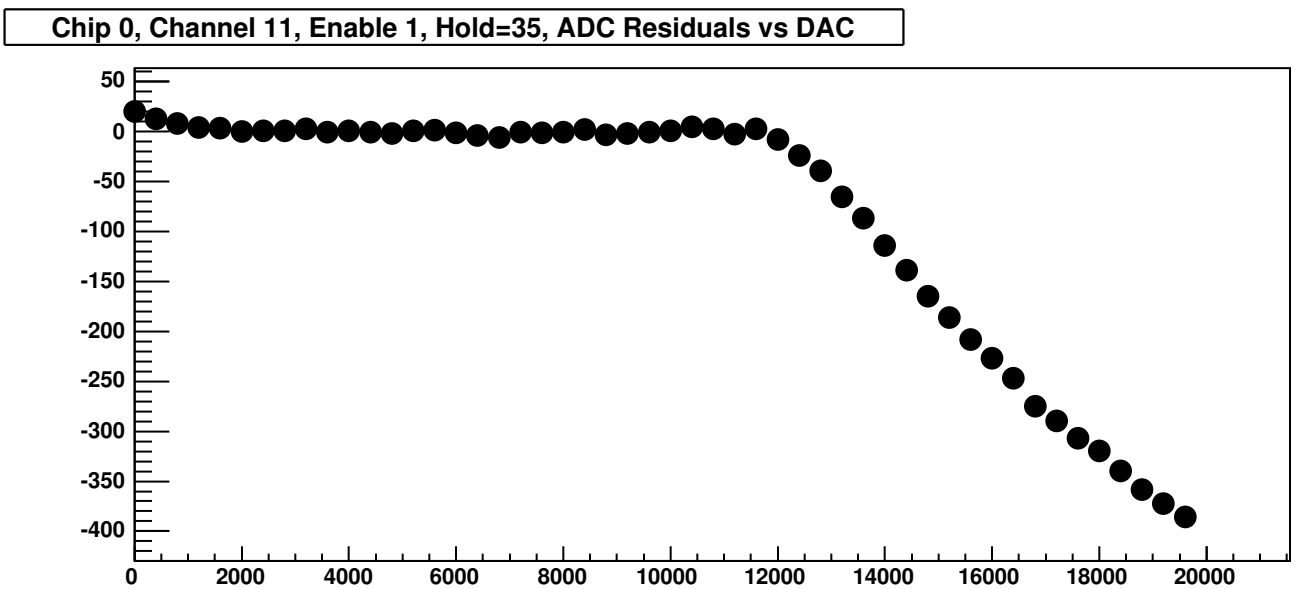

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1960 / 23
p0 545.1 ± 1.719
p1 2.599 ± 0.0006309
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

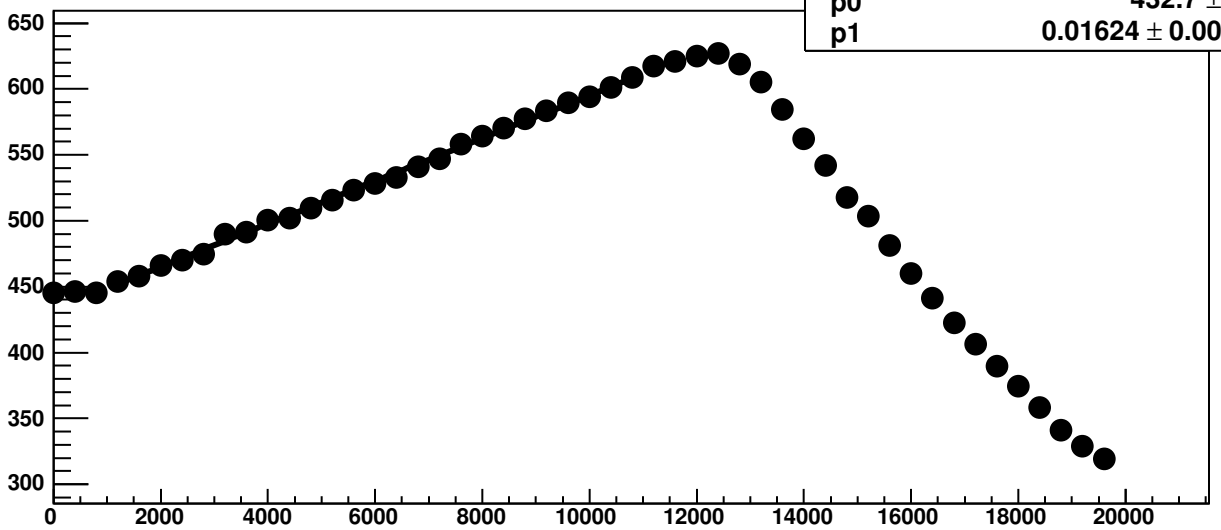

Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 53.19 / 23
p0 430.3 ± 0.8059
p1 0.02102 ± 0.0001235
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

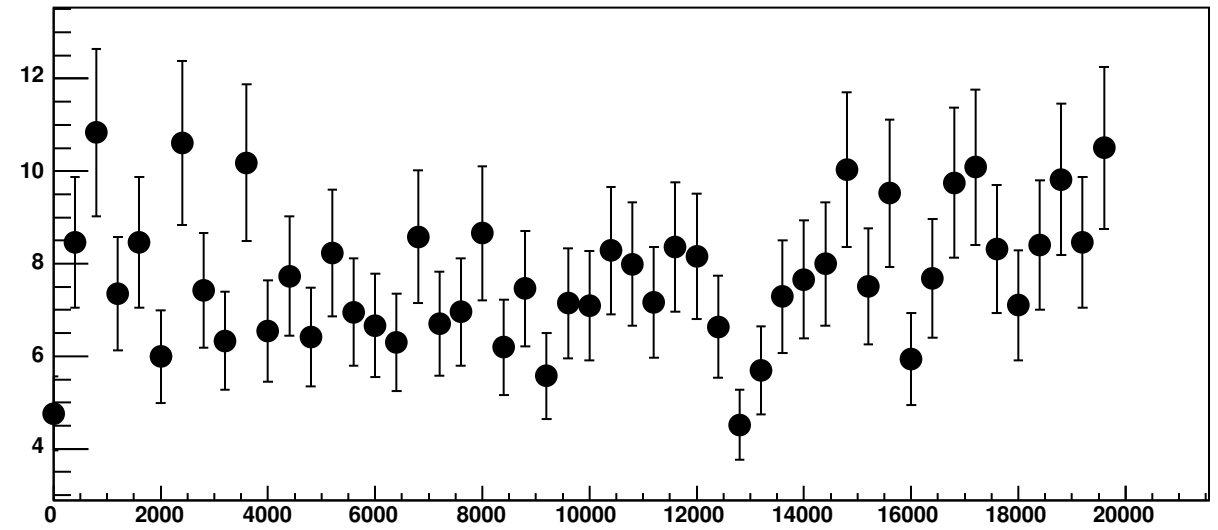

Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

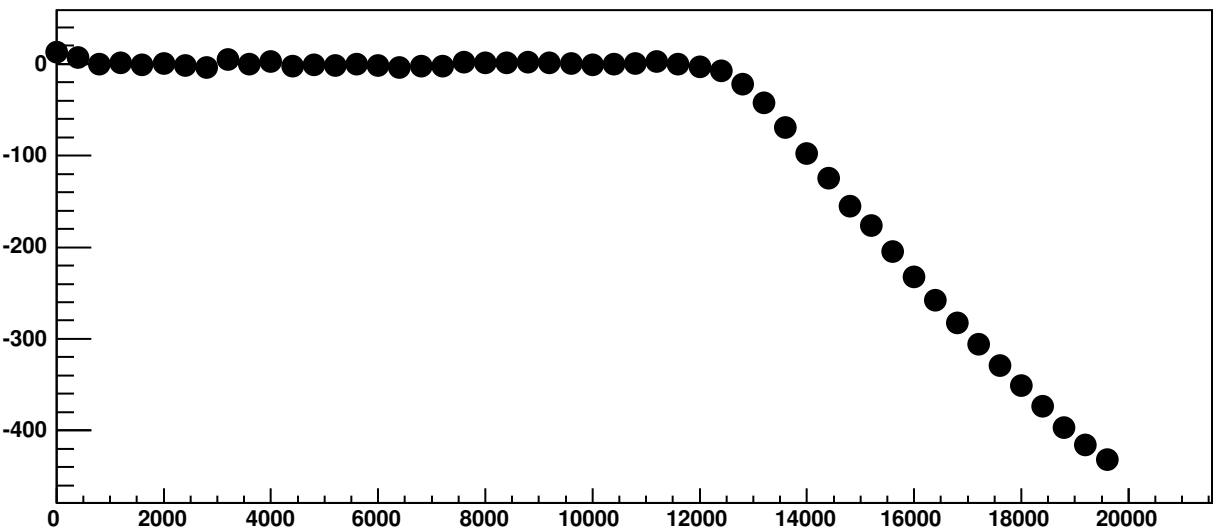
χ^2 / ndf	42.84 / 23
p0	432.7 ± 0.784
p1	0.01624 ± 0.0001168



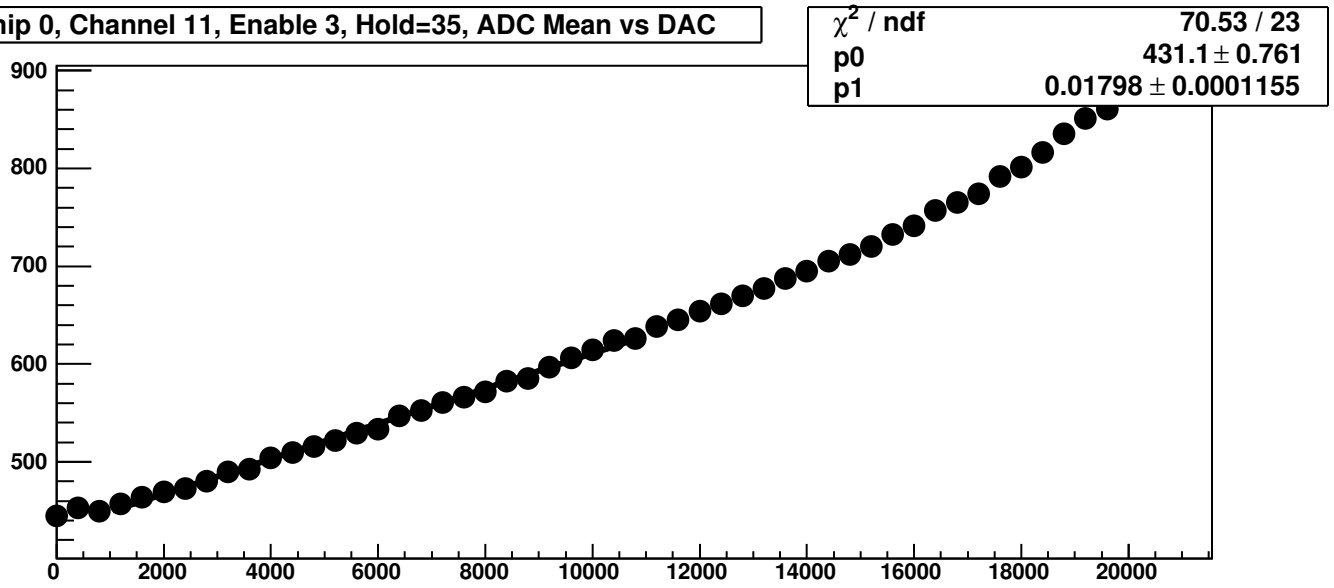
Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



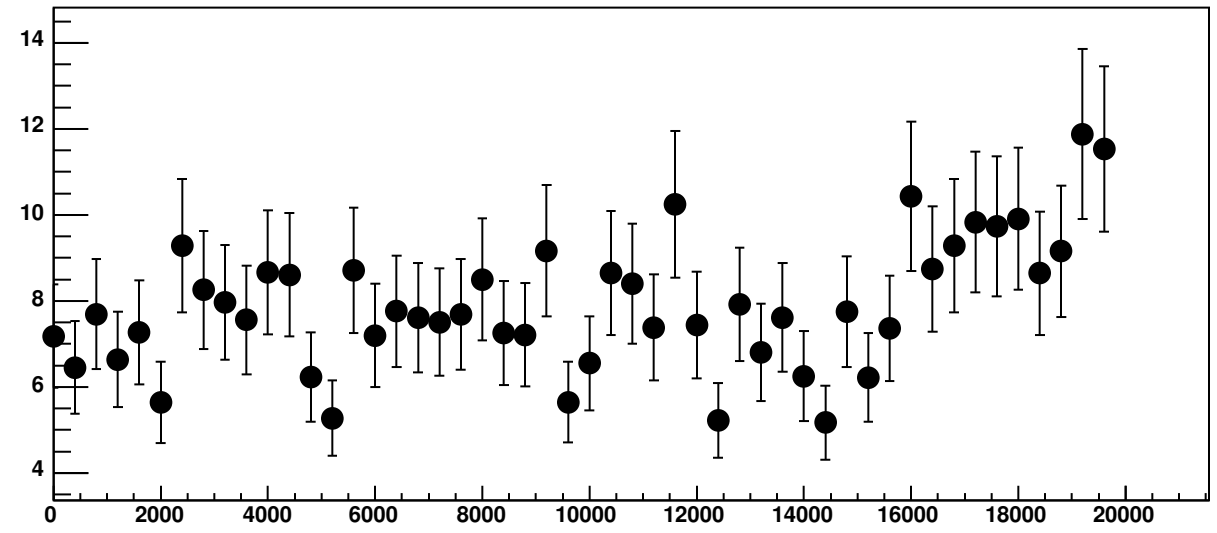
Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



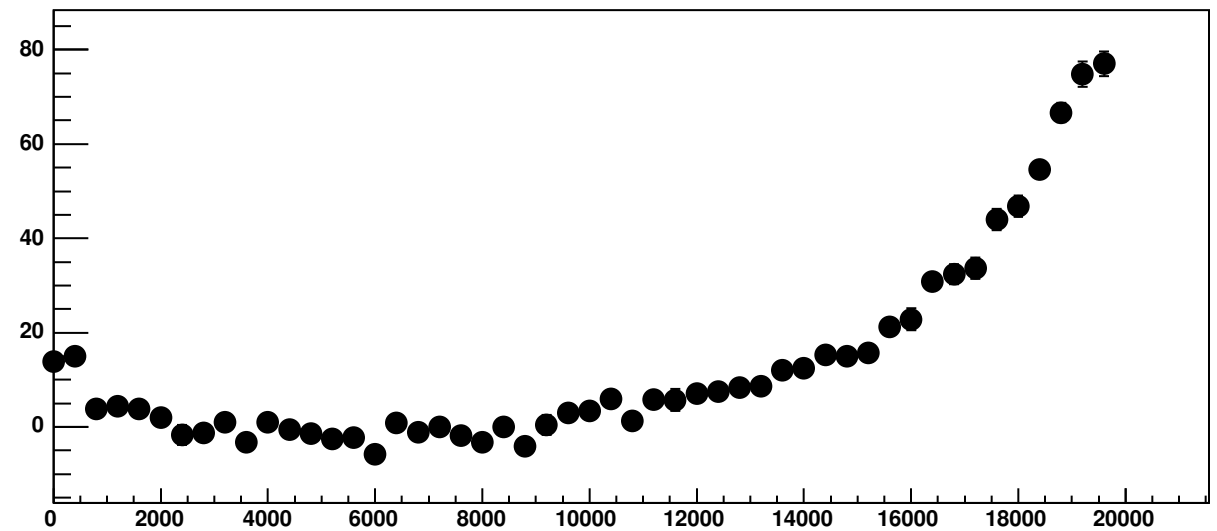
Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

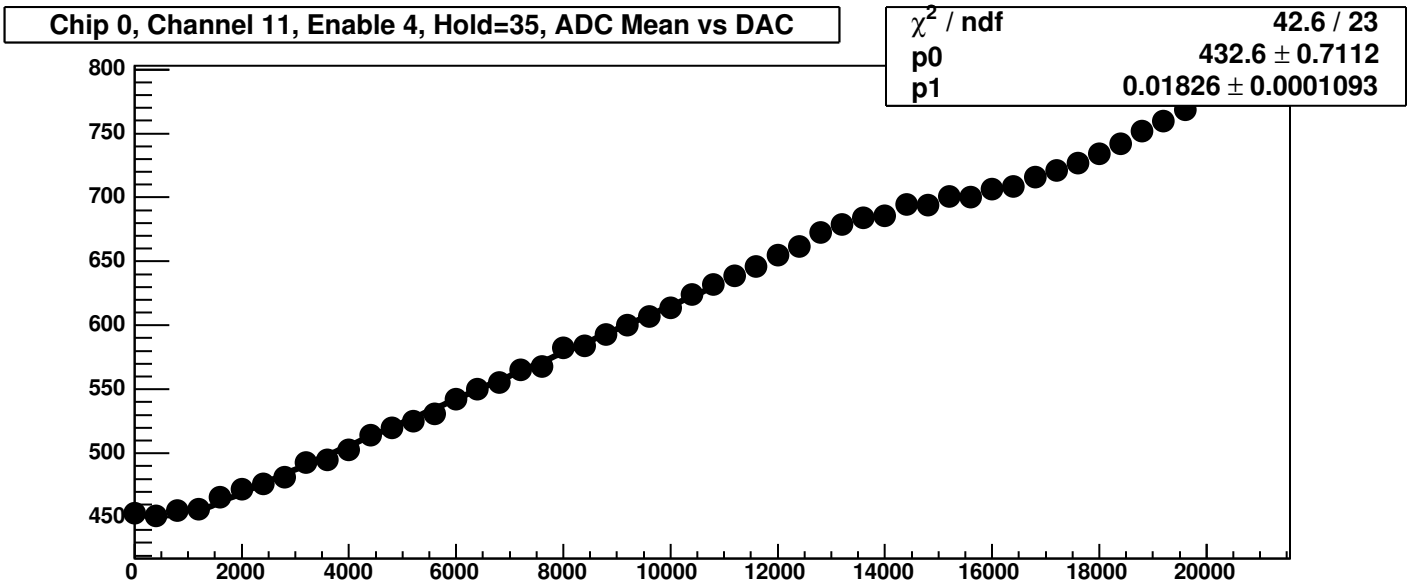


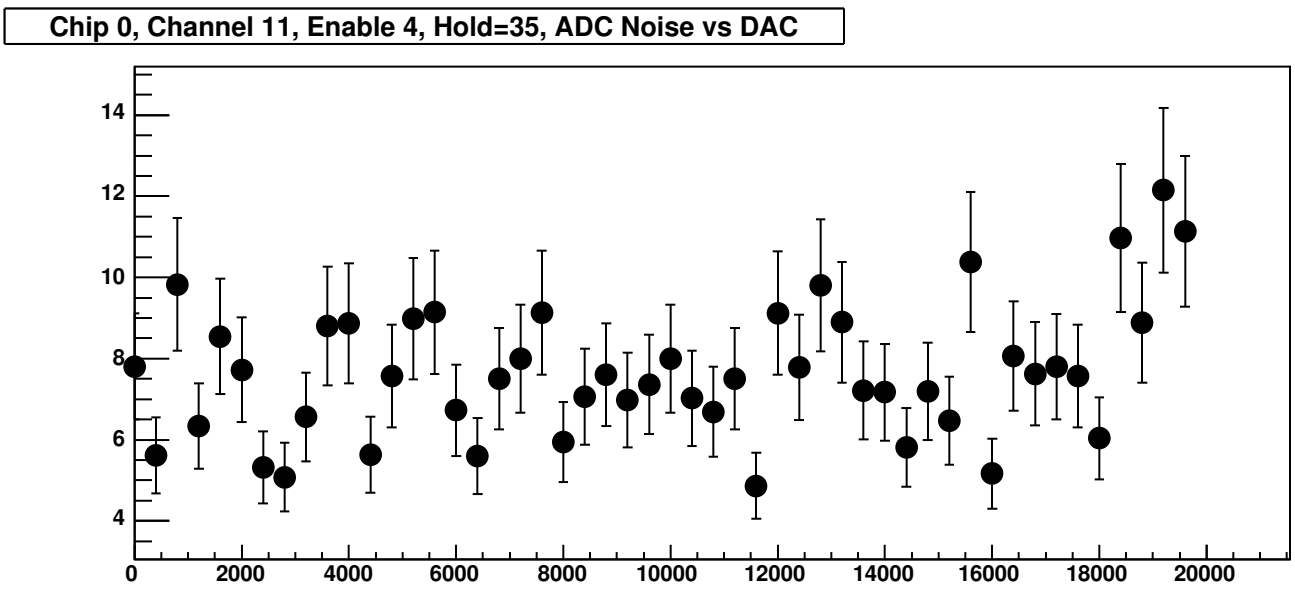
Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC

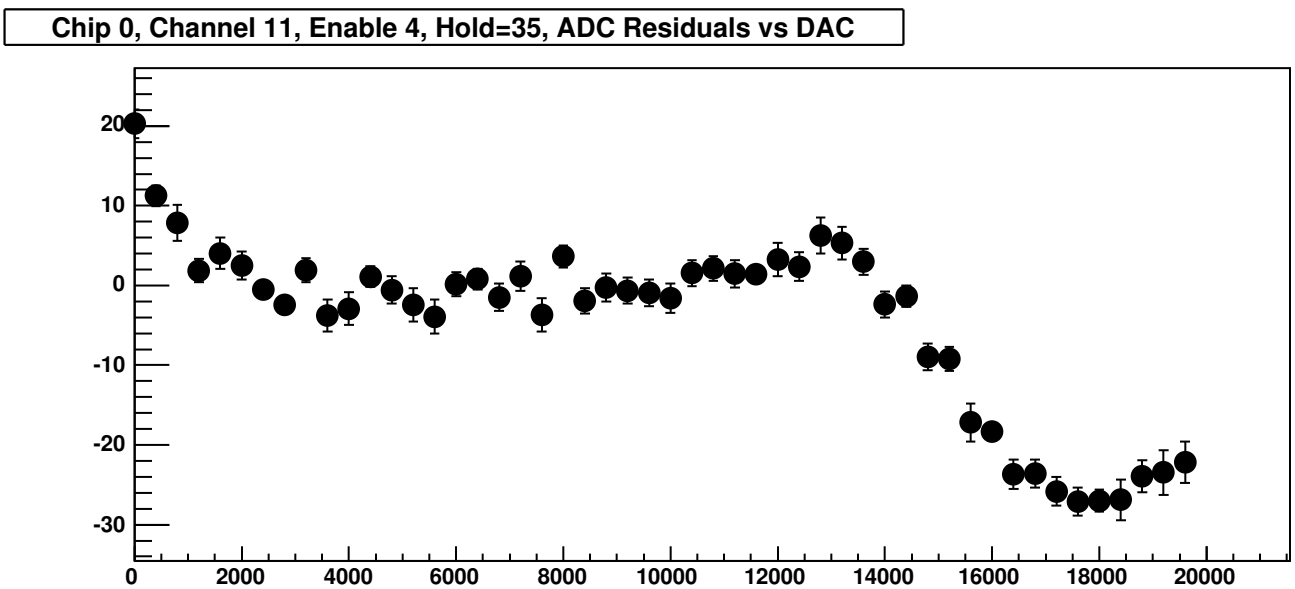


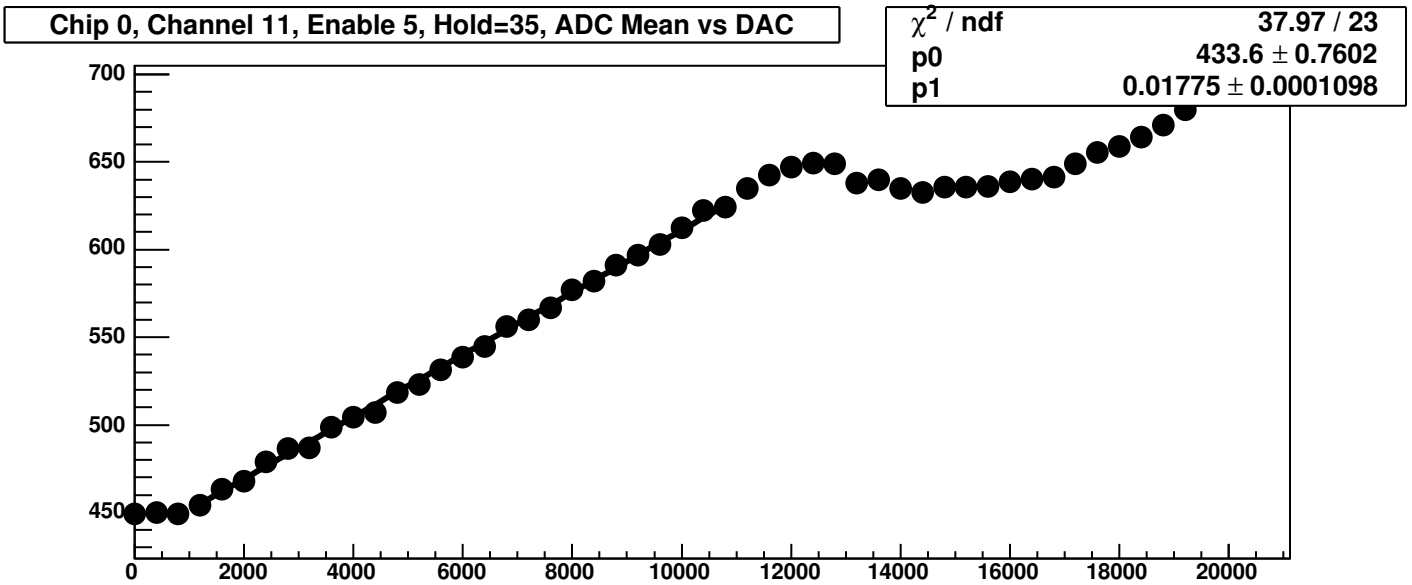
Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

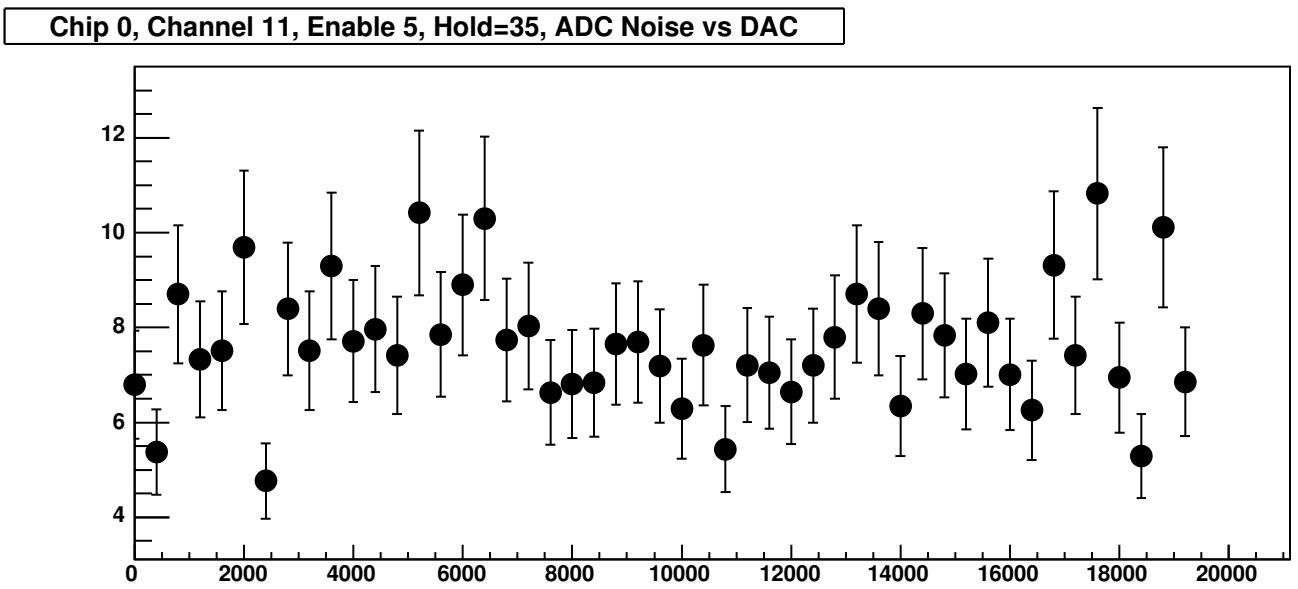


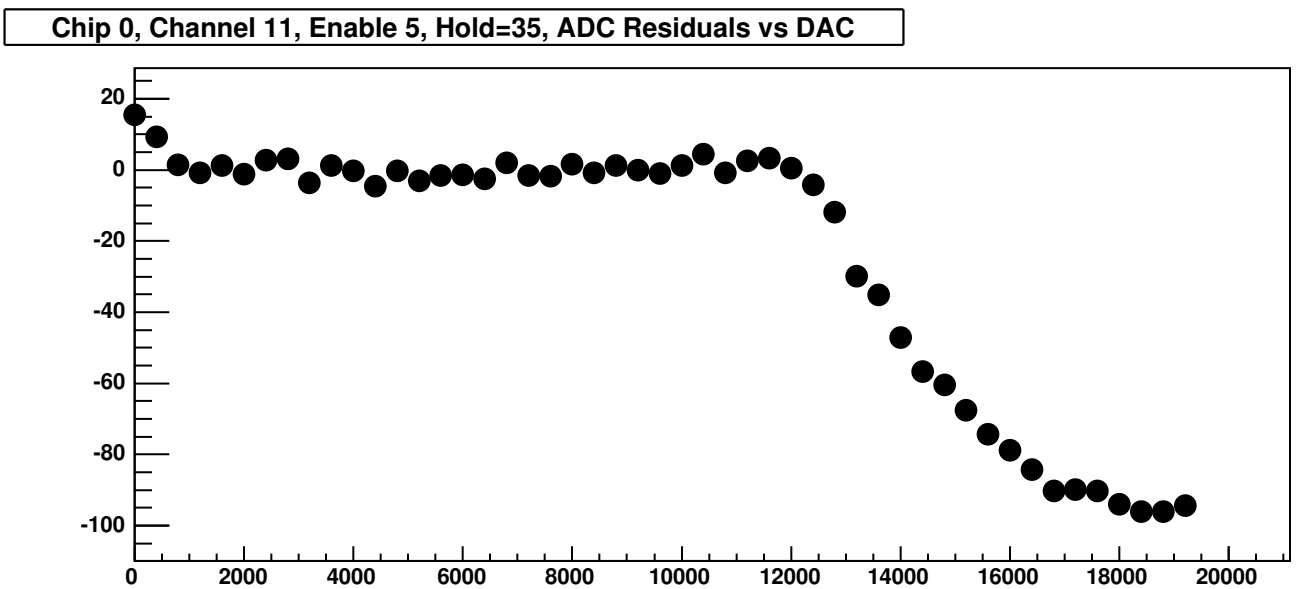

Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 42.6 / 23
p0 432.6 ± 0.7112
p1 0.01826 ± 0.0001093
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

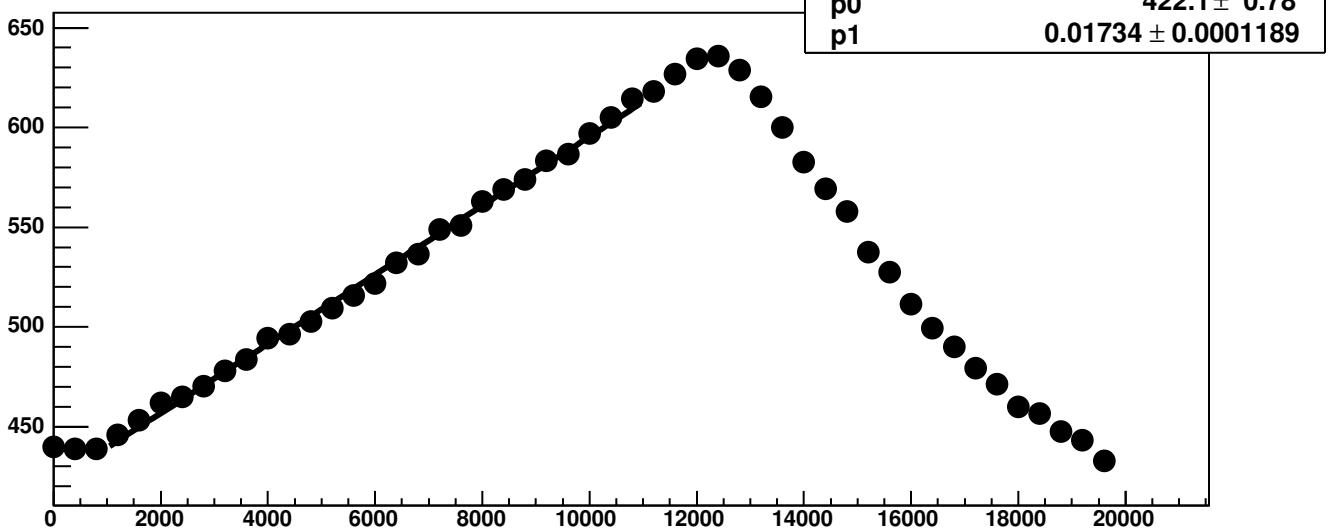

Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 37.97 / 23
p0 433.6 ± 0.7602
p1 0.01775 ± 0.0001098
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

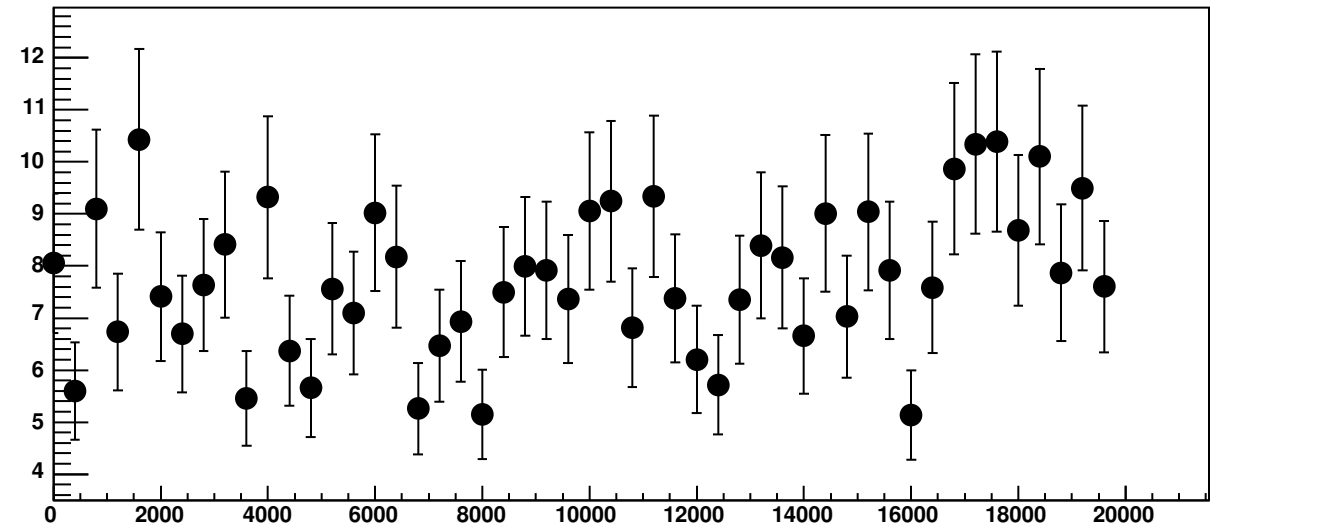

Chip 0, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

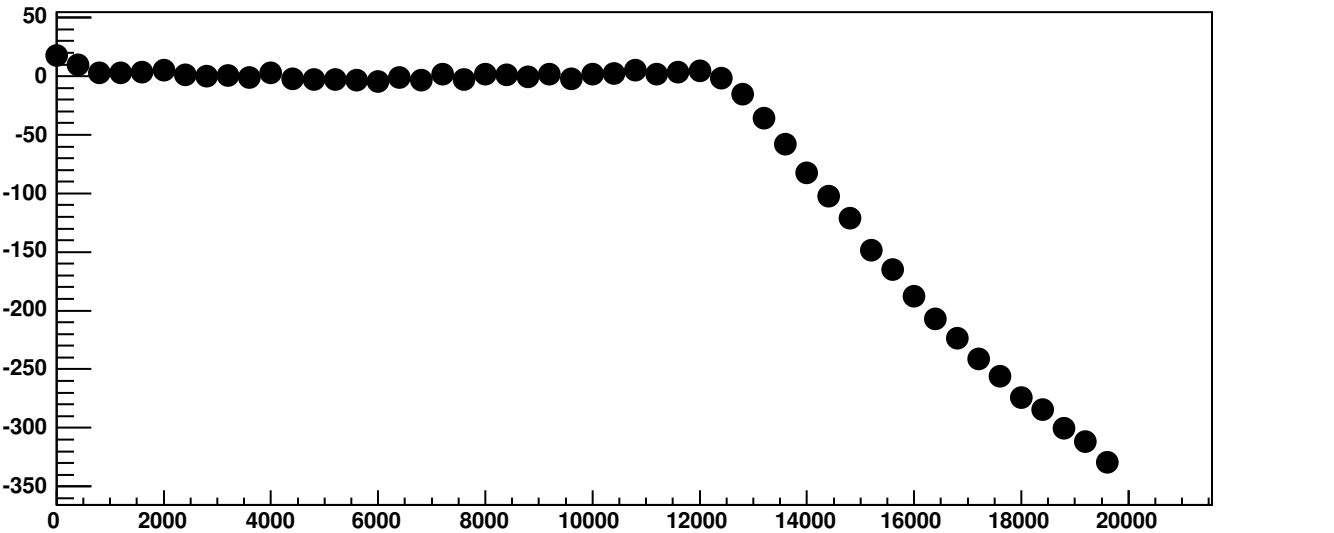
Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

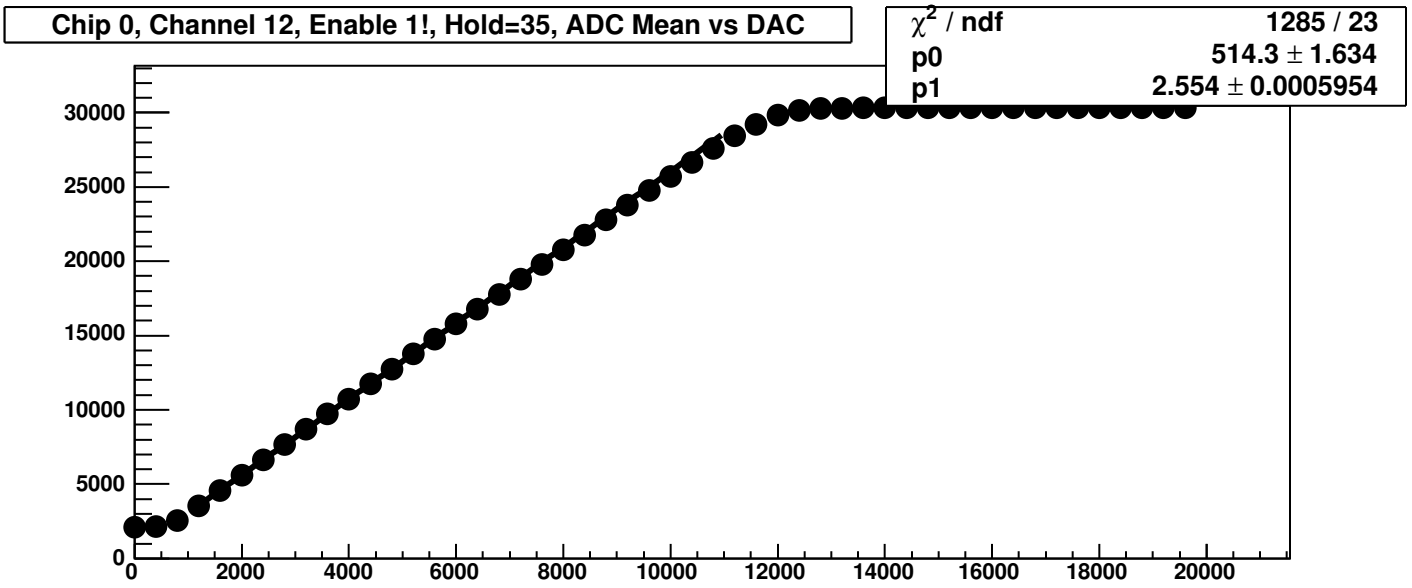


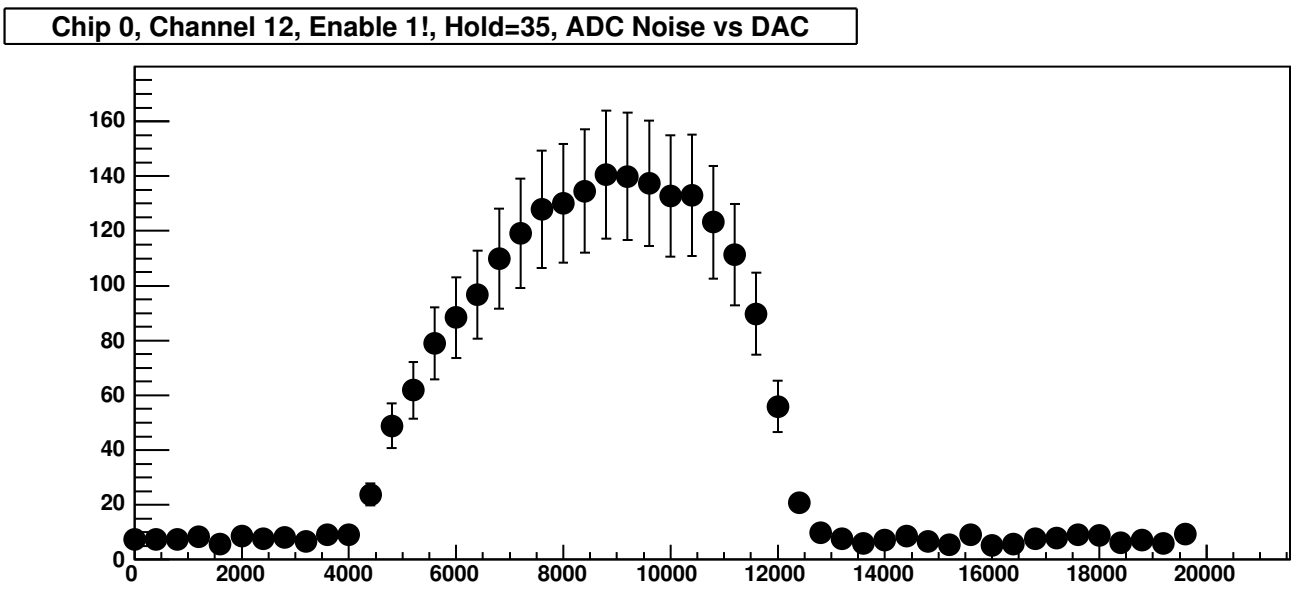
Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

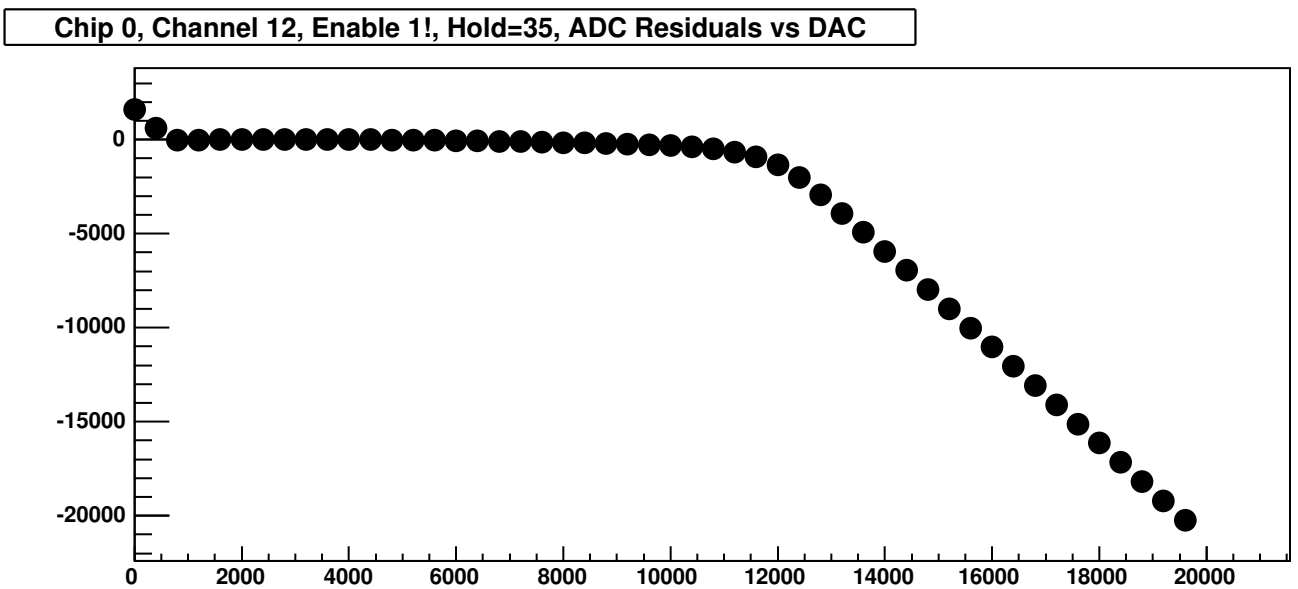


Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



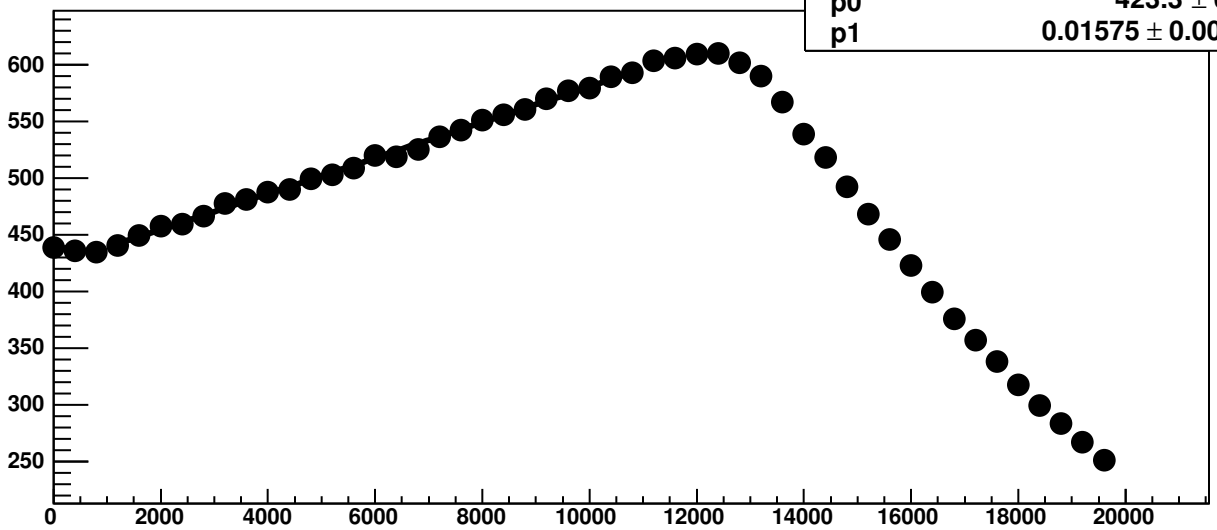

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1285 / 23
p0 514.3 ± 1.634
p1 2.554 ± 0.0005954
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

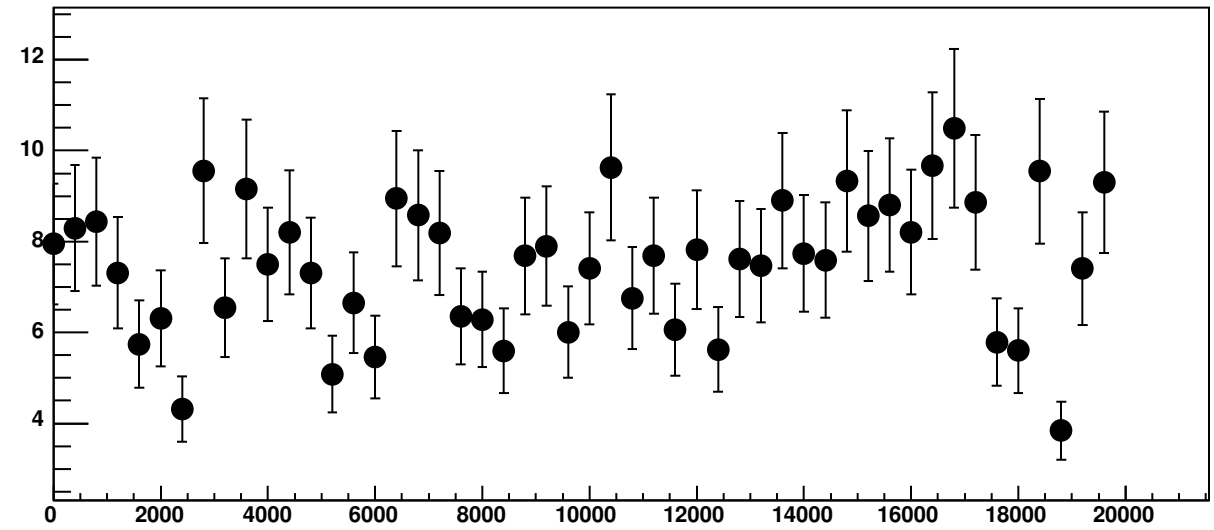

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

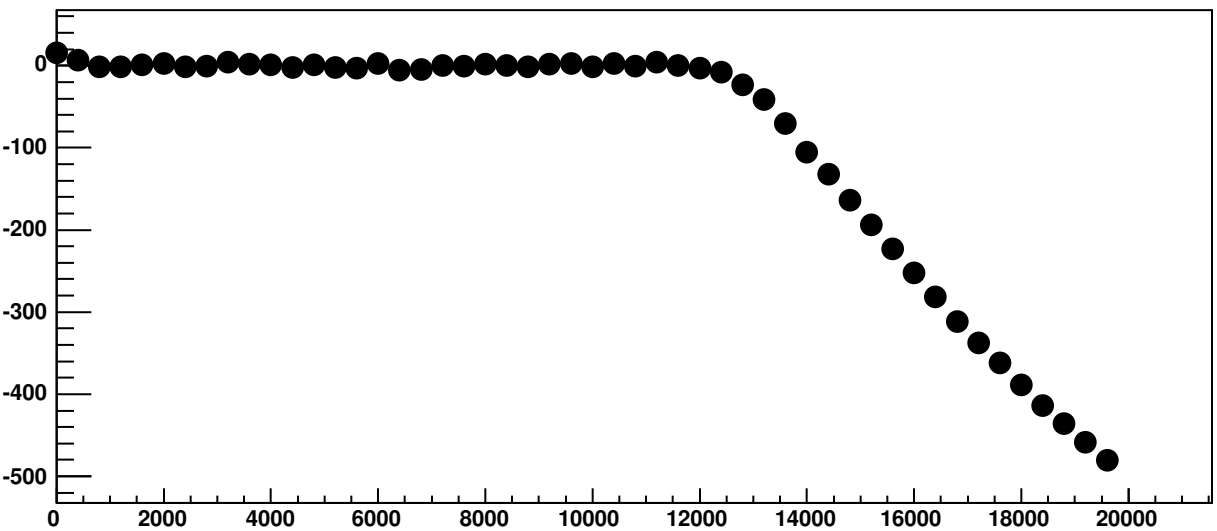
χ^2 / ndf	50.63 / 23
p0	423.3 ± 0.6844
p1	0.01575 ± 0.0001064

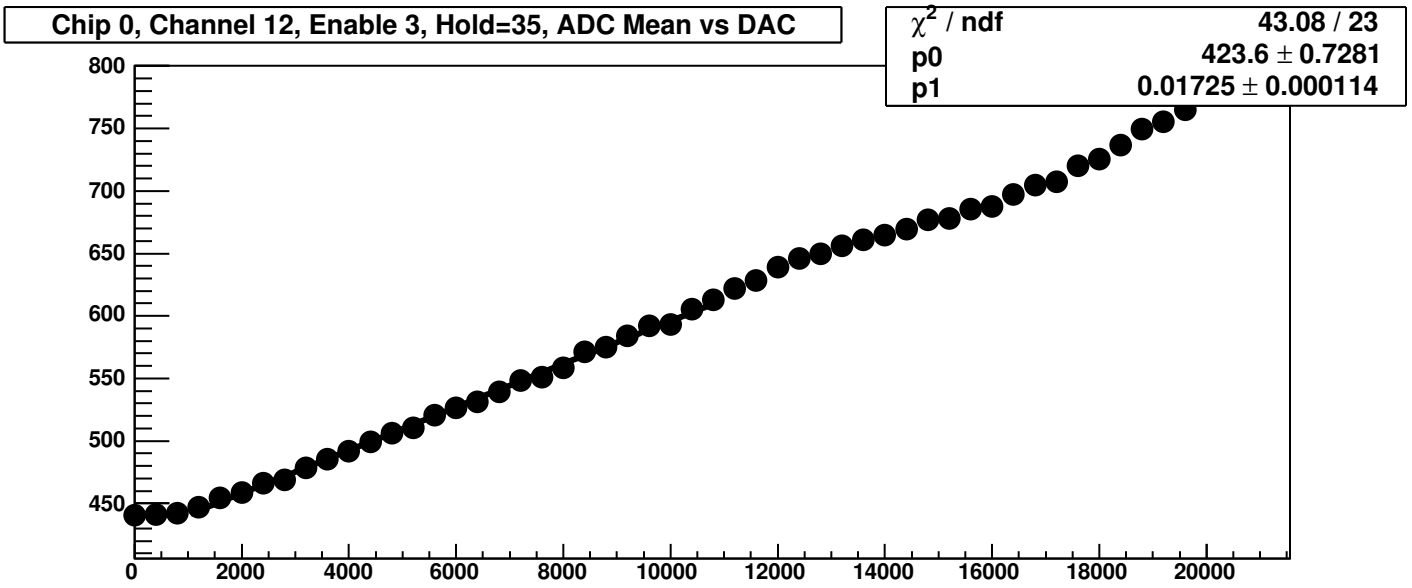


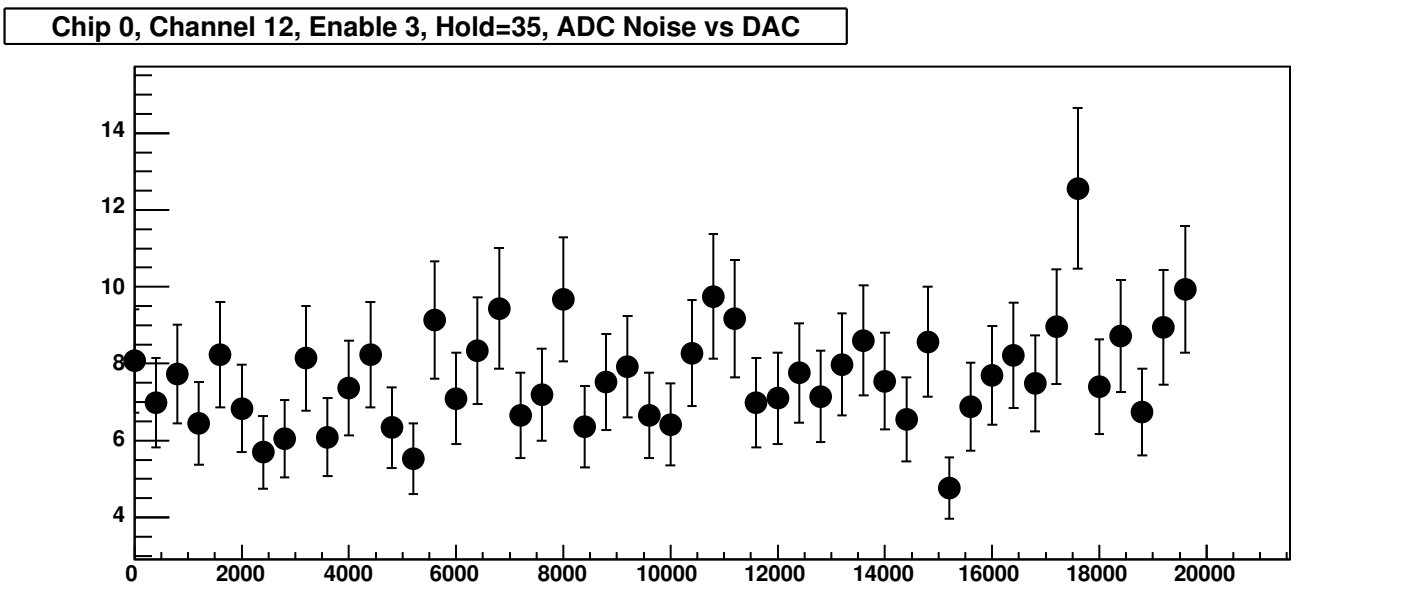
Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC

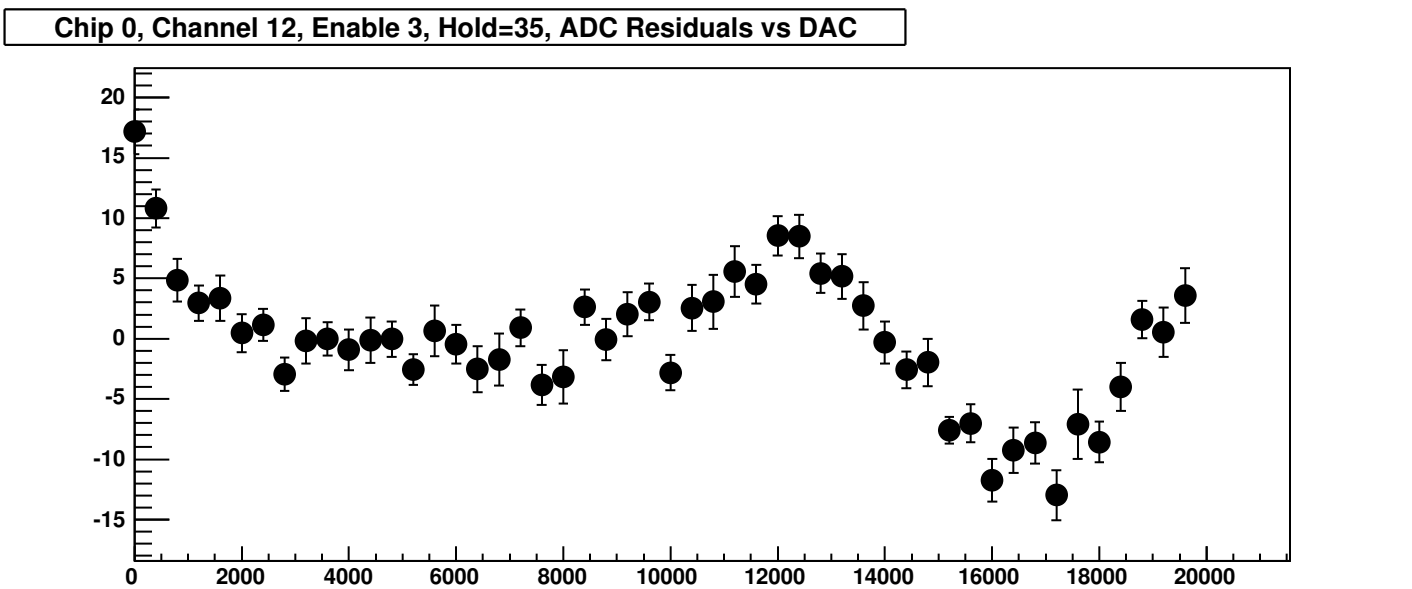


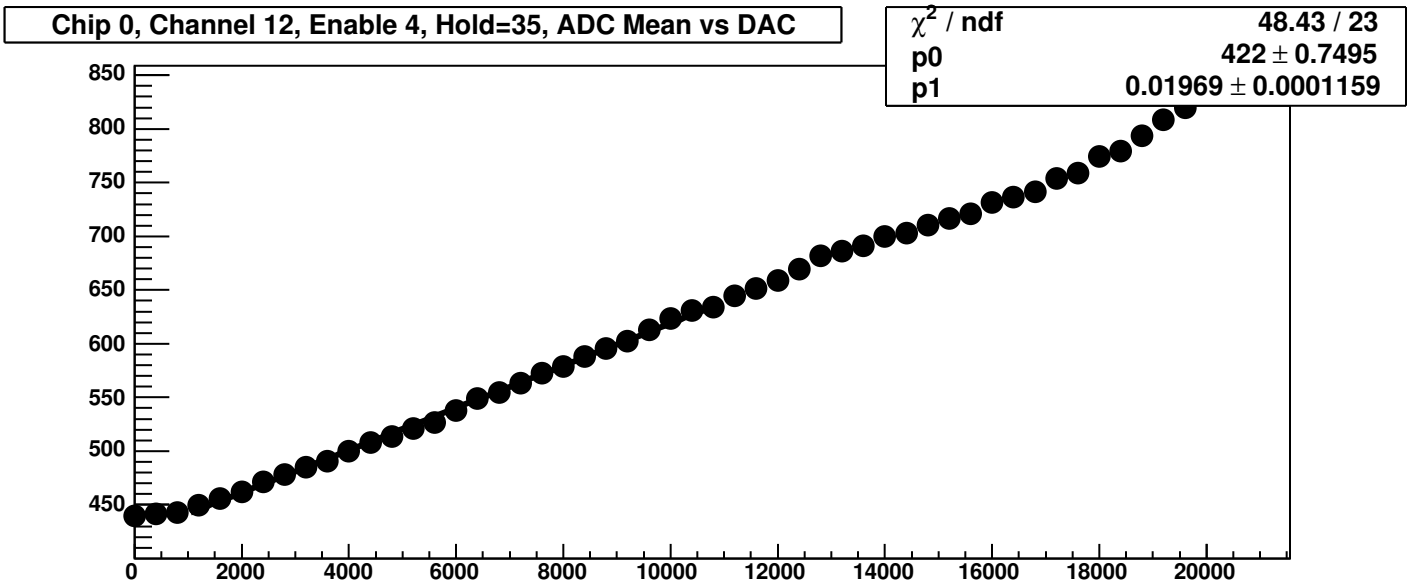
Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

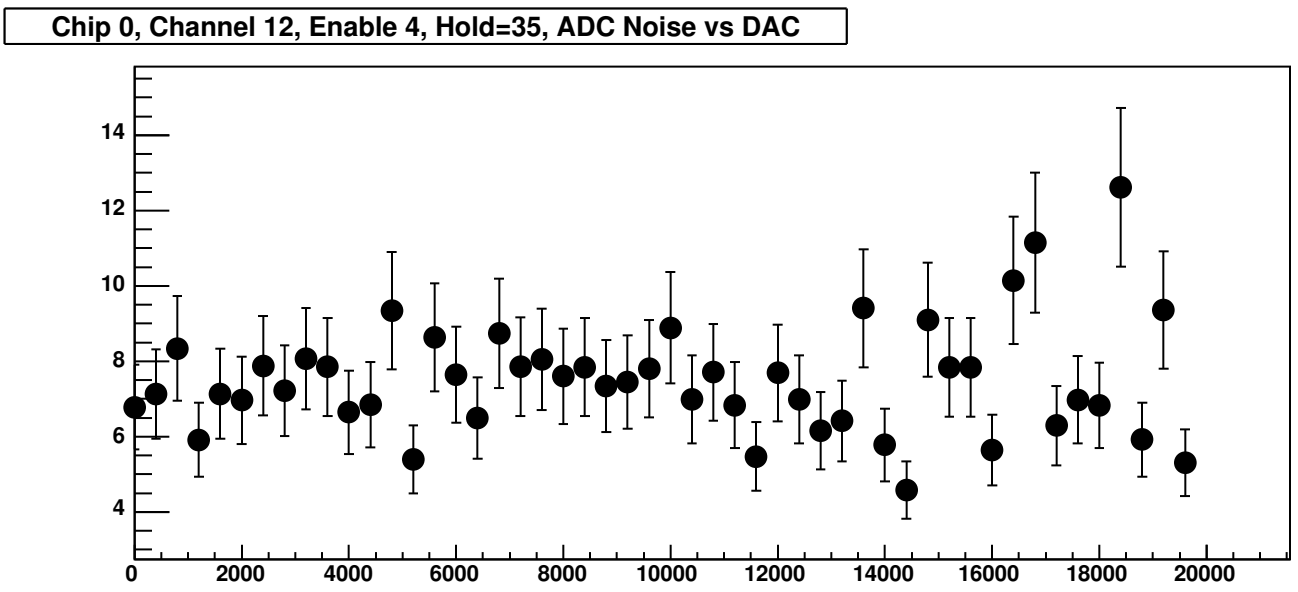


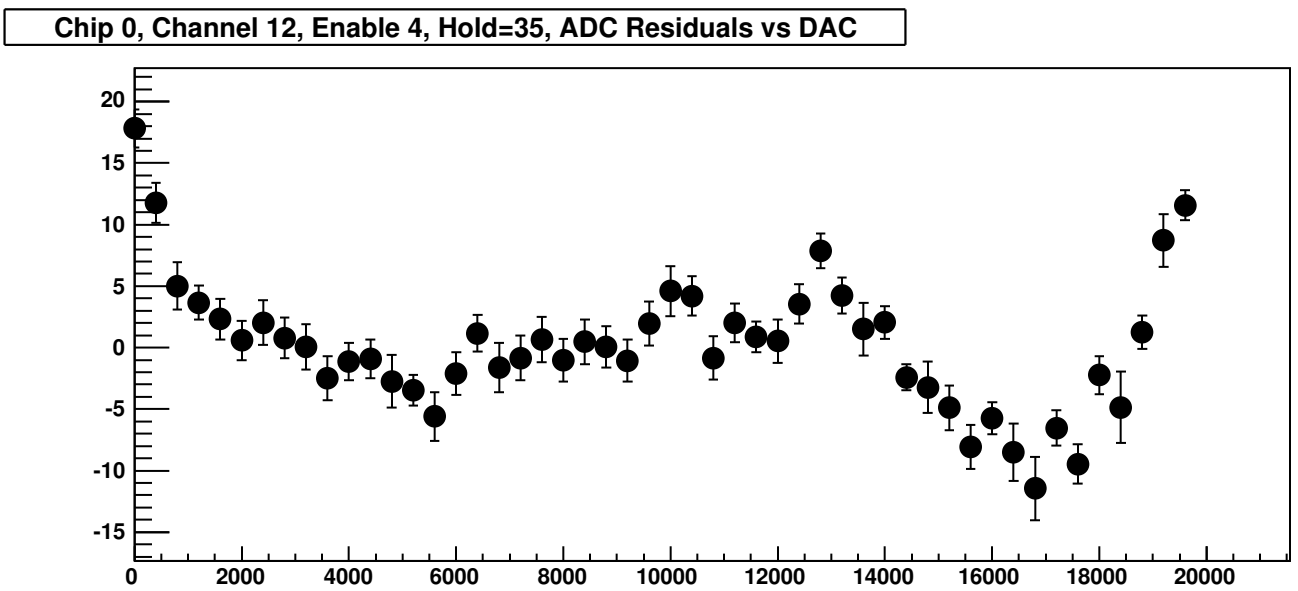

Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 43.08 / 23
p0 423.6 ± 0.7281
p1 0.01725 ± 0.000114
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

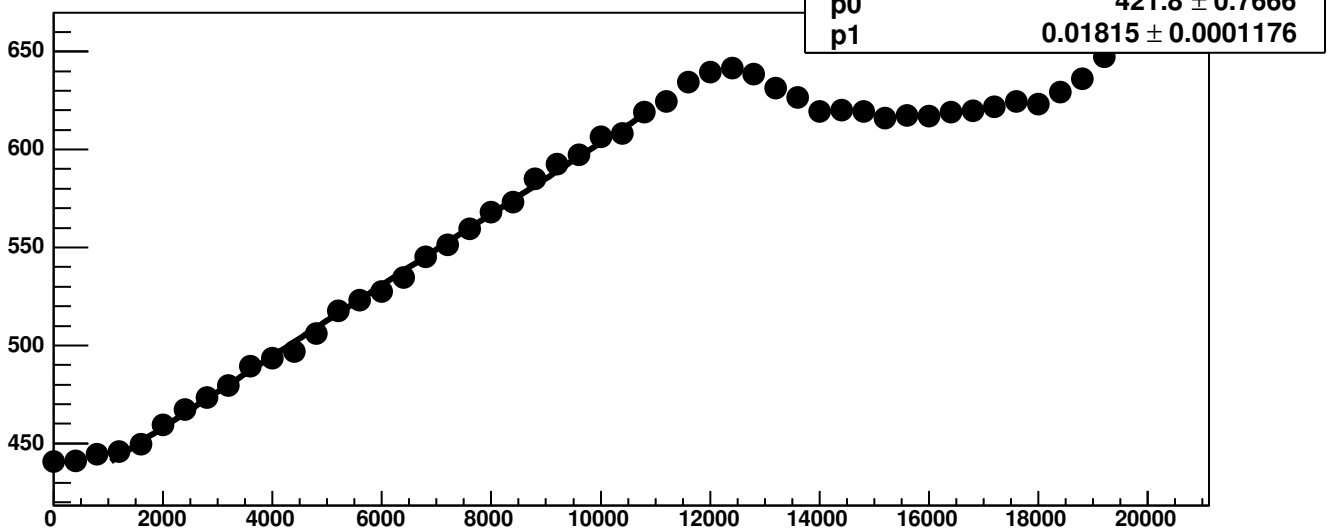

Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 48.43 / 23
p0 422 ± 0.7495
p1 0.01969 ± 0.0001159
850
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

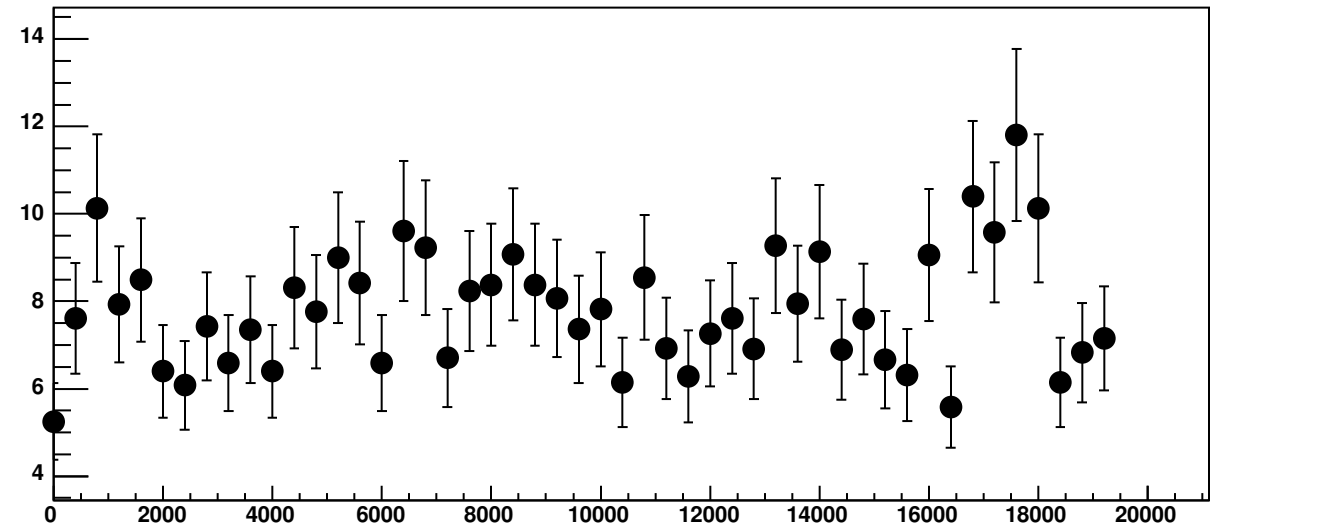

Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

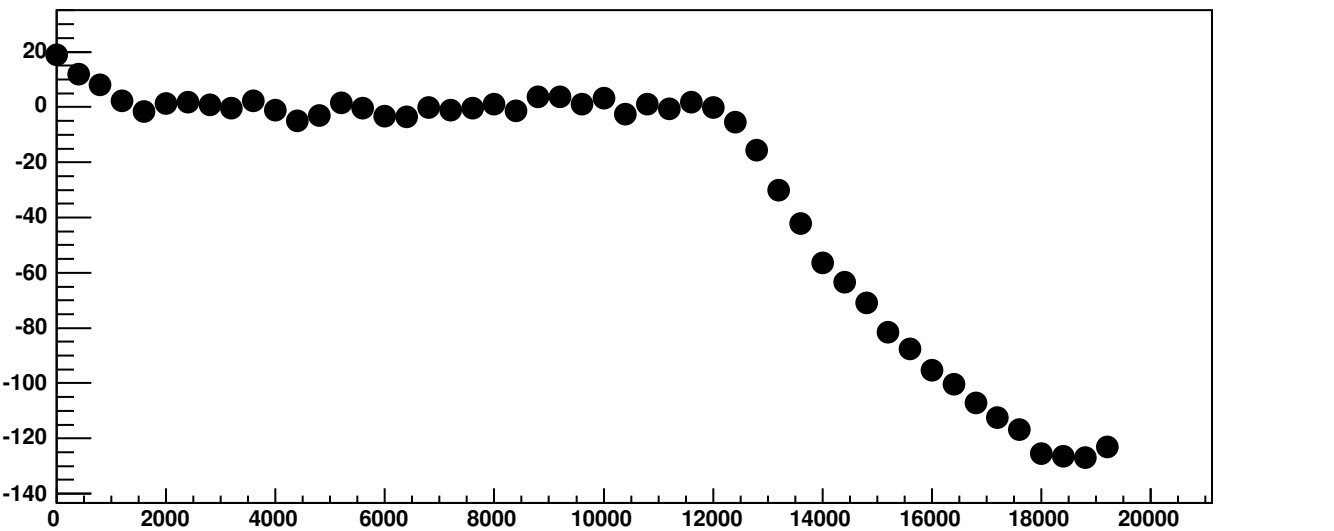
Chip 0, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

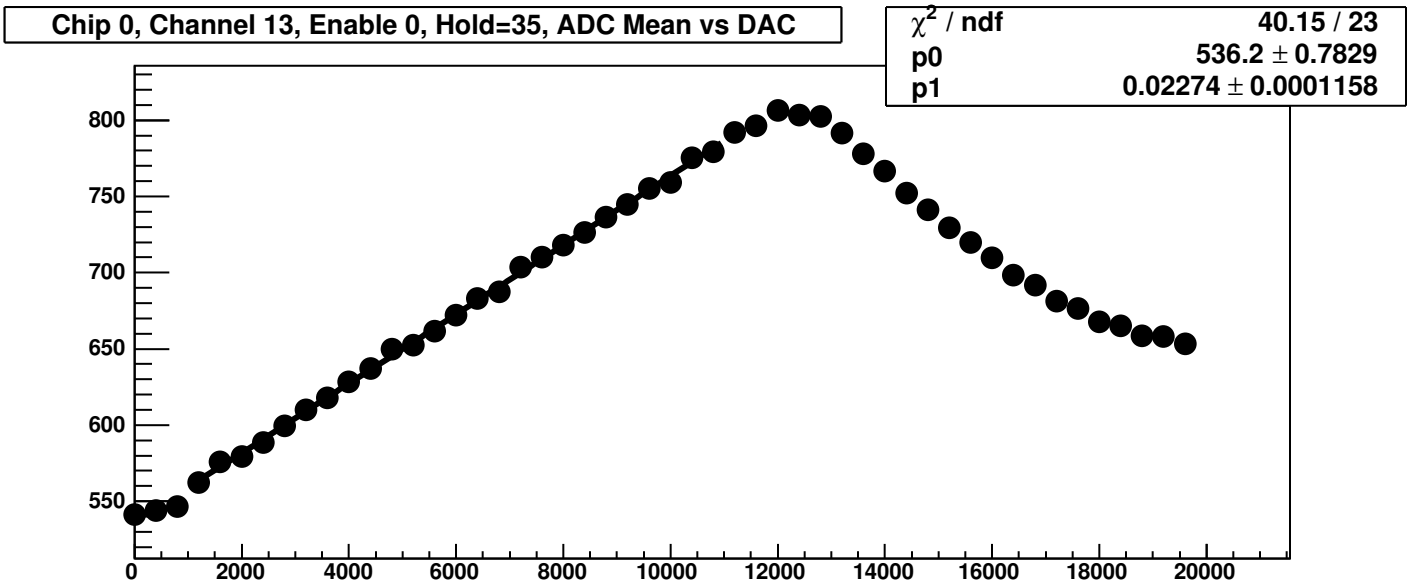


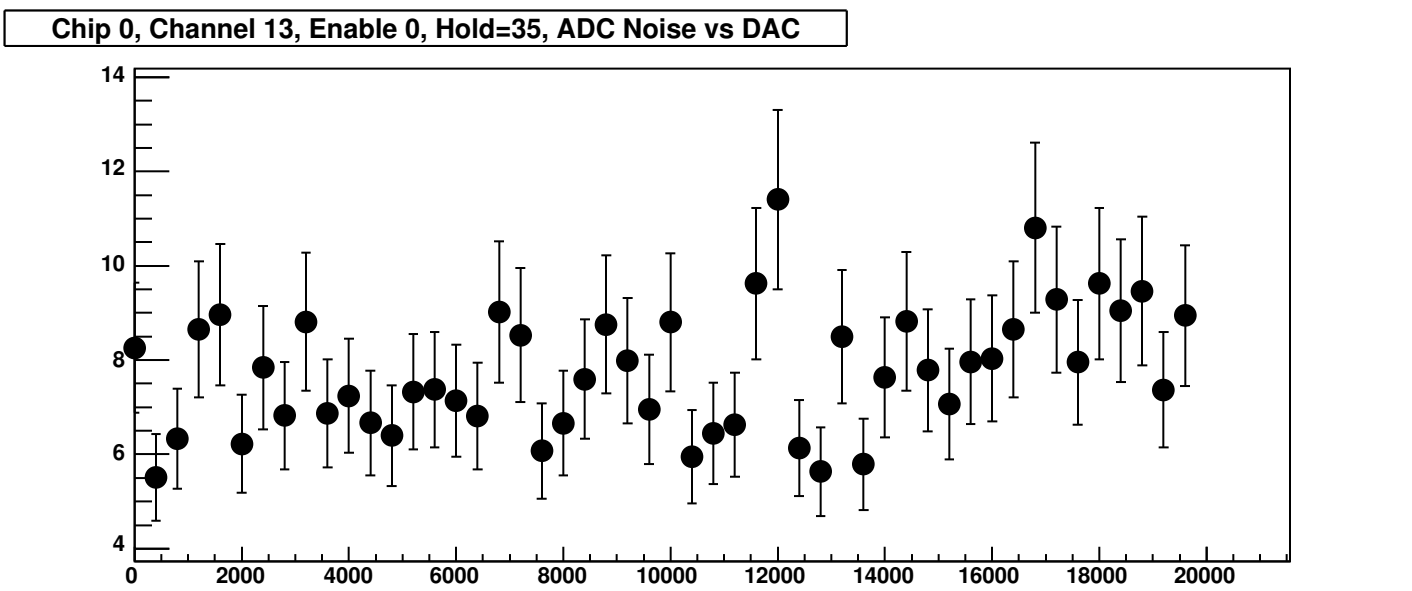
Chip 0, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

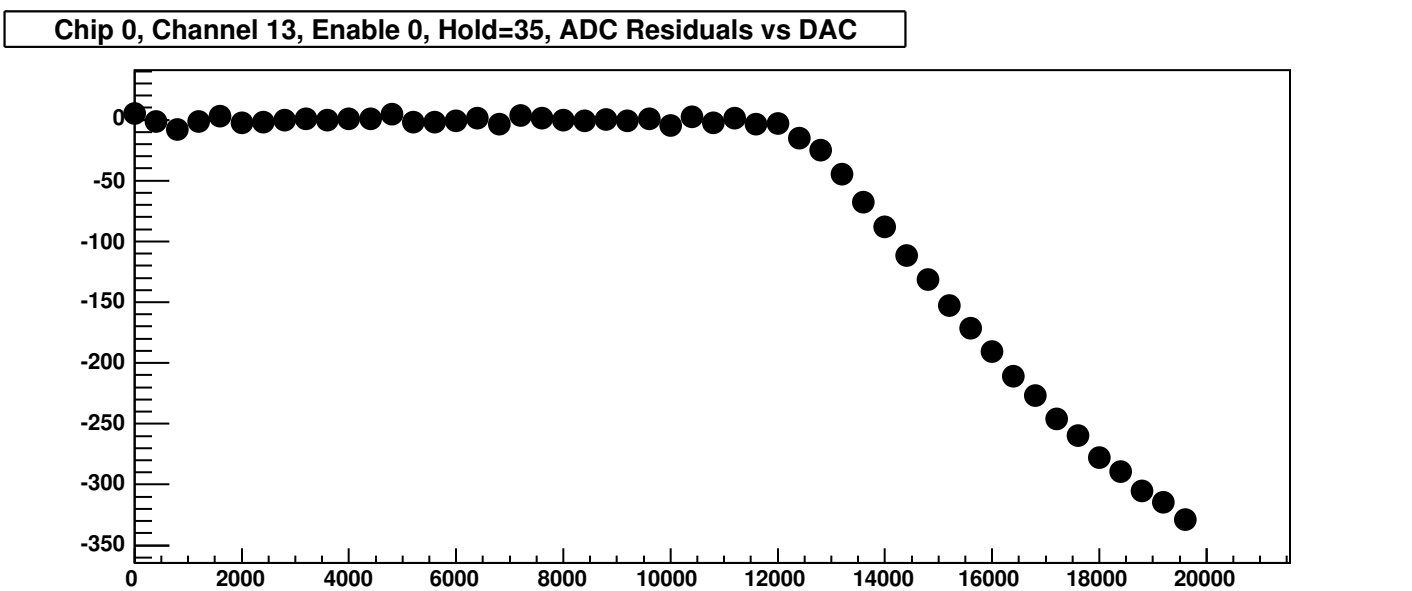


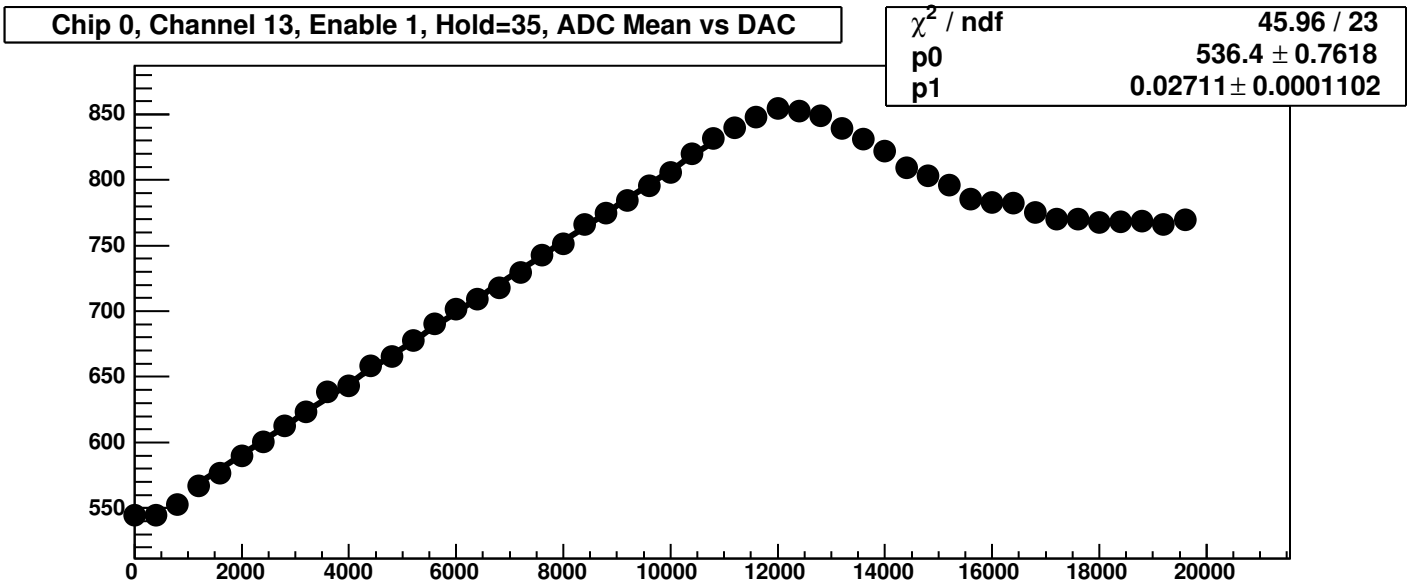
Chip 0, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

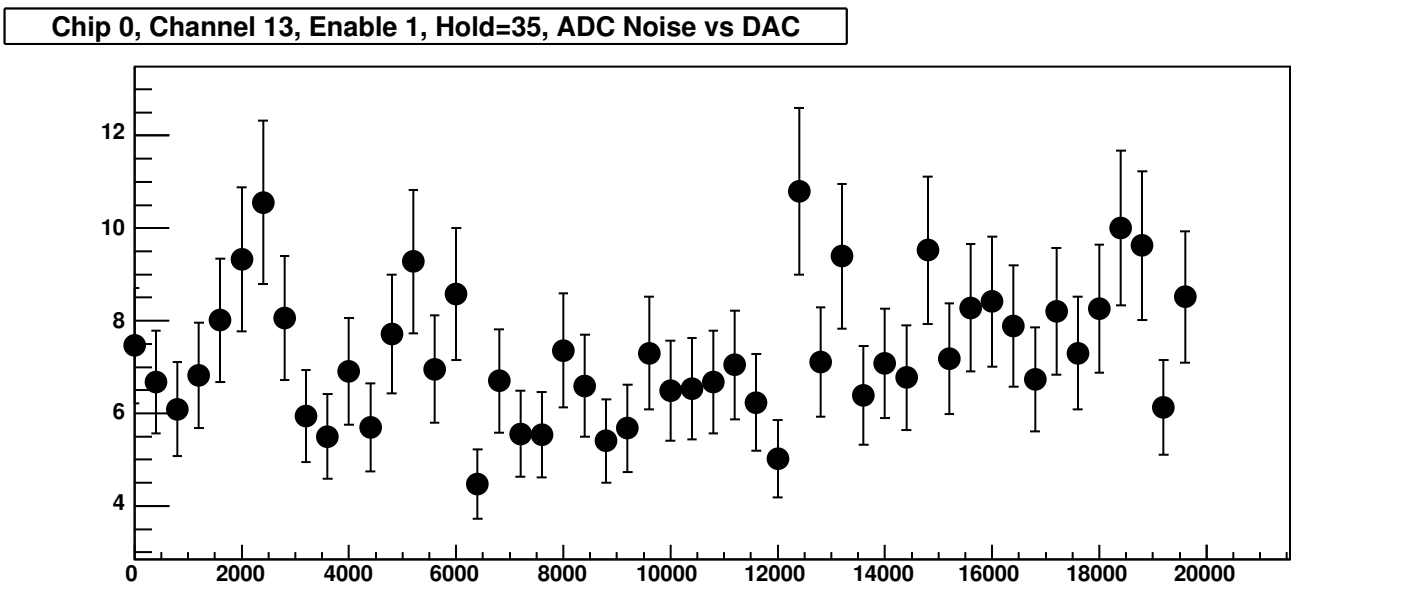


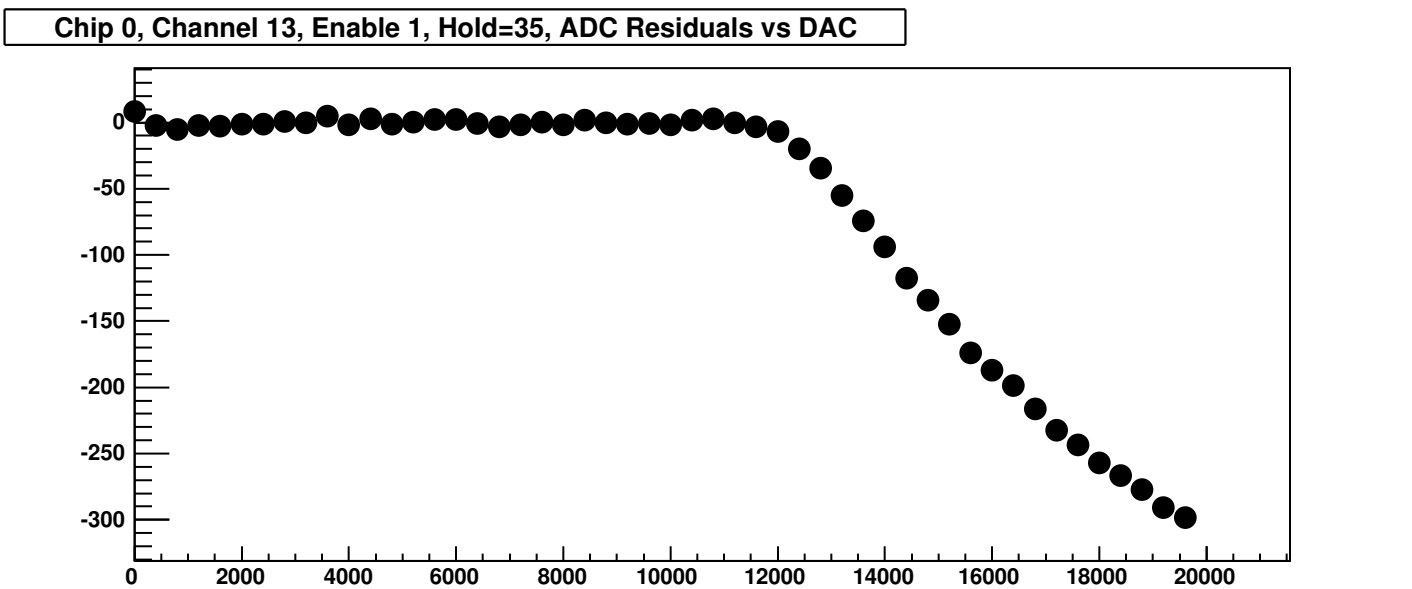

Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 40.15 / 23
p0 536.2 ± 0.7829
p1 0.02274 ± 0.0001158
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

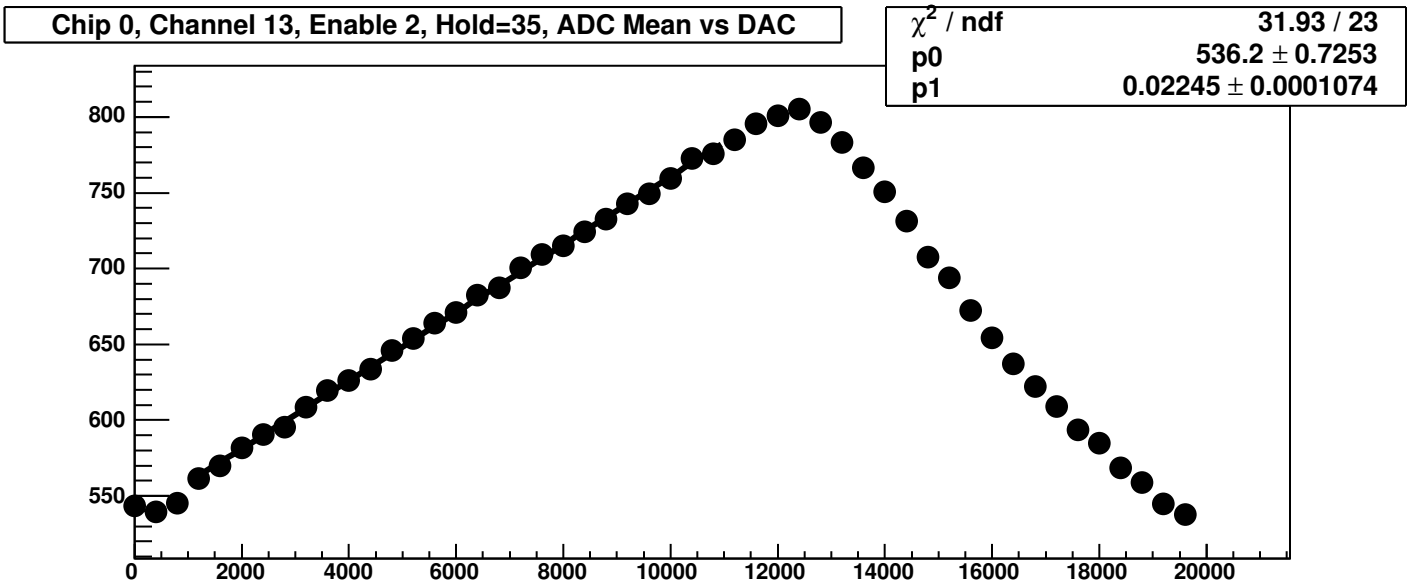

Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

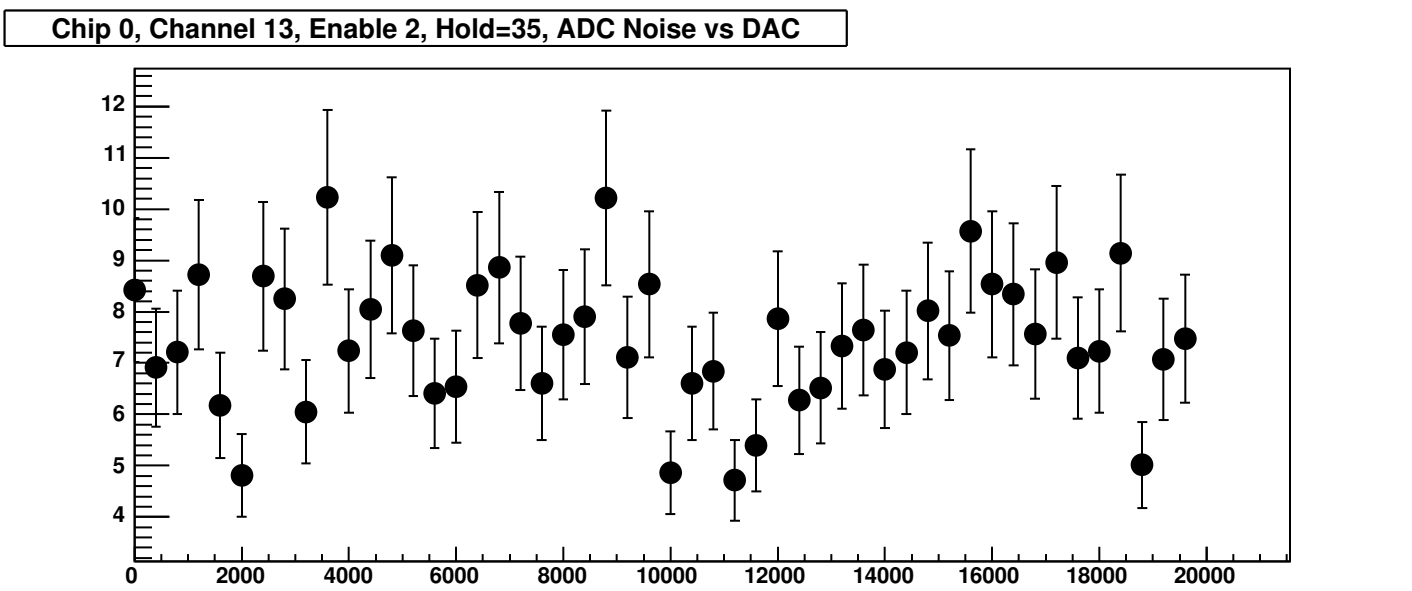

Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

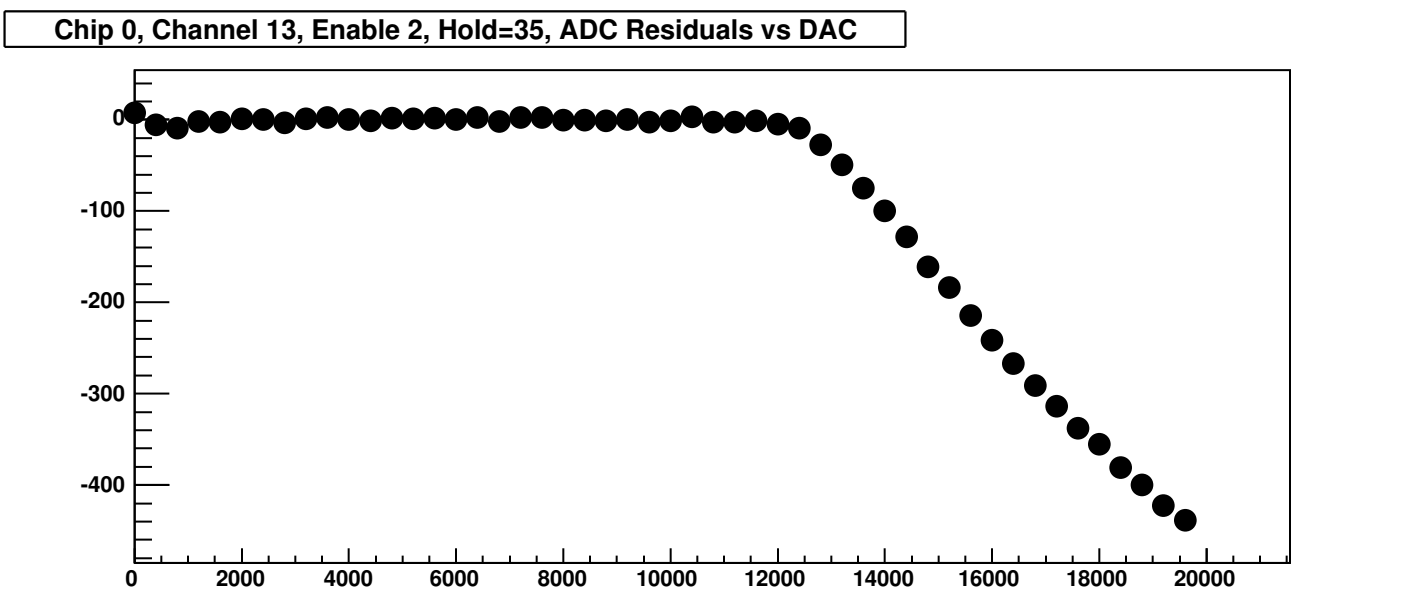

Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 45.96 / 23
p0 536.4 ± 0.7618
p1 0.02711 ± 0.0001102
850
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

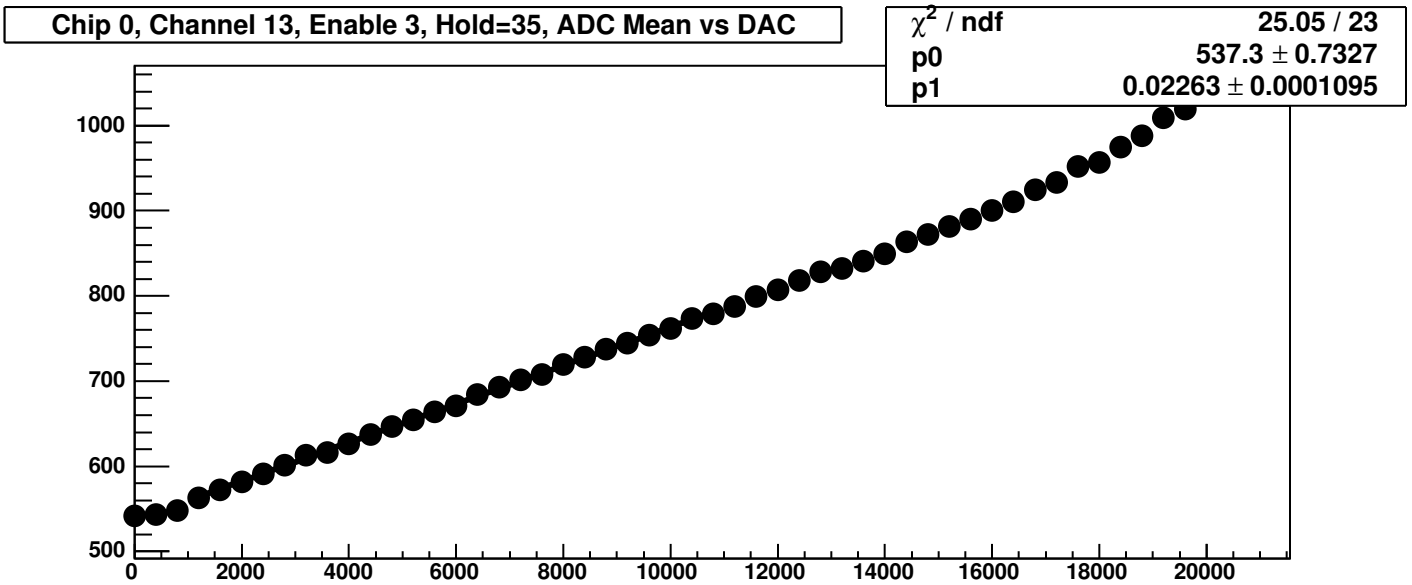

Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

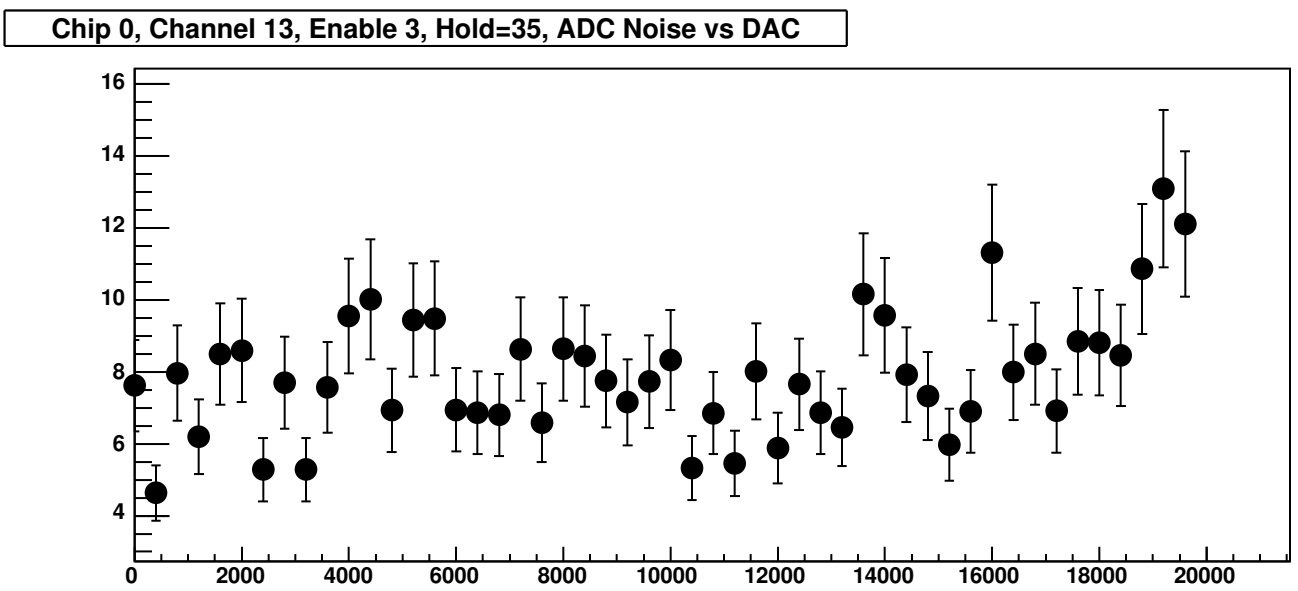

Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

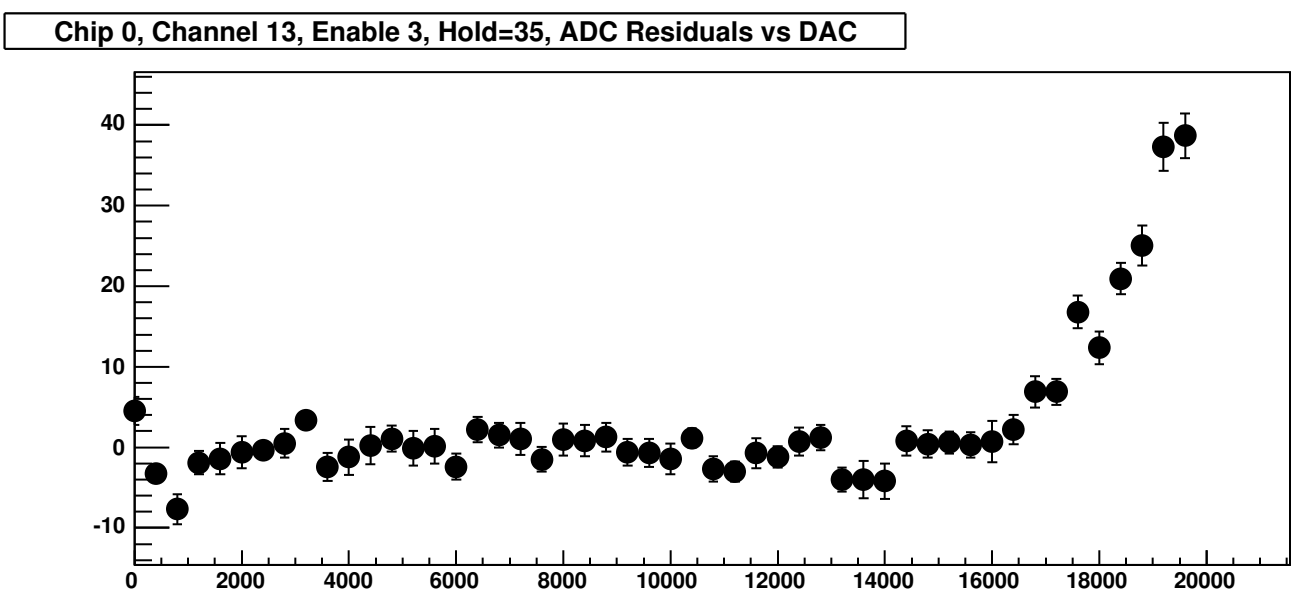

Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 31.93 / 23
p0 536.2 ± 0.7253
p1 0.02245 ± 0.0001074
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

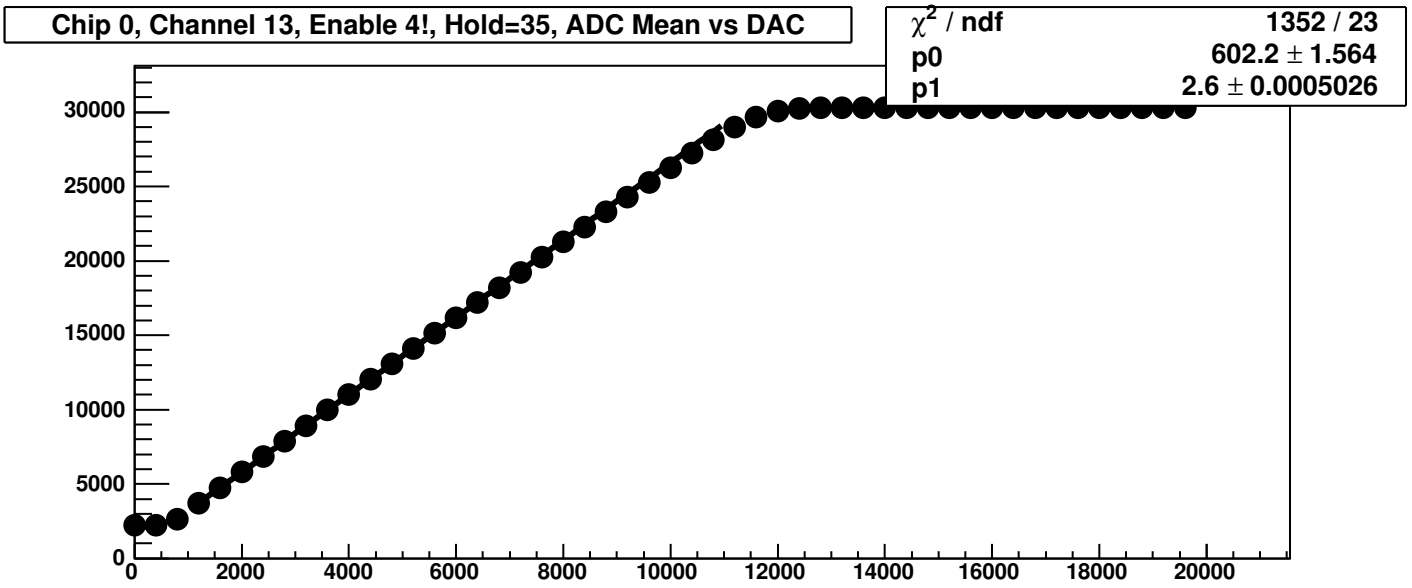

Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

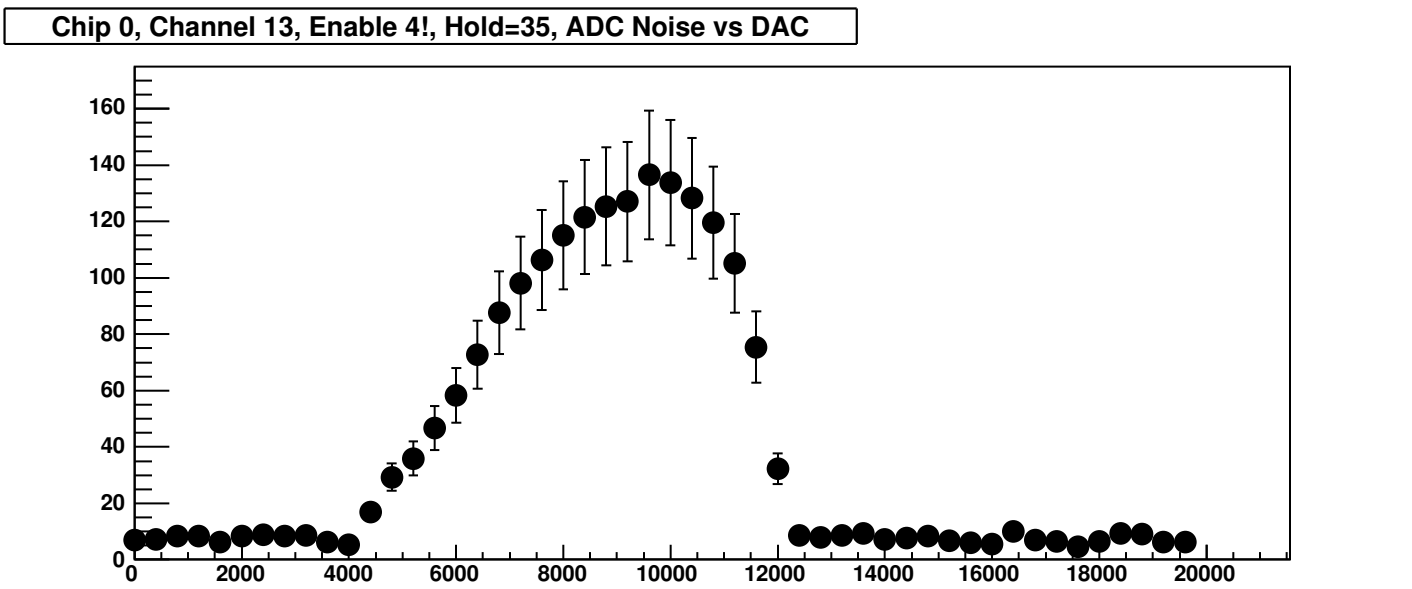

Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

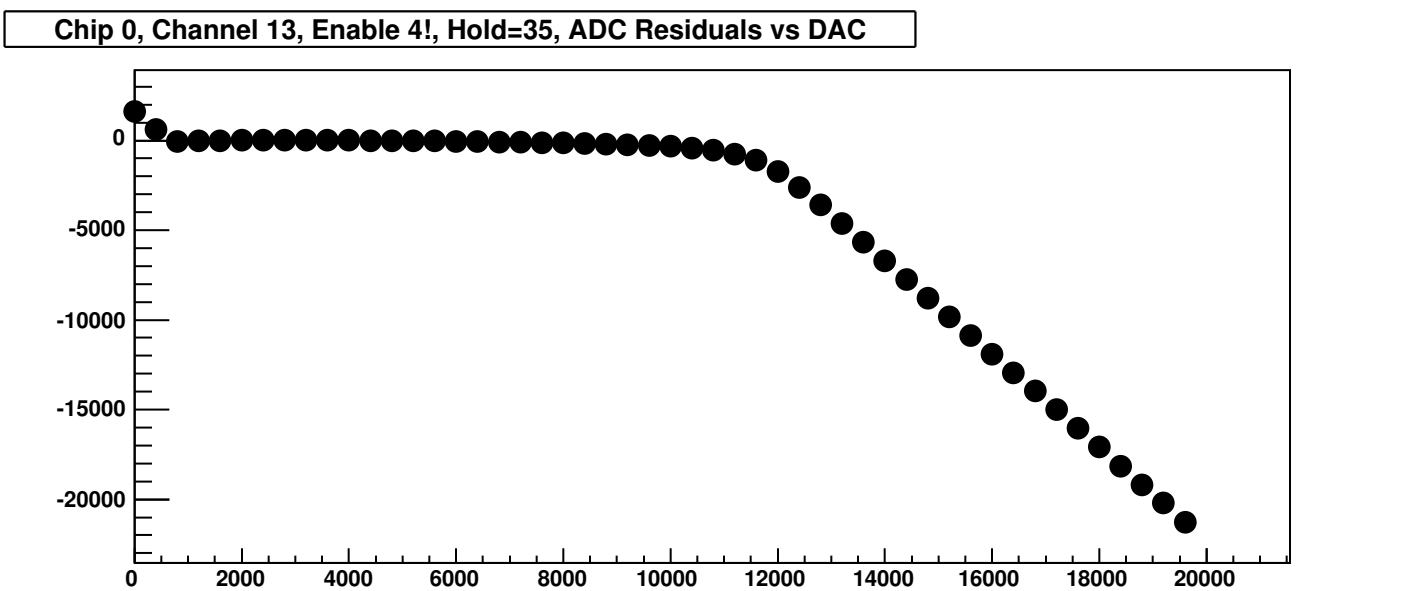

Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.05 / 23
p0 537.3 ± 0.7327
p1 0.02263 ± 0.0001095
1000
900
800
700
600
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

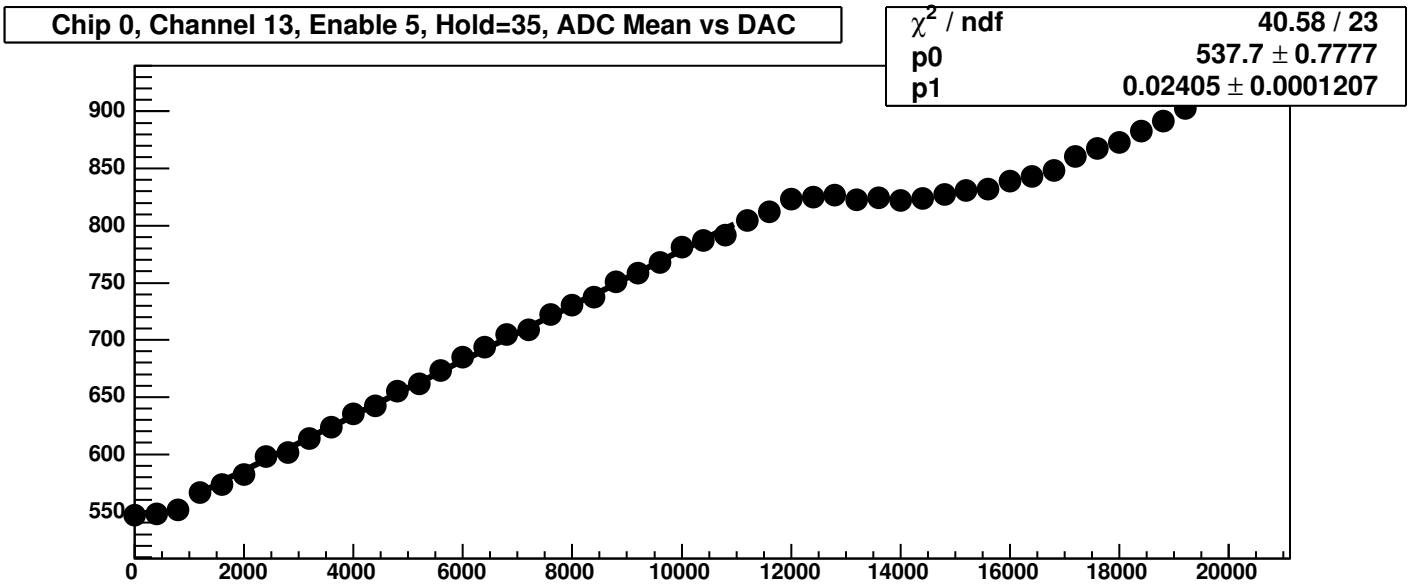

Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

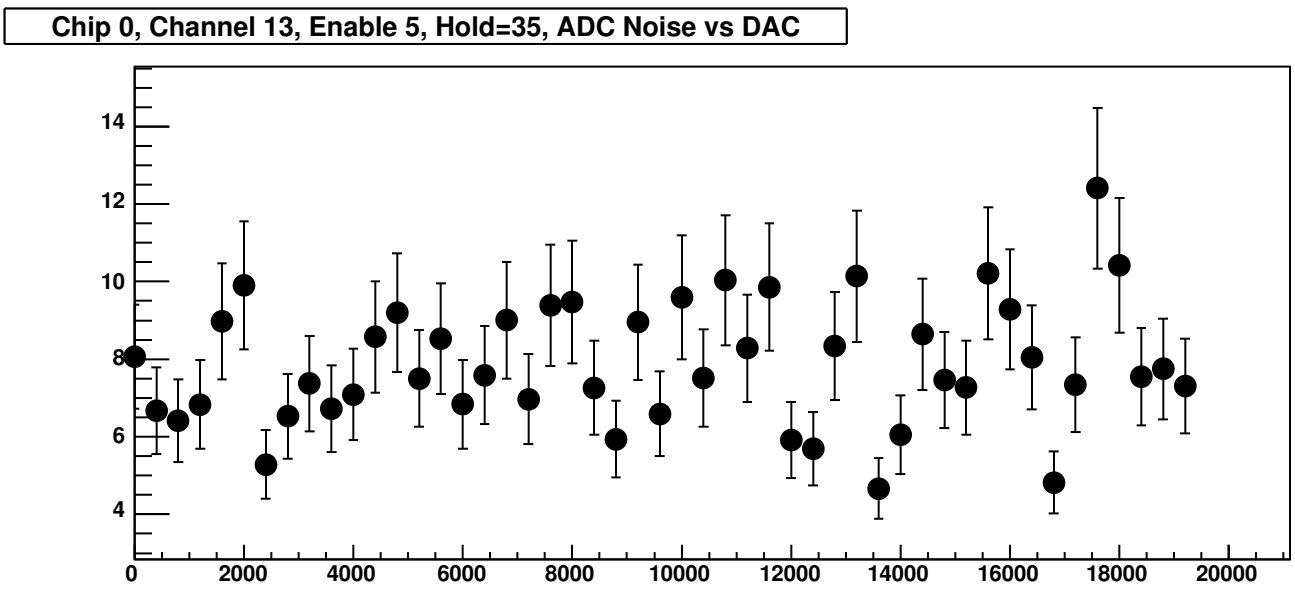

Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

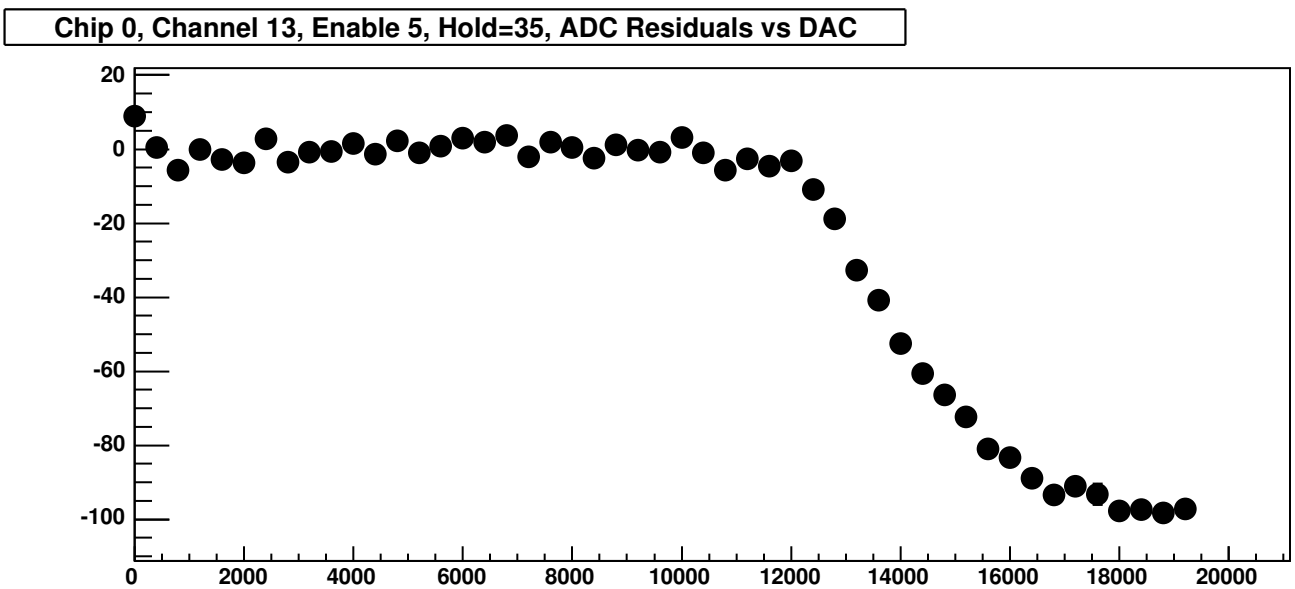

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1352 / 23
p0 602.2 ± 1.564
p1 2.6 ± 0.0005026
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

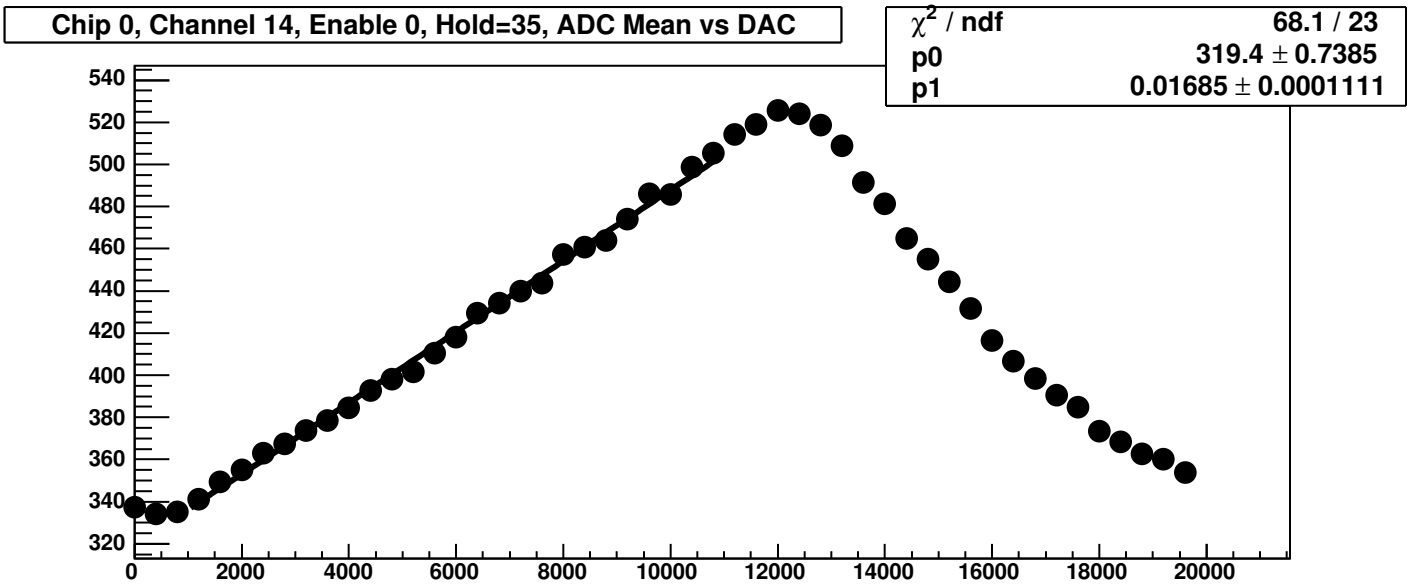

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

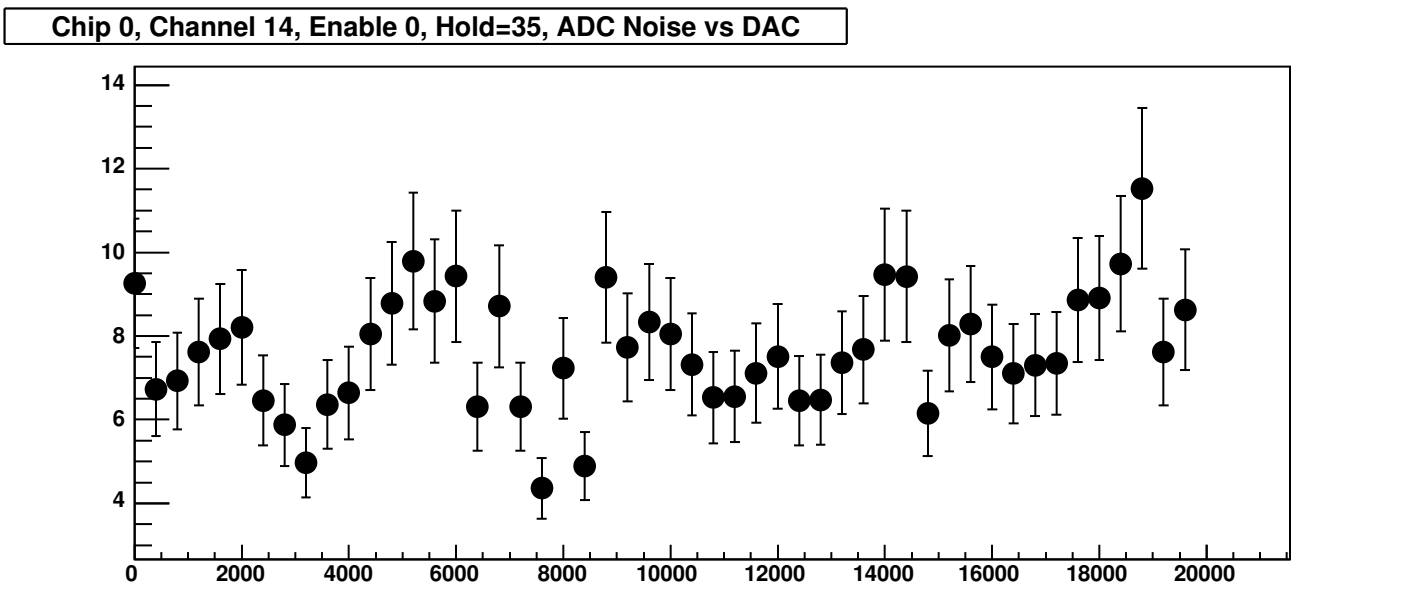

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

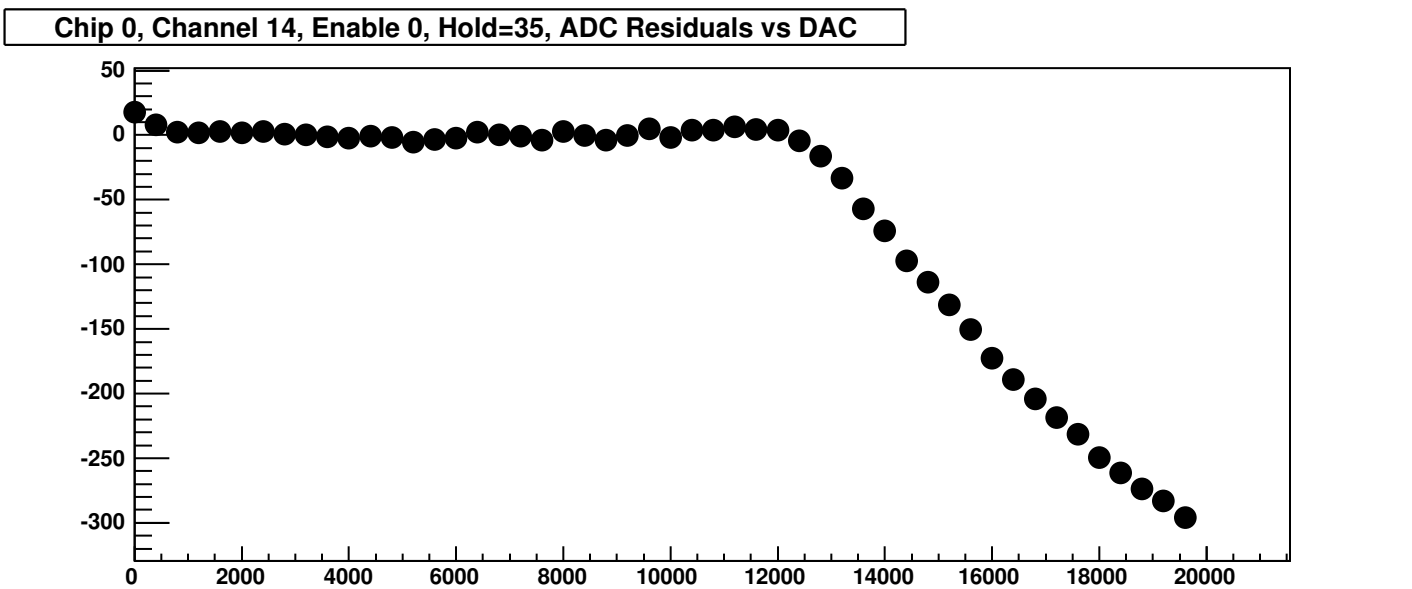

Chip 0, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 40.58 / 23
p0 537.7 ± 0.7777
p1 0.02405 ± 0.0001207
900
850
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

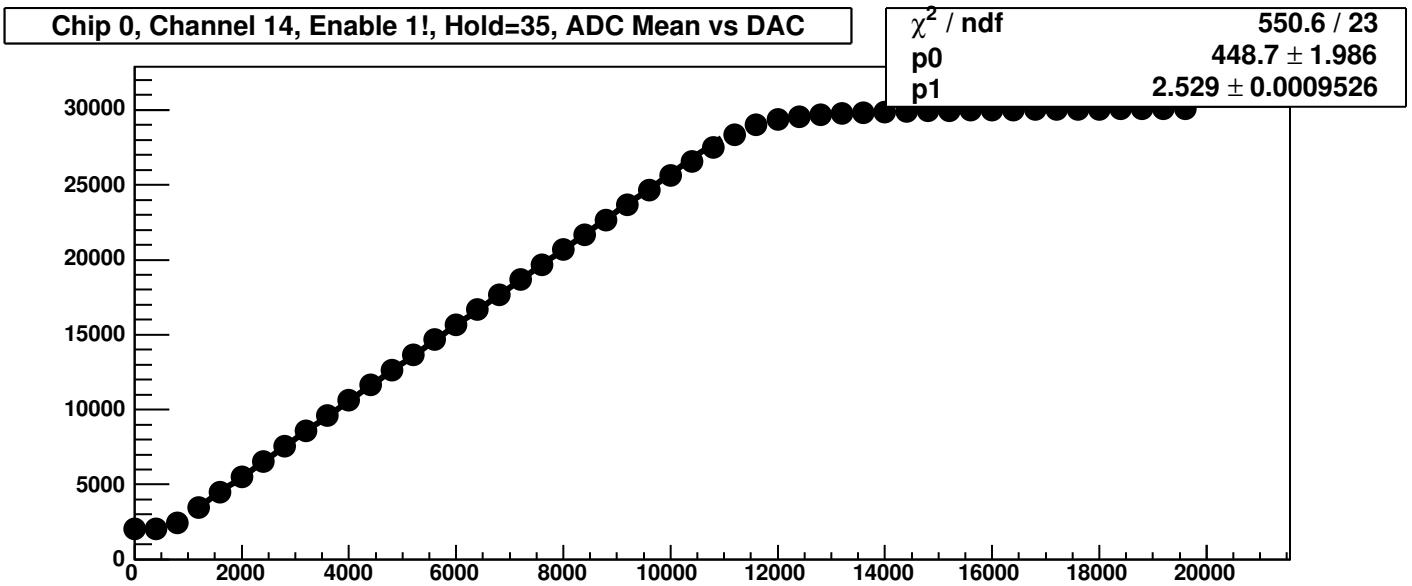

Chip 0, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

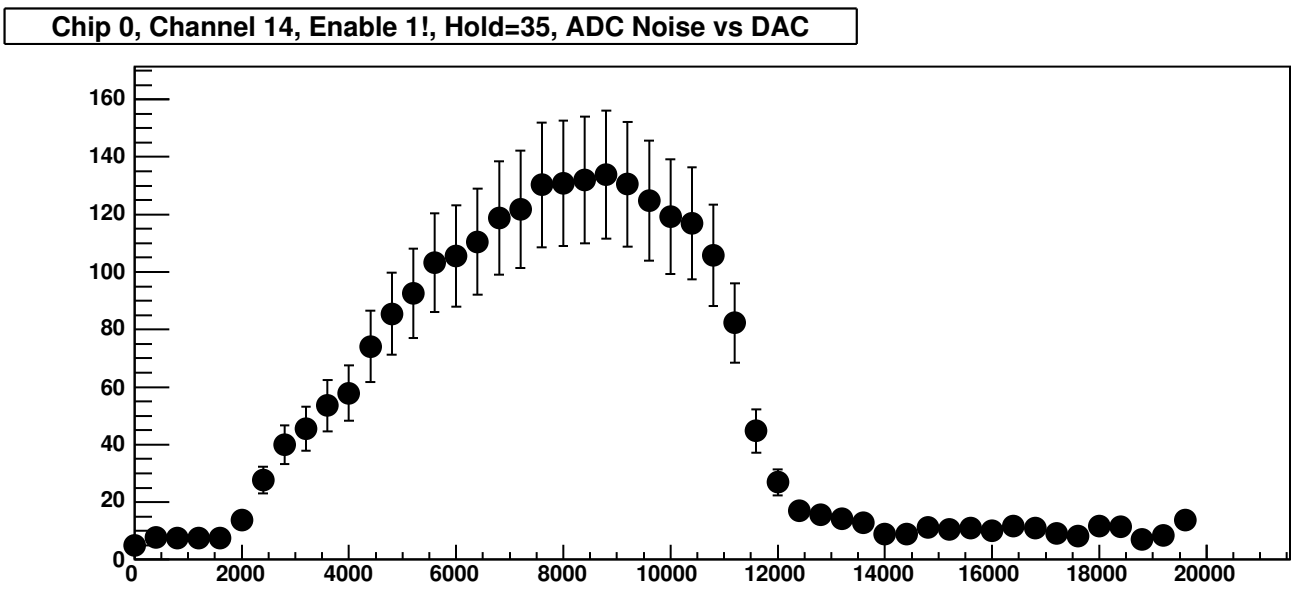

Chip 0, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

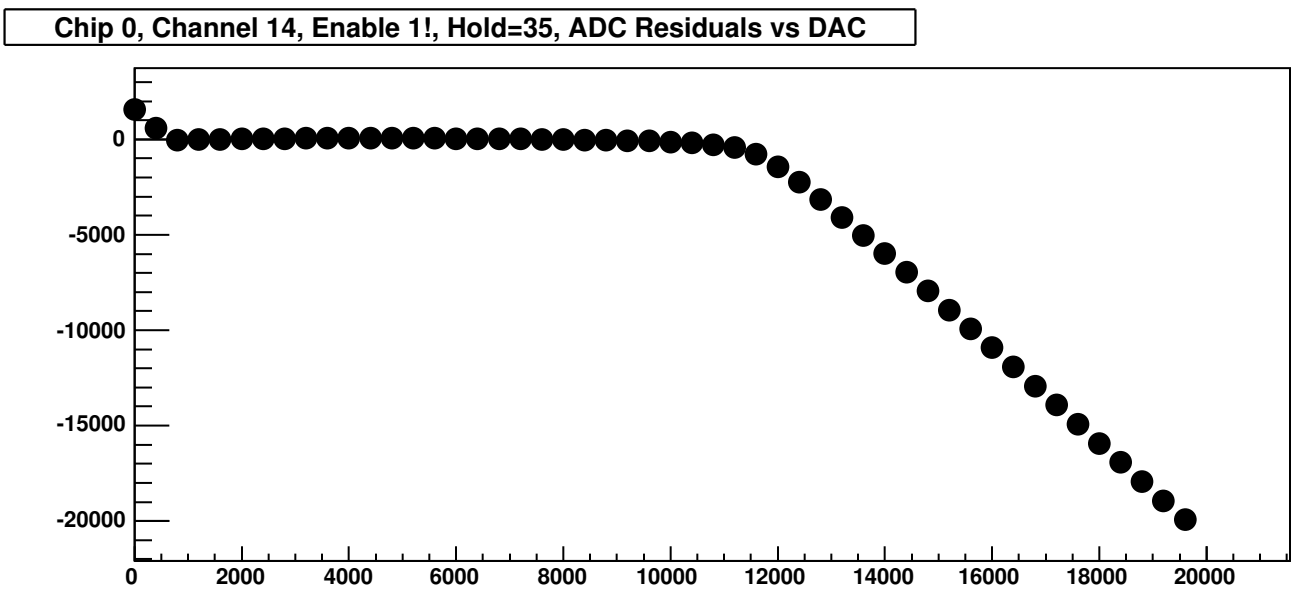

Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 68.1 / 23
p0 319.4 ± 0.7385
p1 0.01685 ± 0.0001111
540
520
500
480
460
440
420
400
380
360
340
320
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

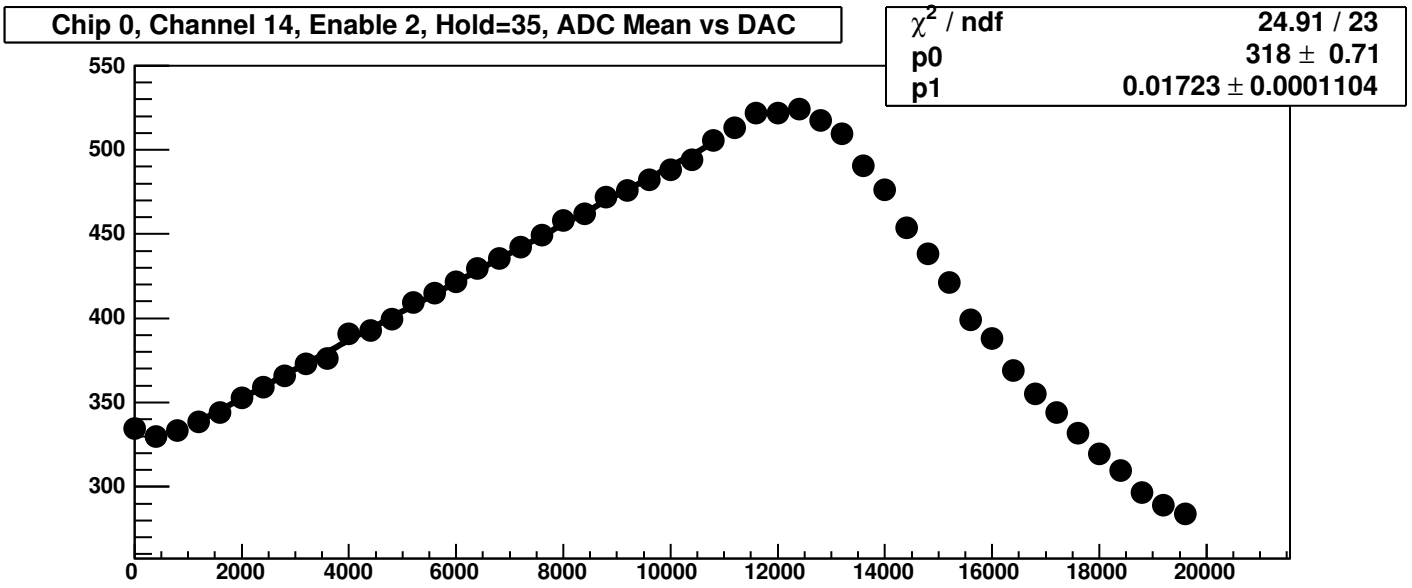

Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

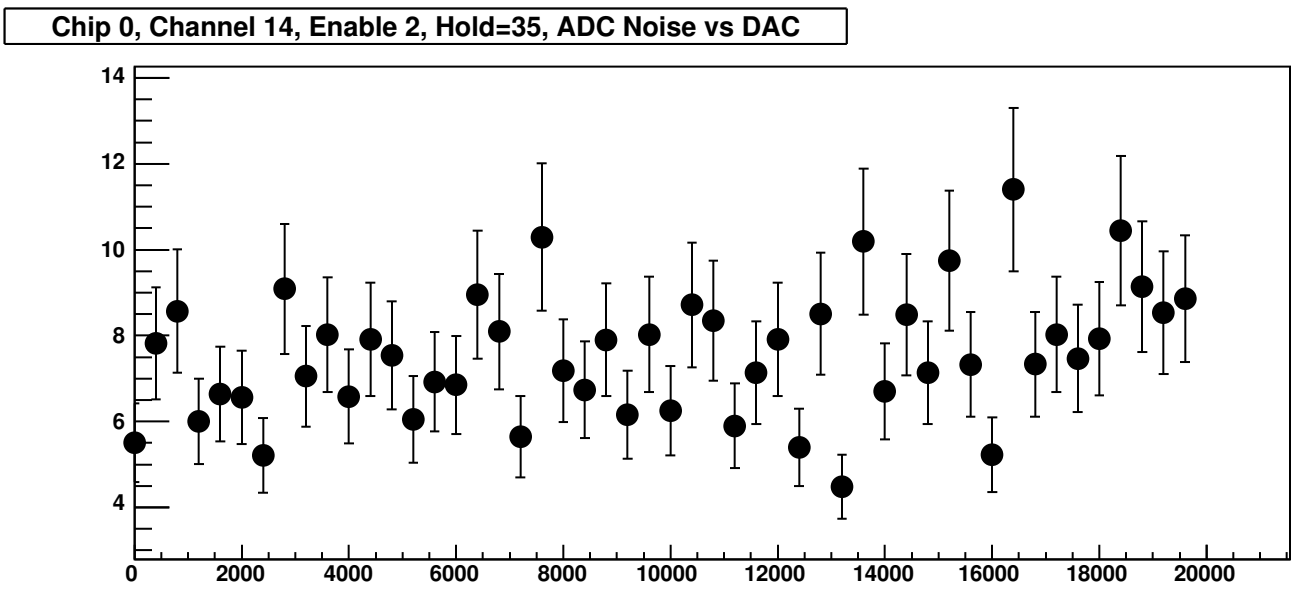

Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

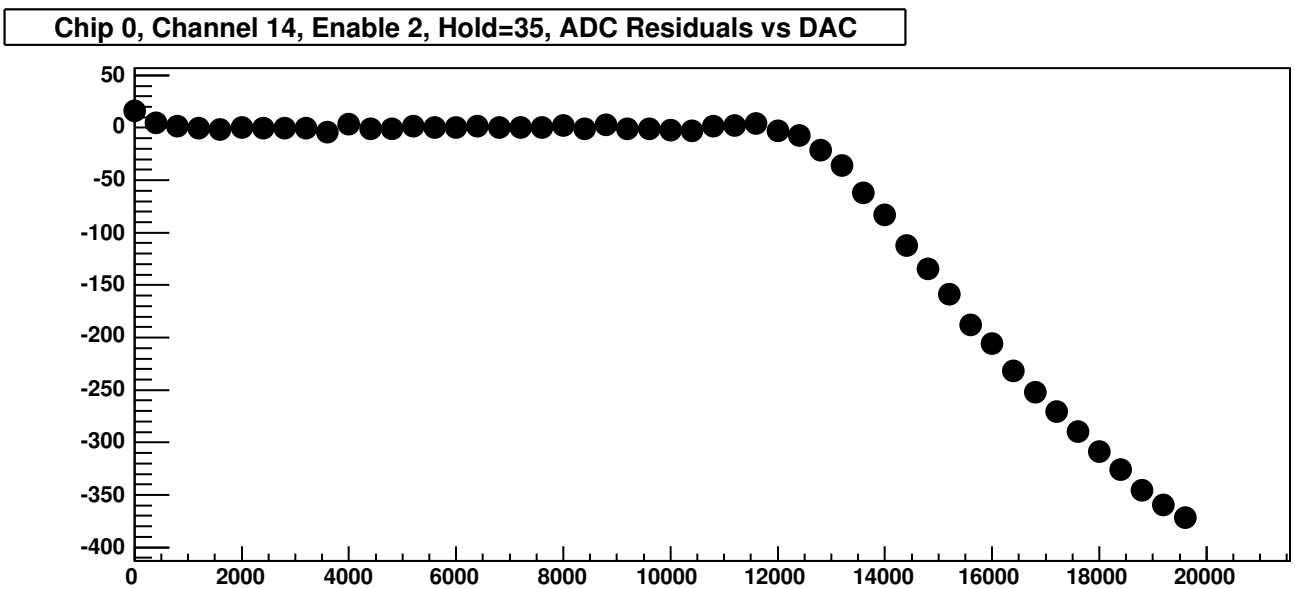

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 550.6 / 23
p0 448.7 ± 1.986
p1 2.529 ± 0.0009526
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

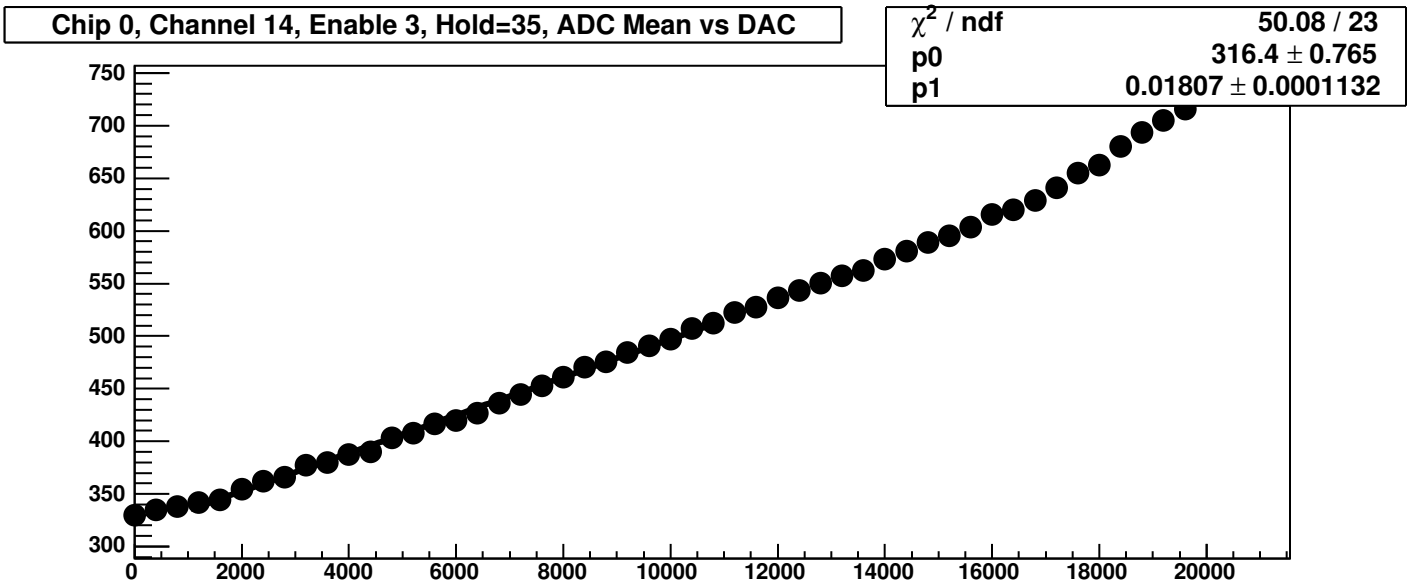

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

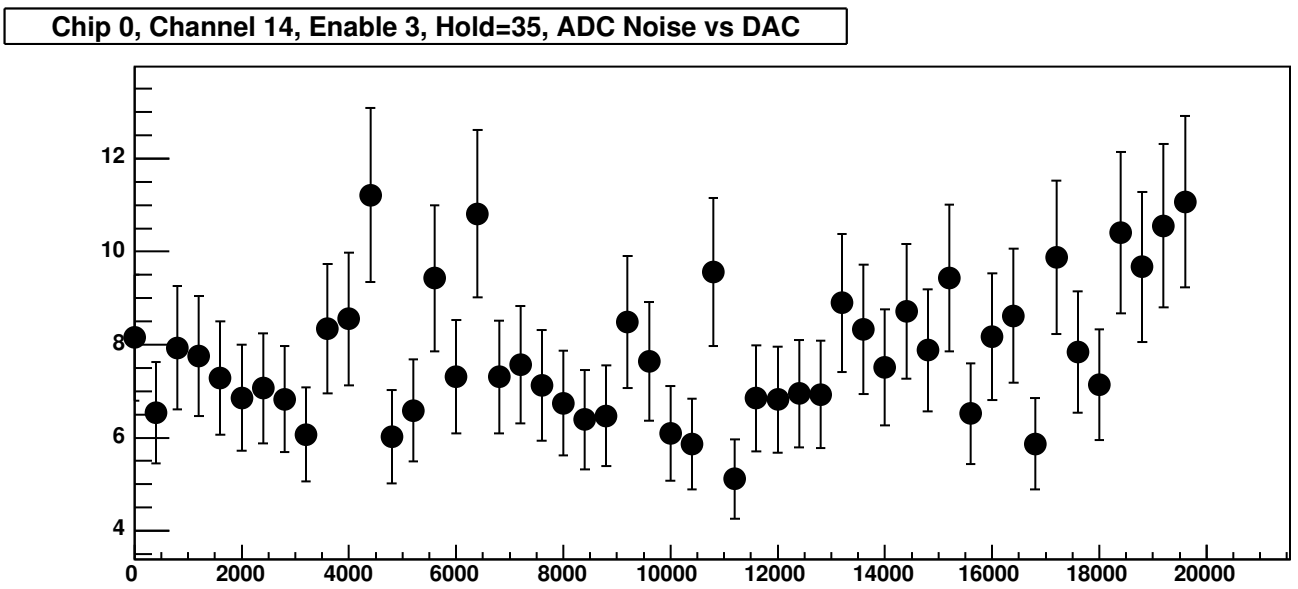

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

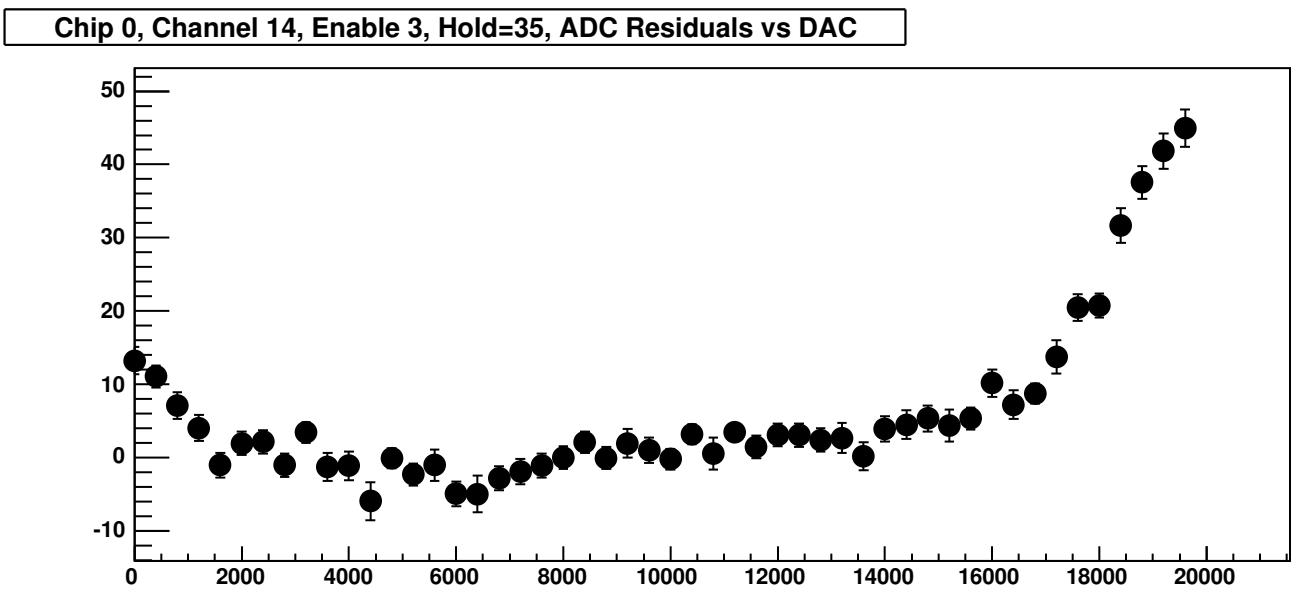

Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 24.91 / 23
p0 318 ± 0.71
p1 0.01723 ± 0.0001104
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

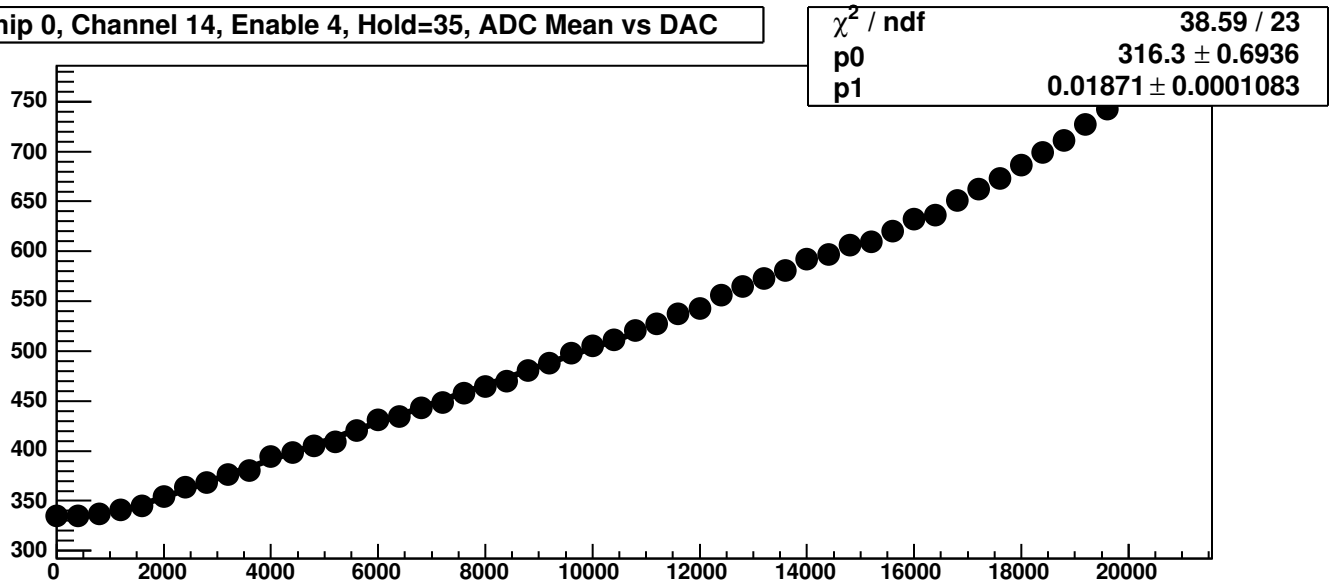

Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.08 / 23
p0 316.4 ± 0.765
p1 0.01807 ± 0.0001132
750
700
650
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

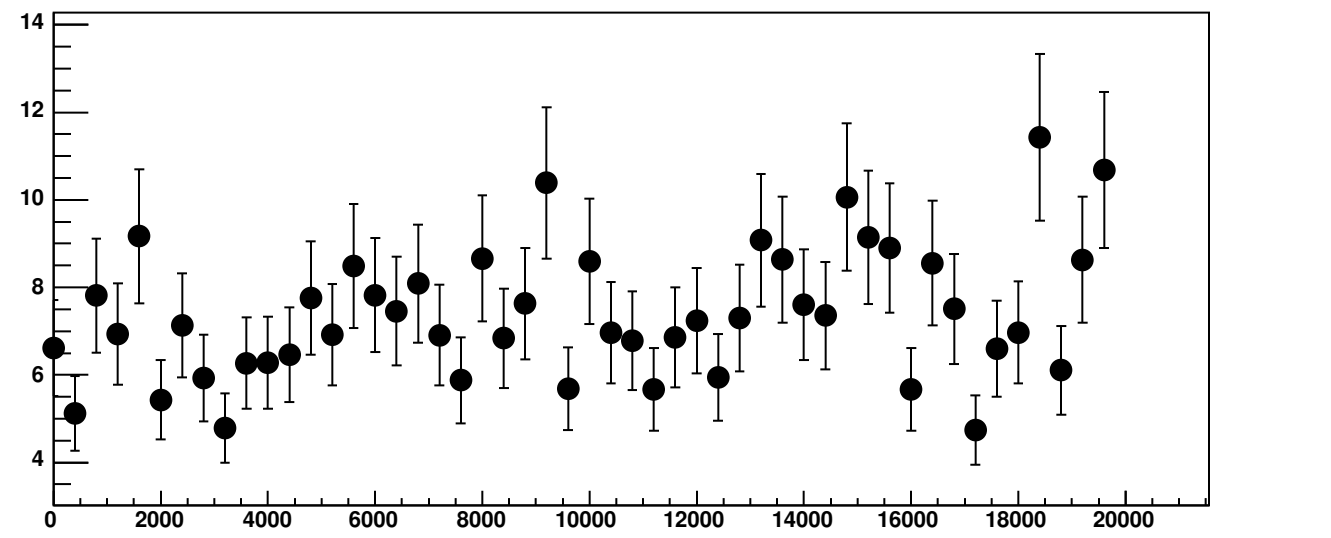

Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

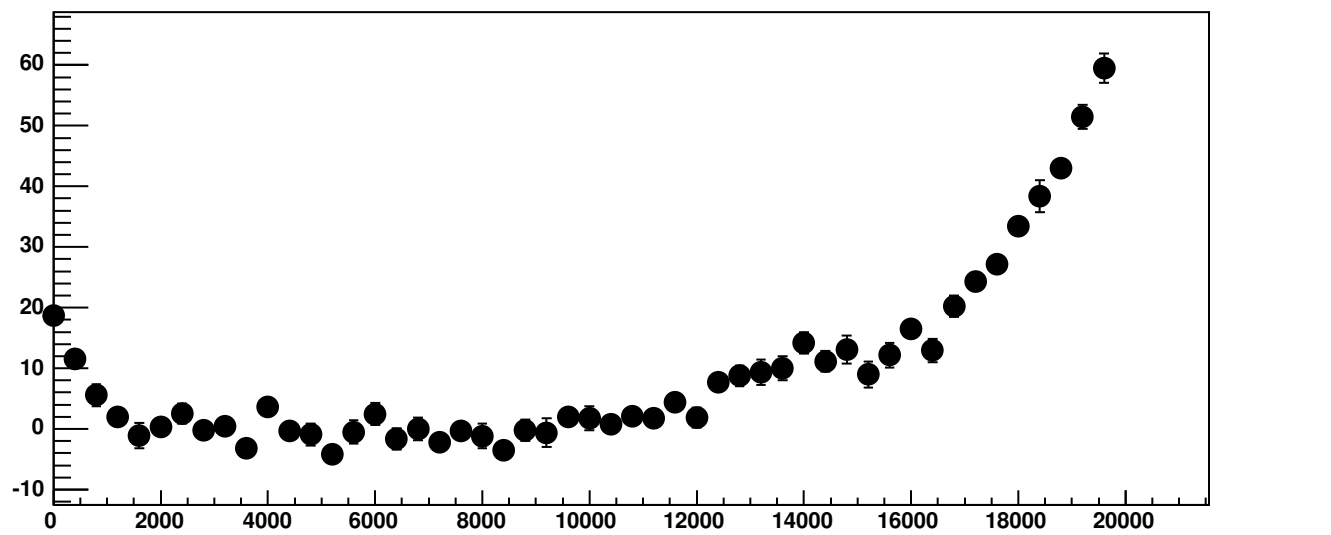
Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

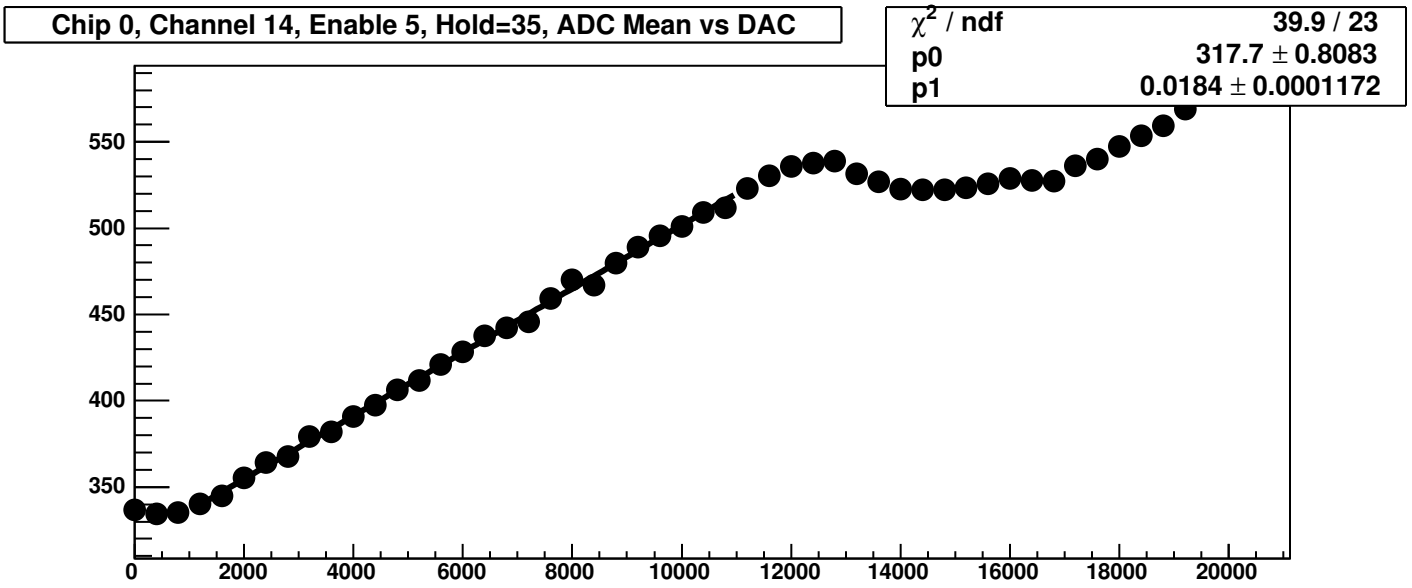


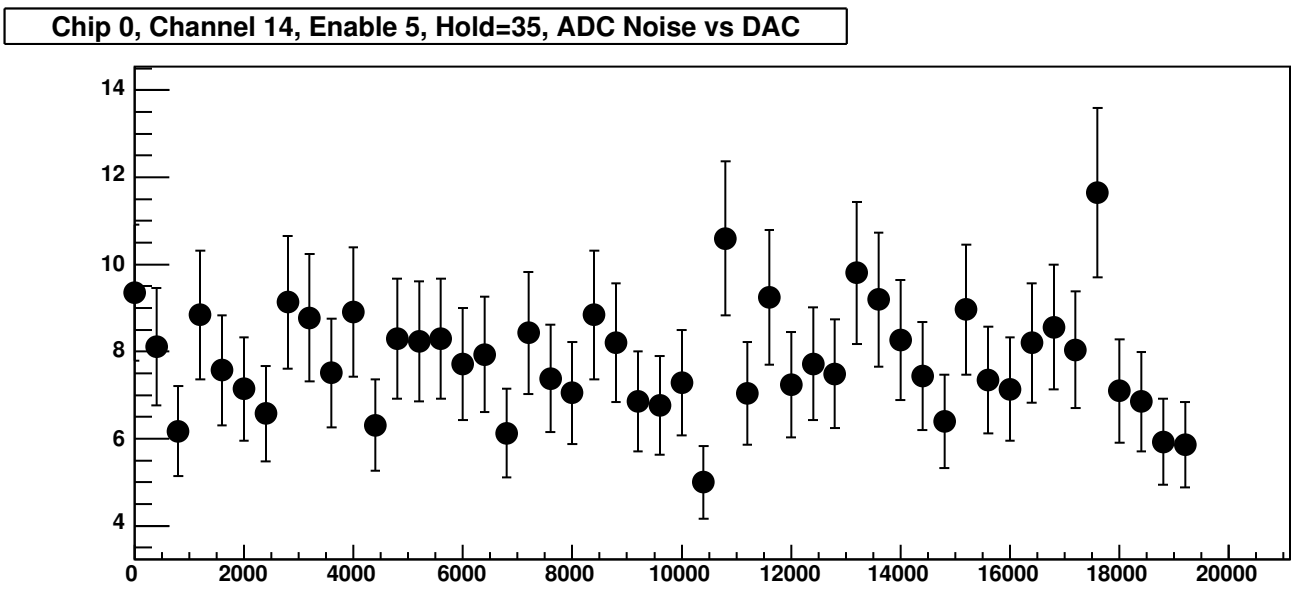
Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC

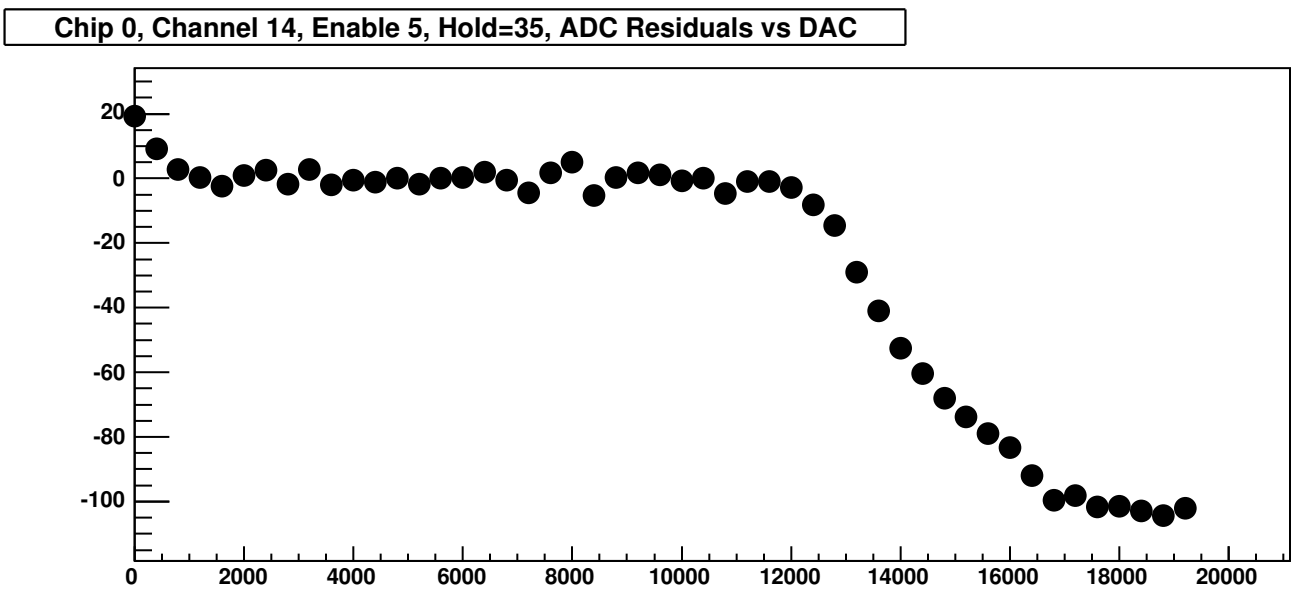


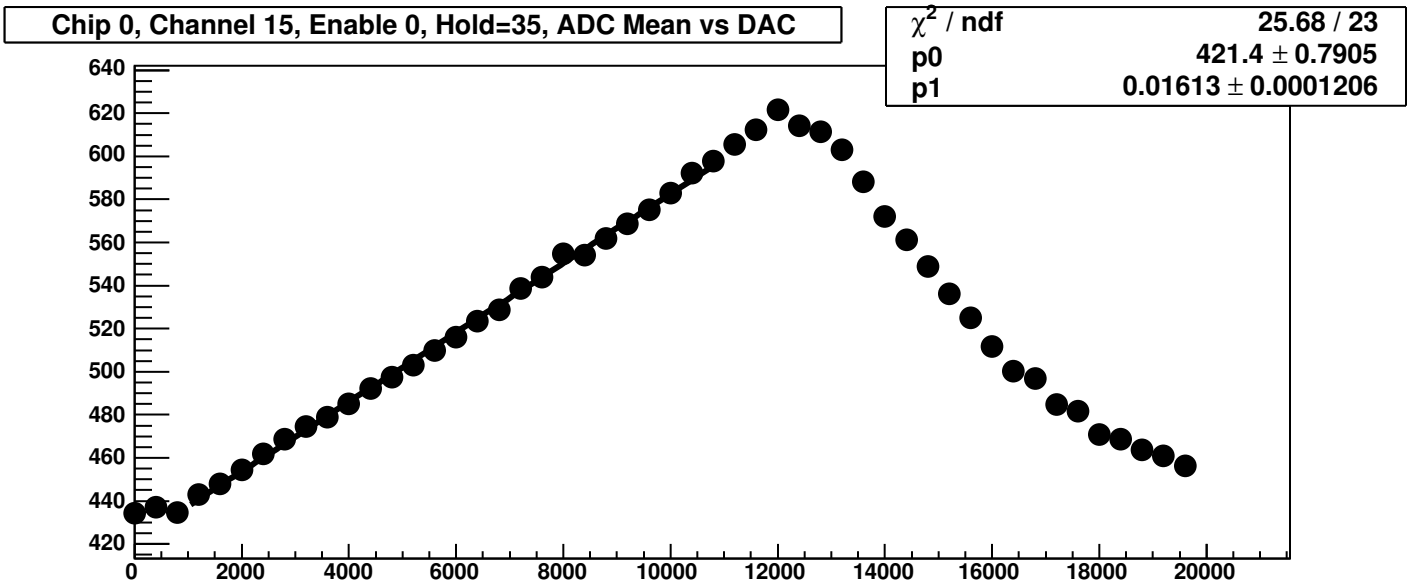
Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

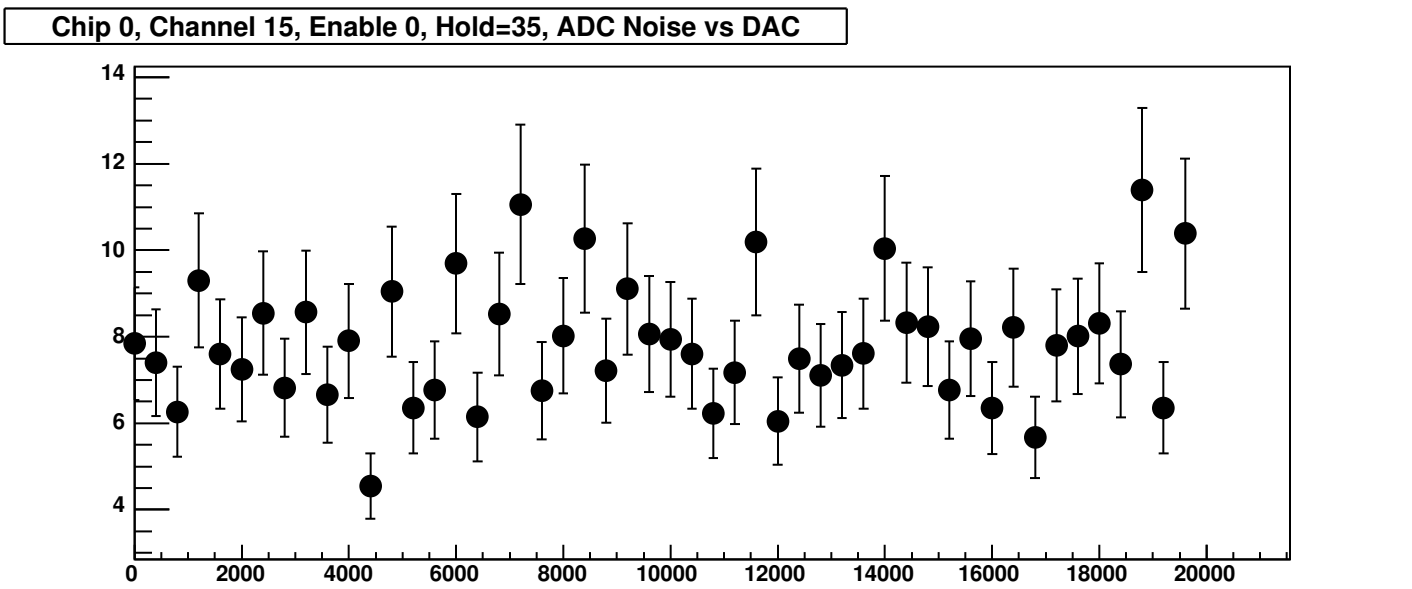


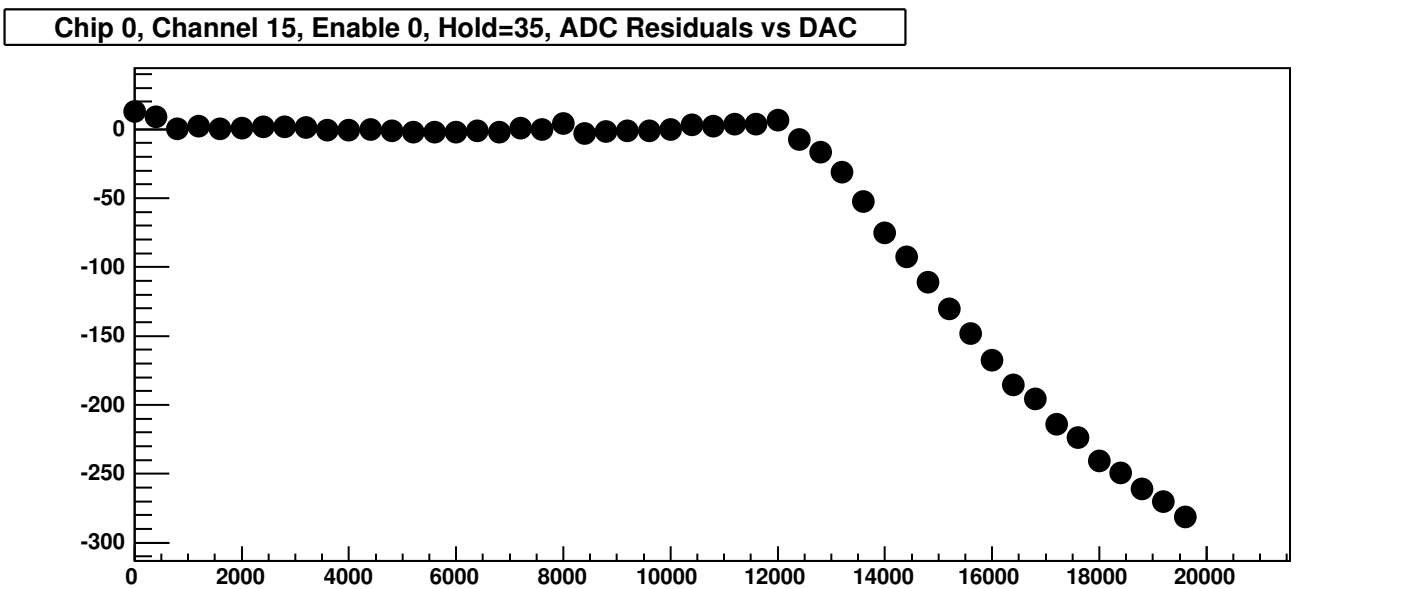

Chip 0, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 39.9 / 23
p0 317.7 ± 0.8083
p1 0.0184 ± 0.0001172
550
500
450
400
350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

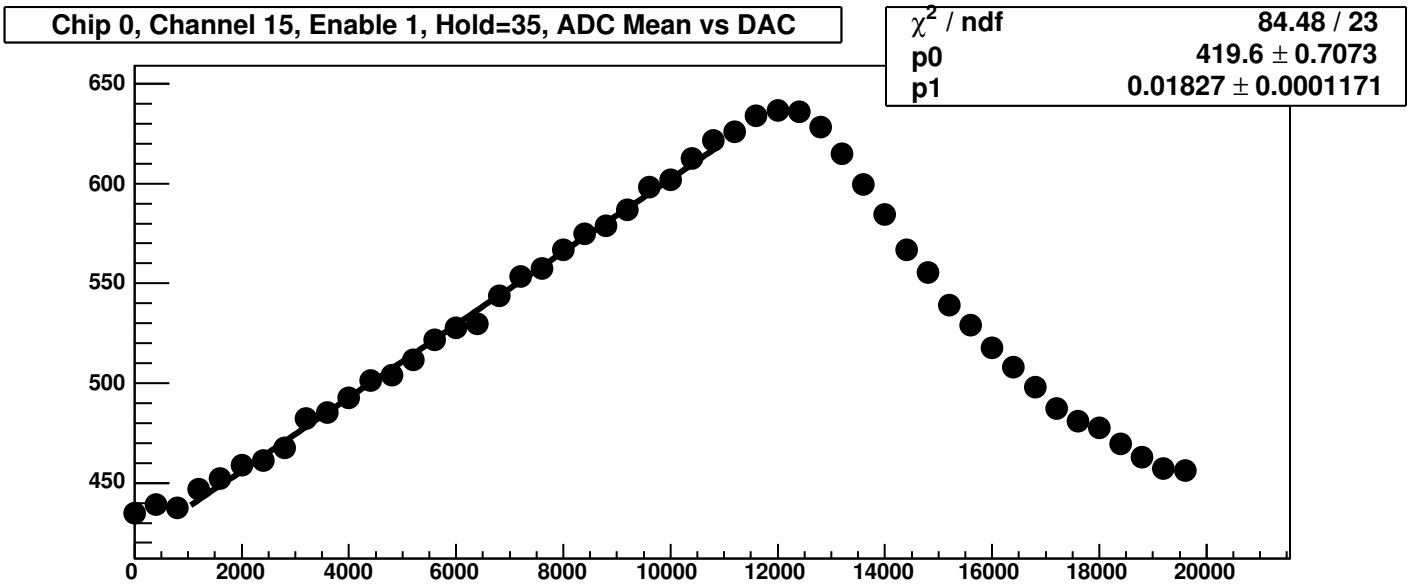

Chip 0, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

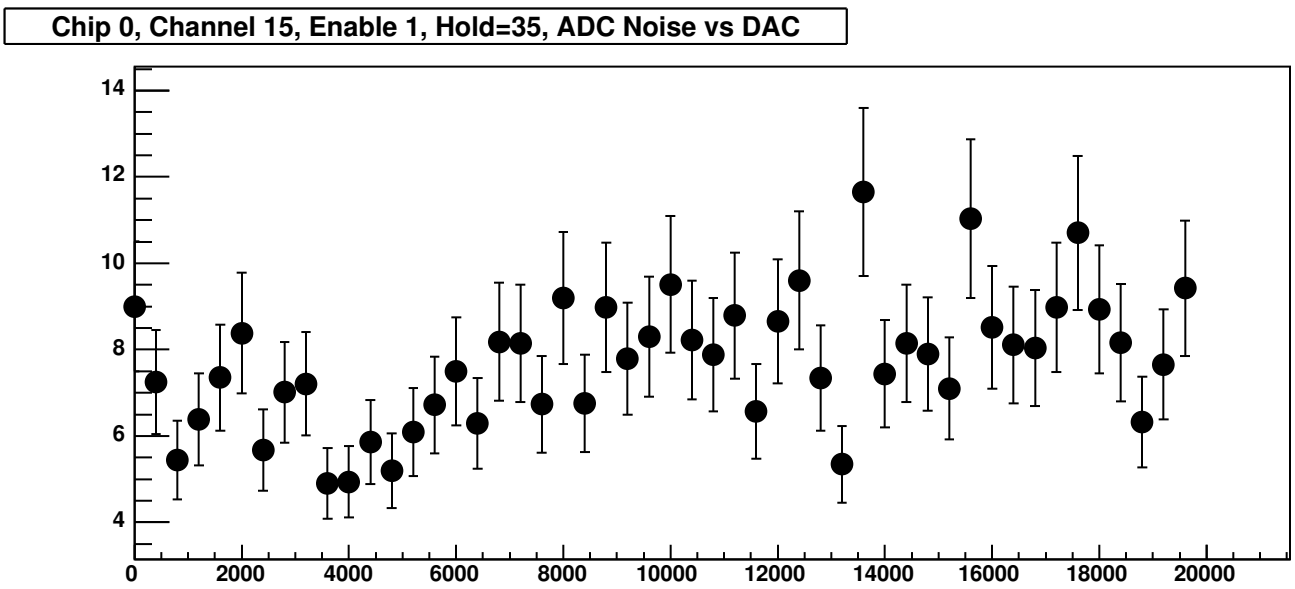

Chip 0, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

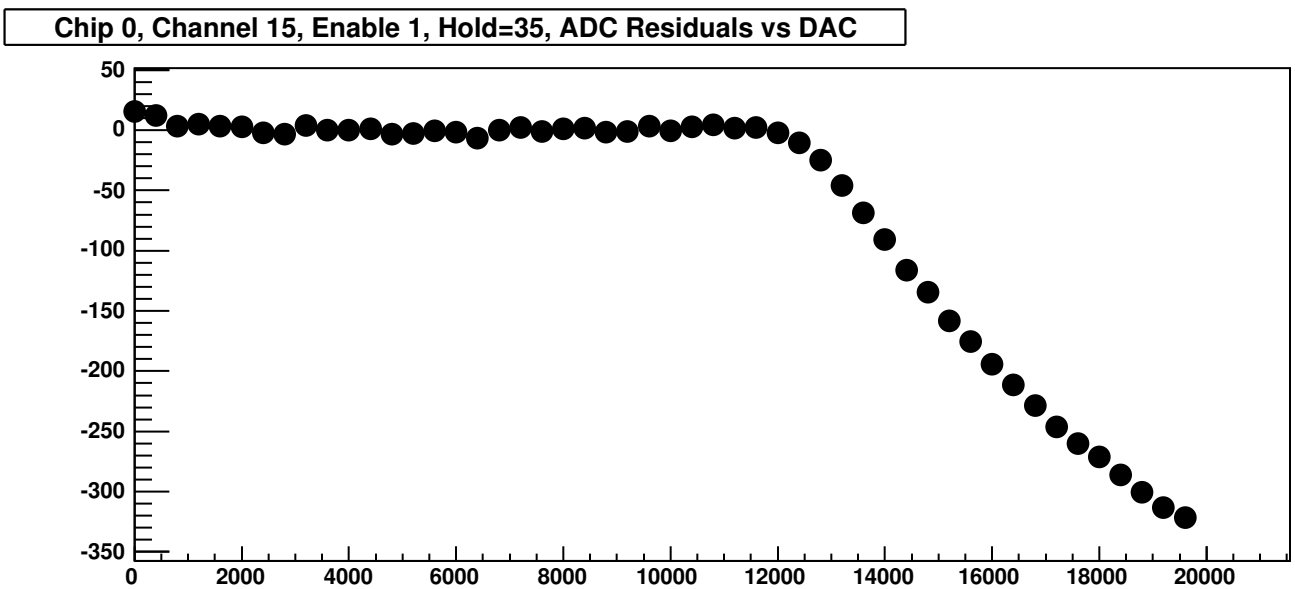

Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 25.68 / 23
p0 421.4 ± 0.7905
p1 0.01613 ± 0.0001206
640
620
600
580
560
540
520
500
480
460
440
420
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

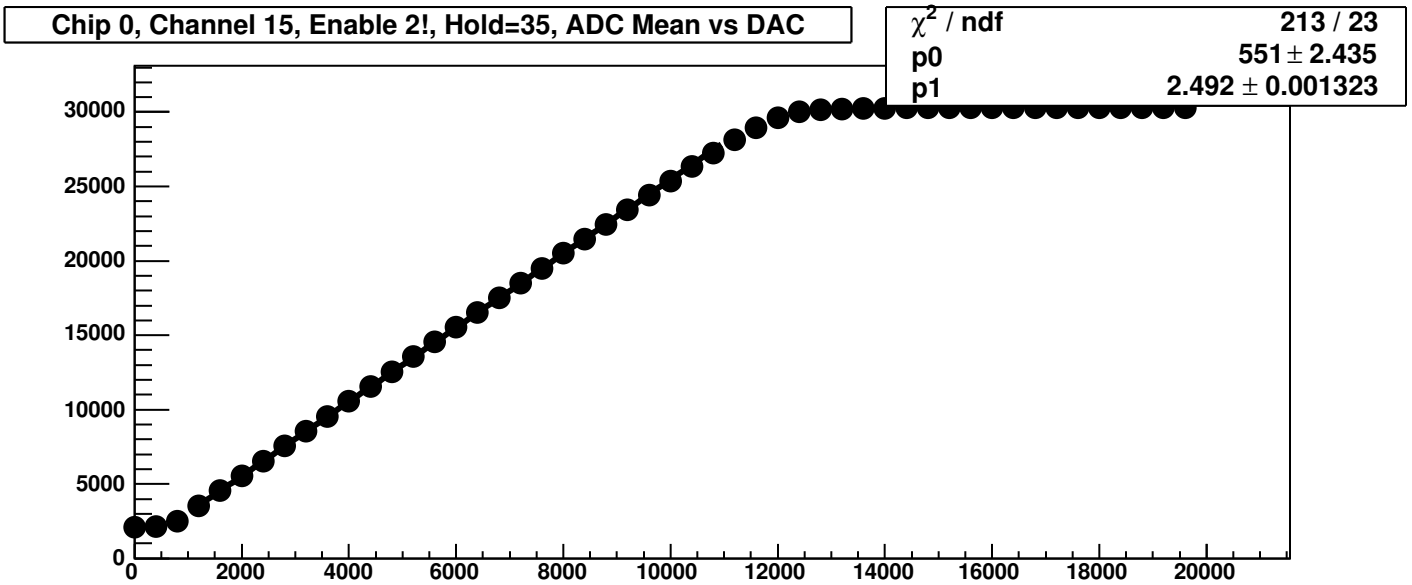

Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

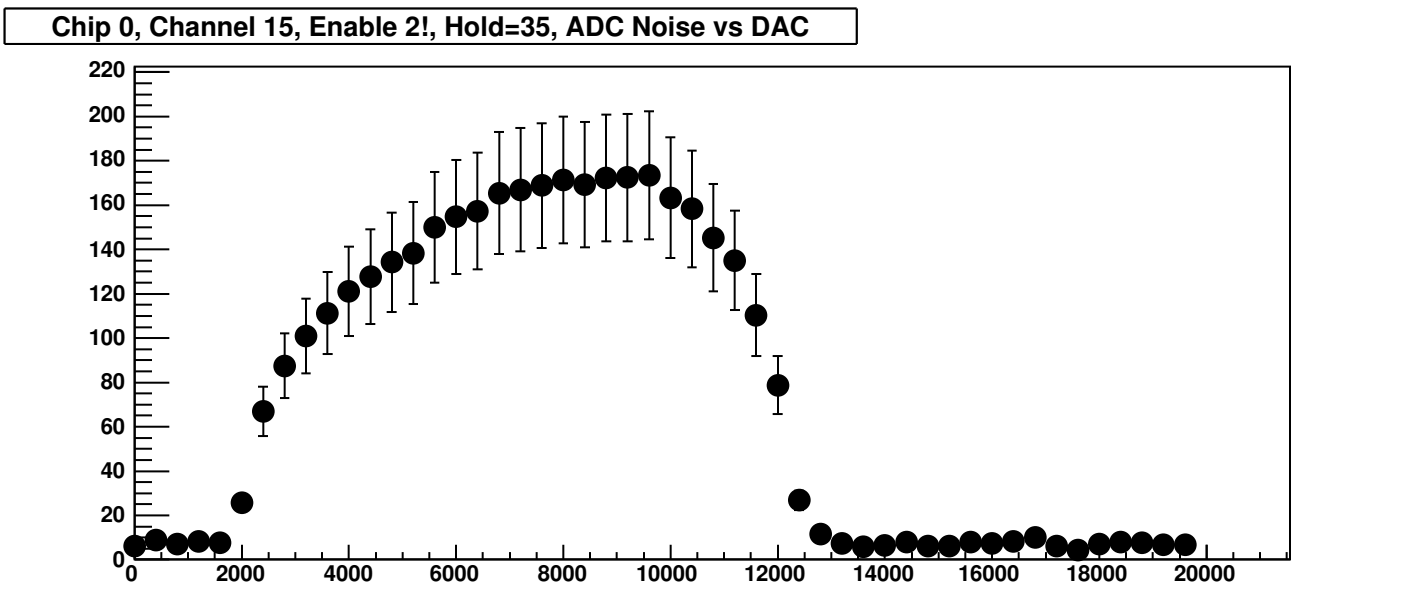

Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

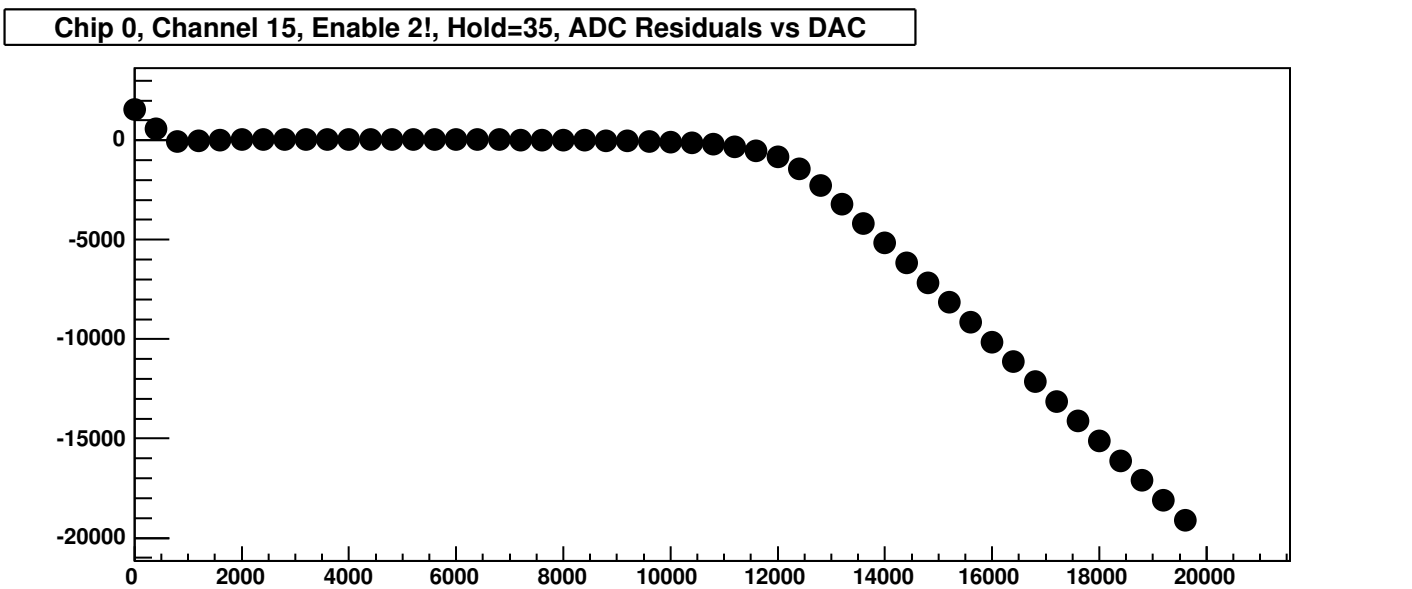

Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 84.48 / 23
p0 419.6 ± 0.7073
p1 0.01827 ± 0.0001171
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

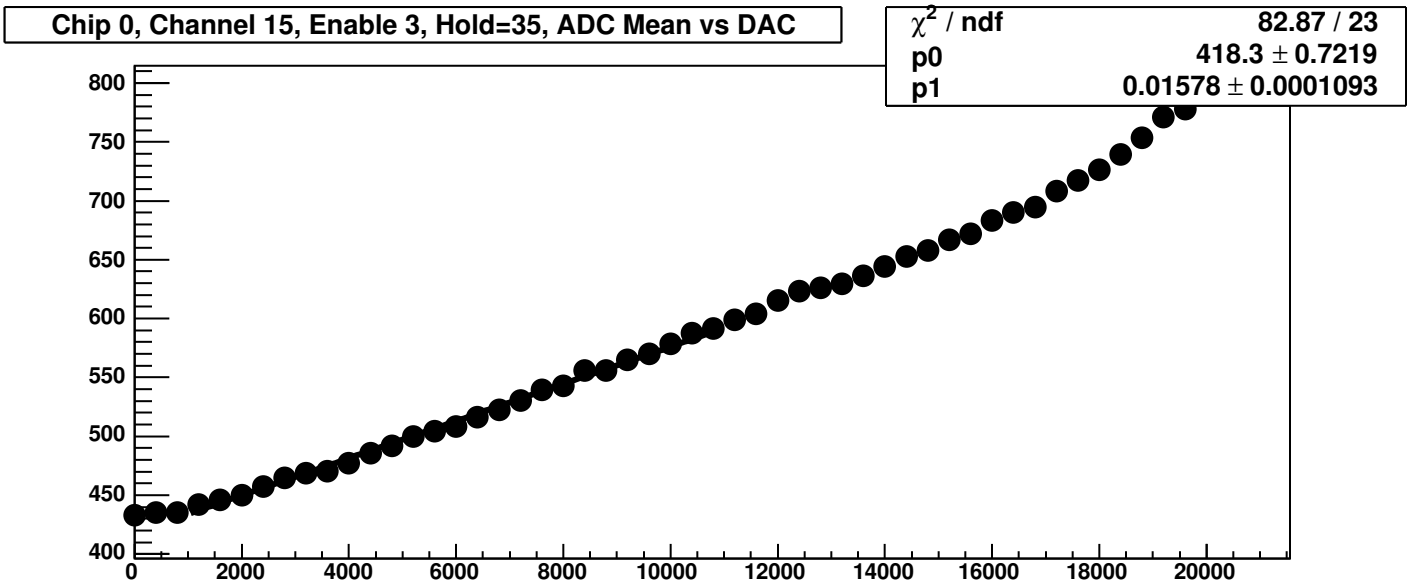

Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

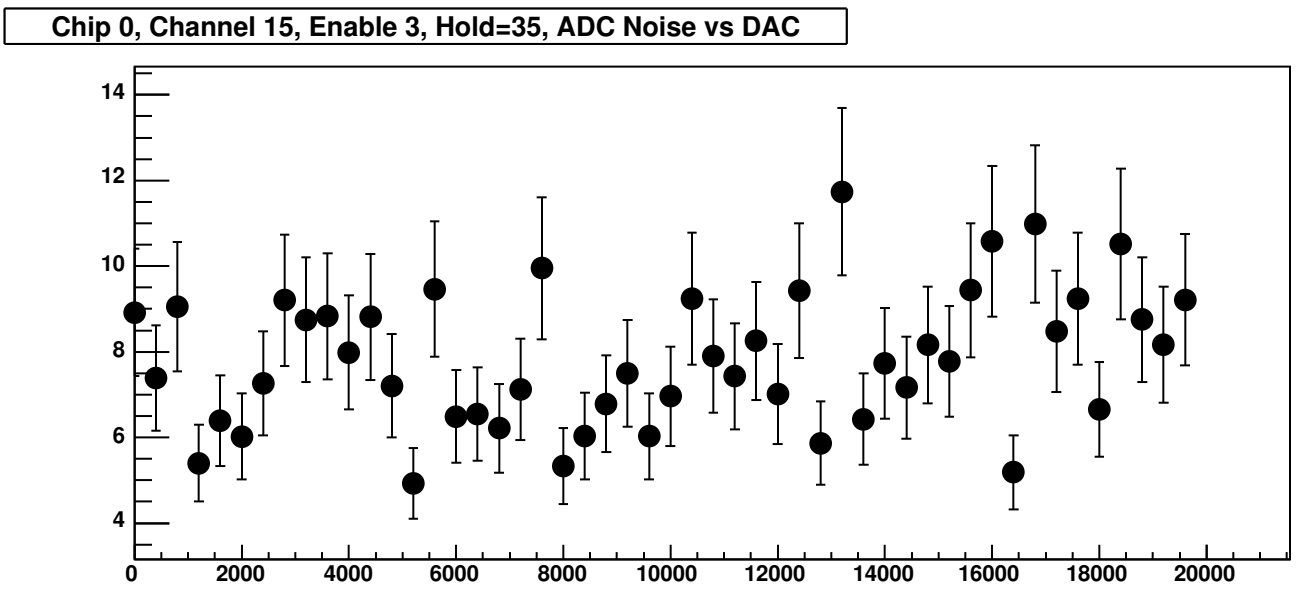

Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

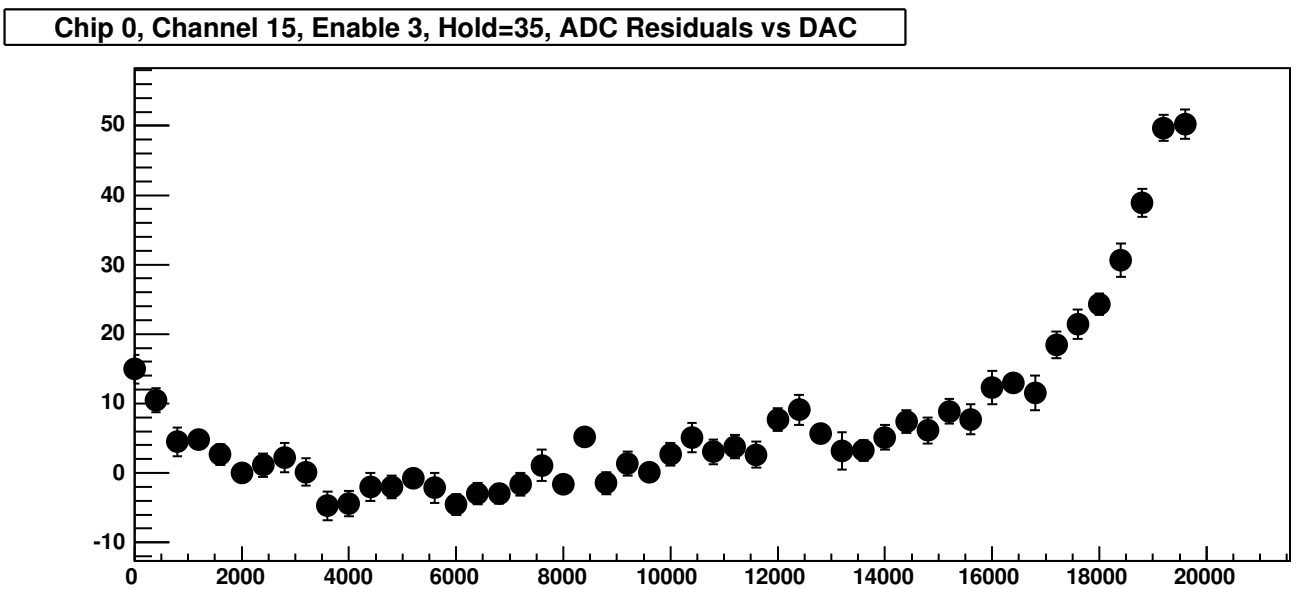

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 213 / 23
p0 551 ± 2.435
p1 2.492 ± 0.001323
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

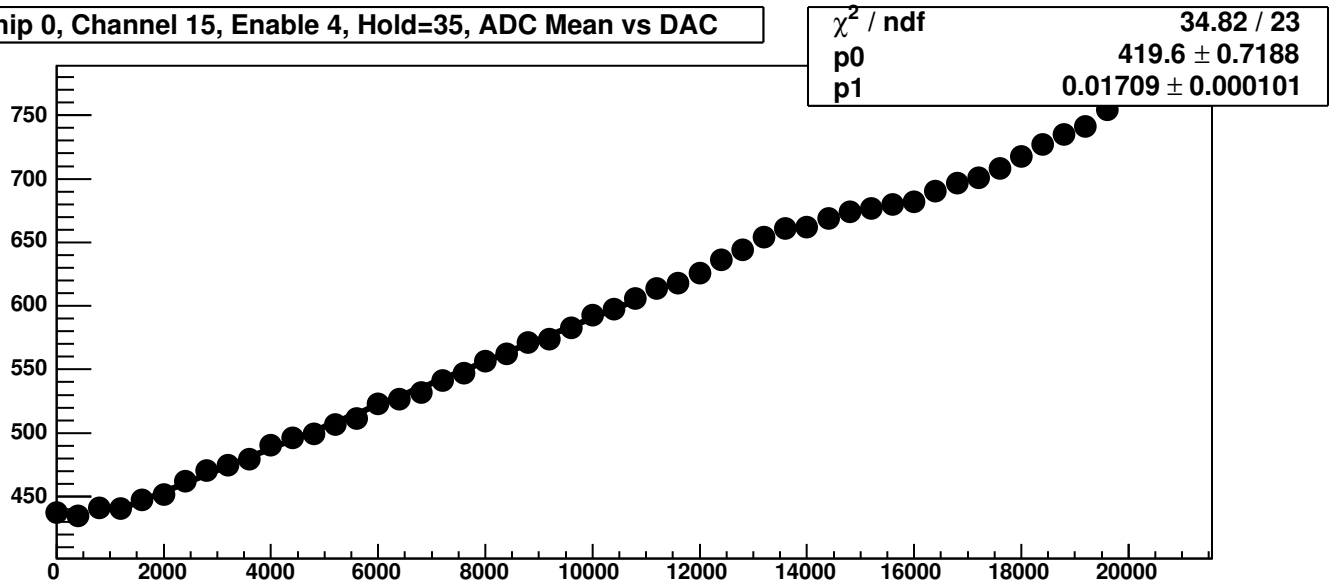

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 82.87 / 23
p0 418.3 ± 0.7219
p1 0.01578 ± 0.0001093
800
750
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

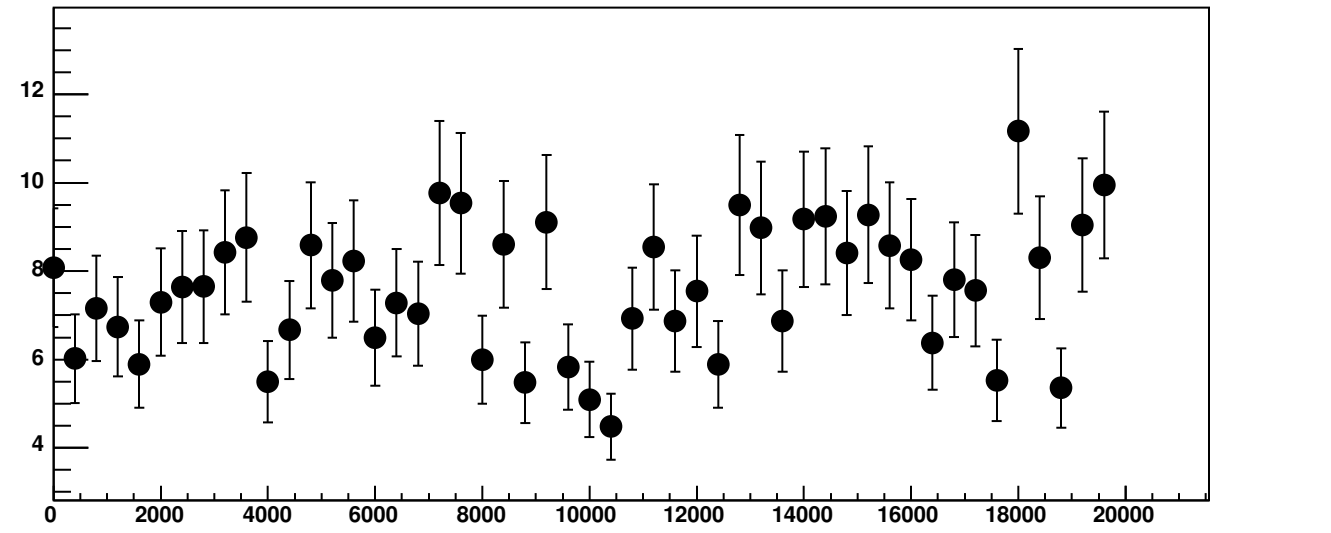

Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

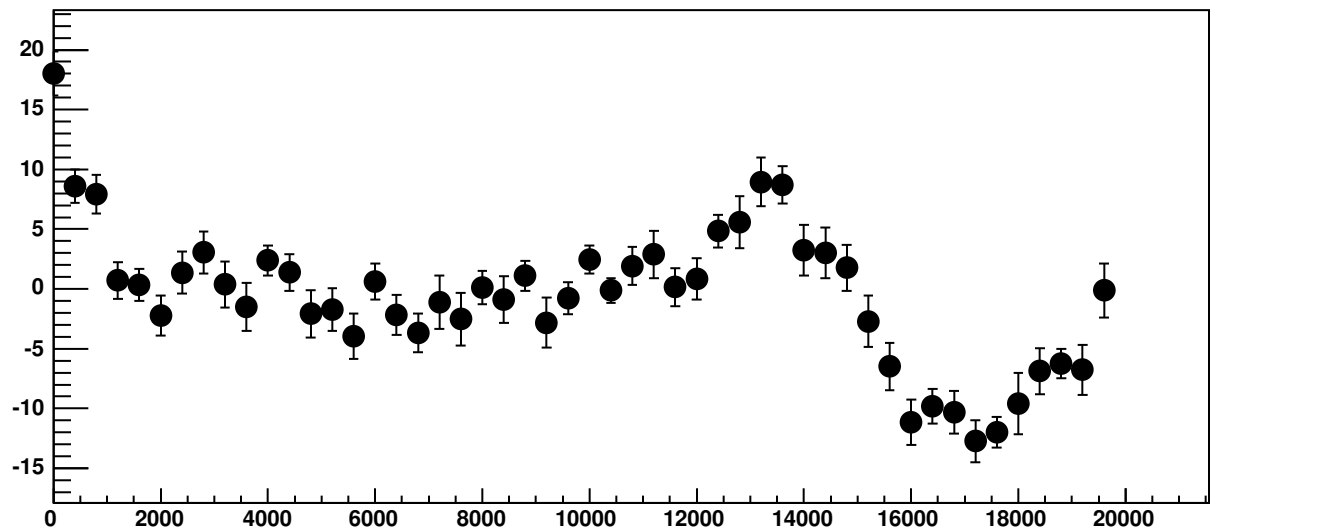
Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

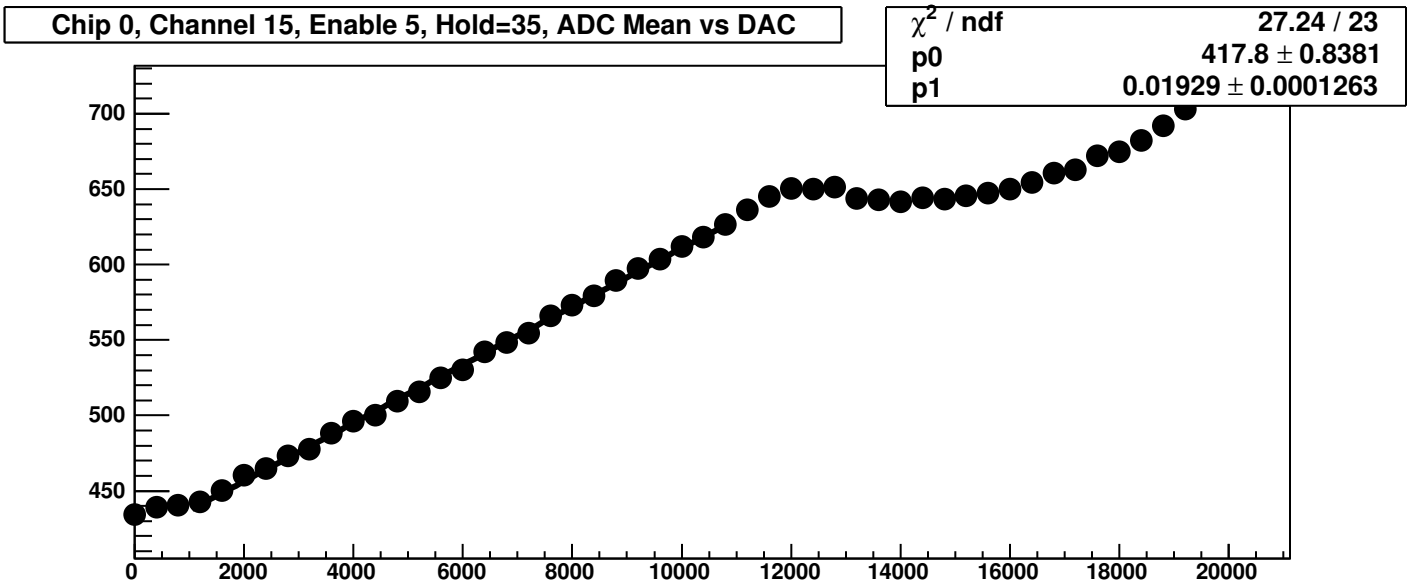


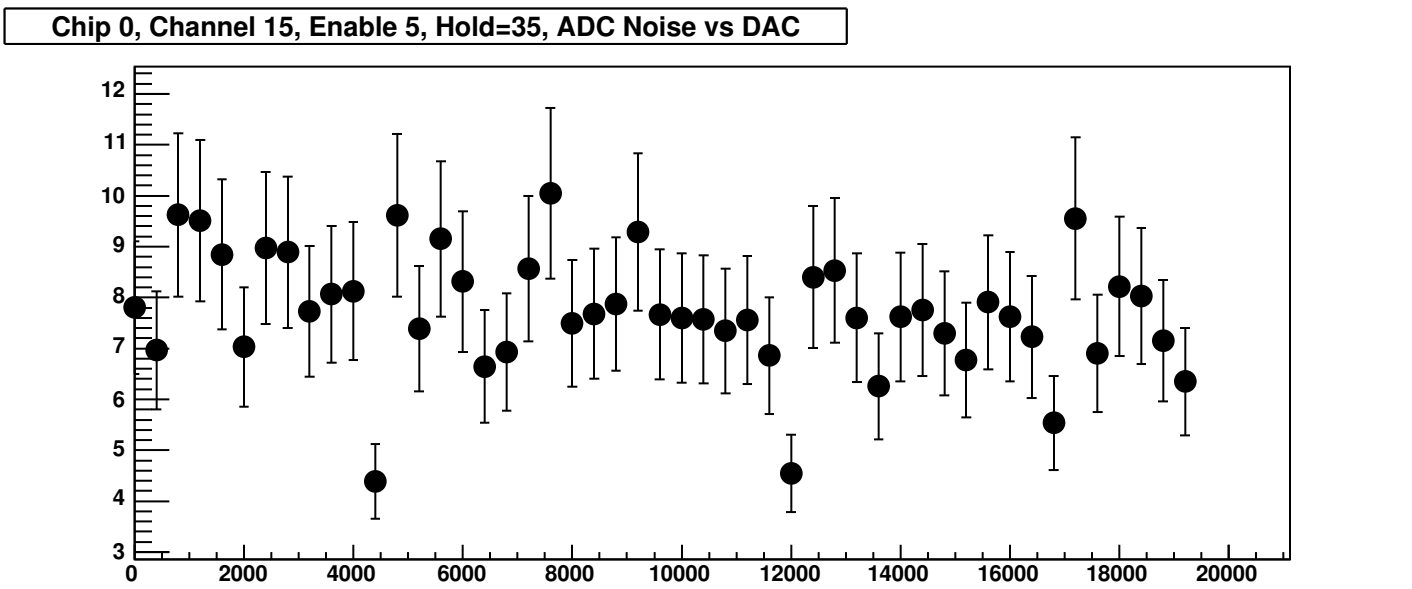
Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

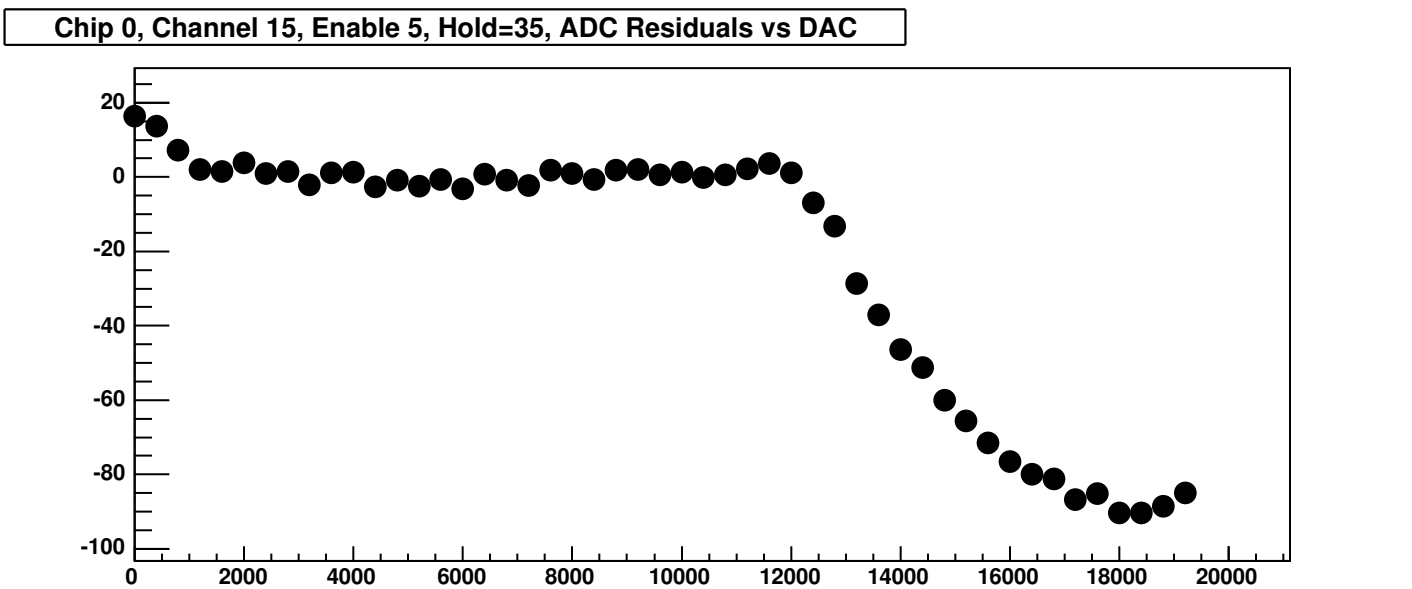


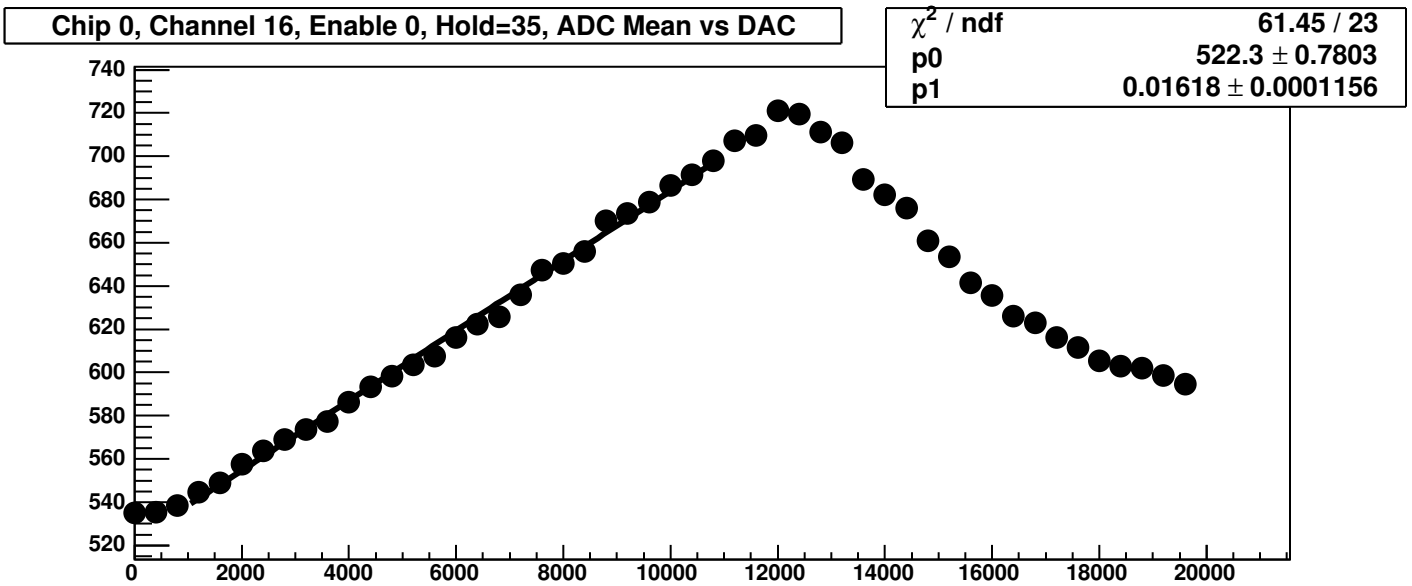
Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

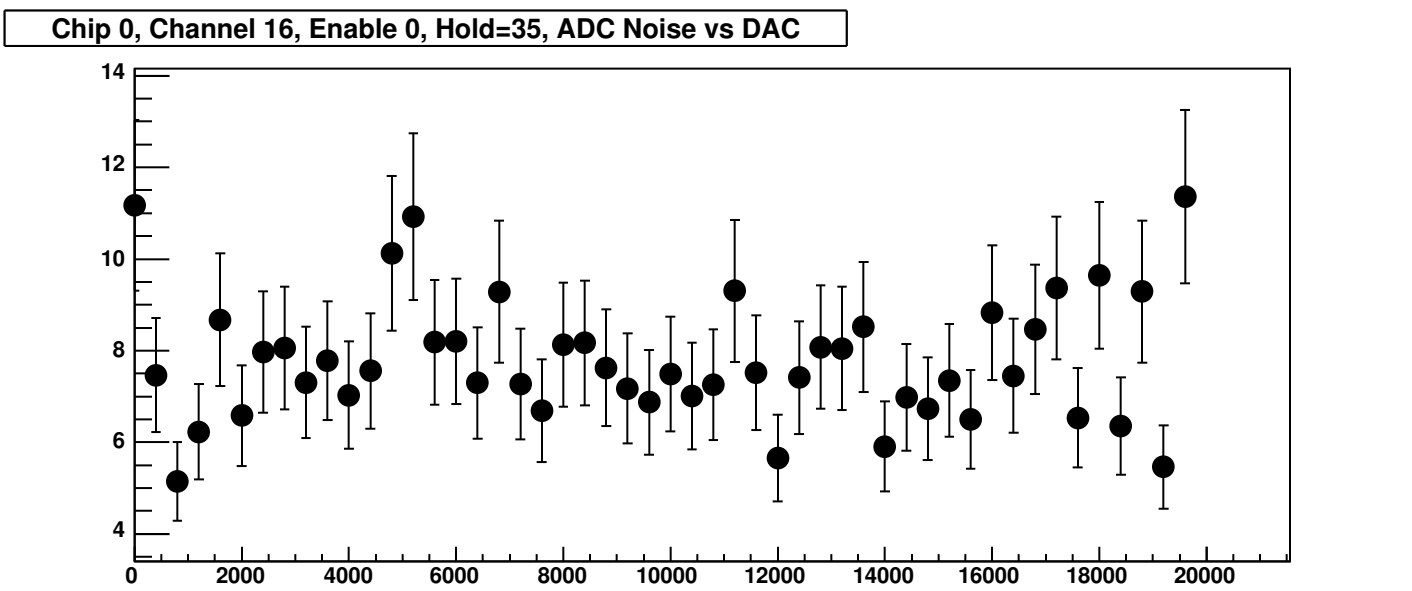


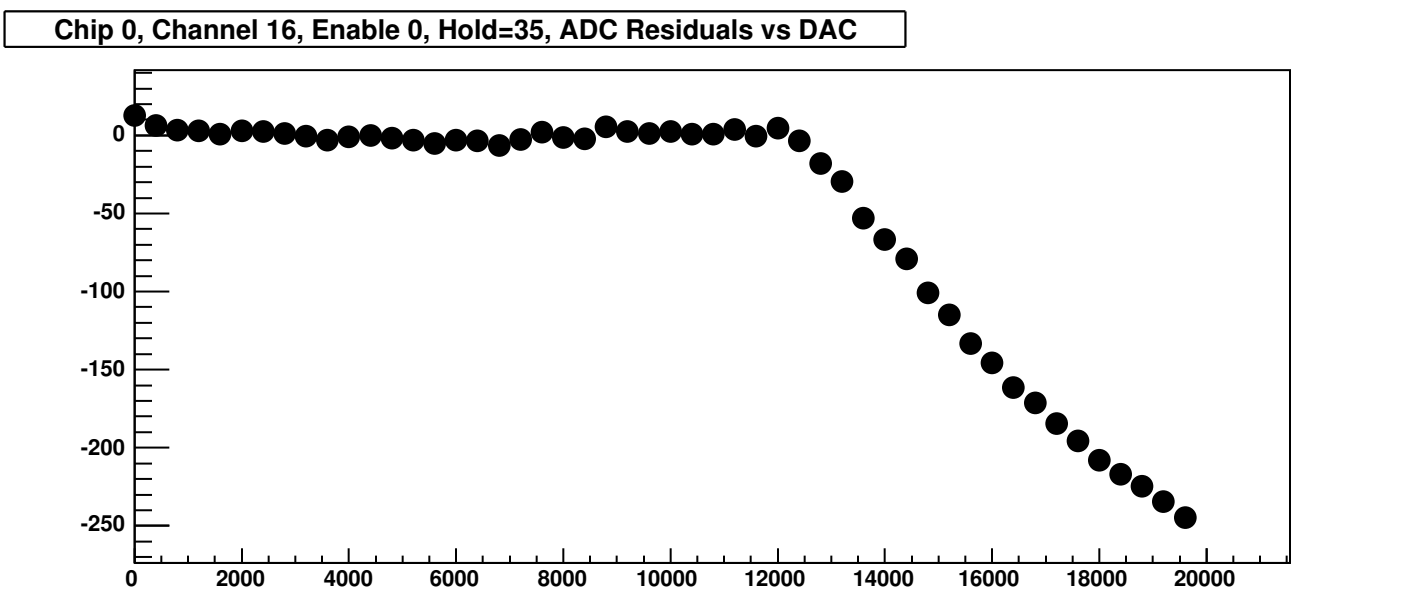

Chip 0, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 27.24 / 23
p0 417.8 ± 0.8381
p1 0.01929 ± 0.0001263
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

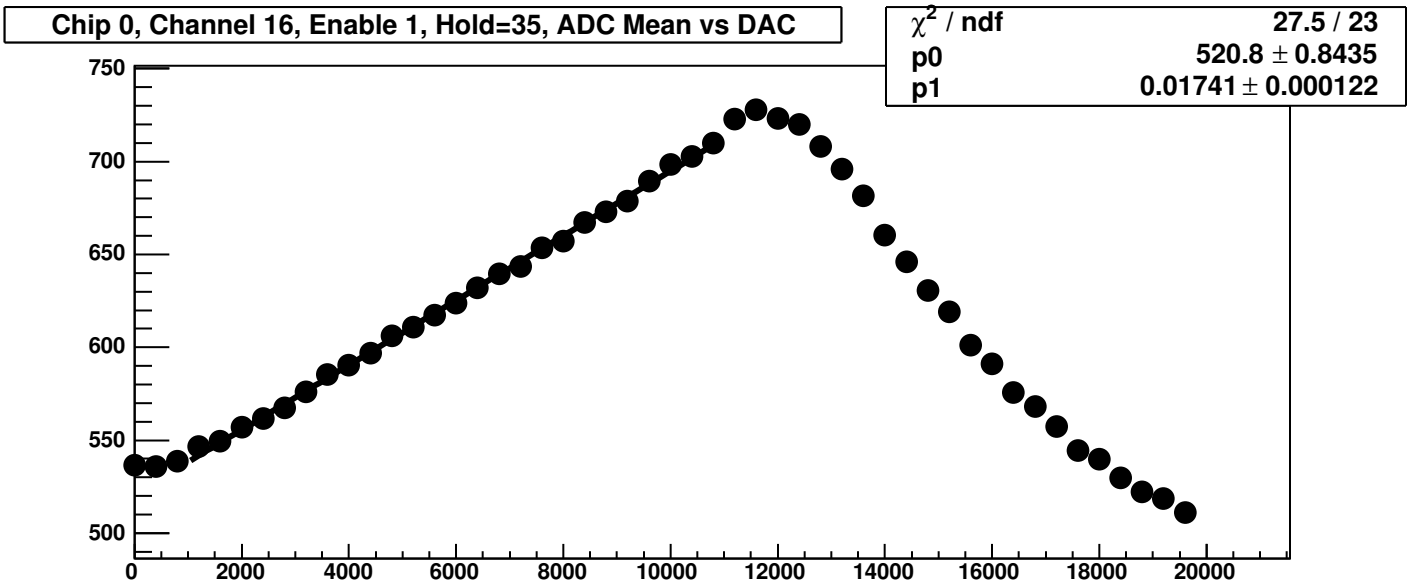

Chip 0, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

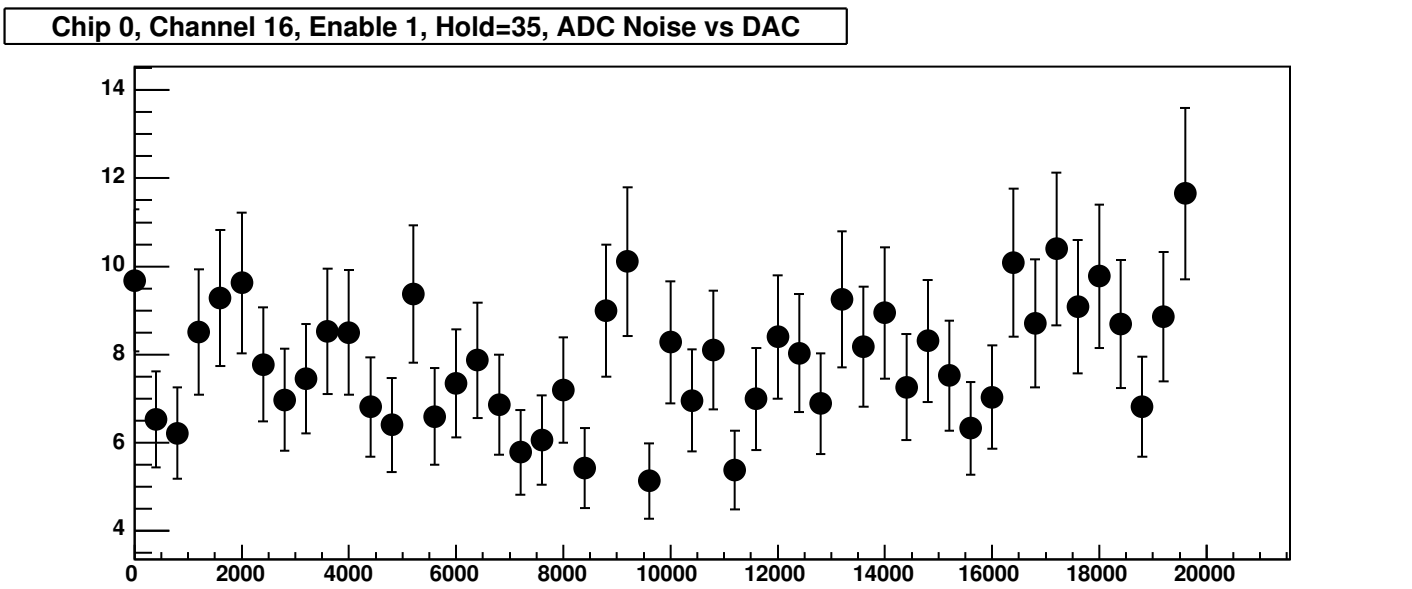

Chip 0, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

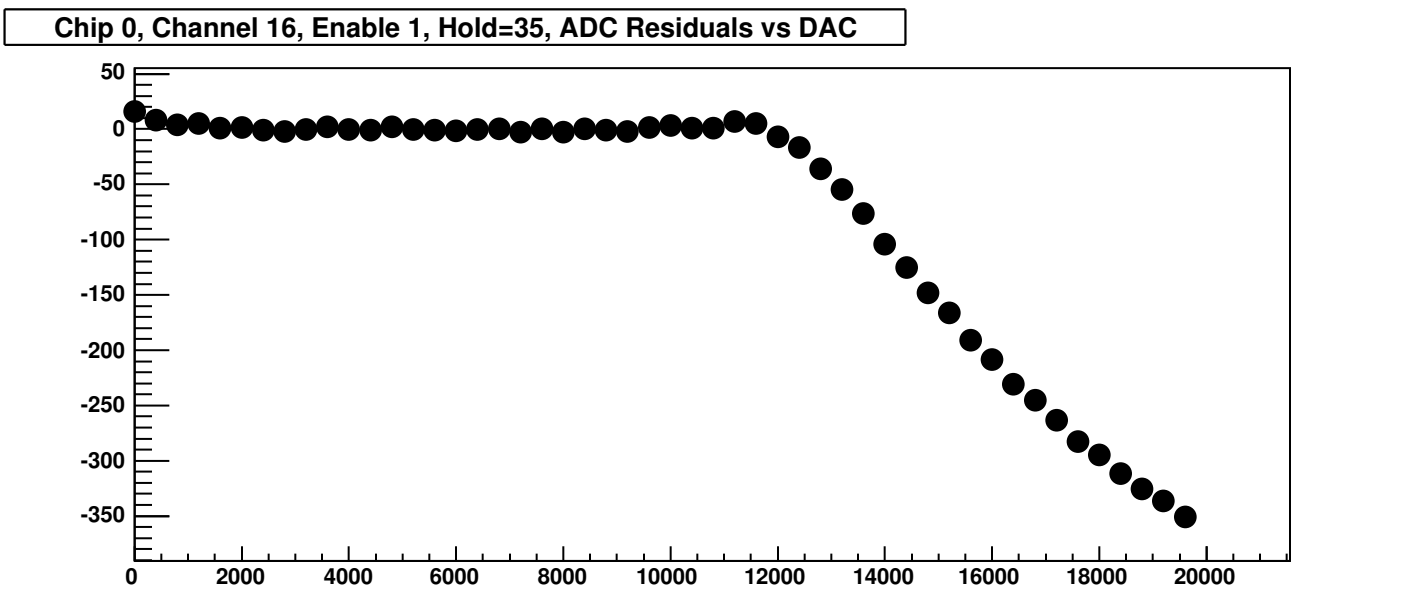

Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 61.45 / 23
p0 522.3 ± 0.7803
p1 0.01618 ± 0.0001156
740
720
700
680
660
640
620
600
580
560
540
520
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

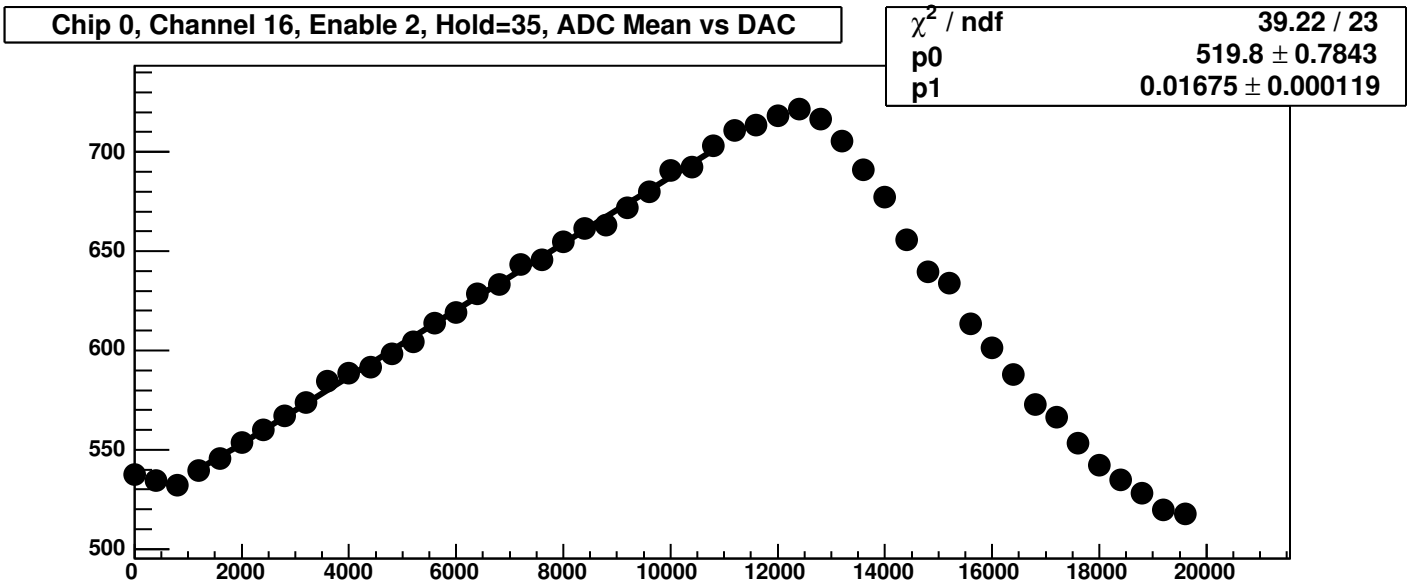

Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

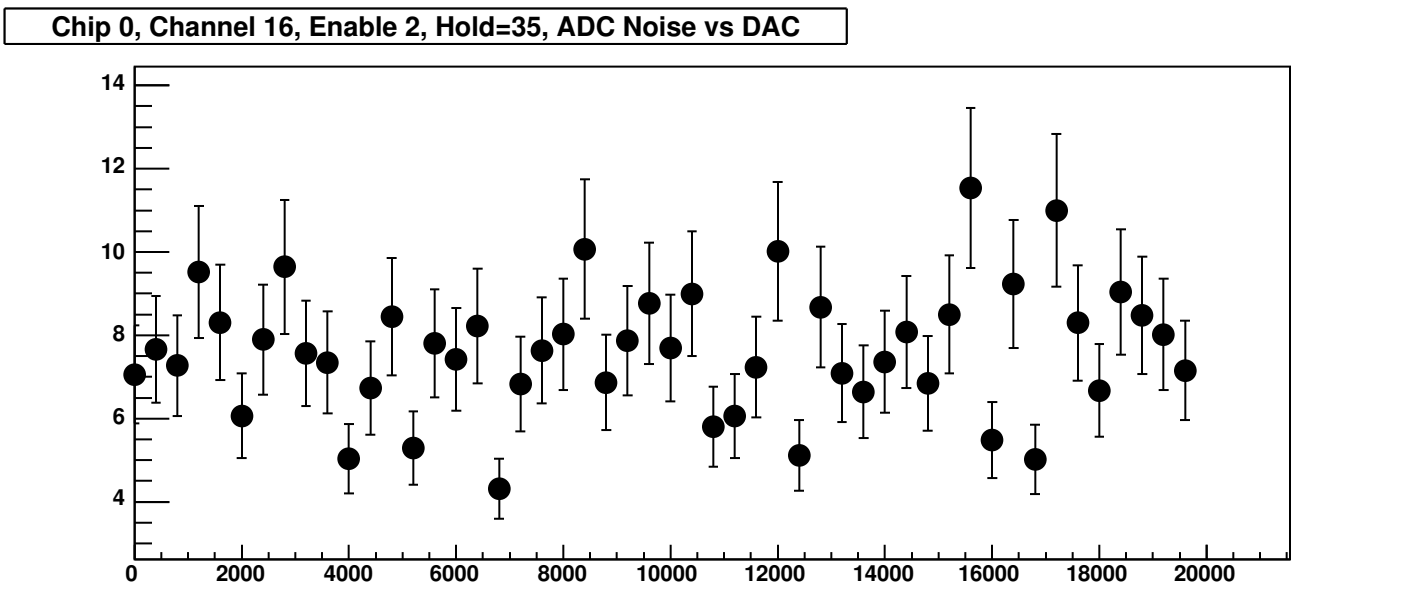

Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

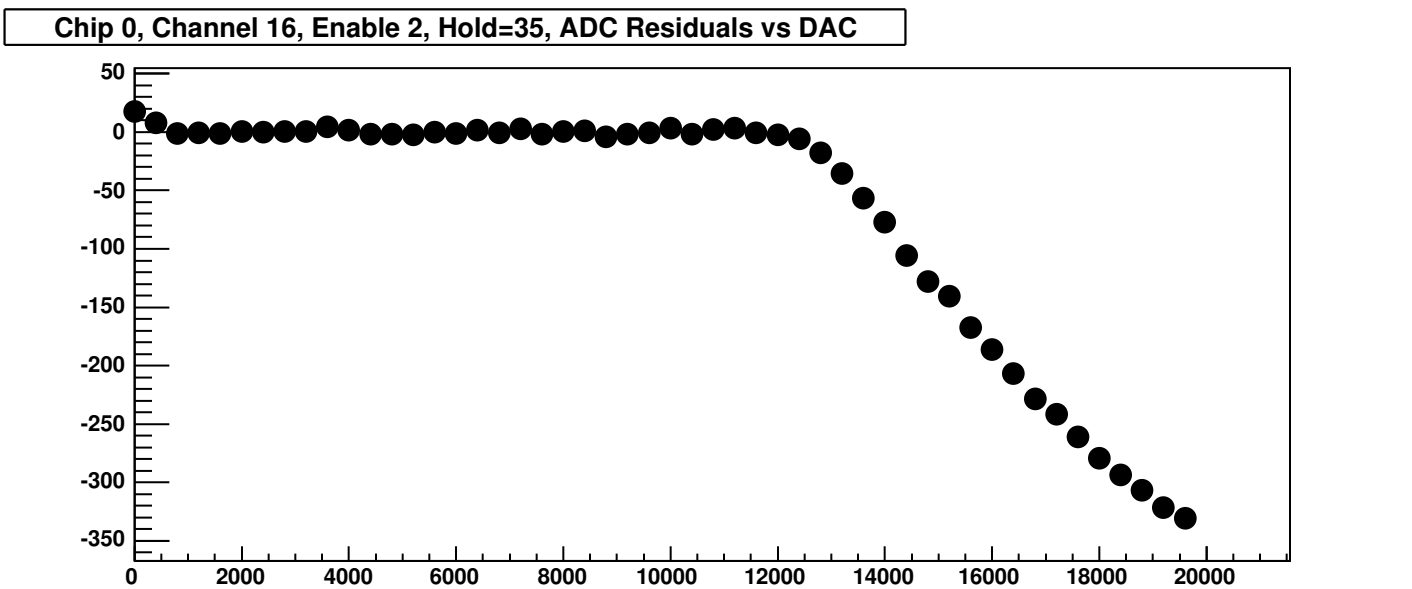

Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.5 / 23
p0 520.8 ± 0.8435
p1 0.01741 ± 0.000122
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

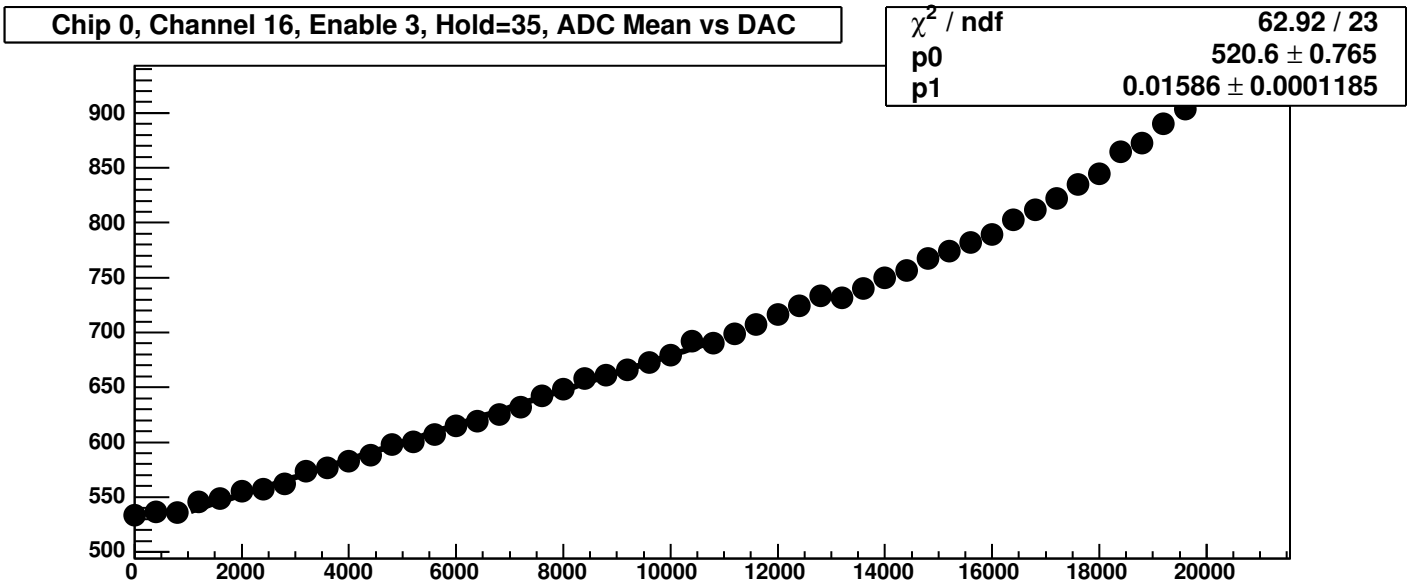

Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

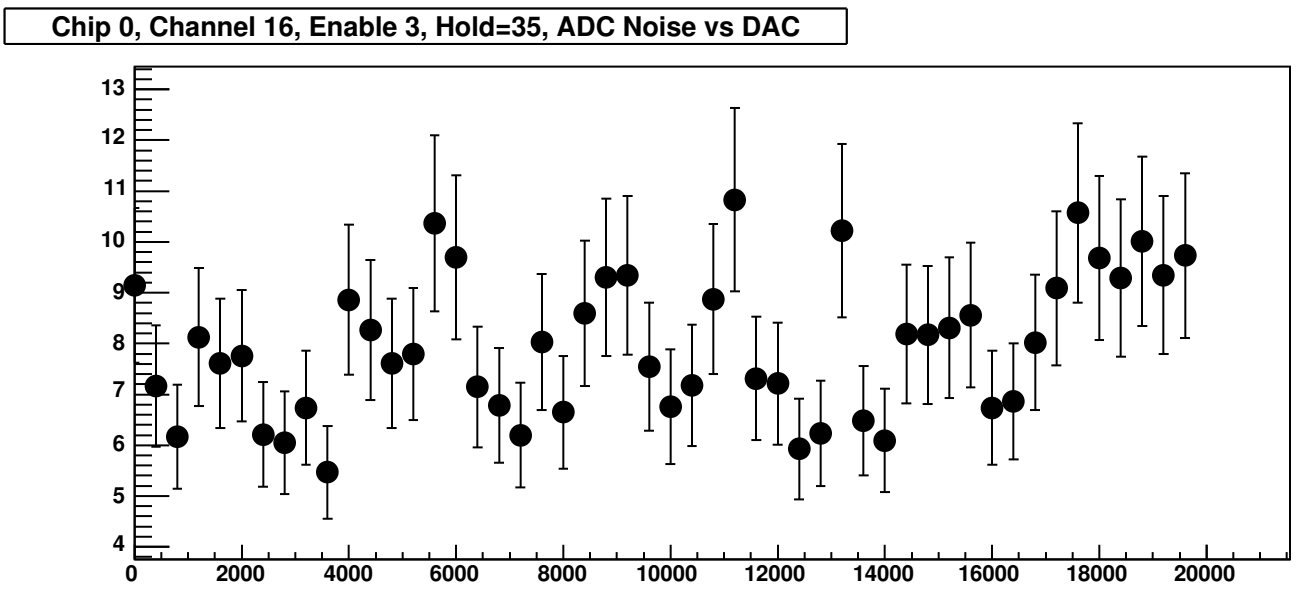

Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

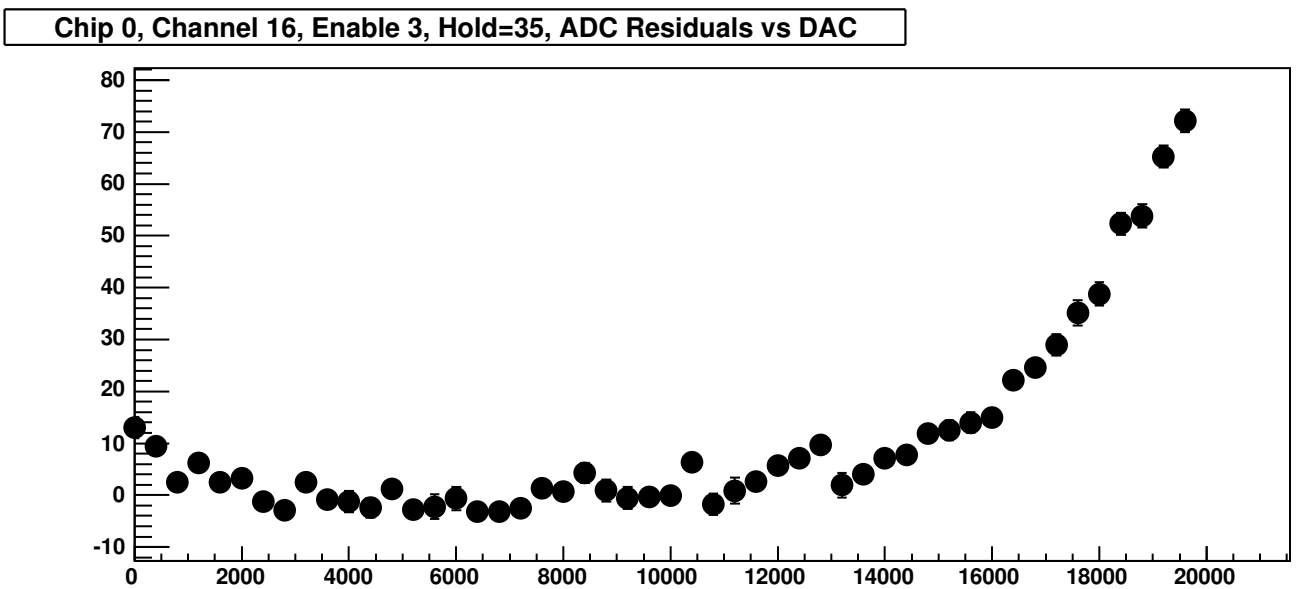

Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39.22 / 23
p0 519.8 ± 0.7843
p1 0.01675 ± 0.000119
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

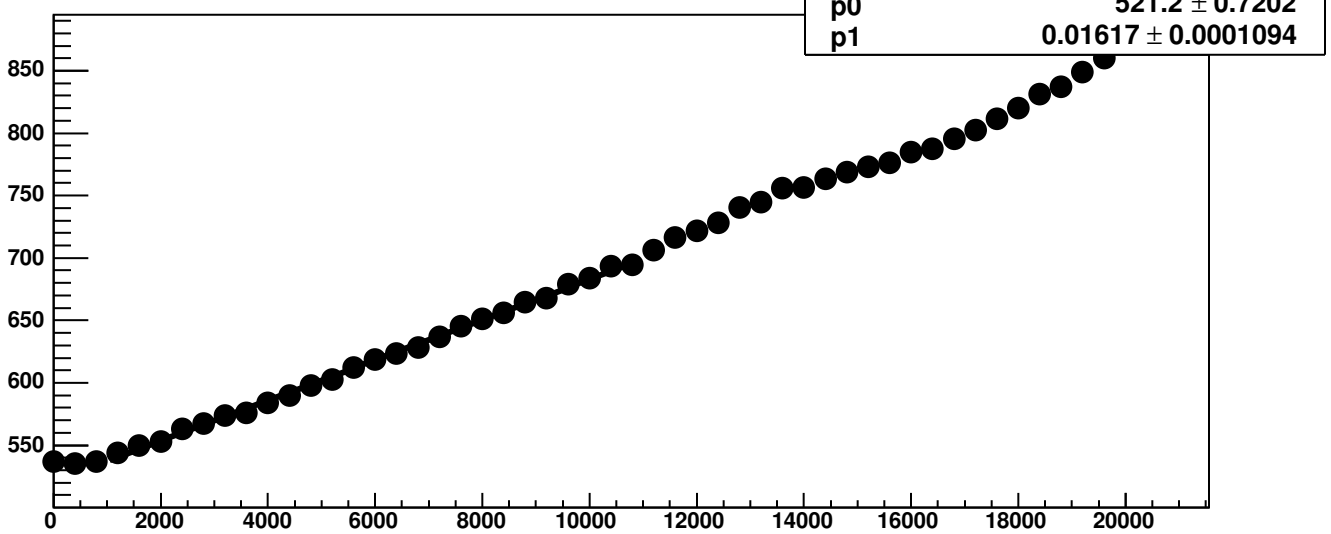

Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 62.92 / 23
p0 520.6 ± 0.765
p1 0.01586 ± 0.0001185
900
850
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

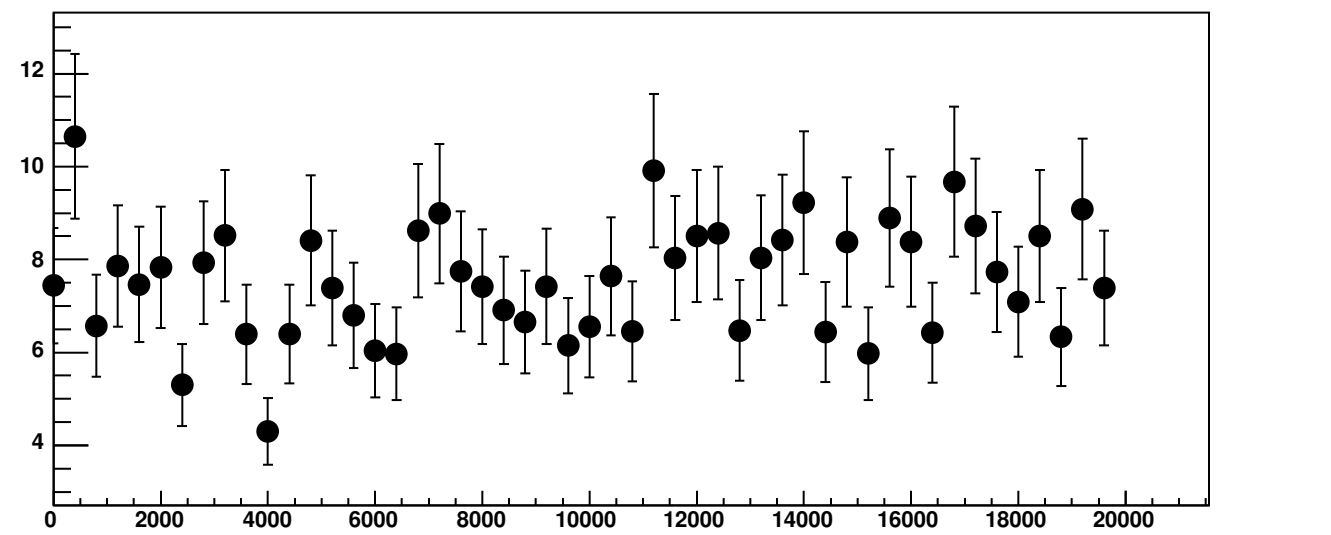

Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
80
70
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

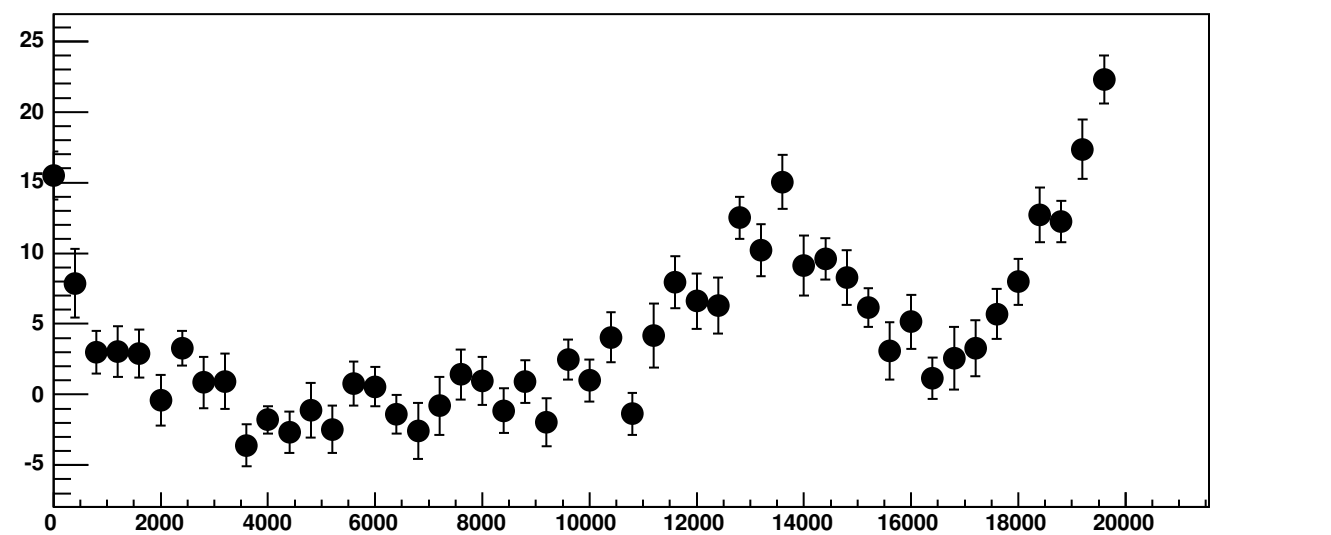
Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

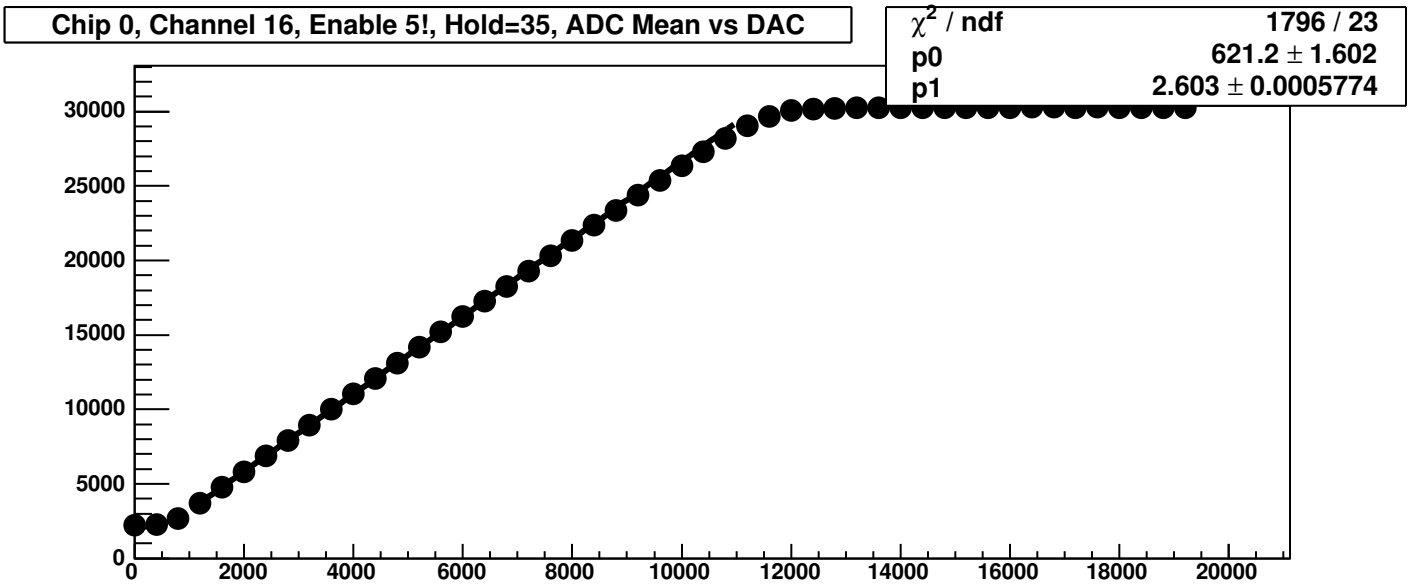


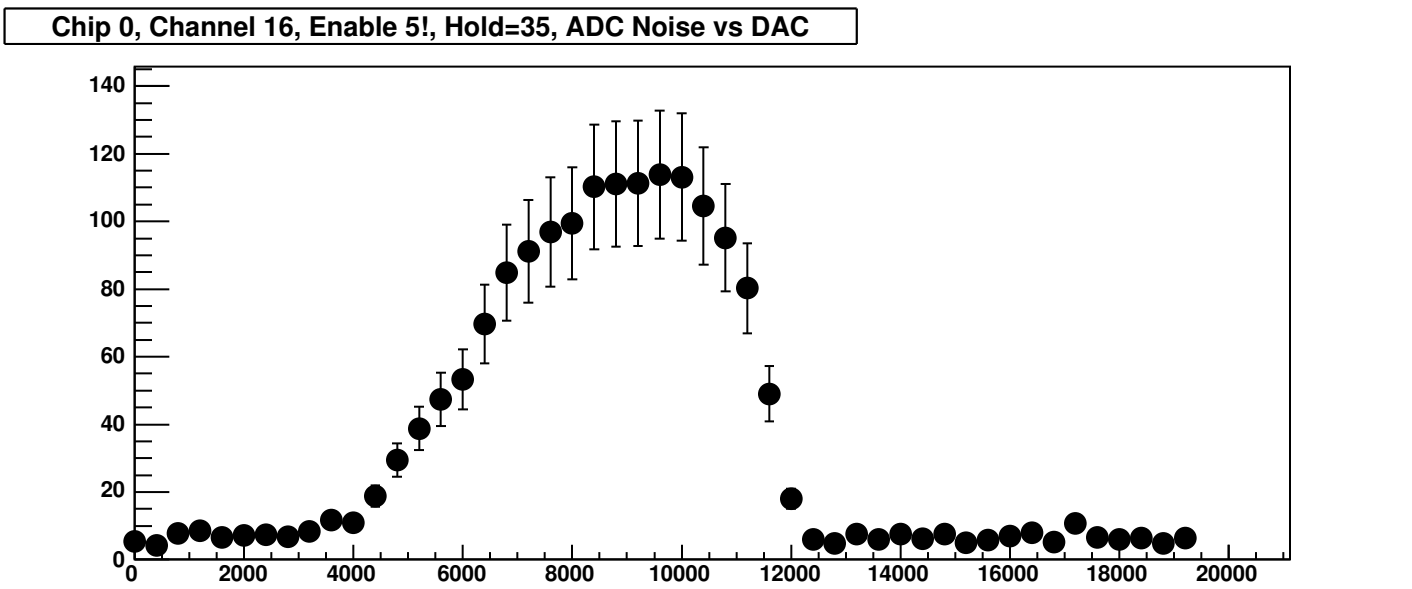
Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

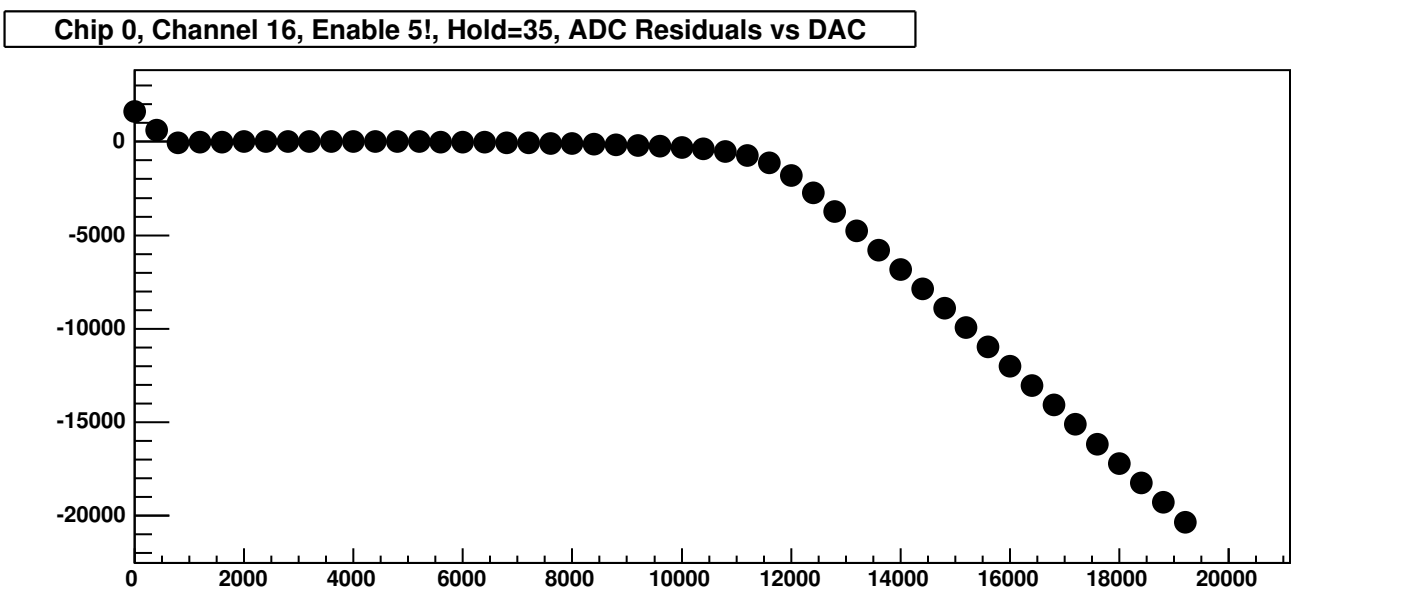


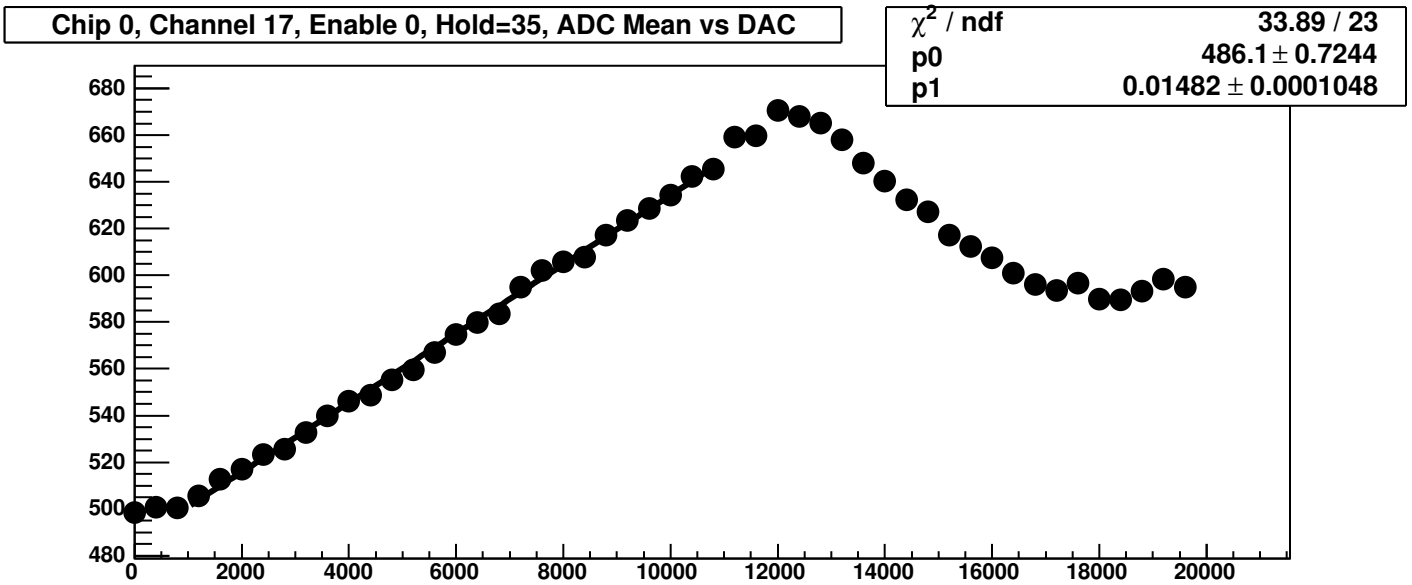
Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

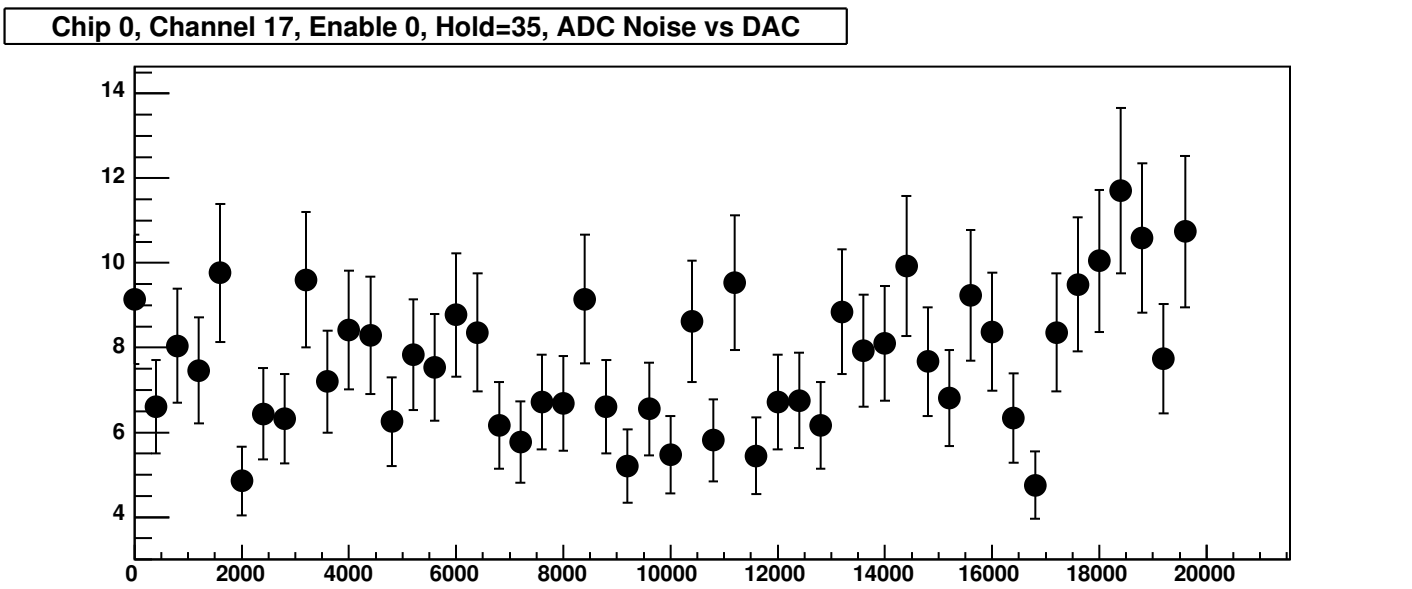


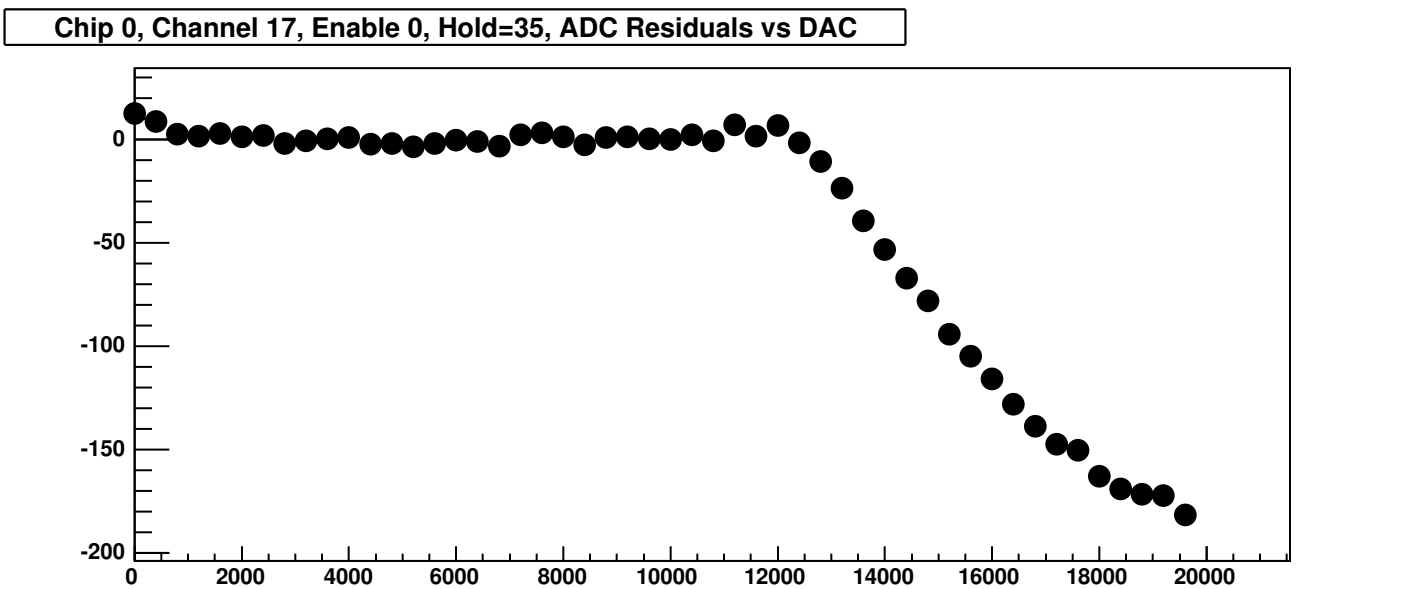

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1796 / 23
p0 621.2 ± 1.602
p1 2.603 ± 0.0005774
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

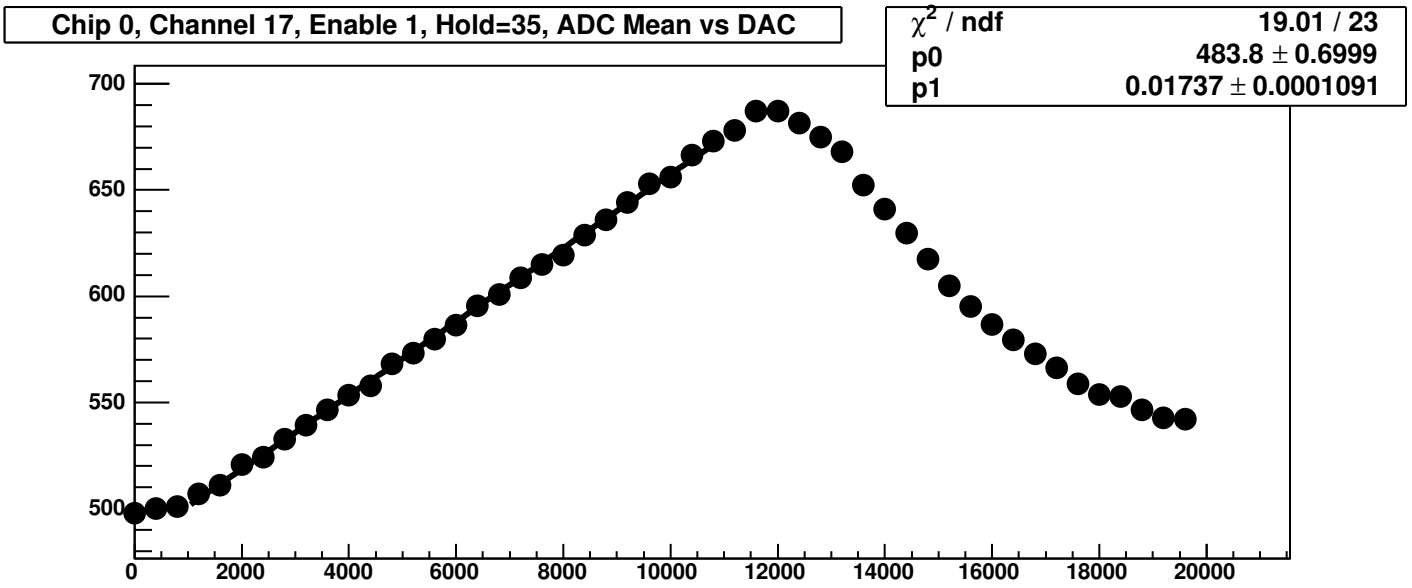

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

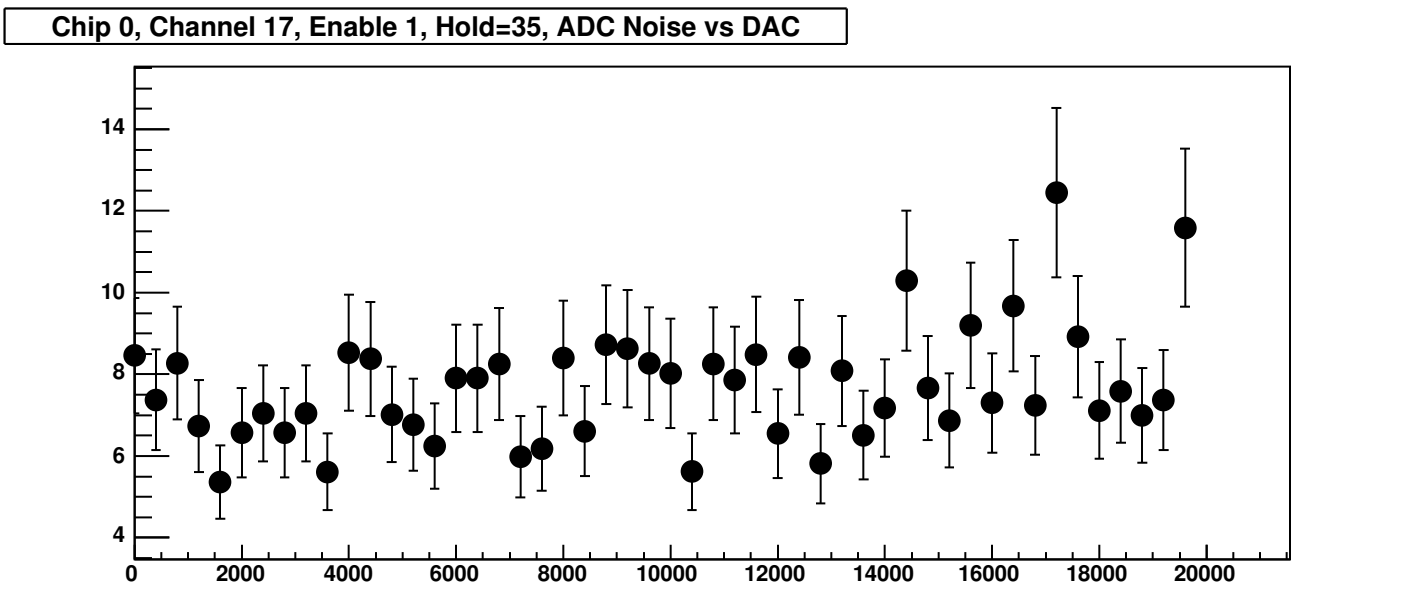

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

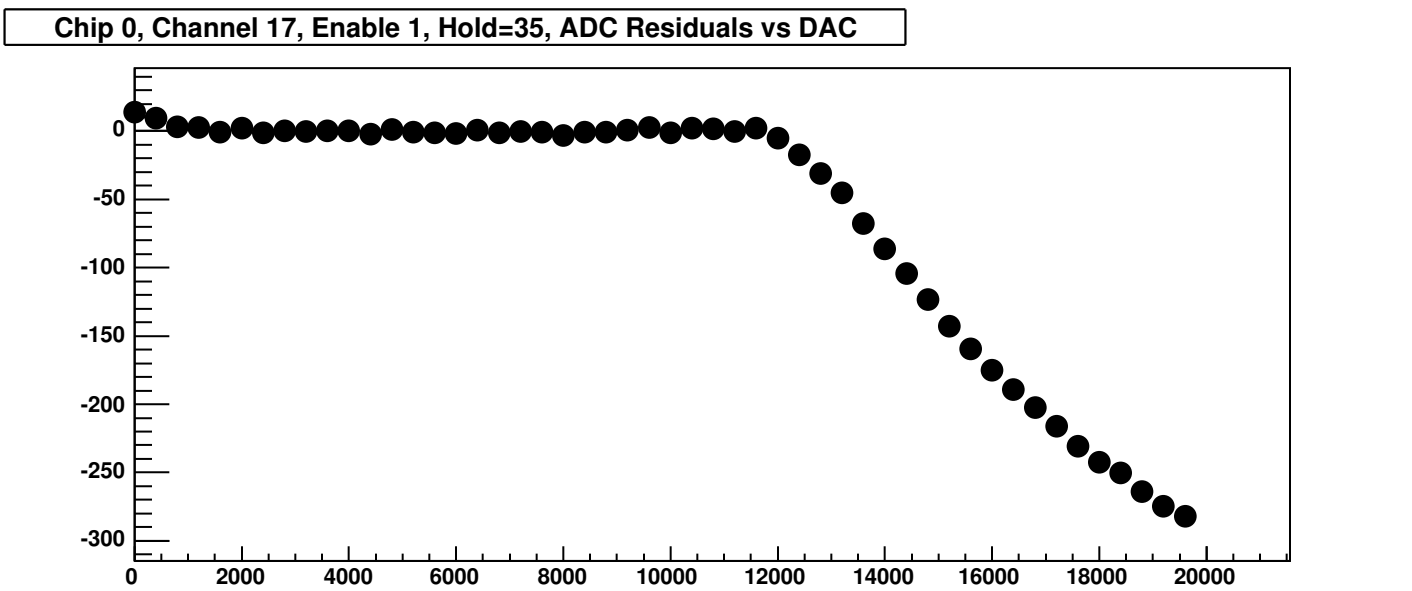

Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 33.89 / 23
p0 486.1 ± 0.7244
p1 0.01482 ± 0.0001048
680
660
640
620
600
580
560
540
520
500
480
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

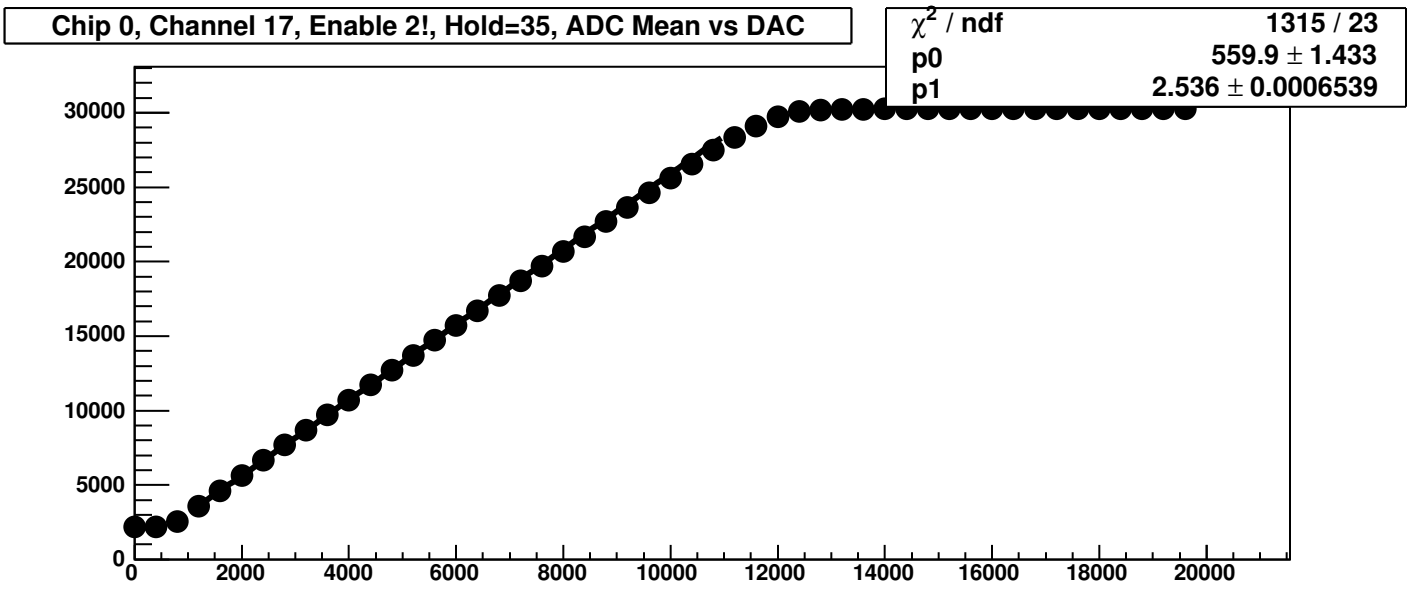

Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

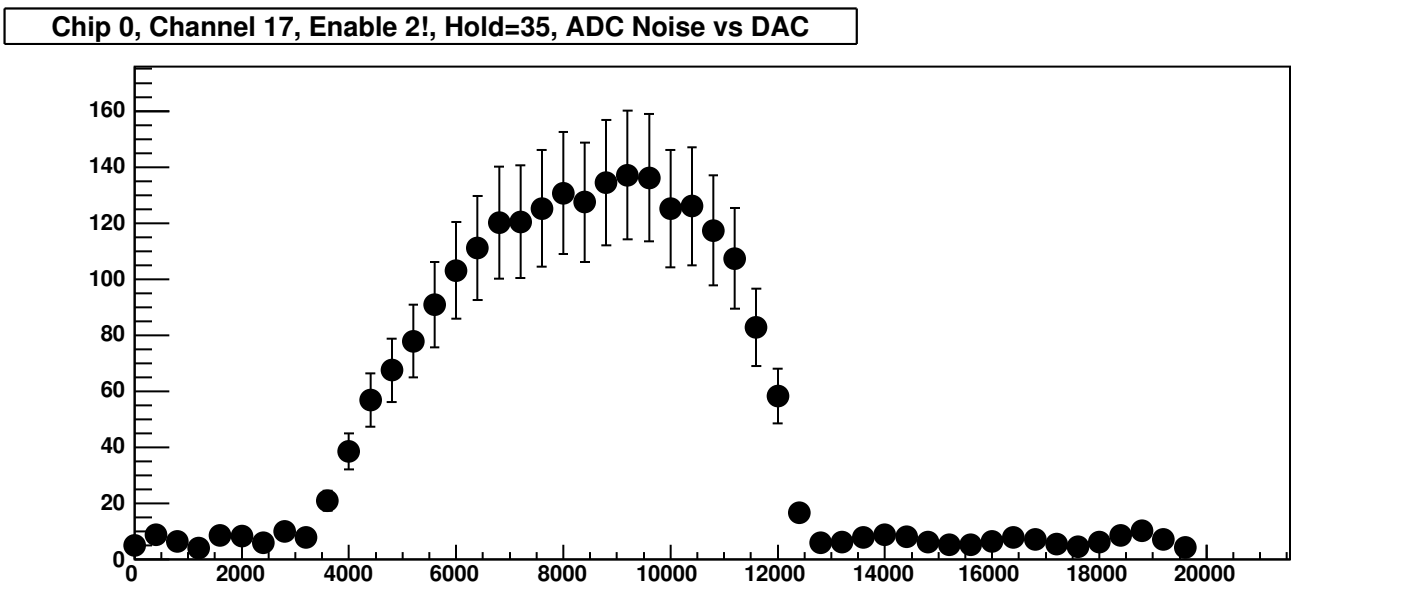

Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

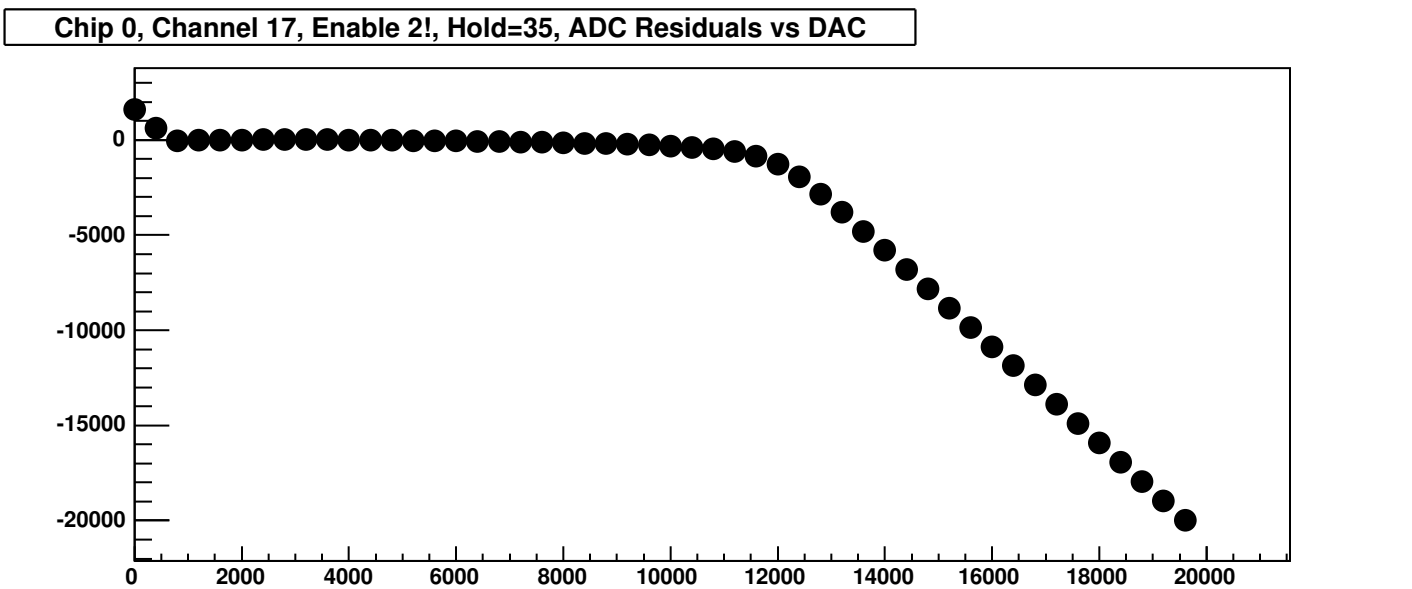

Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 19.01 / 23
p0 483.8 ± 0.6999
p1 0.01737 ± 0.0001091
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

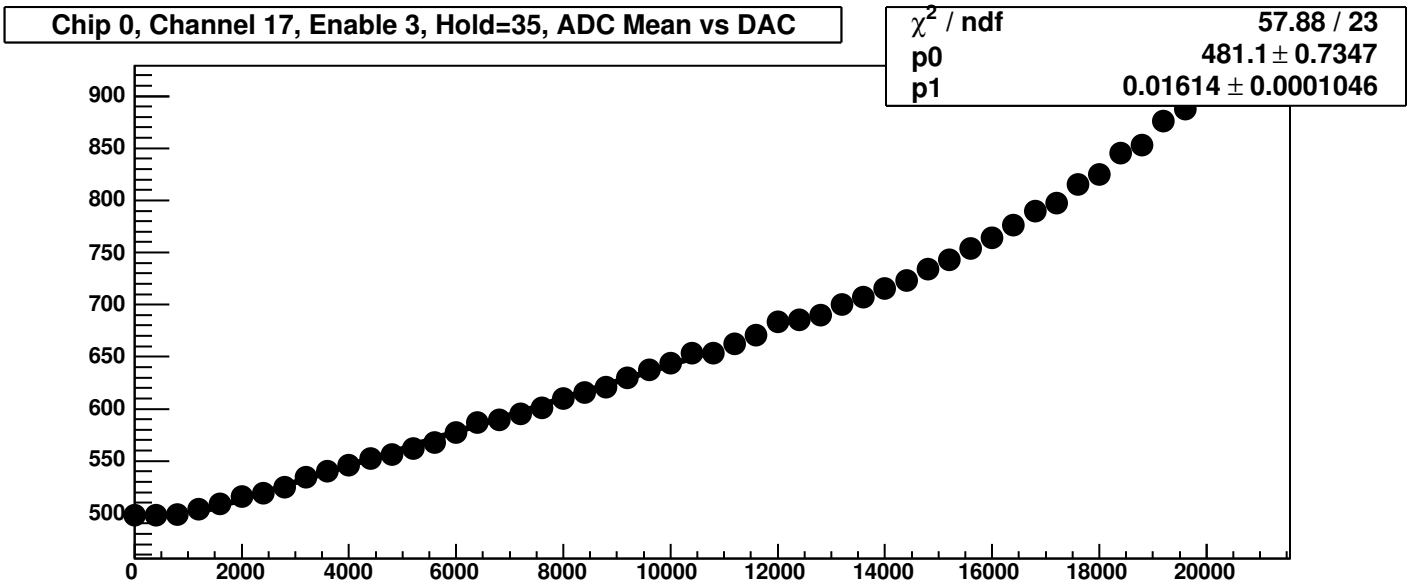

Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

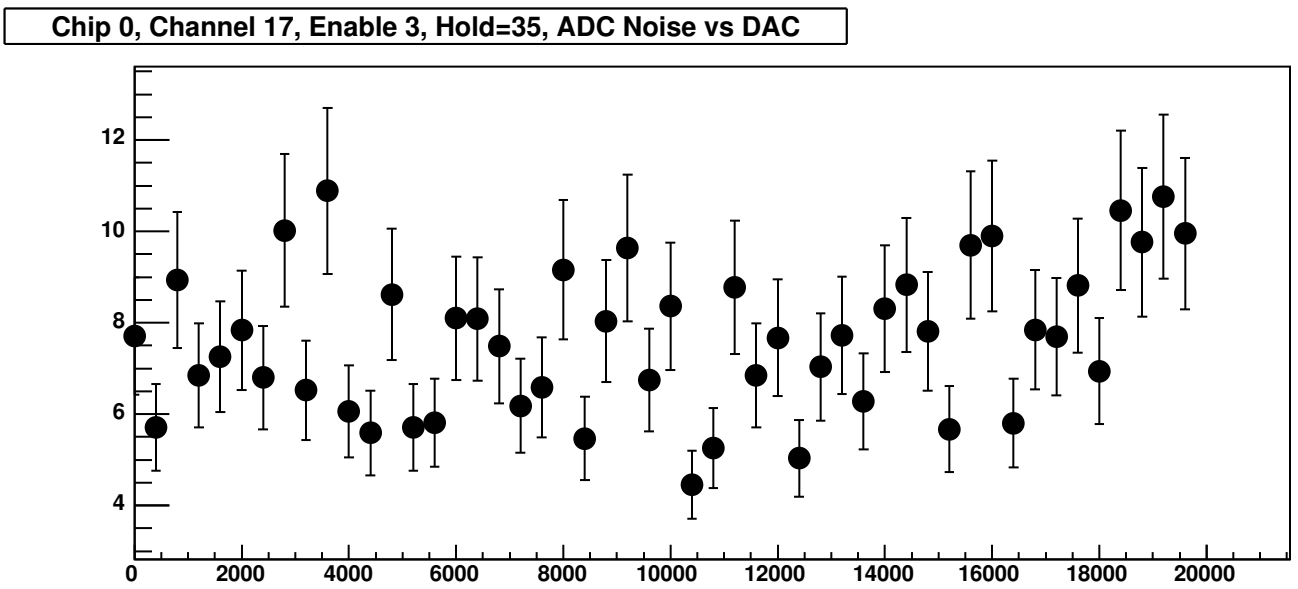

Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

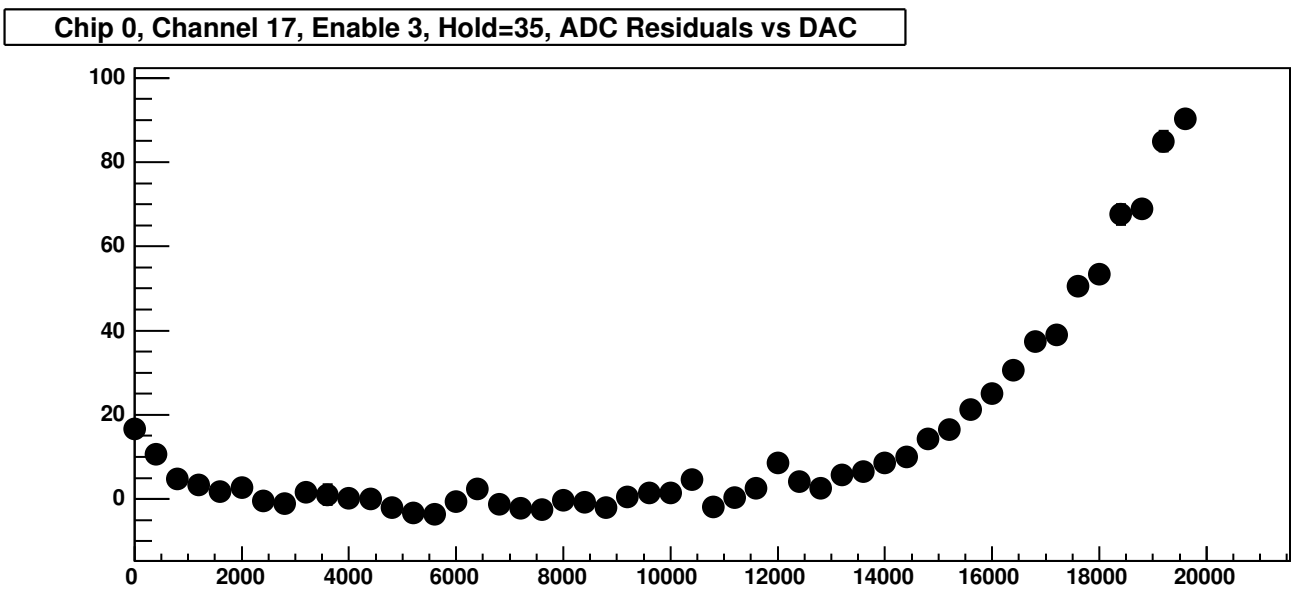

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1315 / 23
p0 559.9 ± 1.433
p1 2.536 ± 0.0006539
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

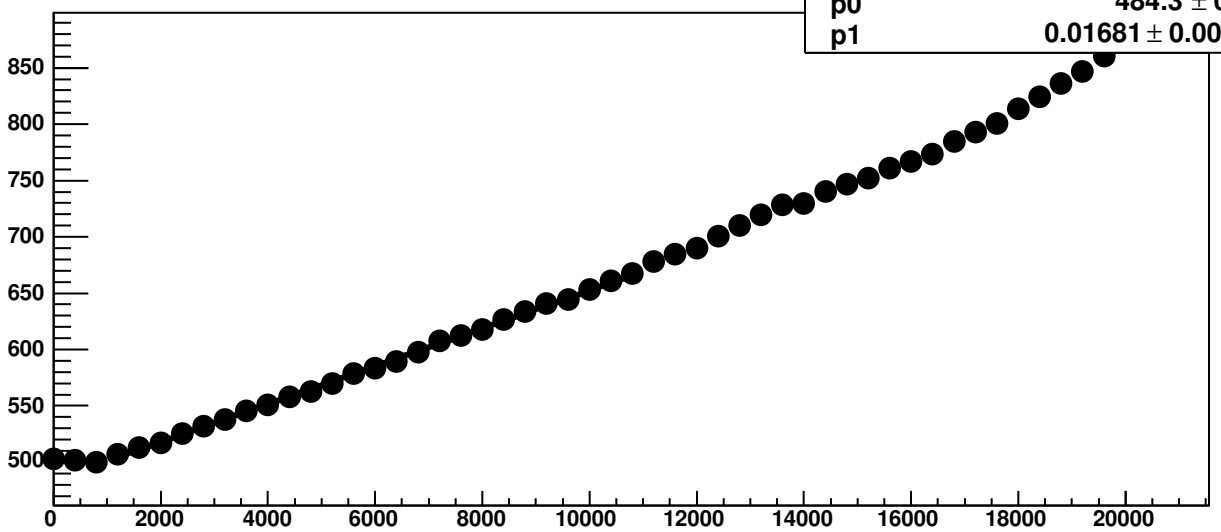

Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 57.88 / 23
p0 481.1 ± 0.7347
p1 0.01614 ± 0.0001046
900
850
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

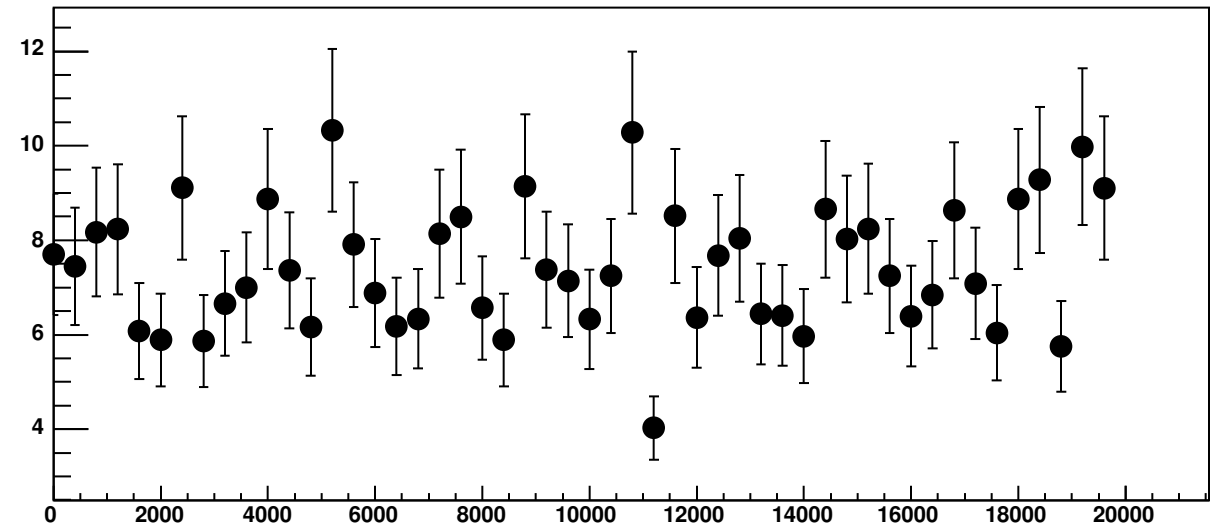

Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

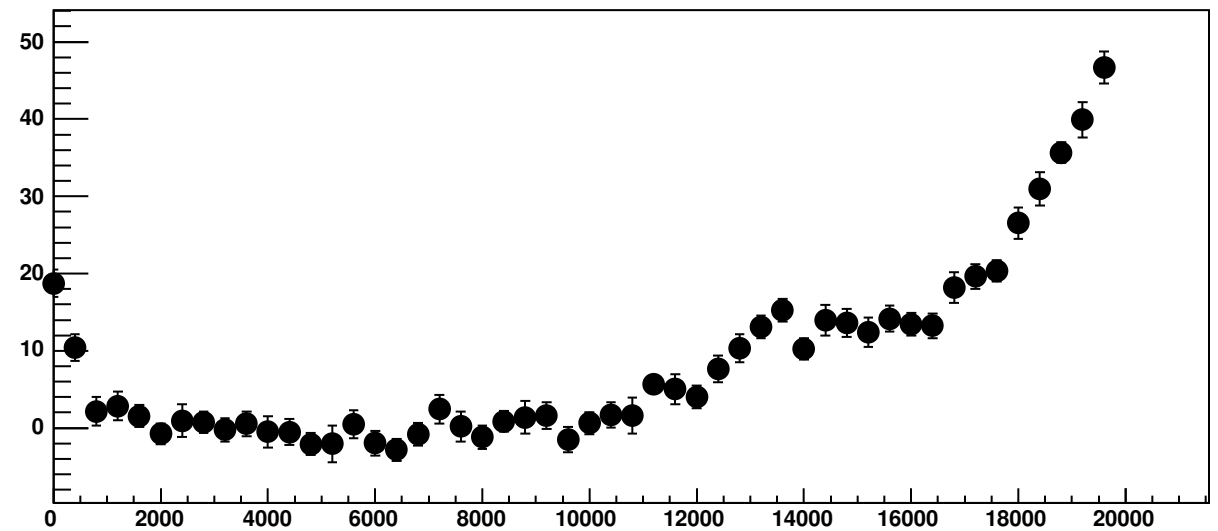
χ^2 / ndf	20.13 / 23
p0	484.3 ± 0.7412
p1	0.01681 ± 0.0001138

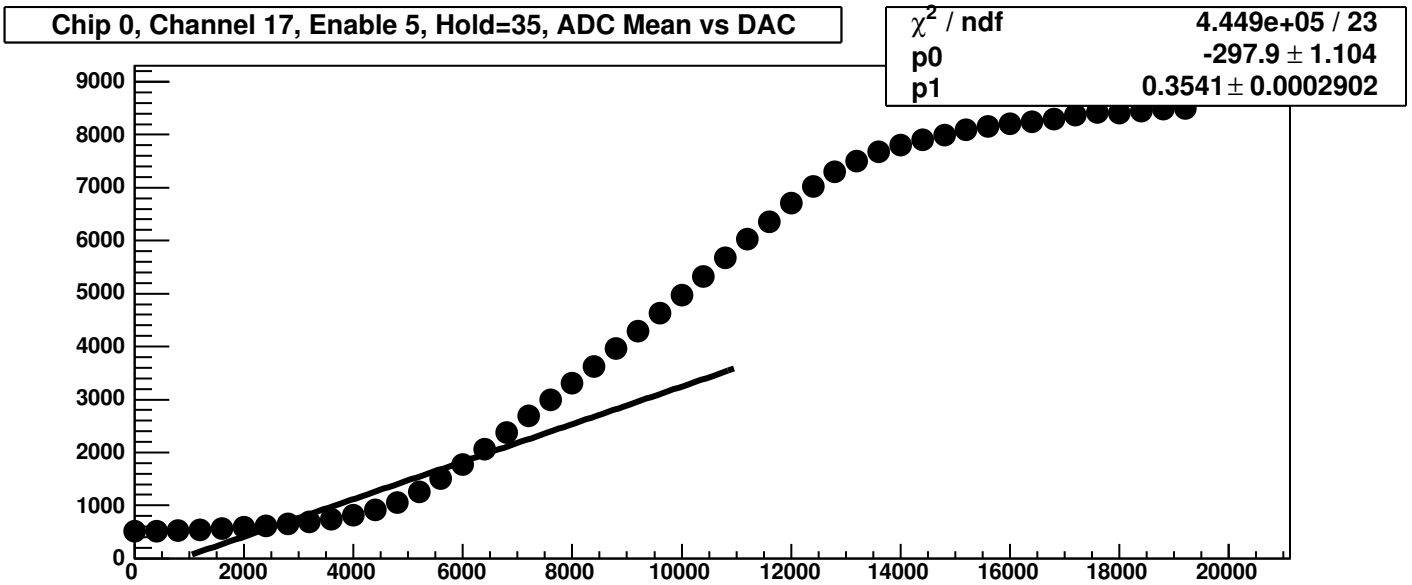


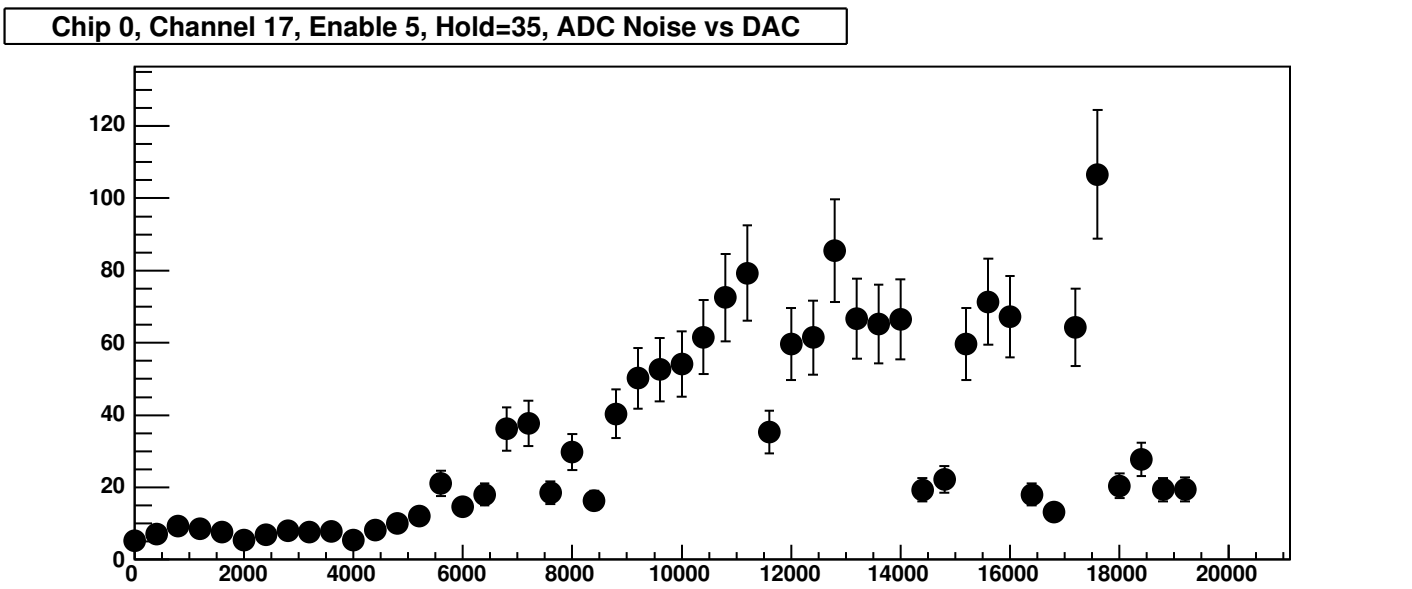
Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

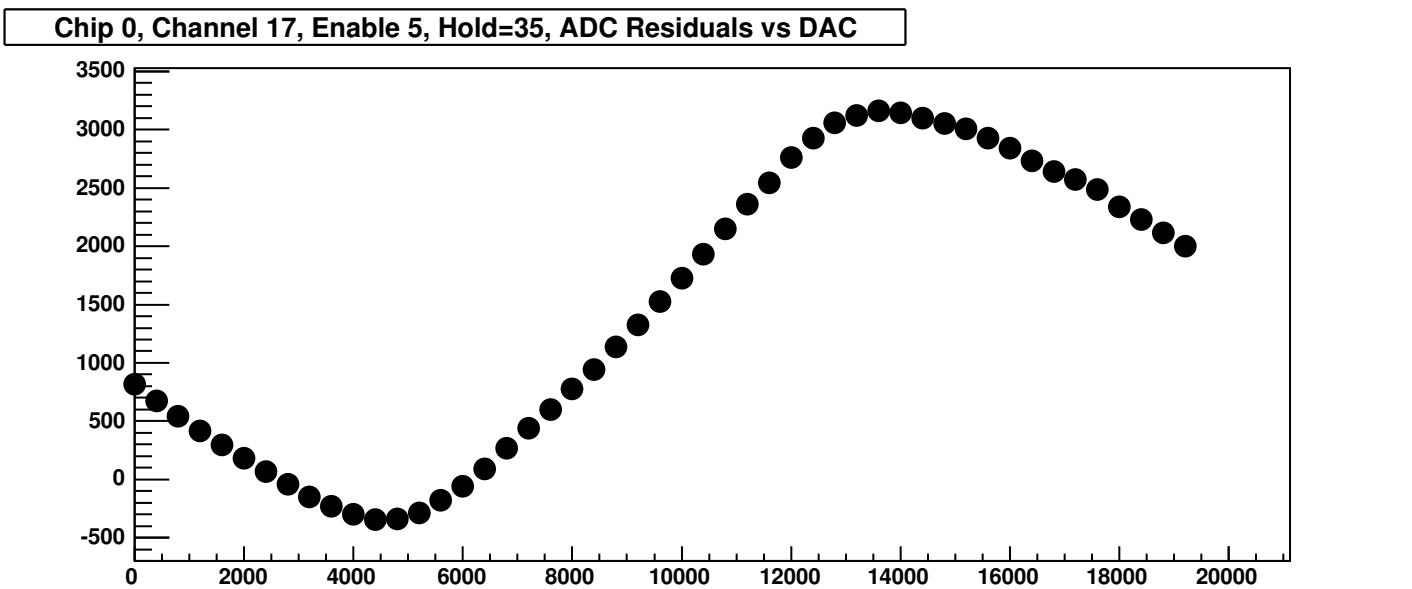


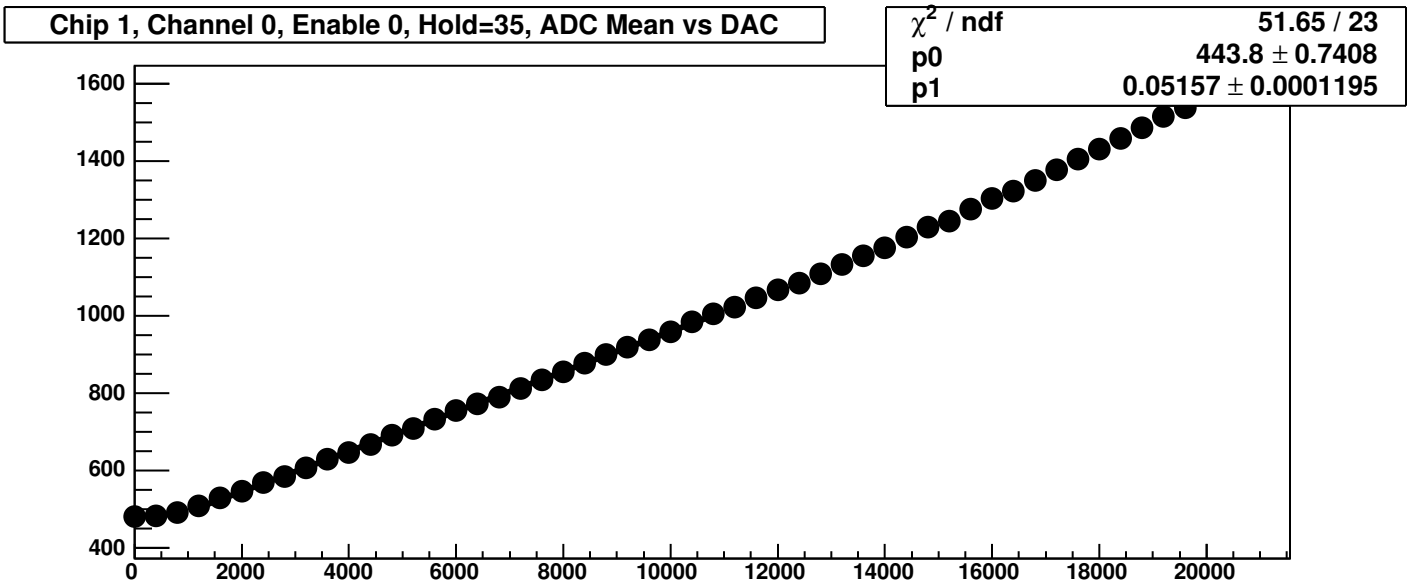
Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

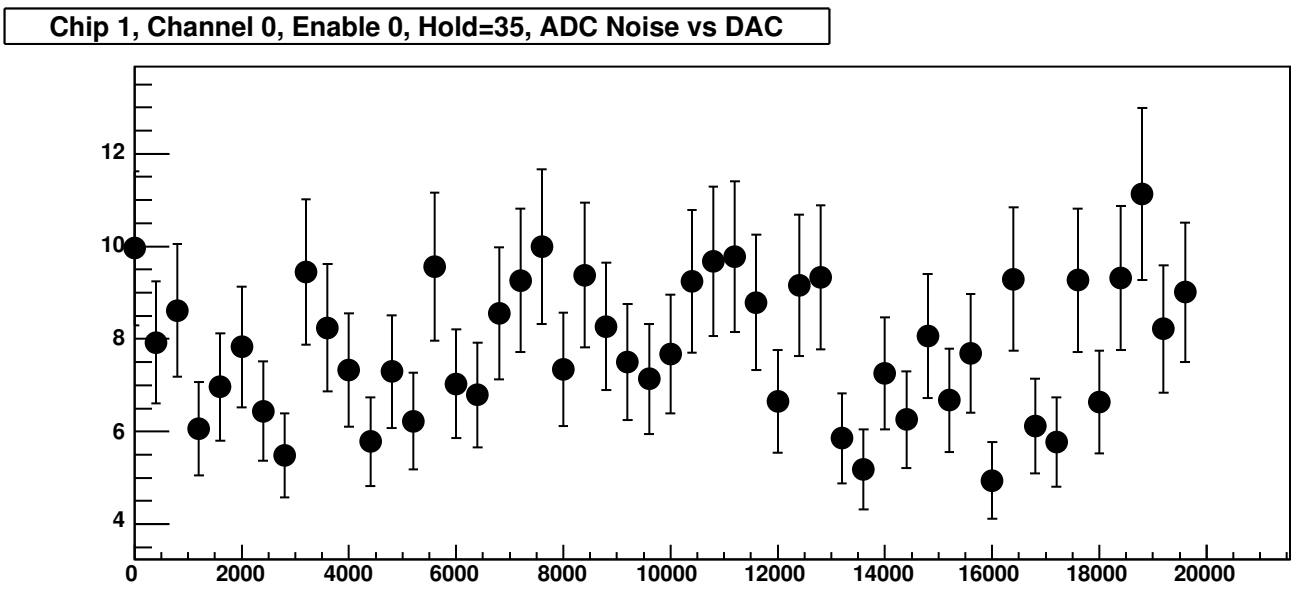


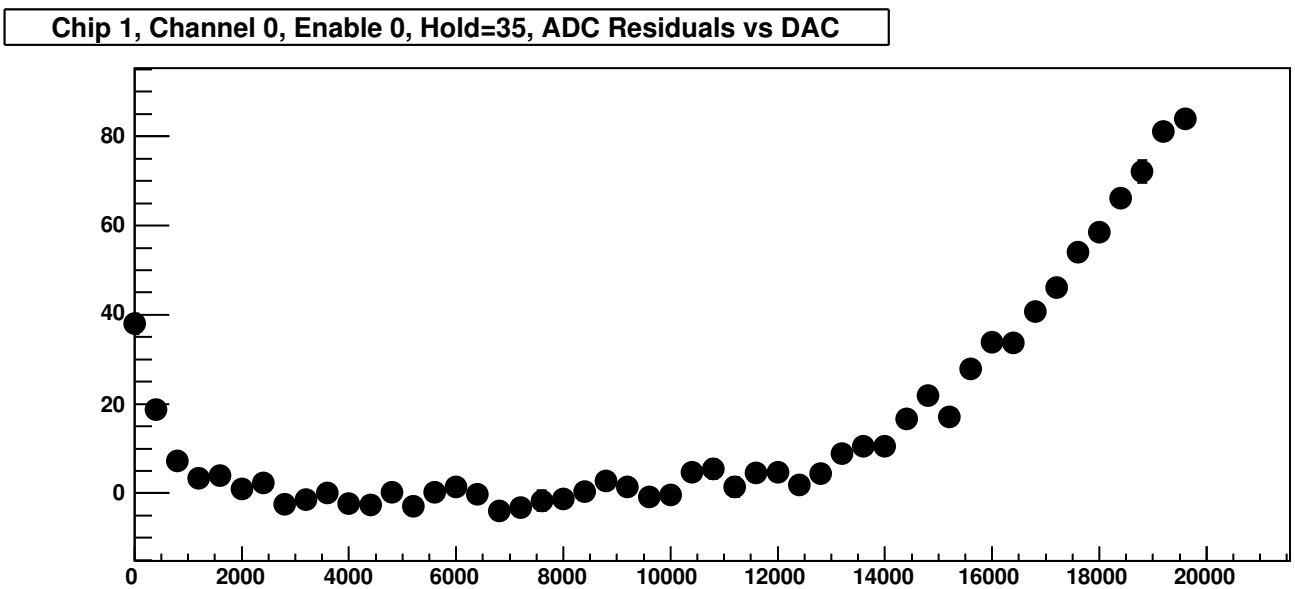

Chip 0, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.449e+05 / 23
p0 -297.9 ± 1.104
p1 0.3541 ± 0.0002902
9000
8000
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 0, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

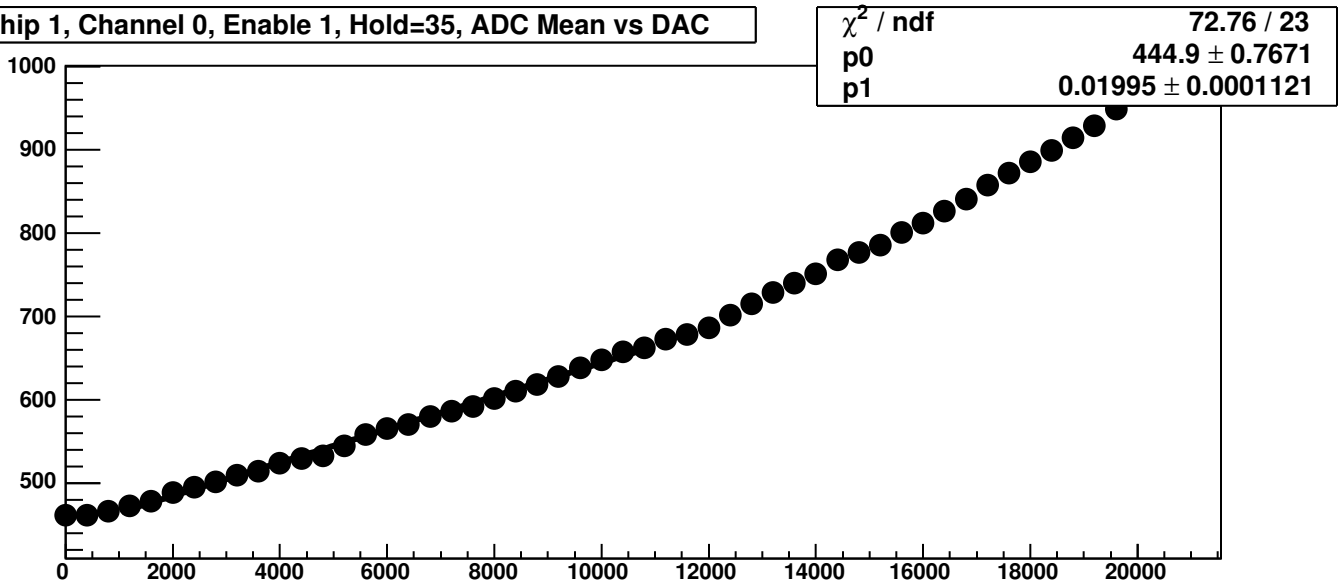

Chip 0, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 51.65 / 23
p0 443.8 ± 0.7408
p1 0.05157 ± 0.0001195
1600
1400
1200
1000
800
600
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

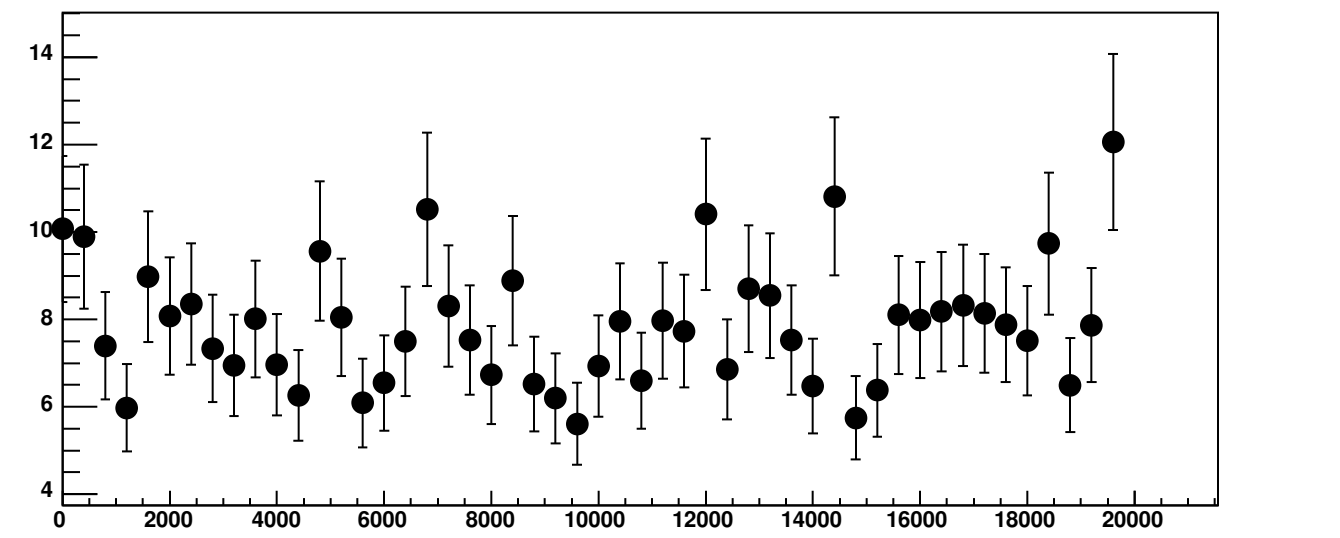

Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

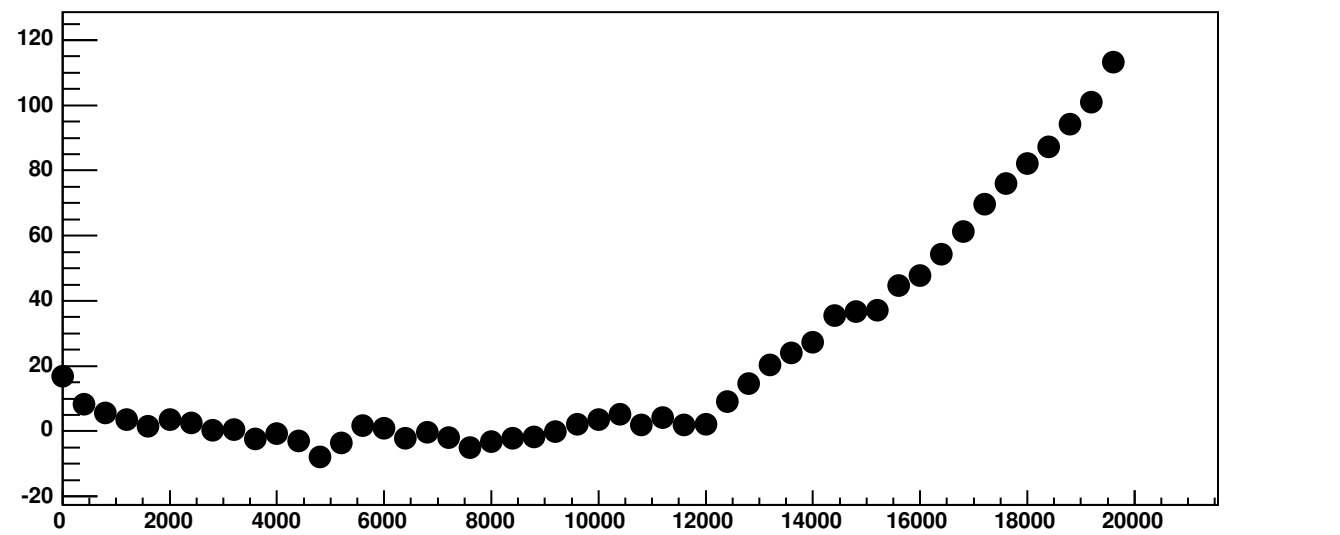
Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

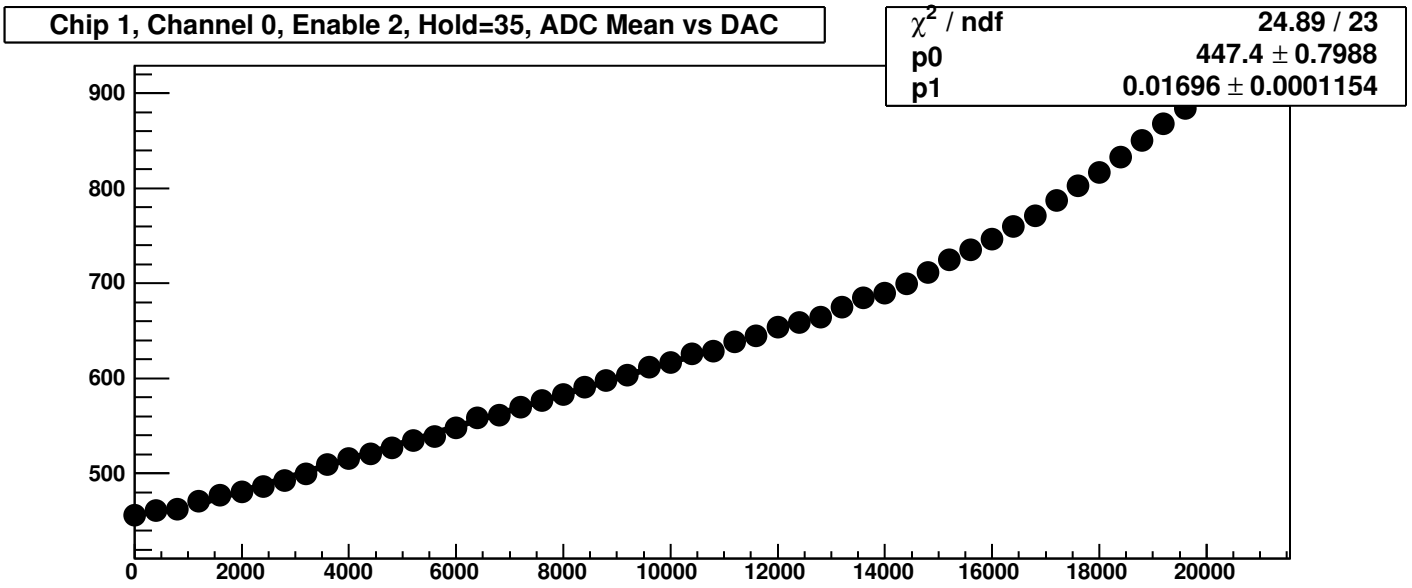


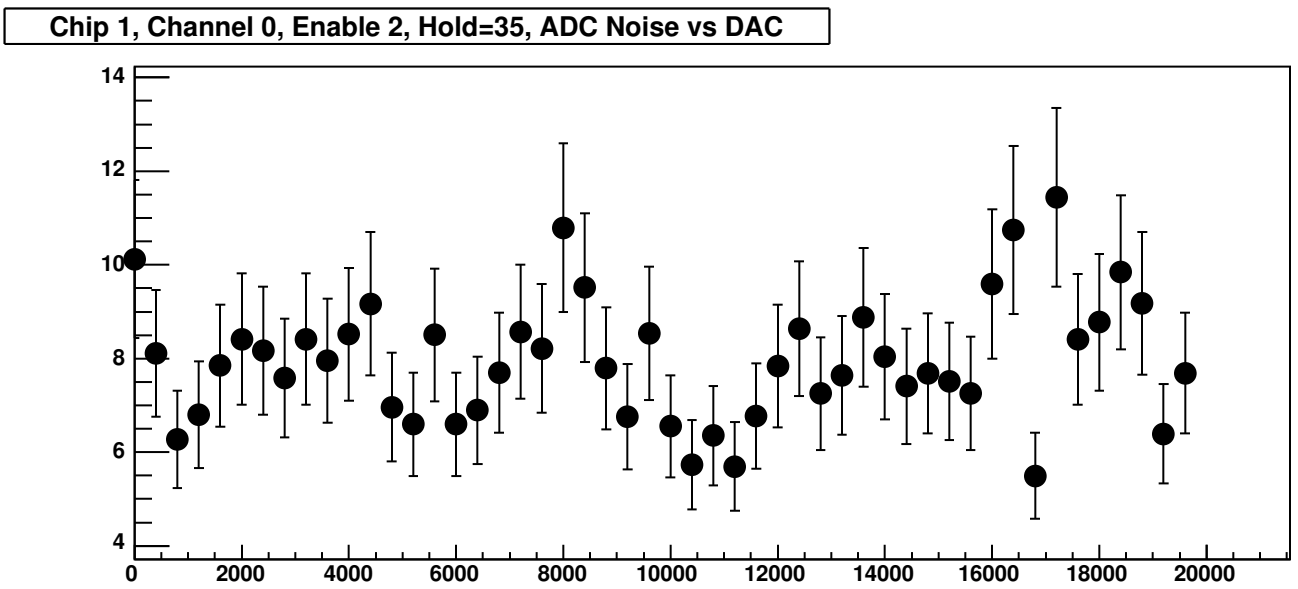
Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC

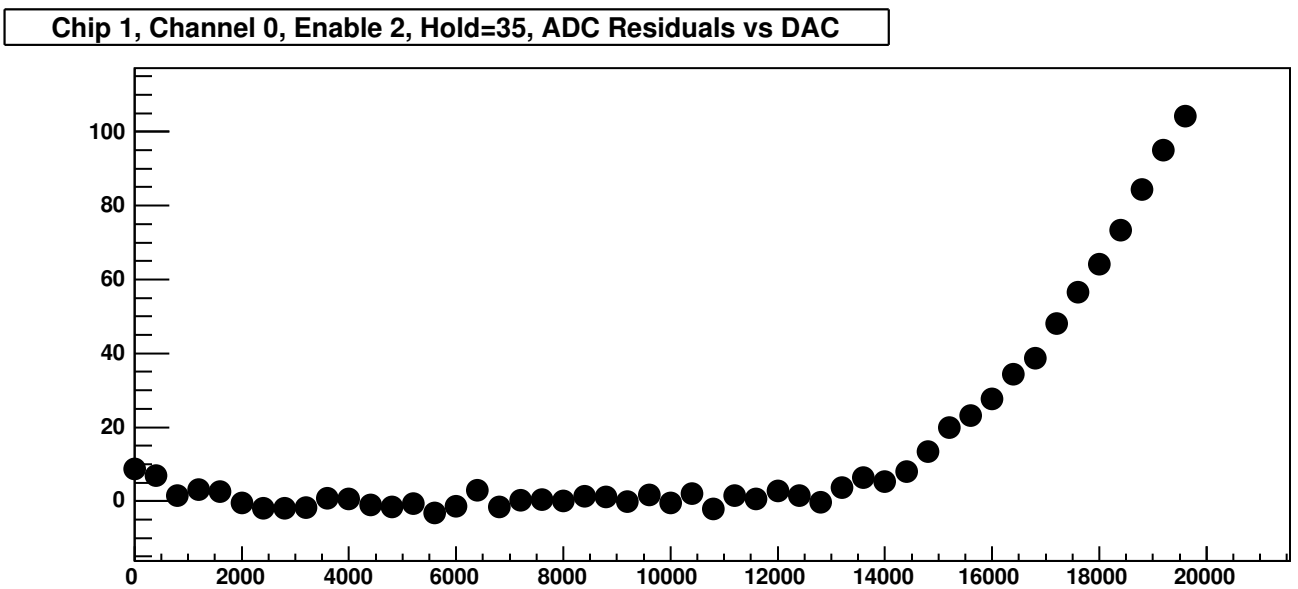


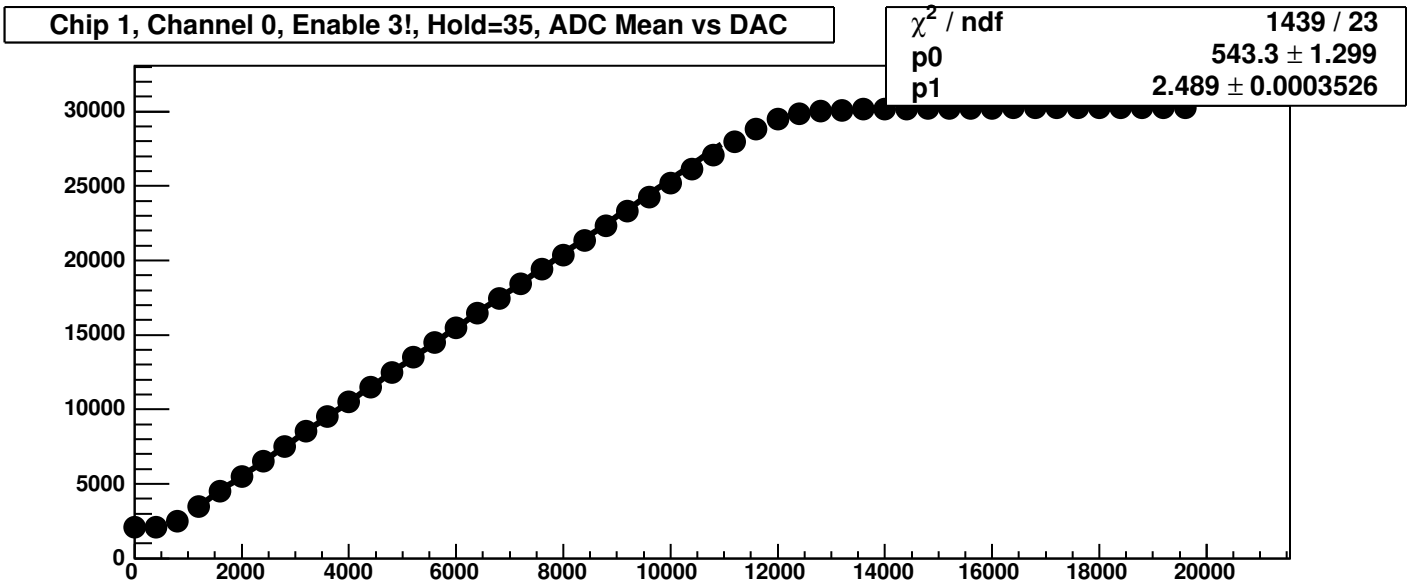
Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

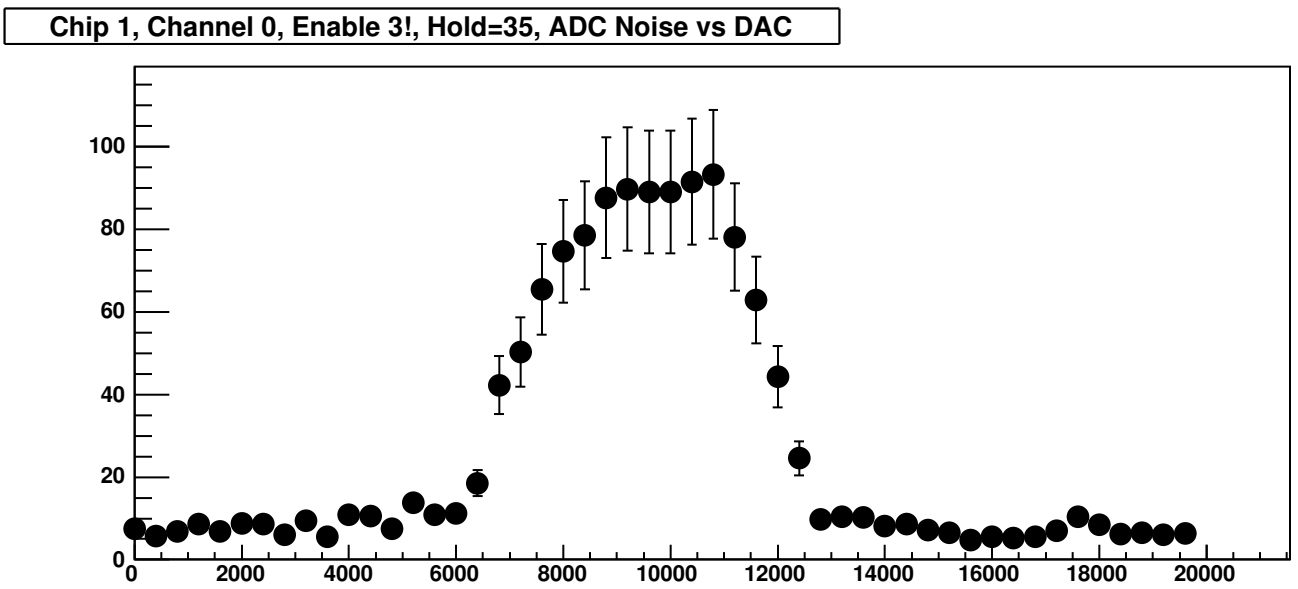


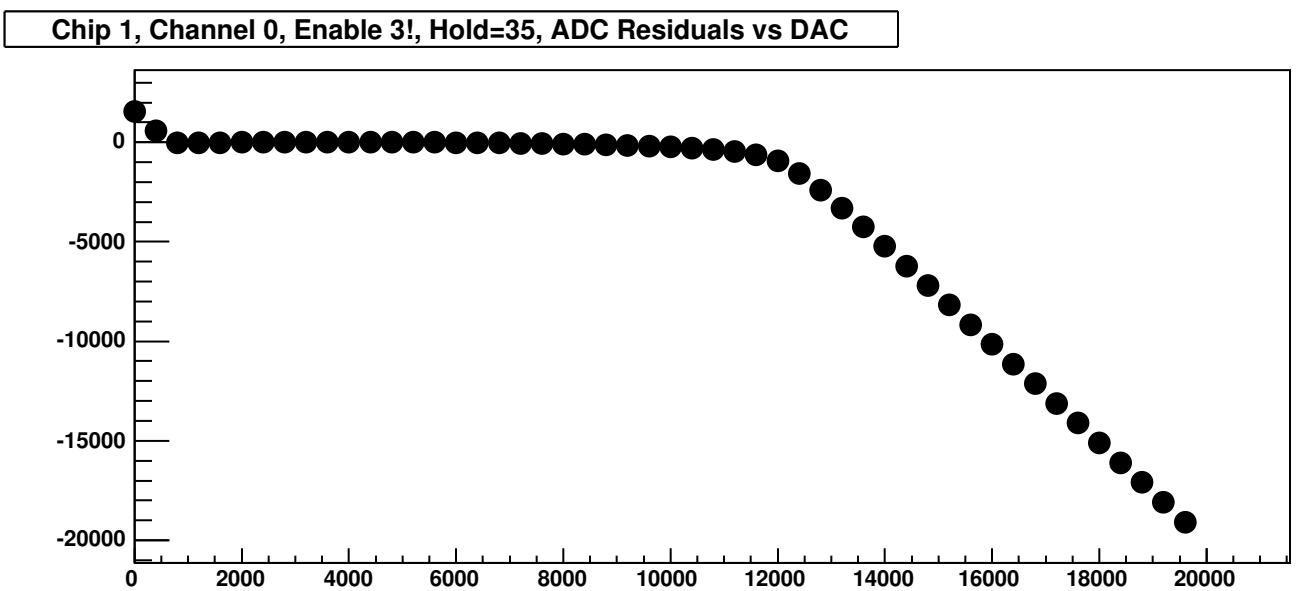

Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 24.89 / 23
p0 447.4 ± 0.7988
p1 0.01696 ± 0.0001154
900
800
700
600
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

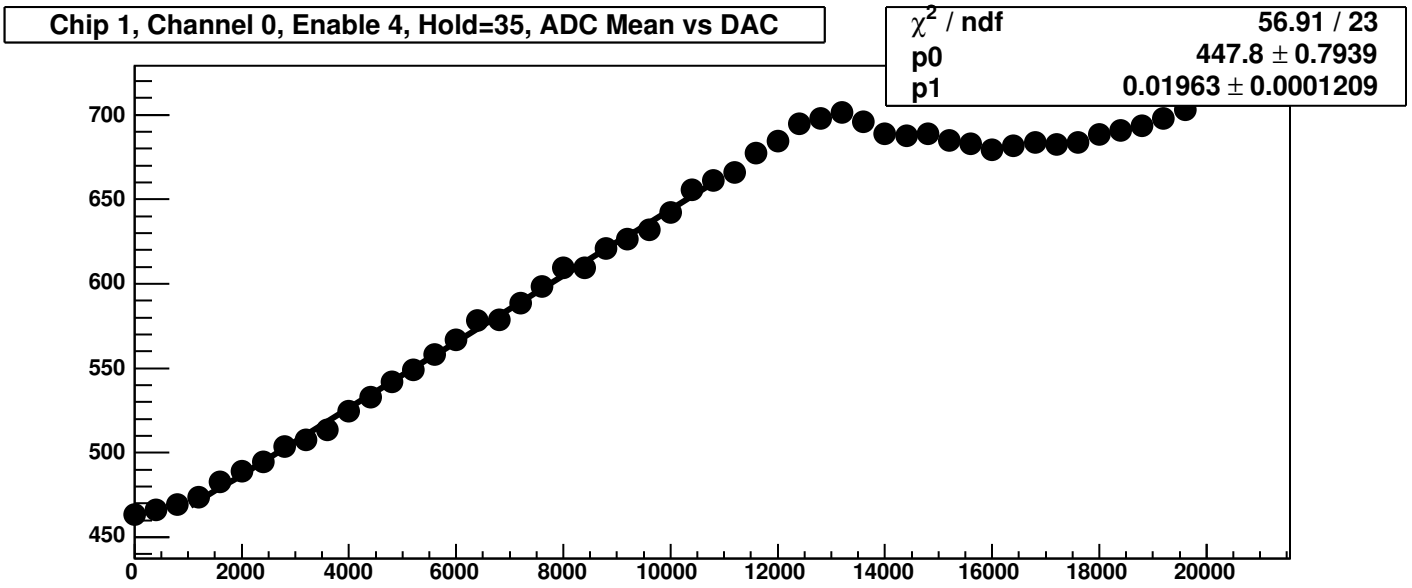

Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

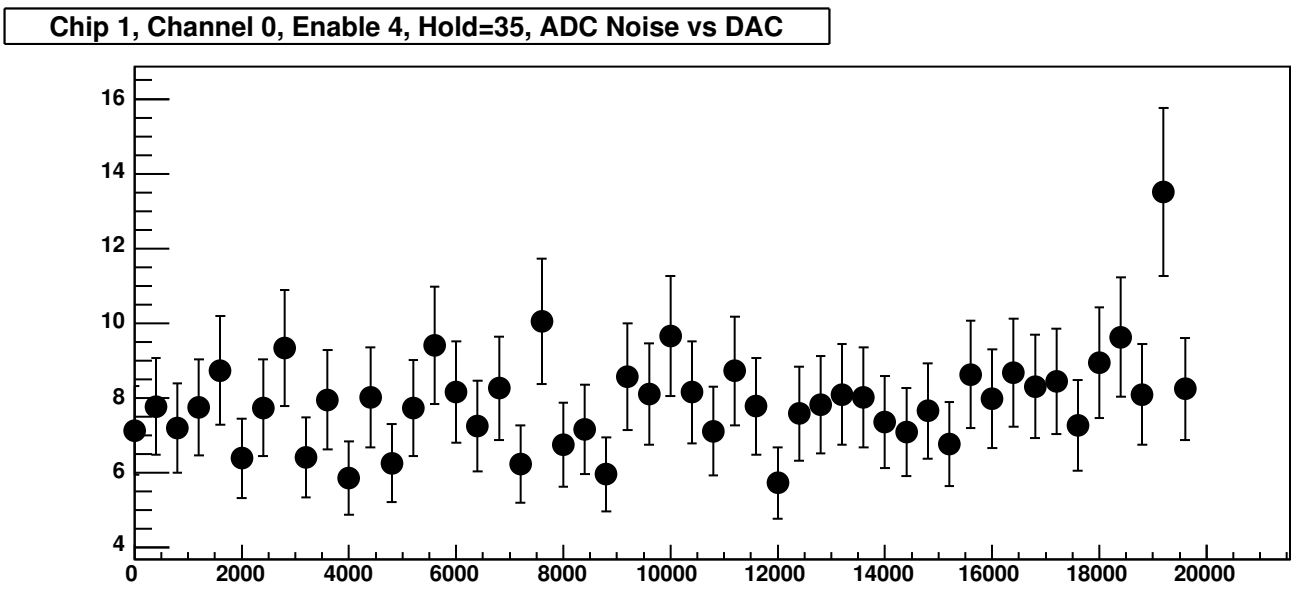

Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

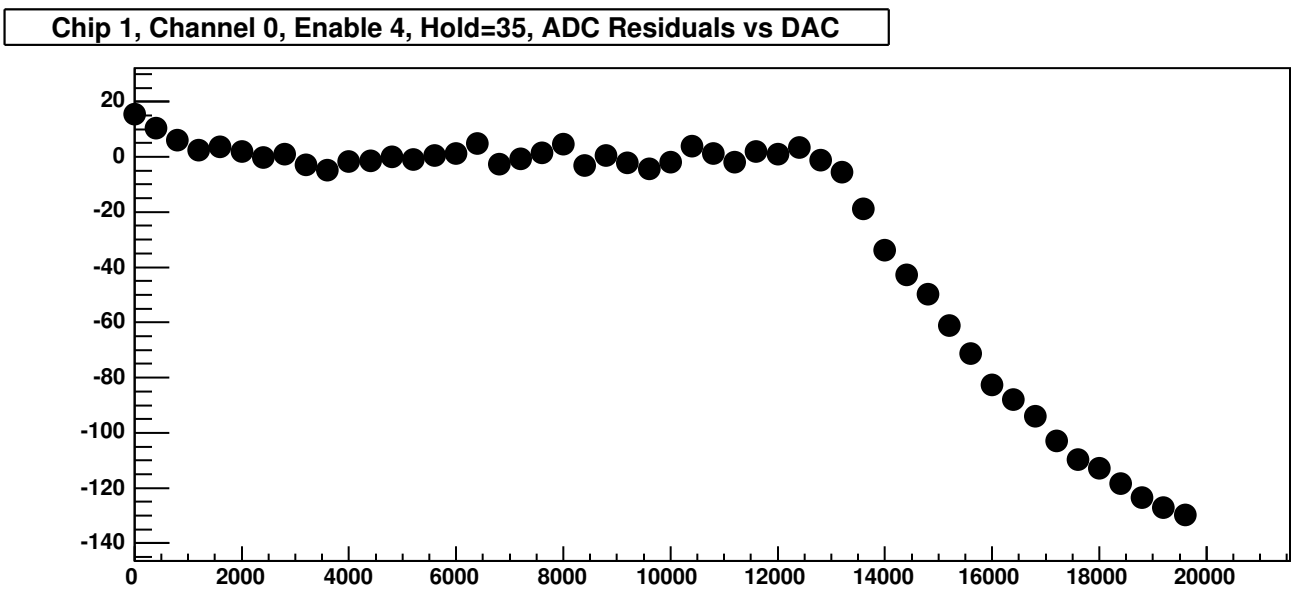

Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ^2 / ndf 1439 / 23
p0 543.3 ± 1.299
p1 2.489 ± 0.0003526
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

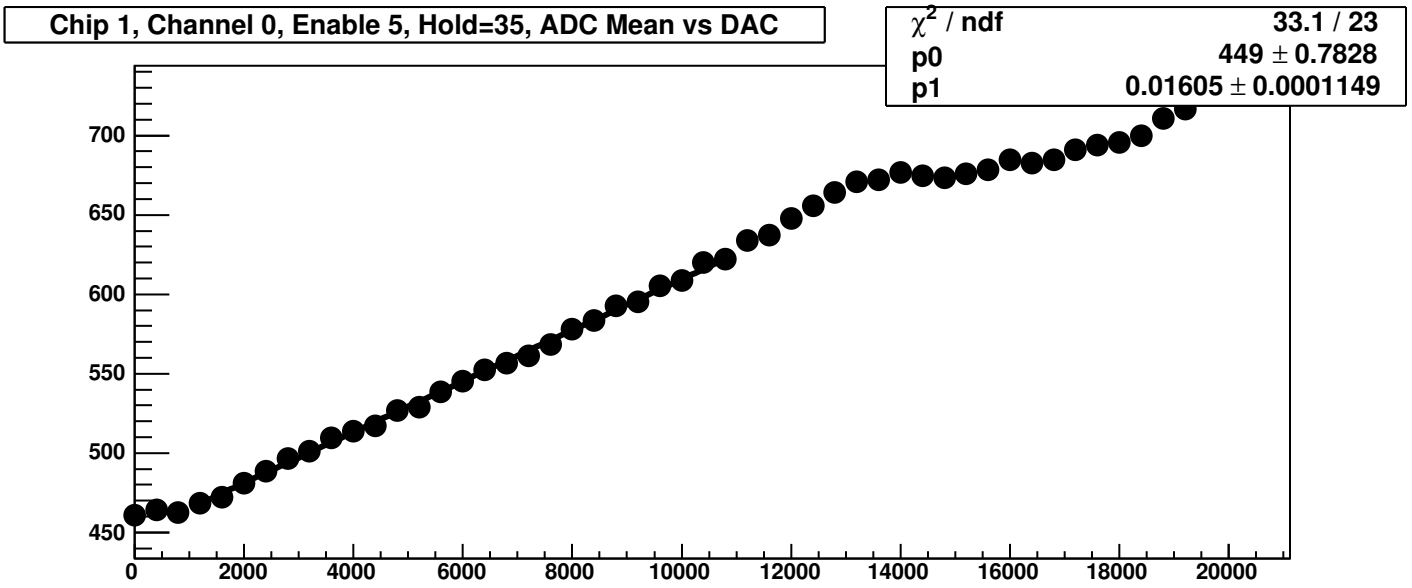

Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

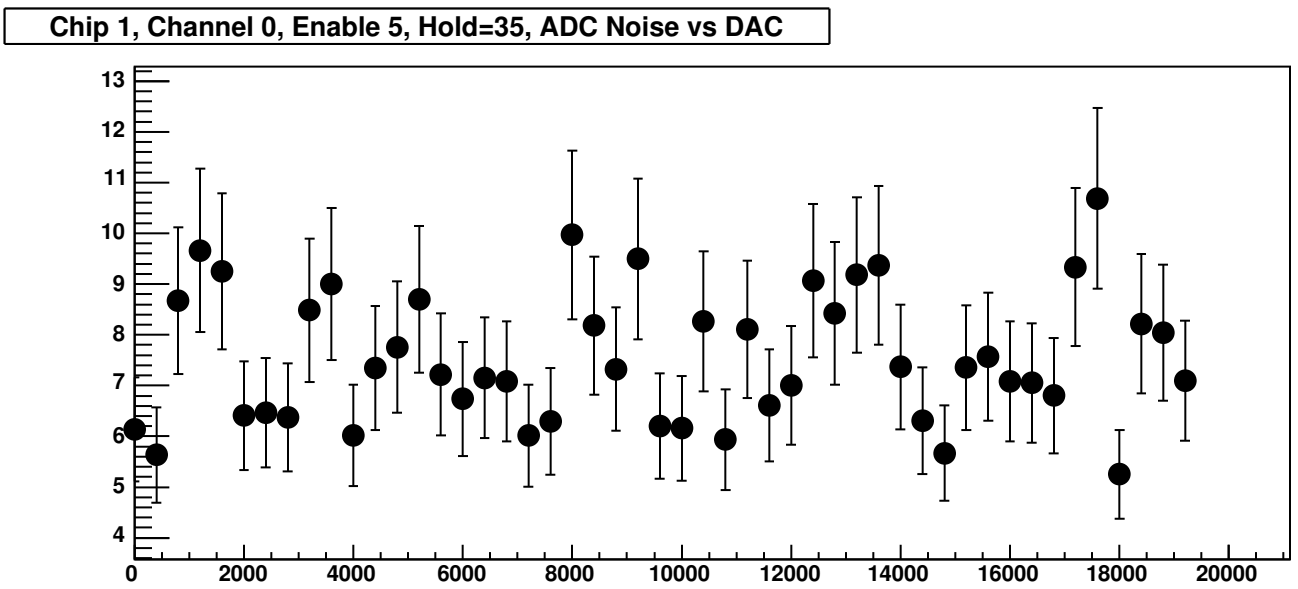

Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

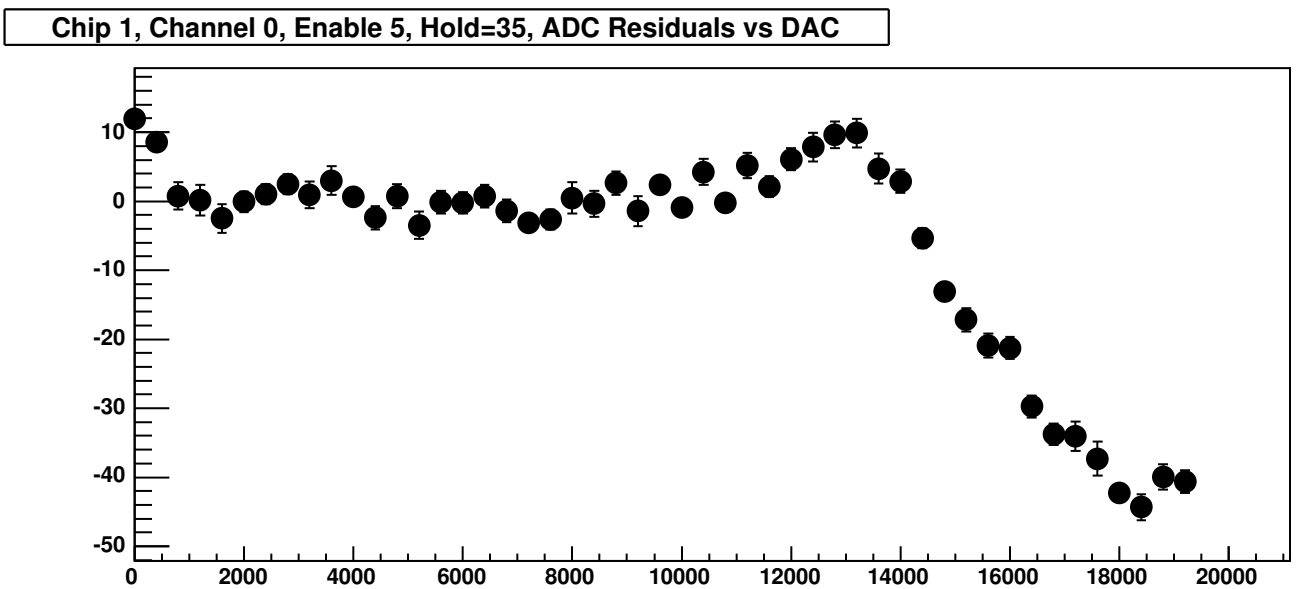

Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 56.91 / 23
p0 447.8 ± 0.7939
p1 0.01963 ± 0.0001209
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

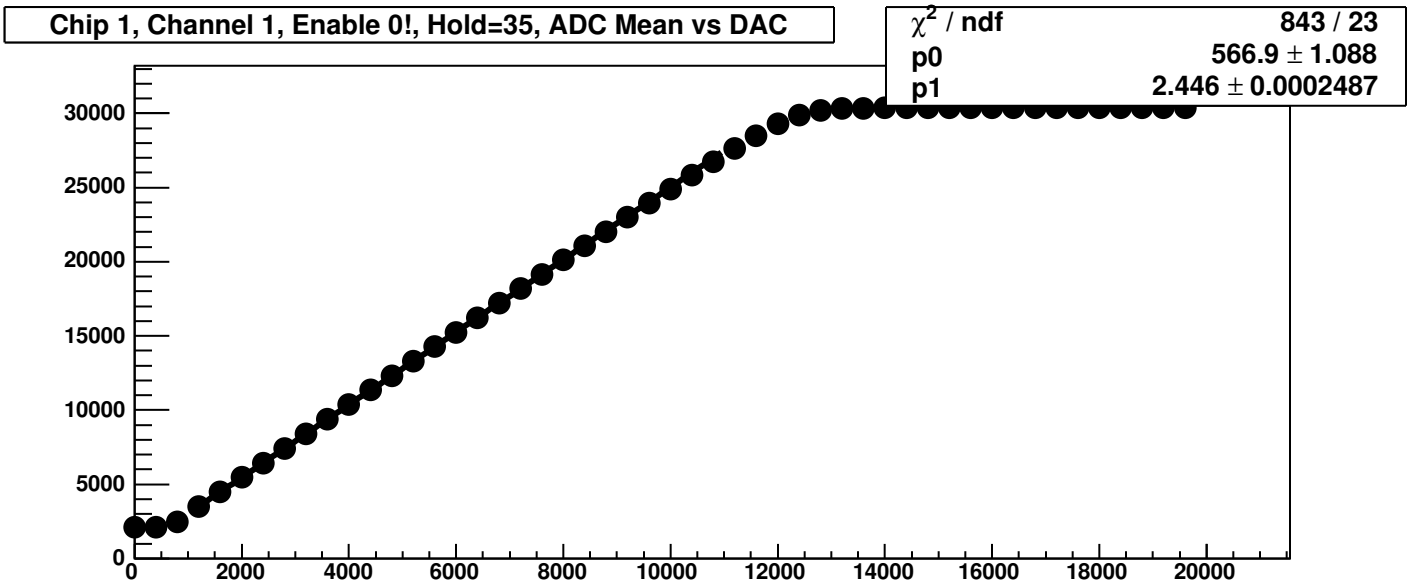

Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

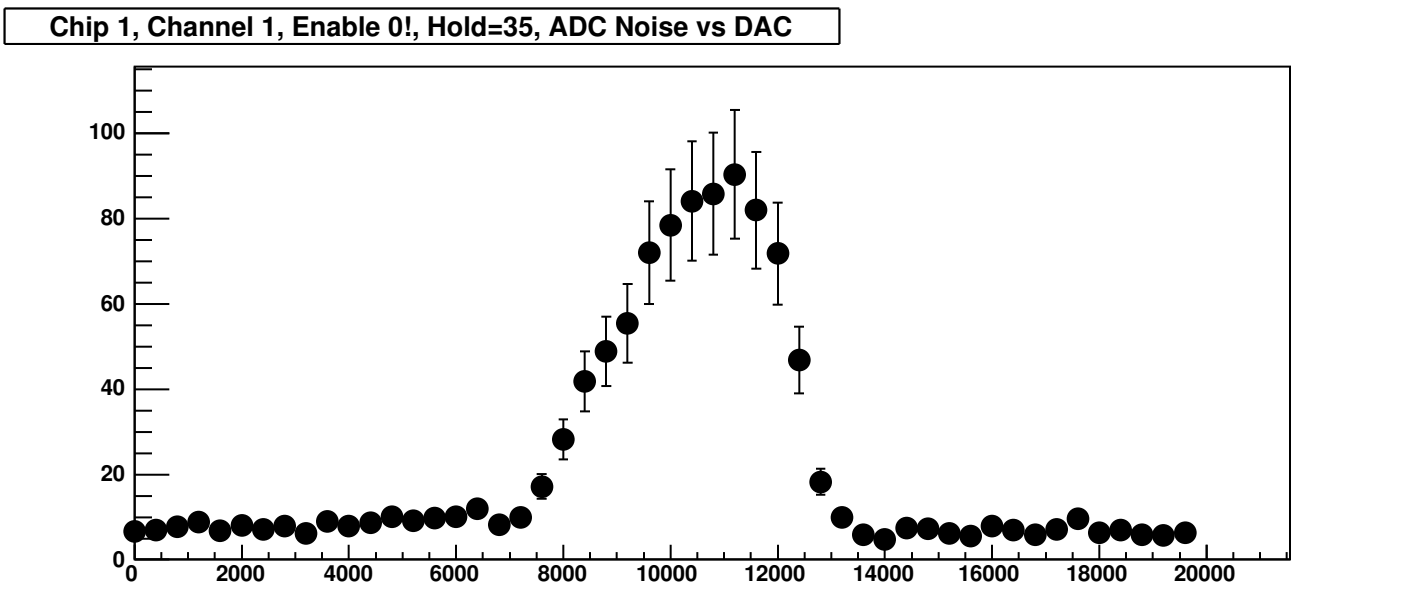

Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

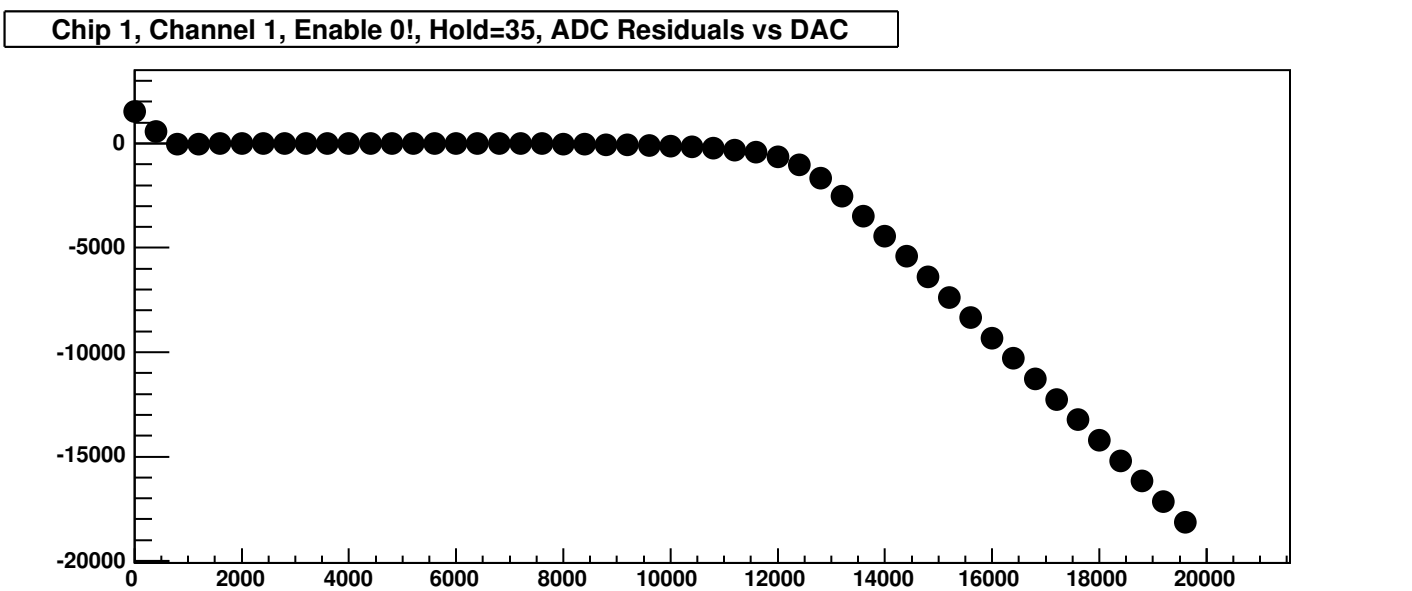

Chip 1, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 33.1 / 23
p0 449 ± 0.7828
p1 0.01605 ± 0.0001149
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

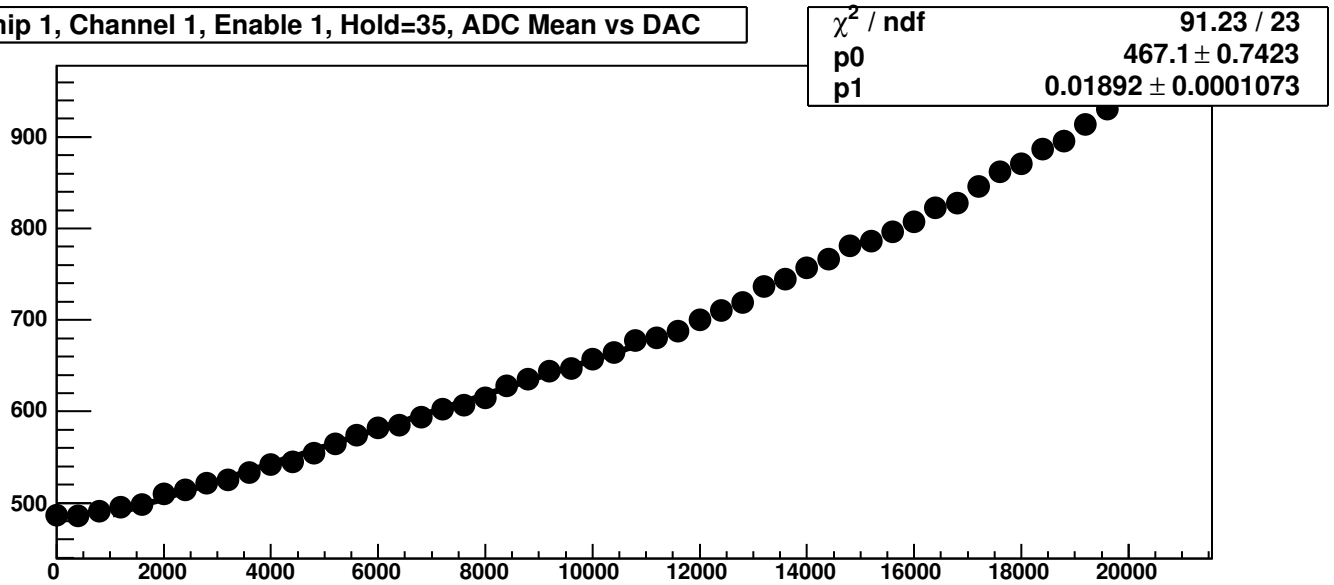

Chip 1, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 843 / 23
p0 566.9 ± 1.088
p1 2.446 ± 0.0002487
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

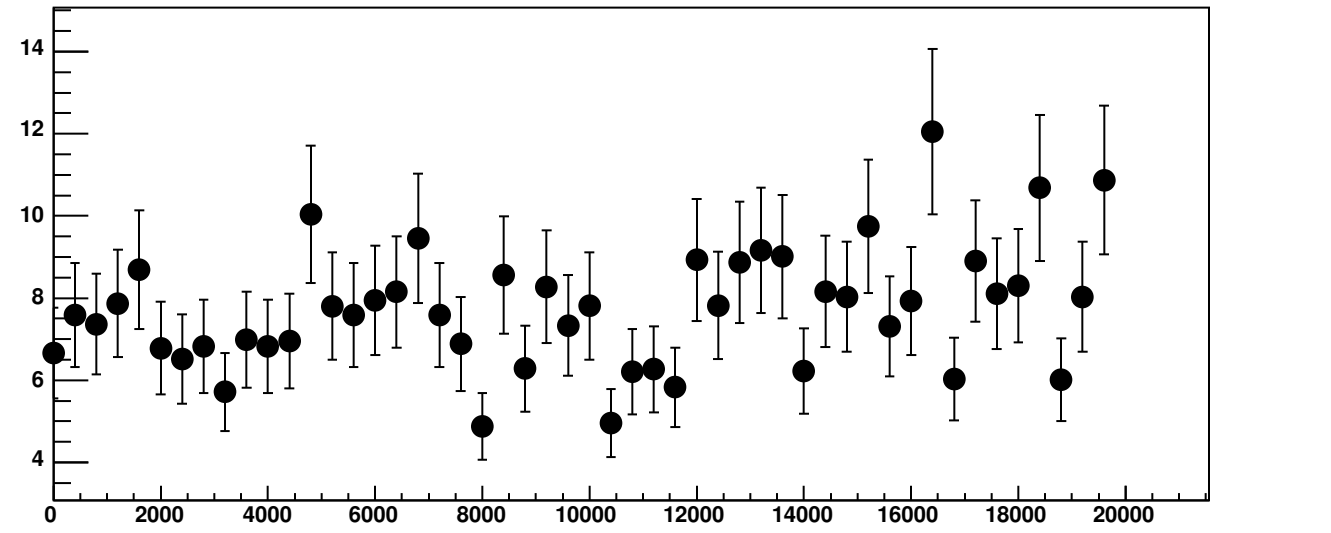

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

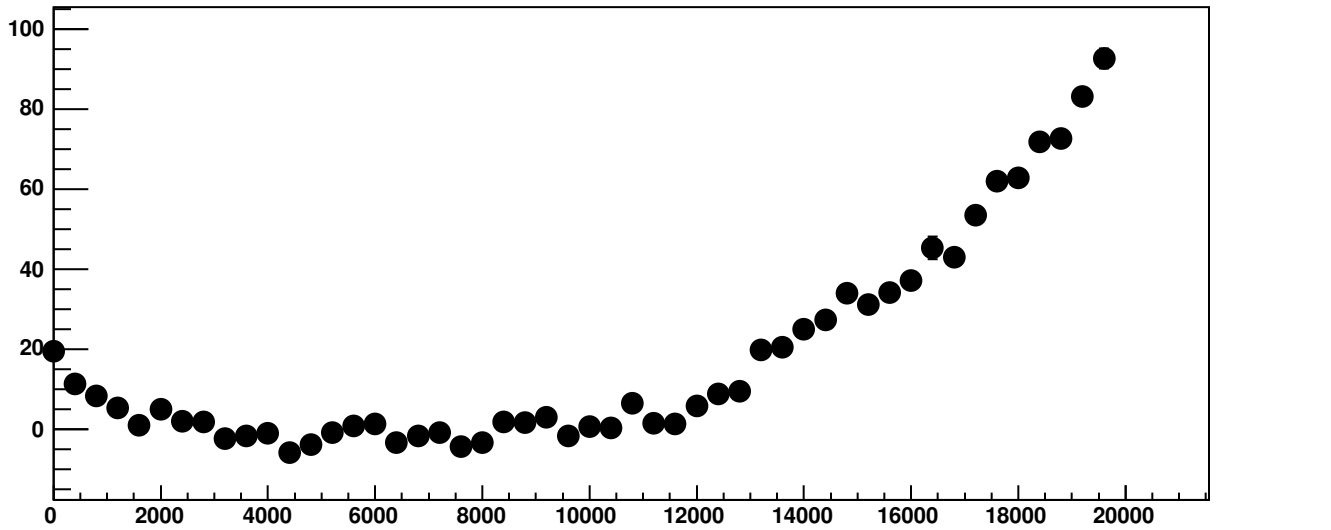
Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



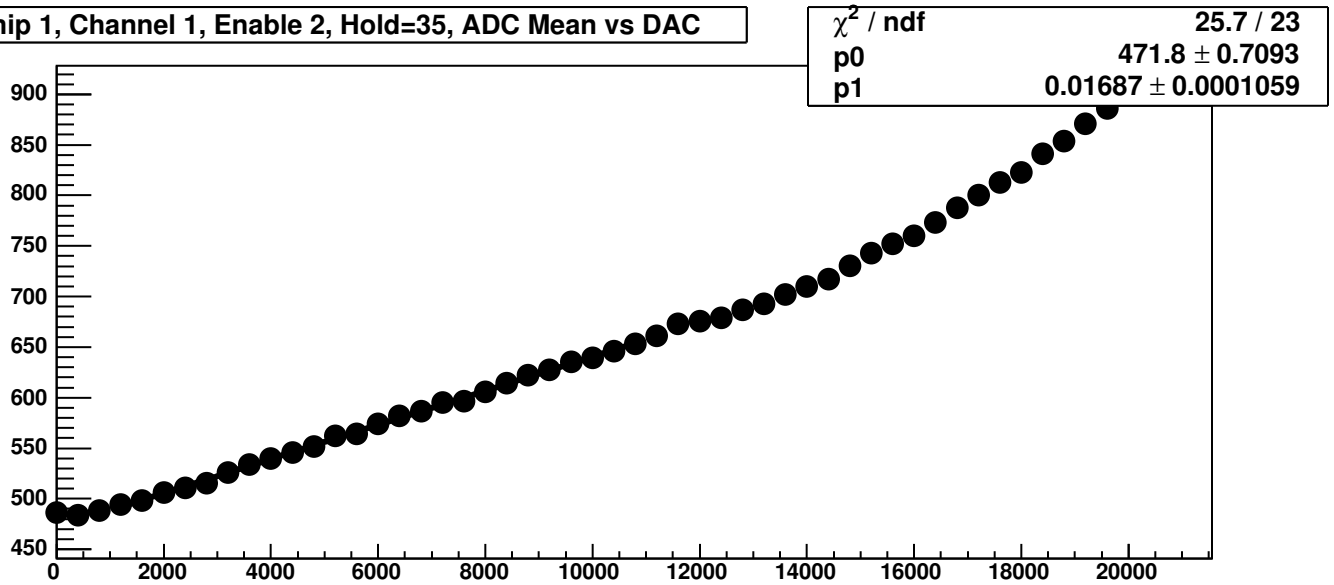
Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



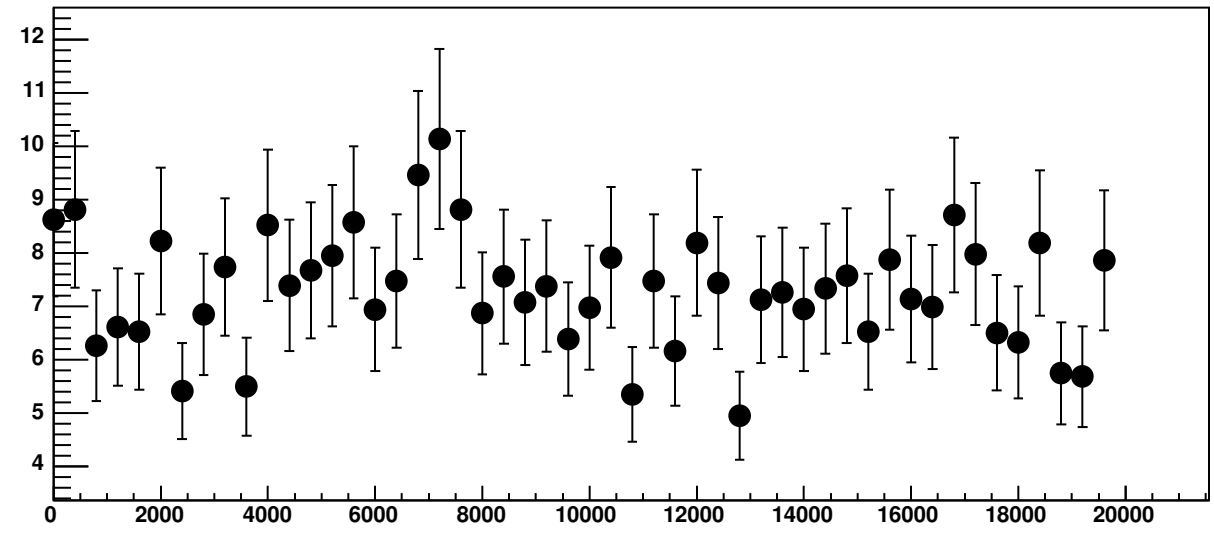
Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



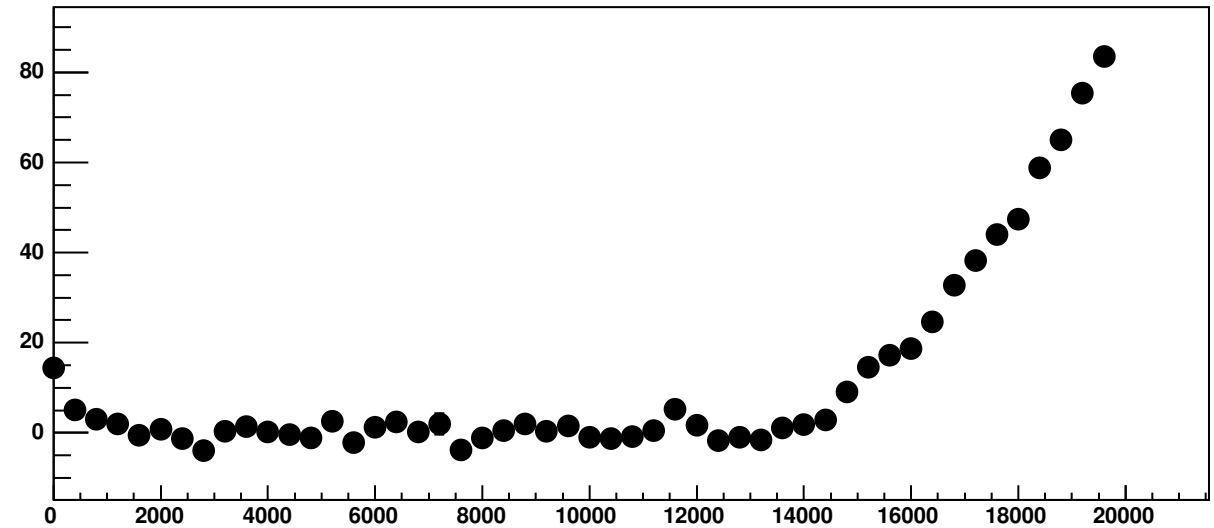
Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

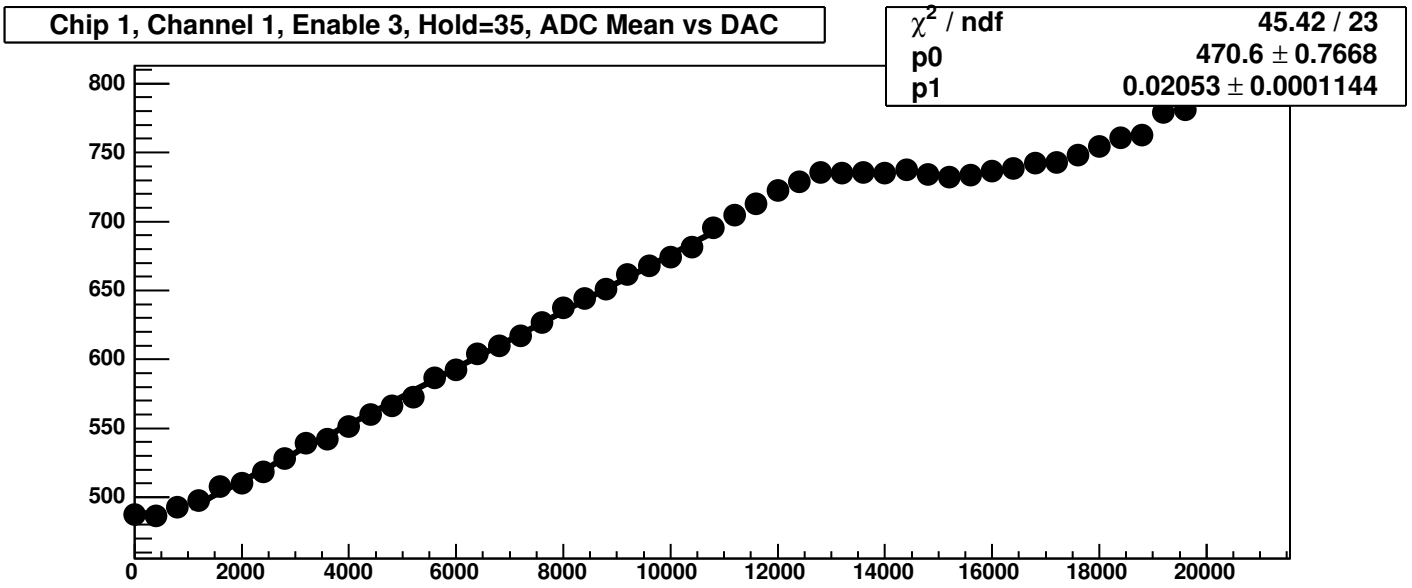


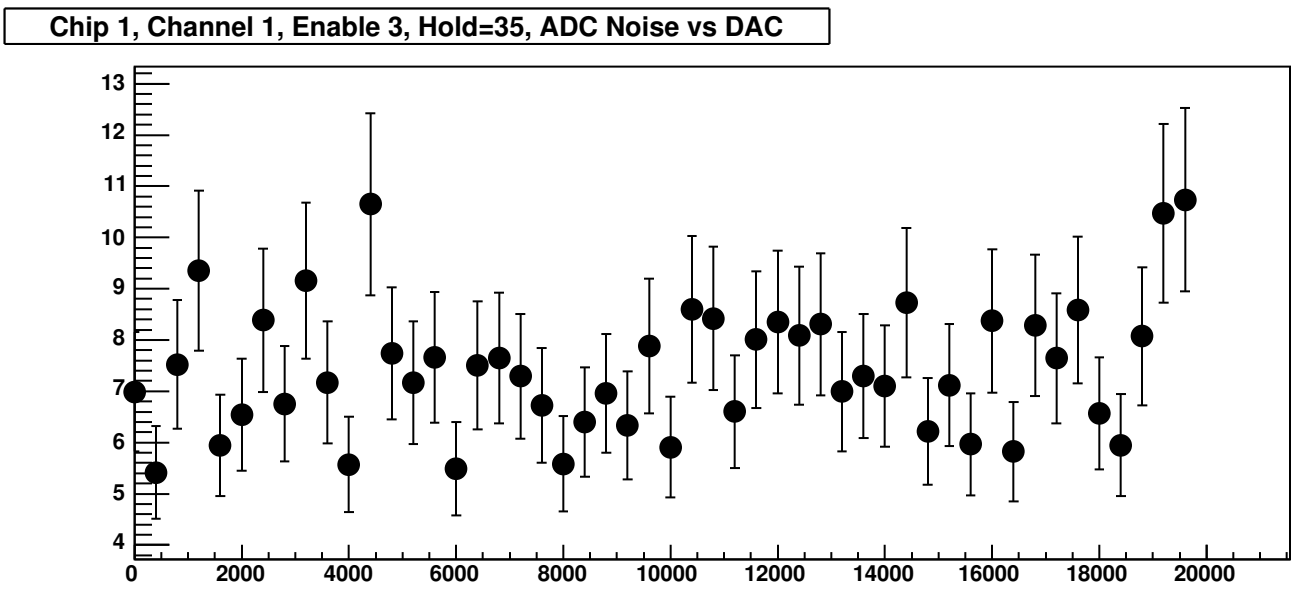
Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

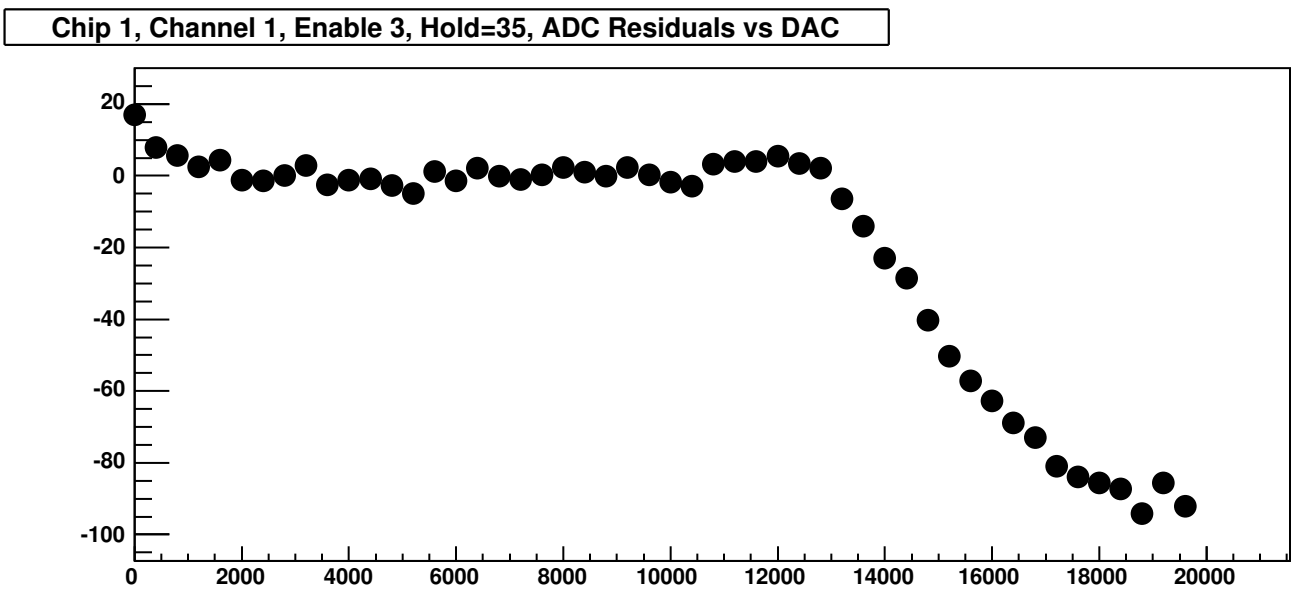


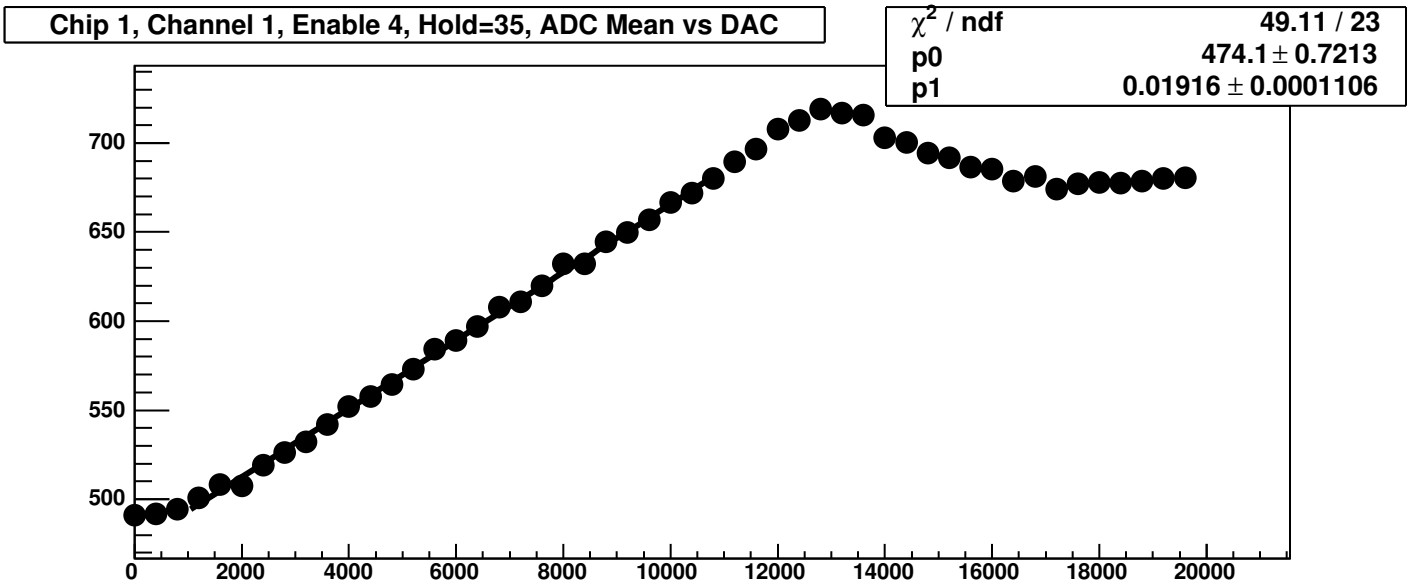
Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

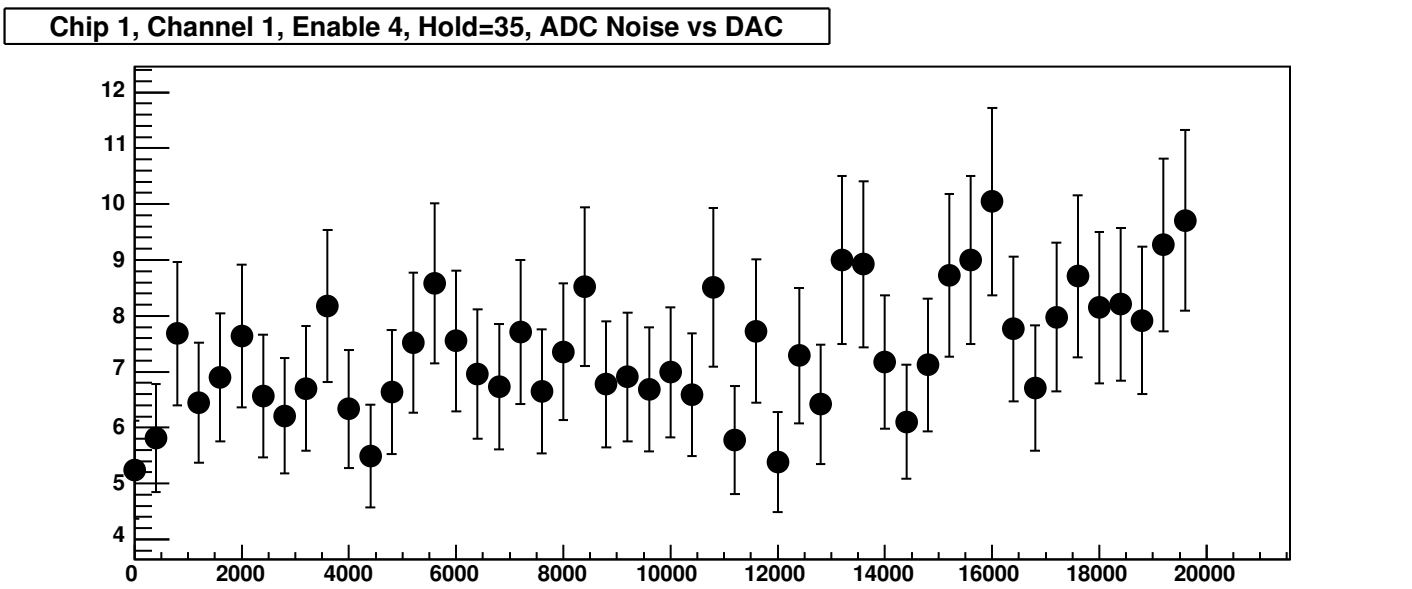


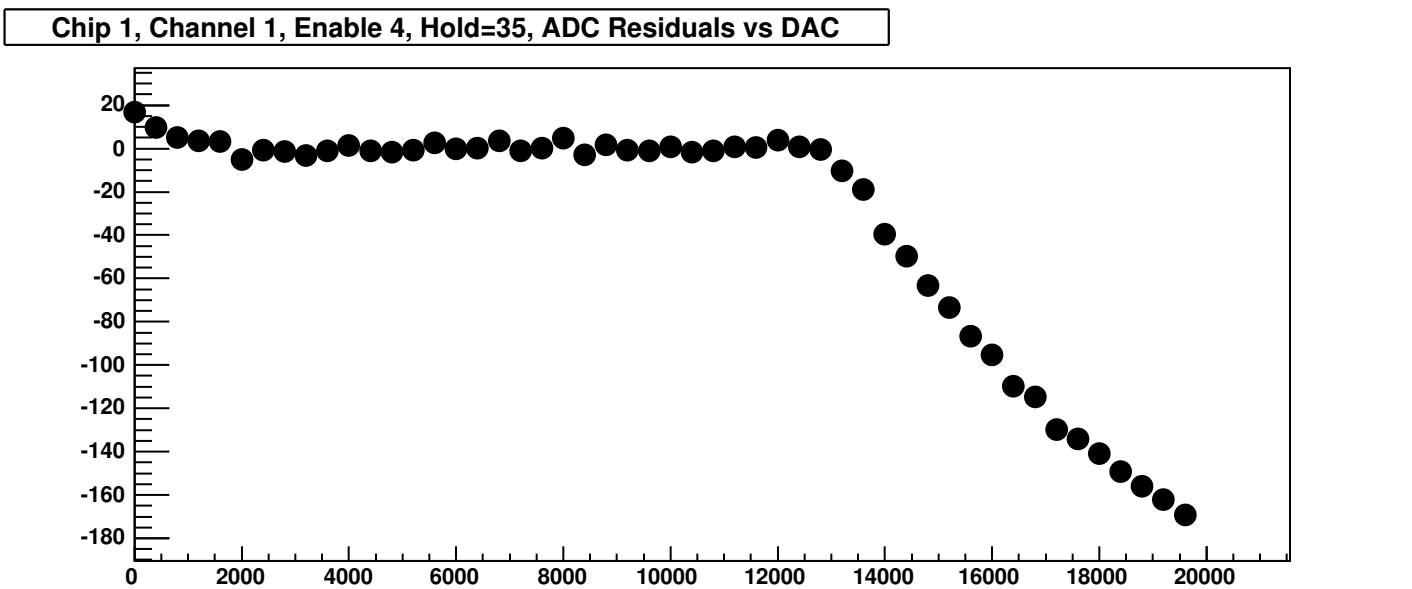

Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 45.42 / 23
p0 470.6 ± 0.7668
p1 0.02053 ± 0.0001144
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

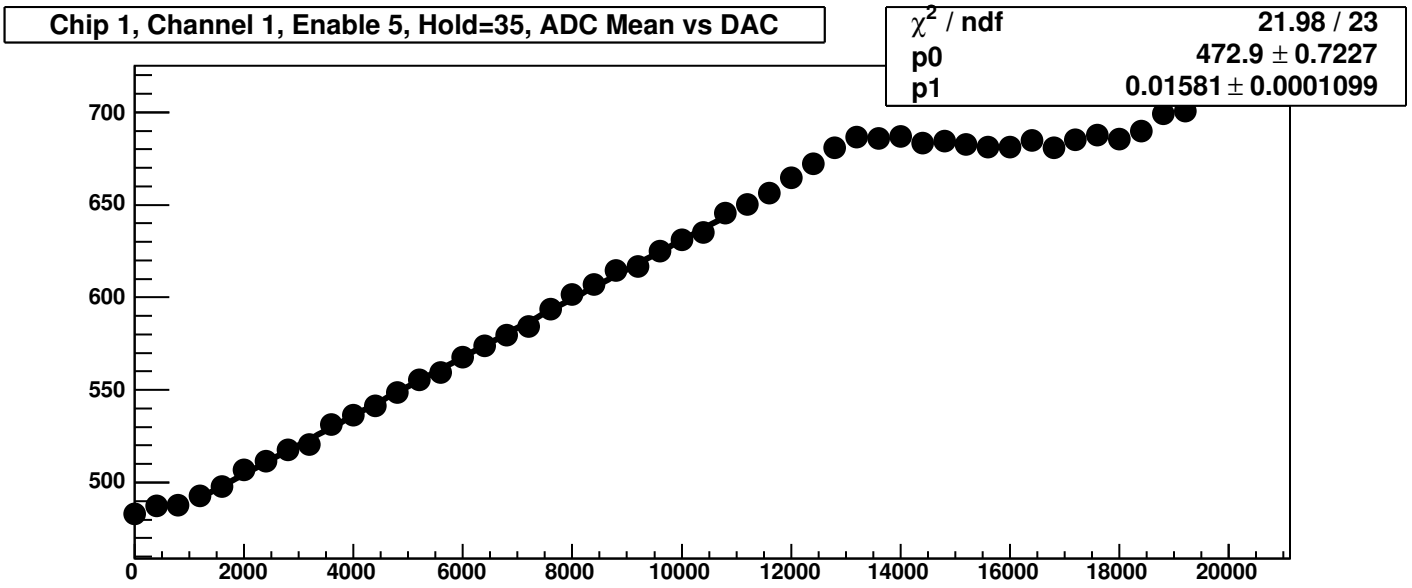

Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

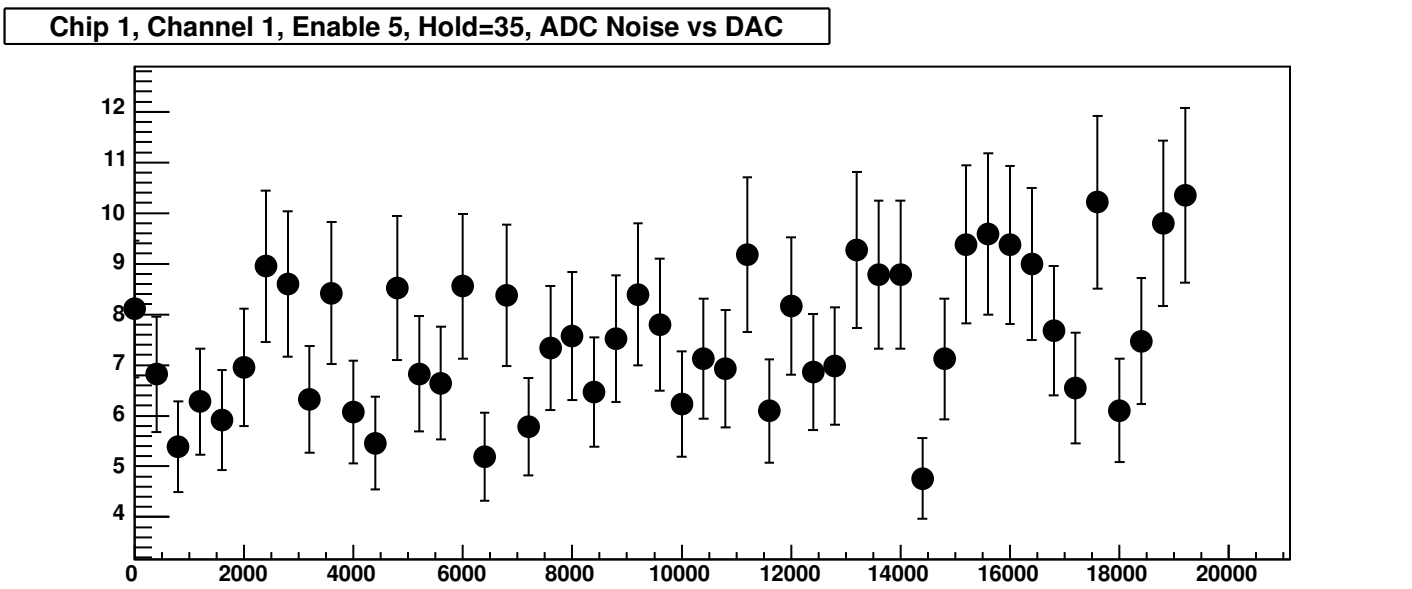

Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

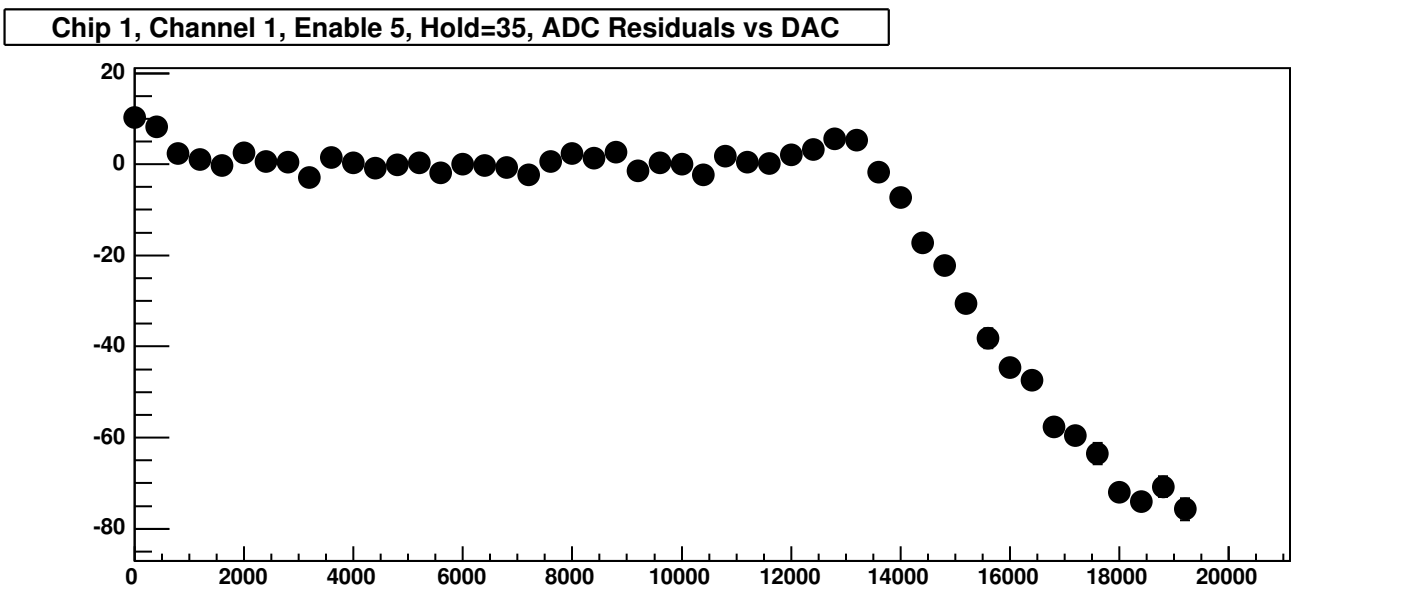

Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 49.11 / 23
p0 474.1 ± 0.7213
p1 0.01916 ± 0.0001106
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

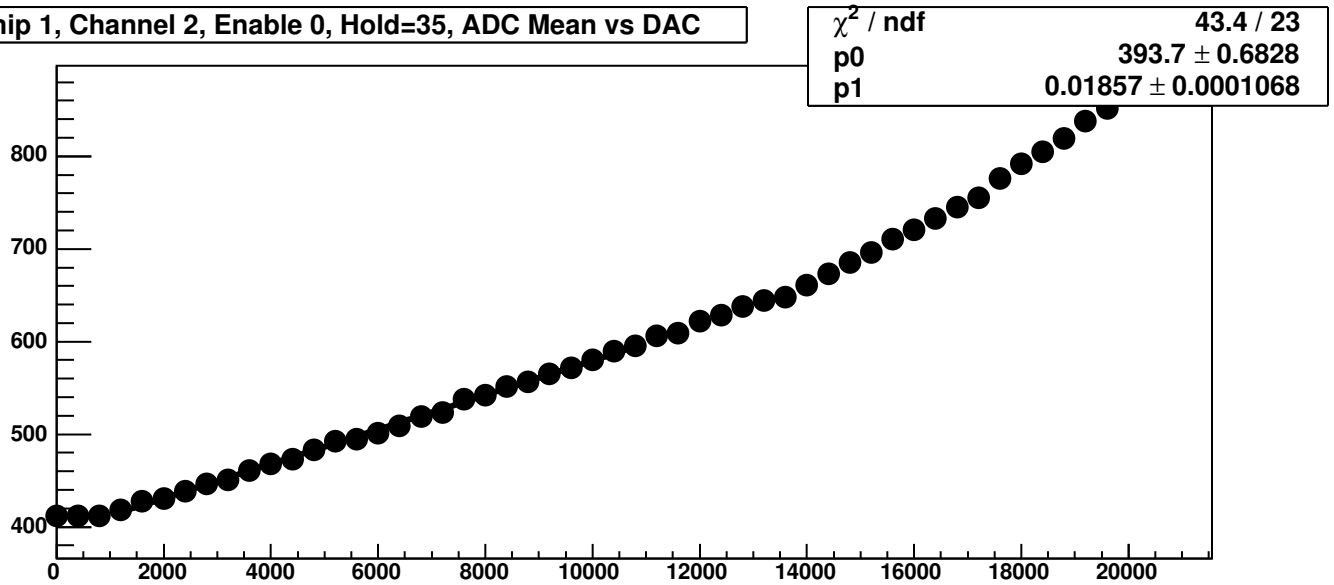

Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 21.98 / 23
p0 472.9 ± 0.7227
p1 0.01581 ± 0.0001099
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

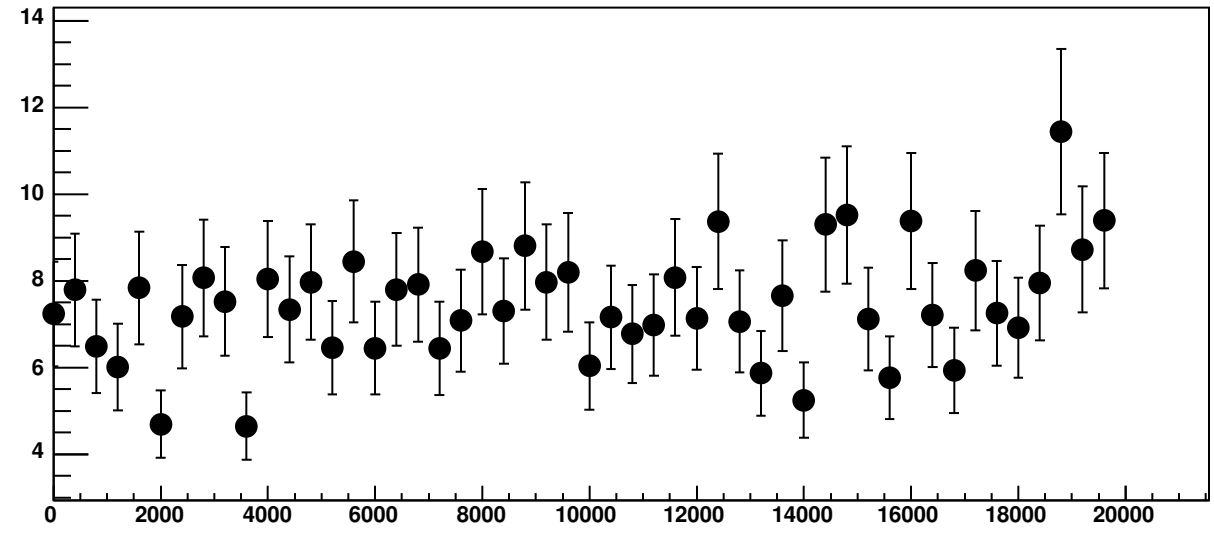

Chip 1, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

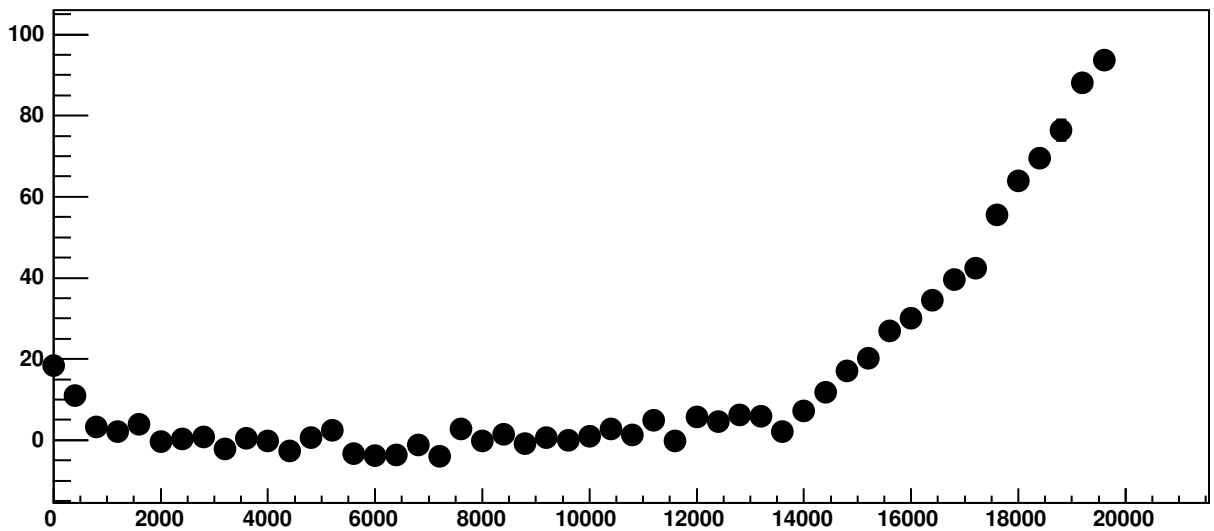
Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



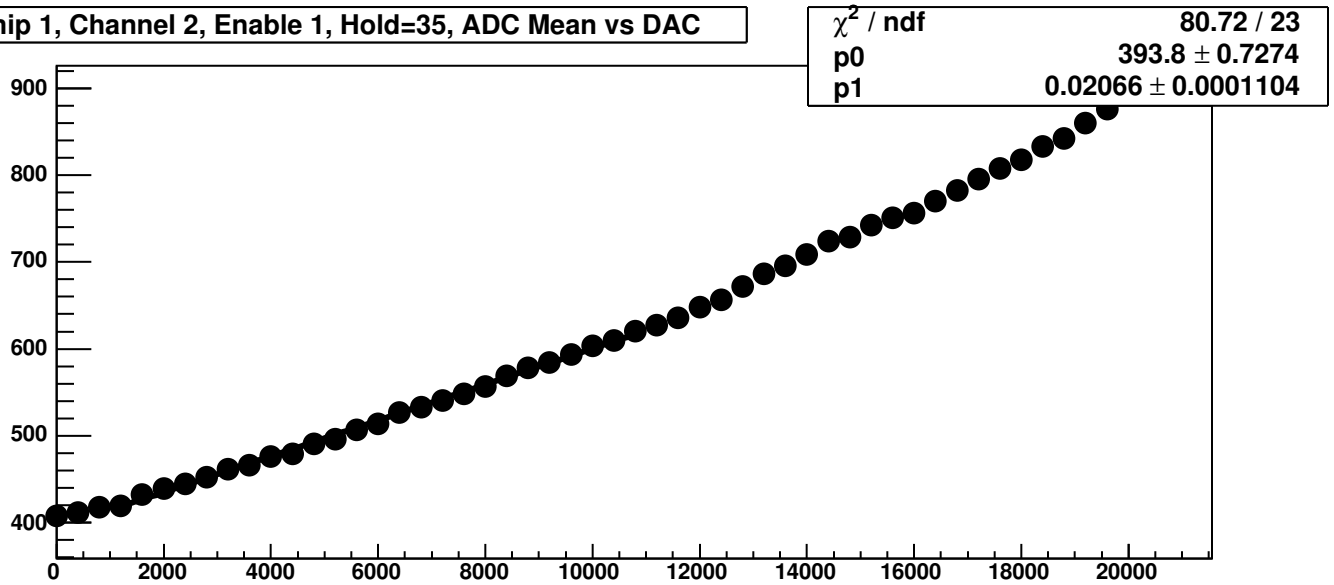
Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



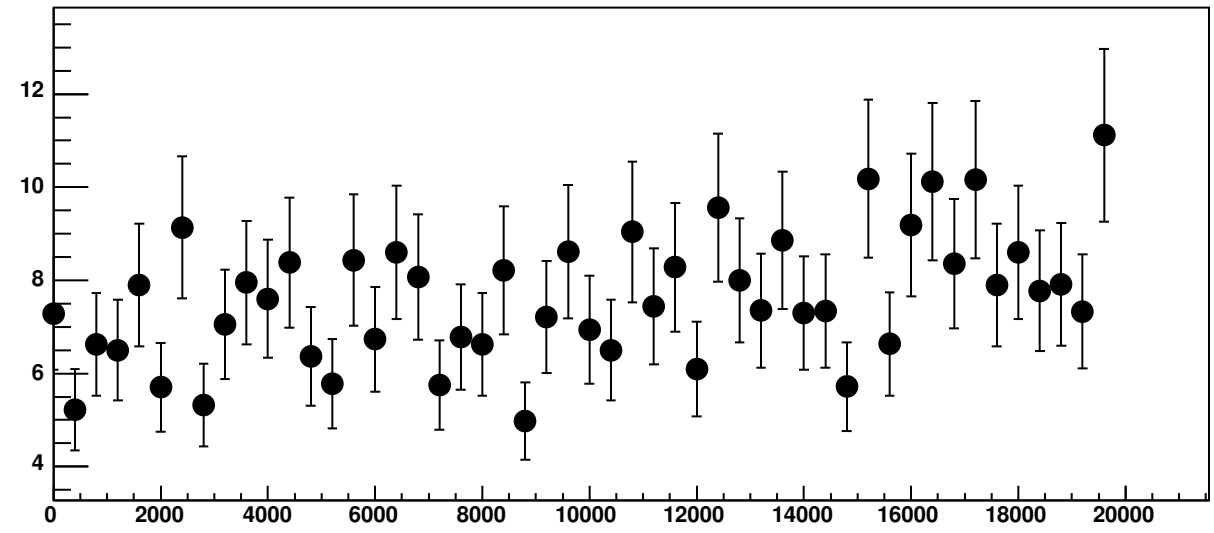
Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



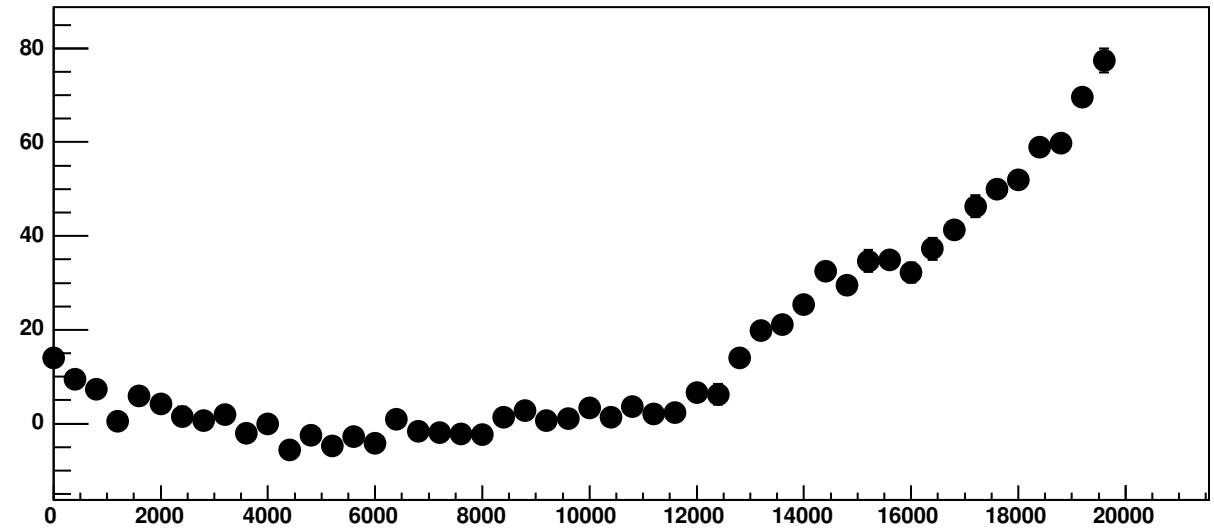
Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



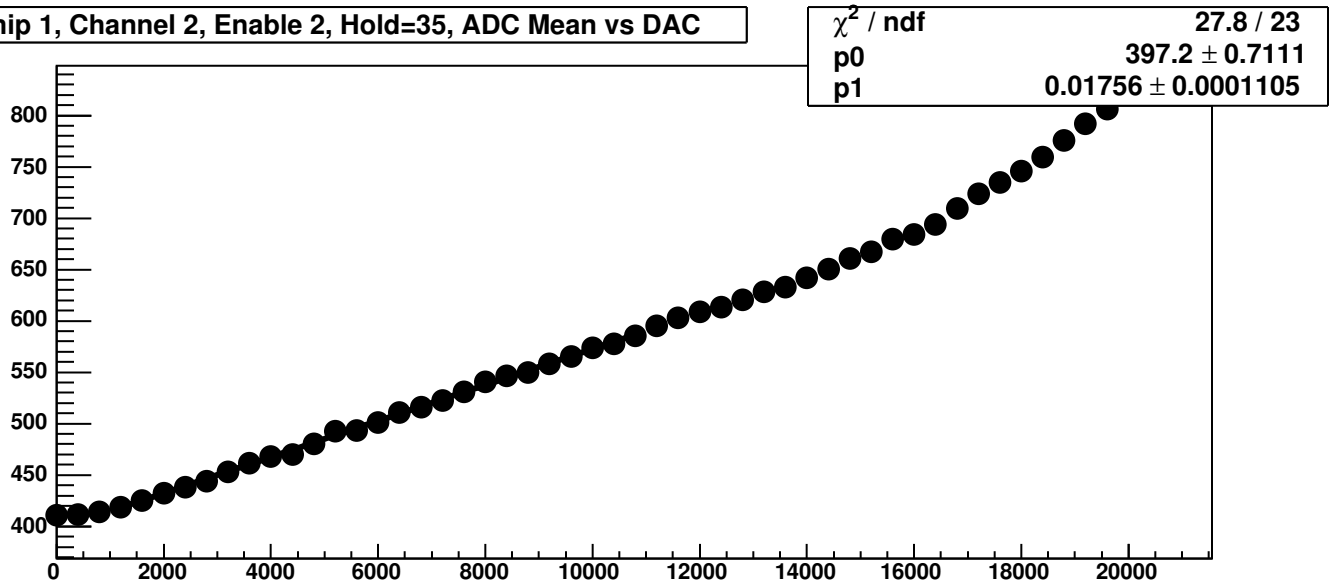
Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



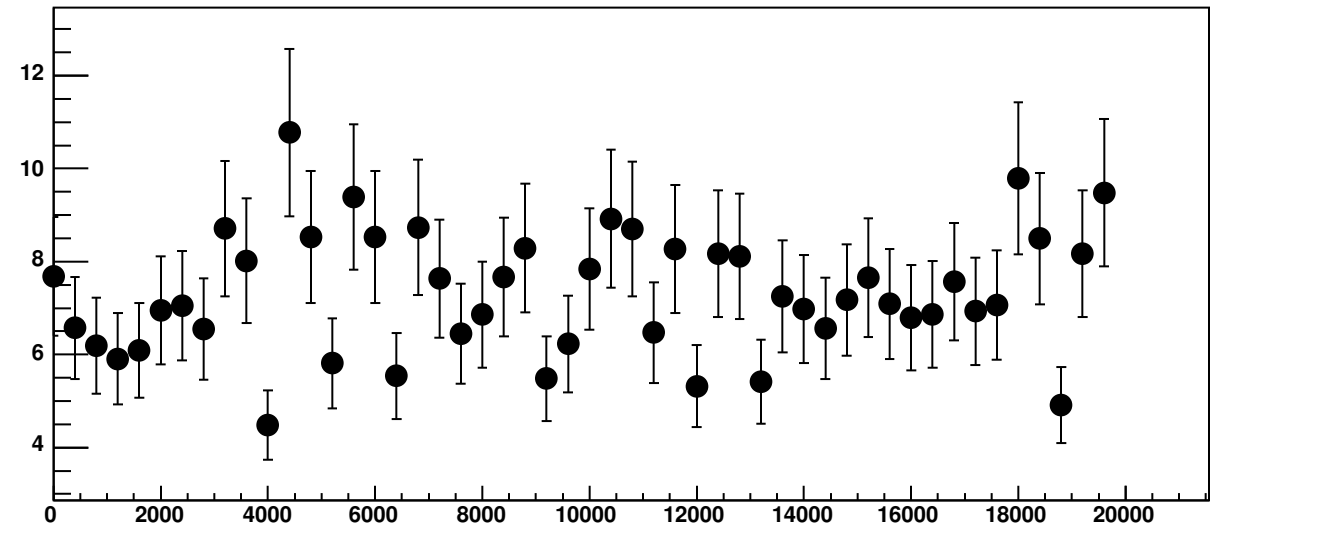
Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



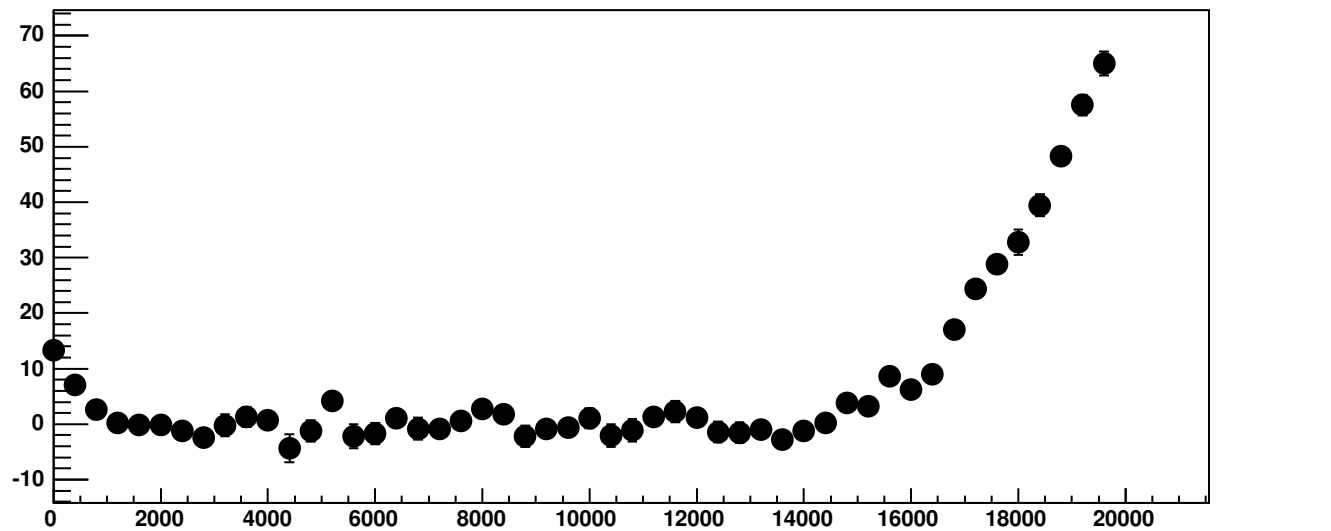
Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

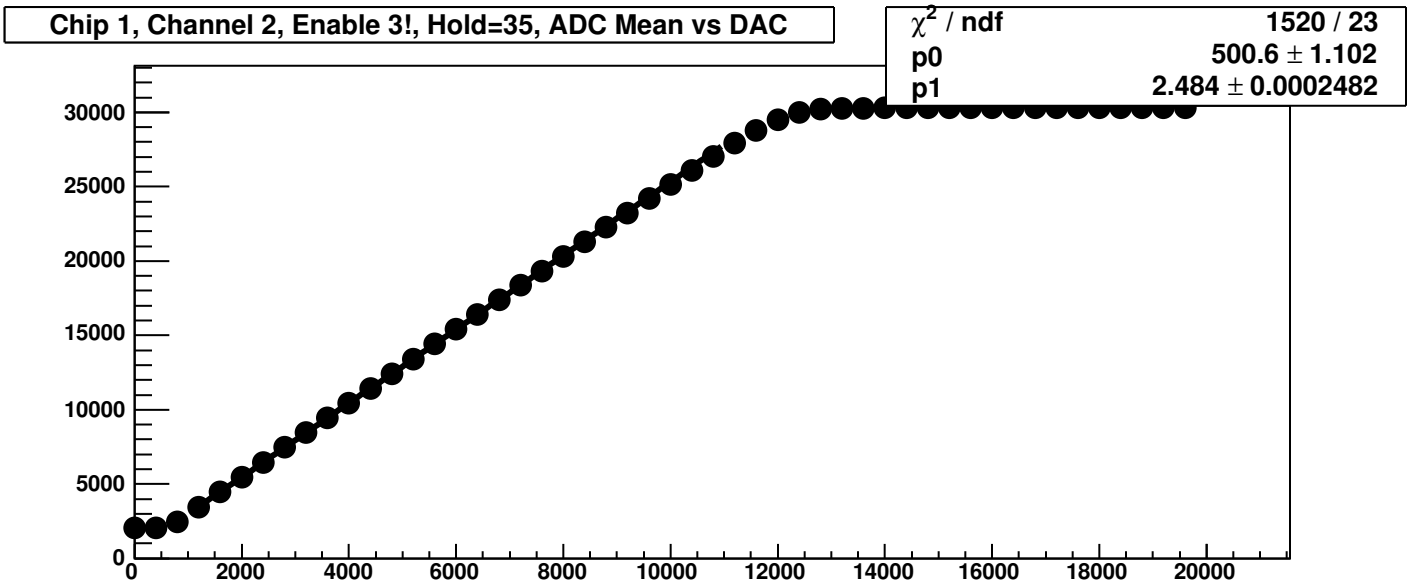


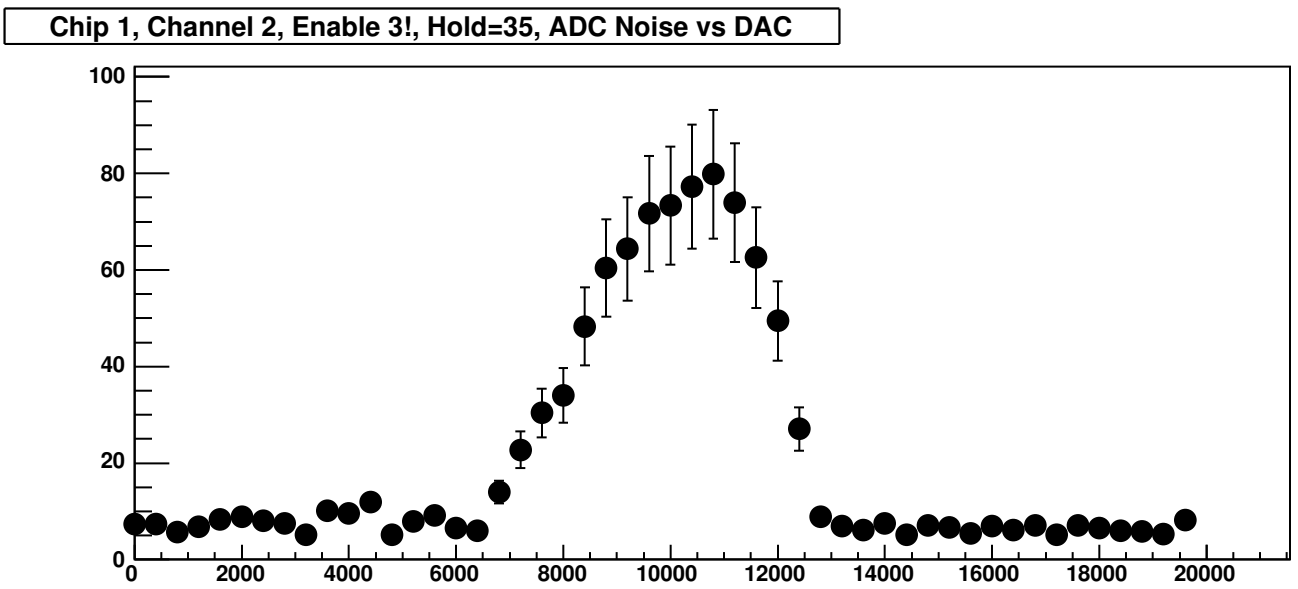
Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

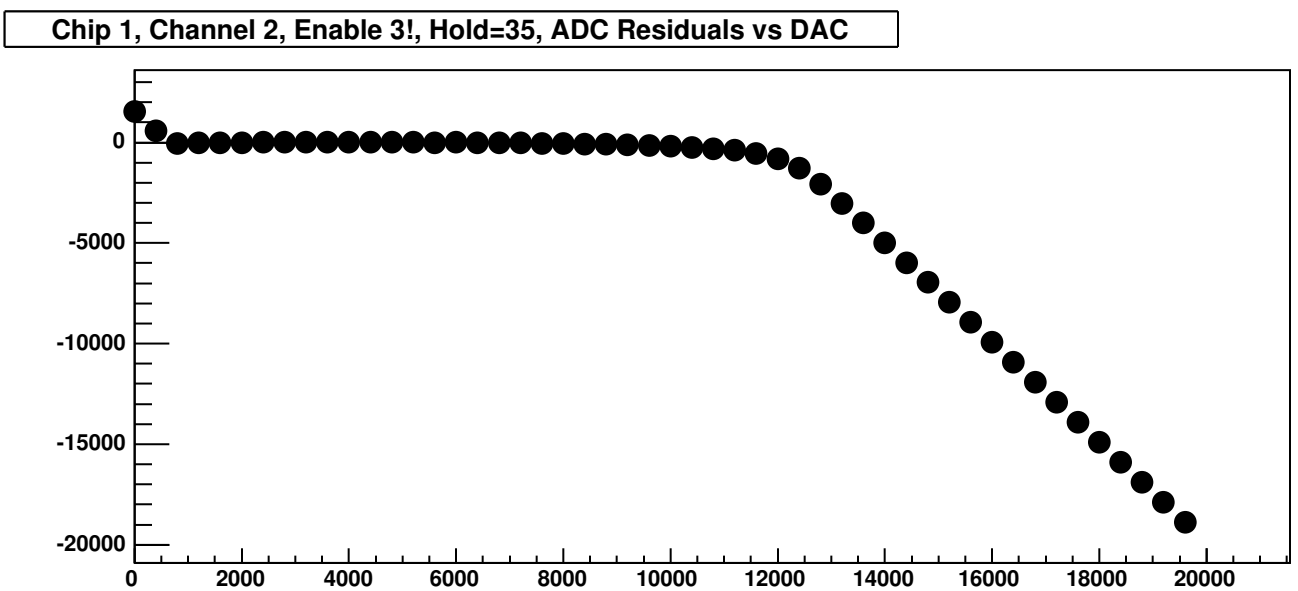


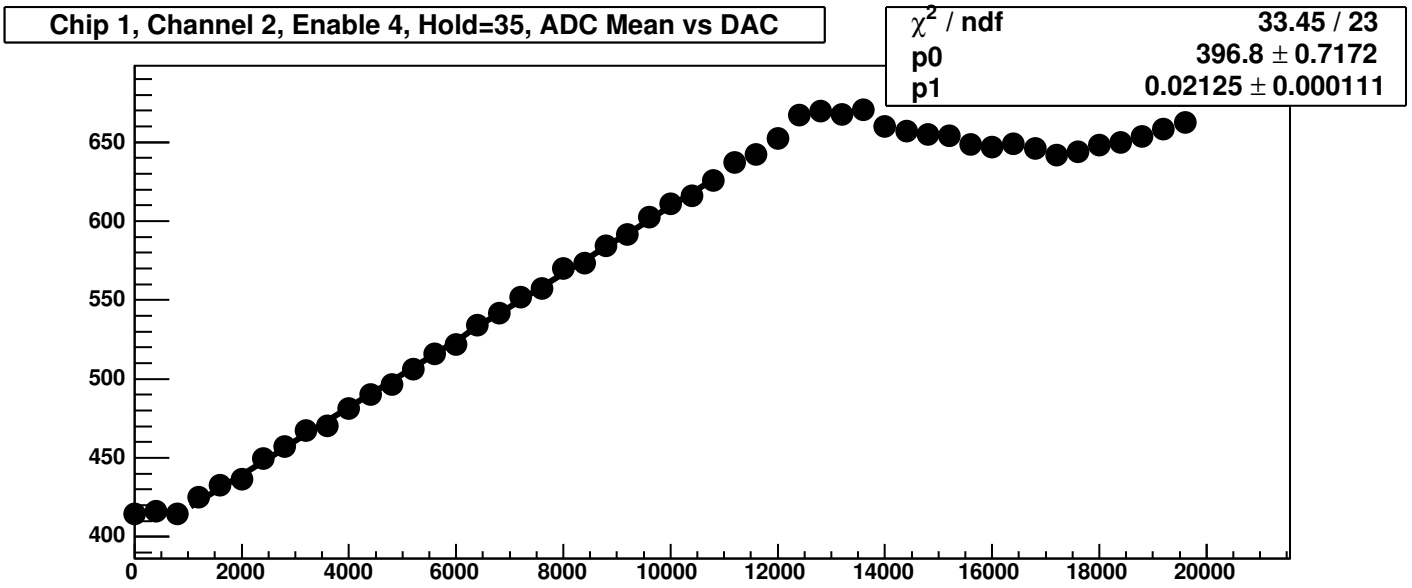
Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

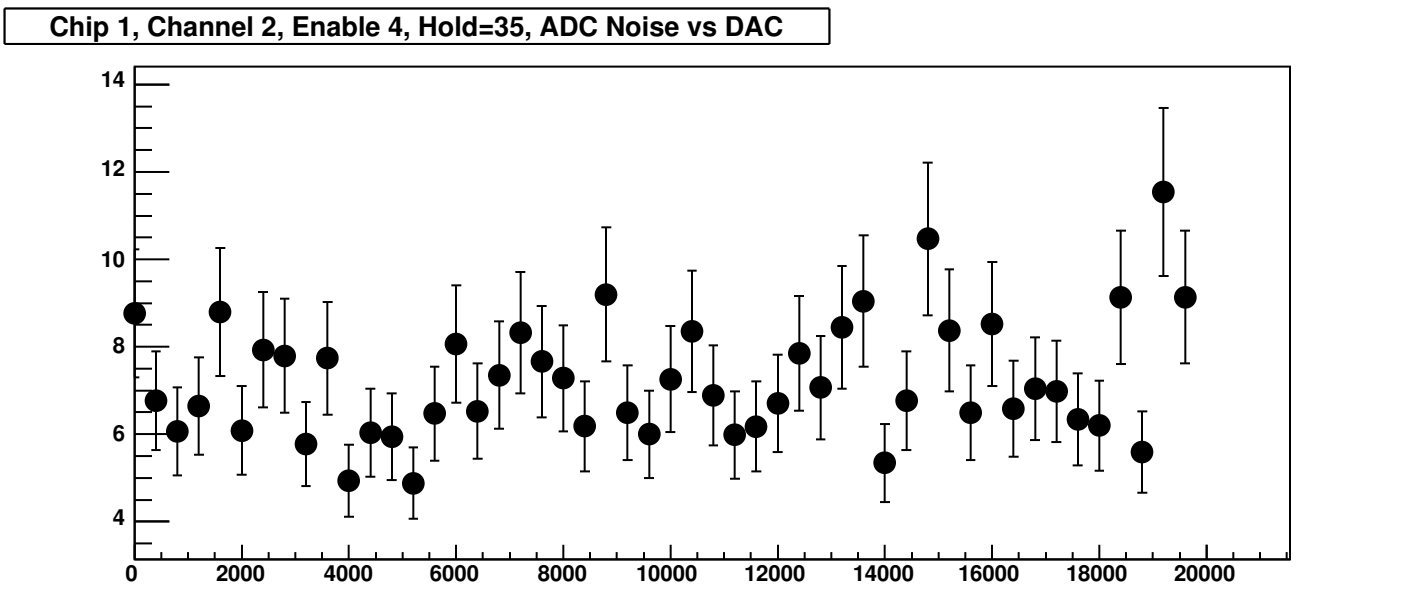


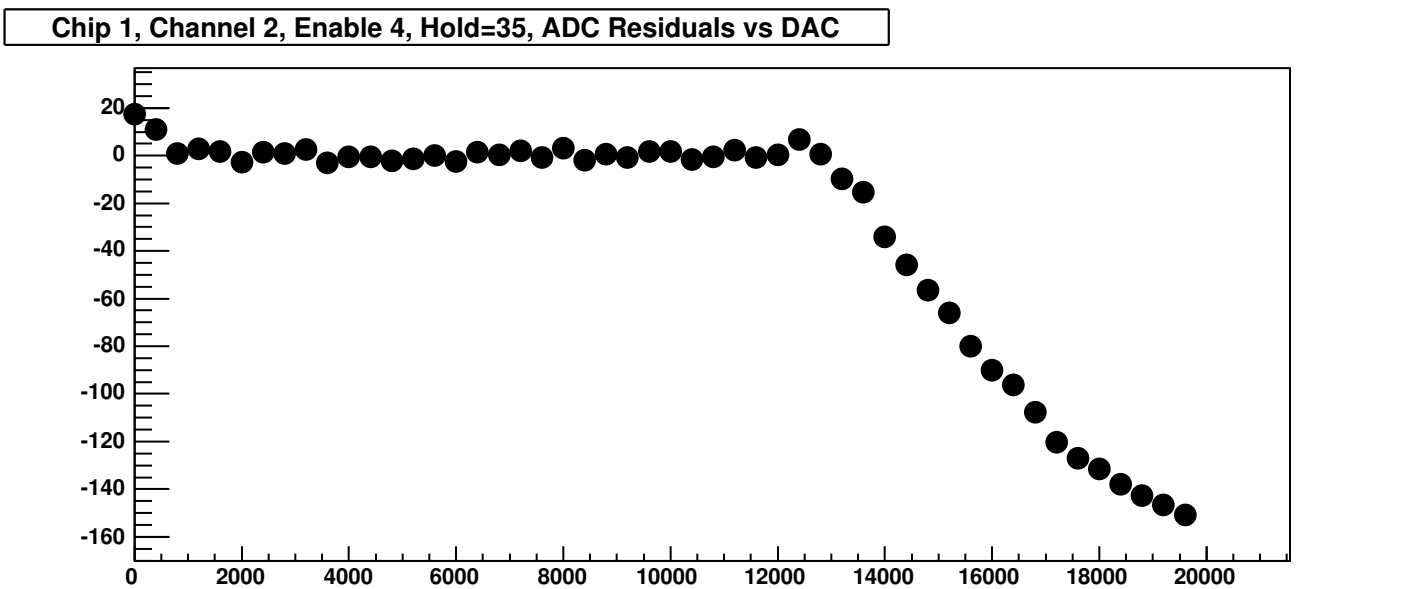

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1520 / 23
p0 500.6 ± 1.102
p1 2.484 ± 0.0002482
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

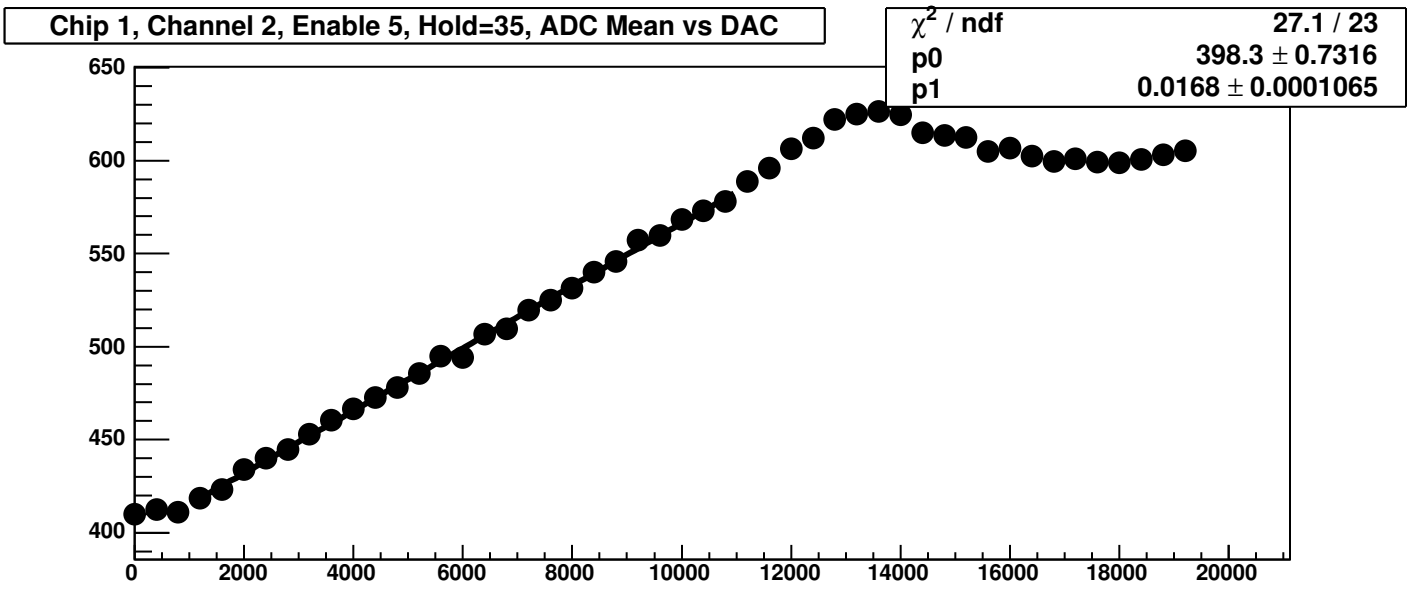

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

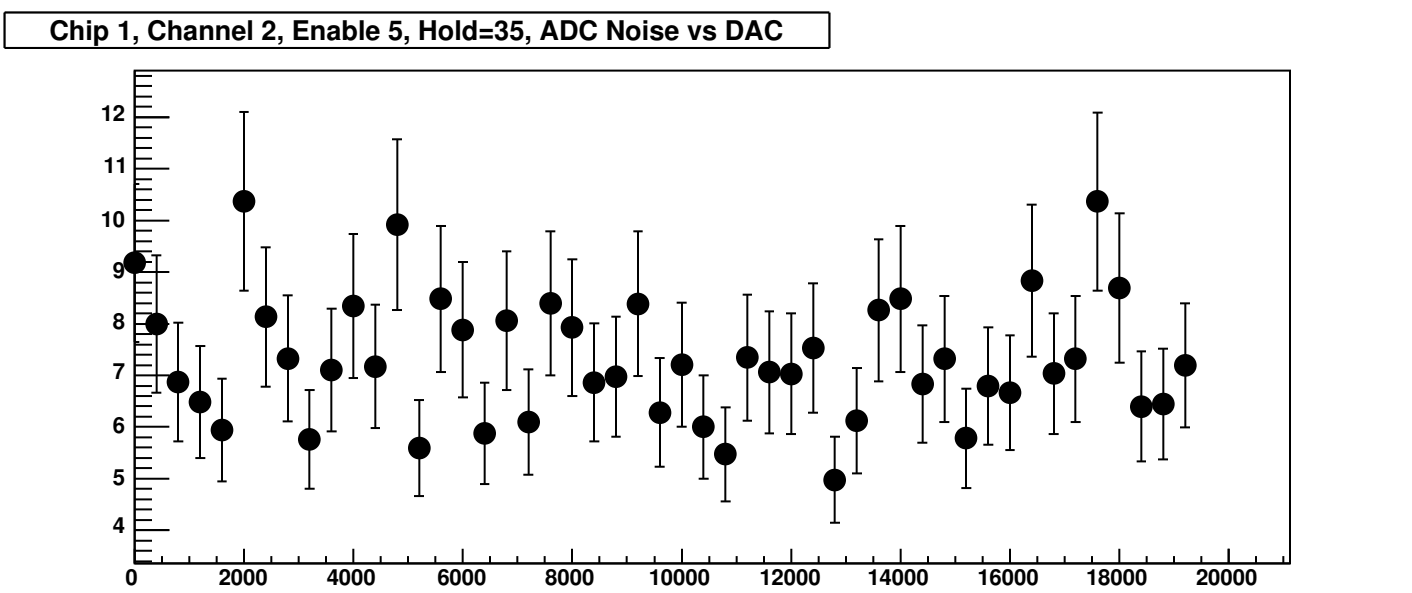

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

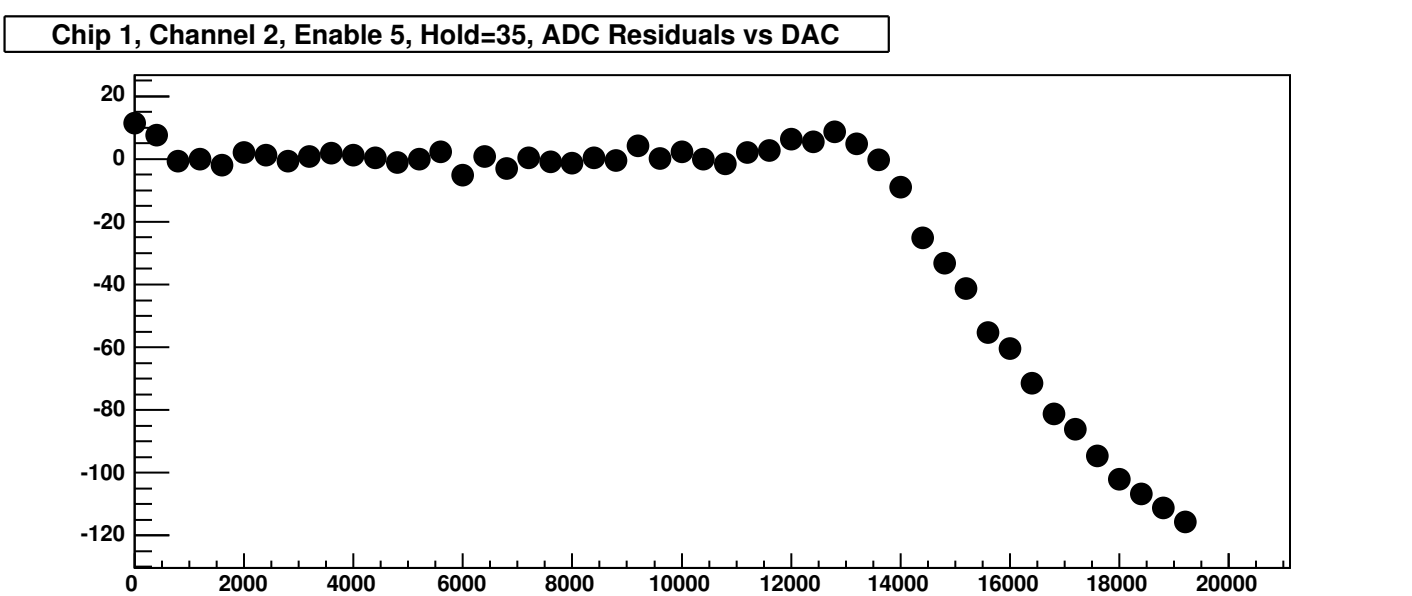

Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.45 / 23
p0 396.8 ± 0.7172
p1 0.02125 ± 0.000111
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

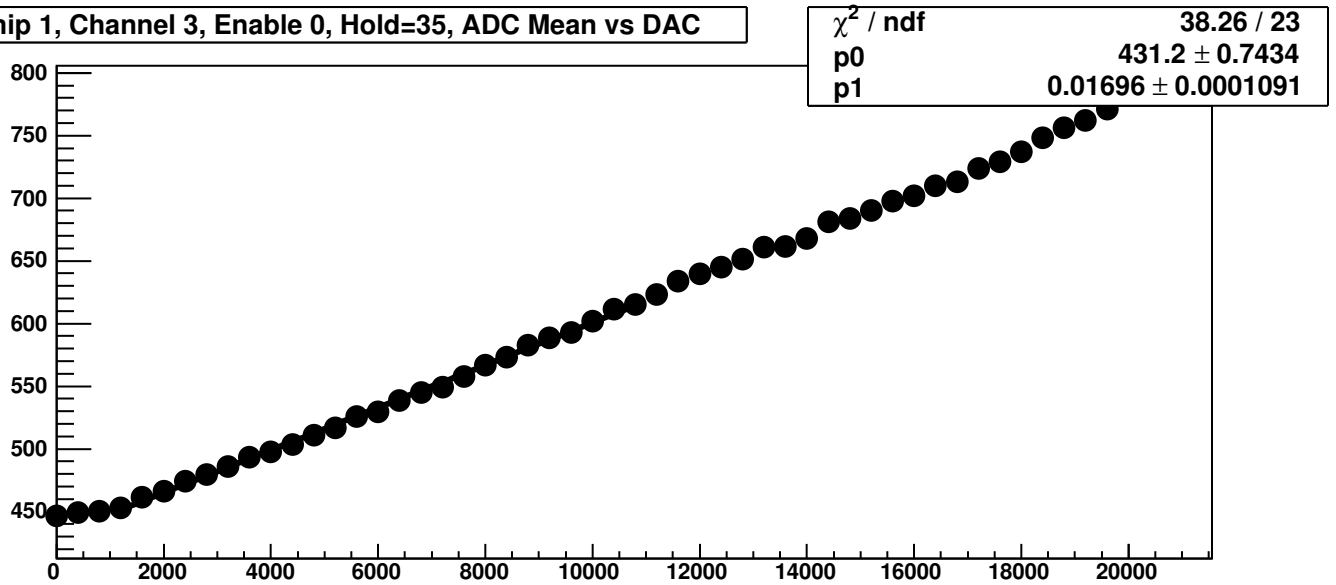

Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 27.1 / 23
p0 398.3 ± 0.7316
p1 0.0168 ± 0.0001065
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

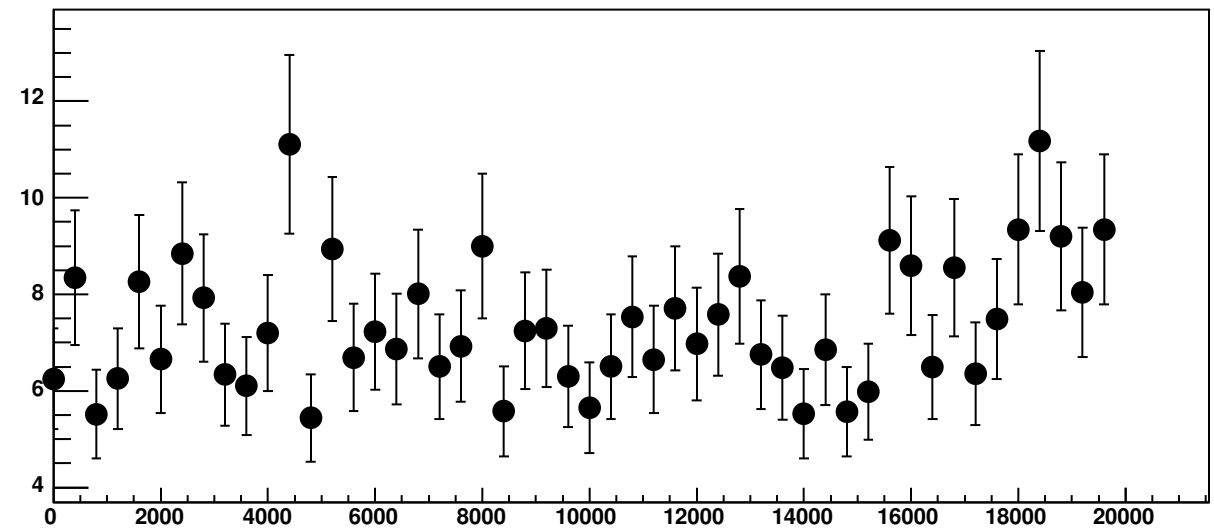

Chip 1, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

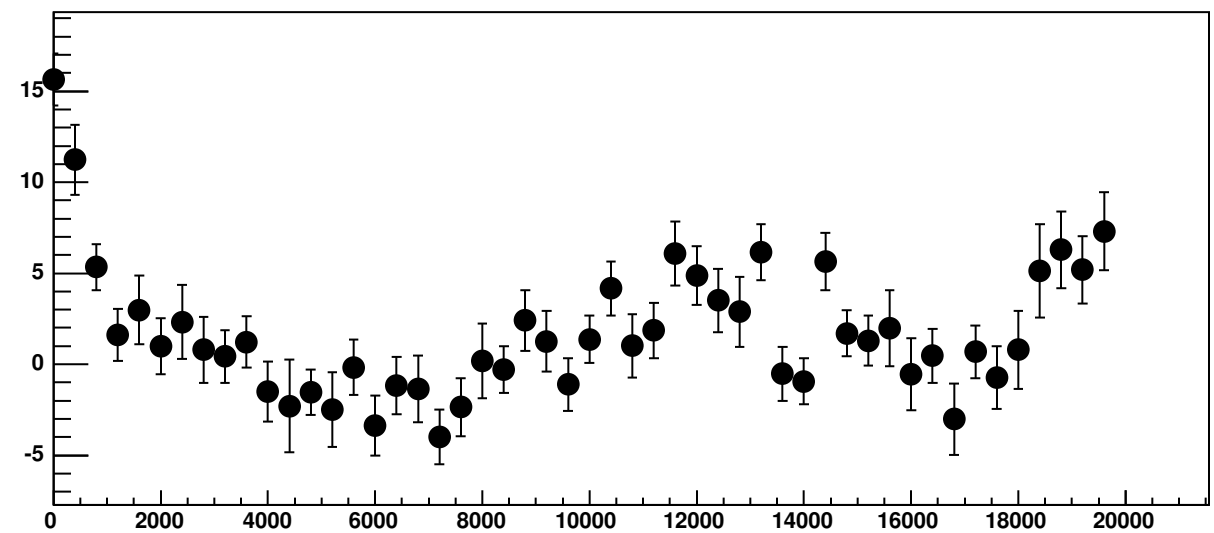
Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

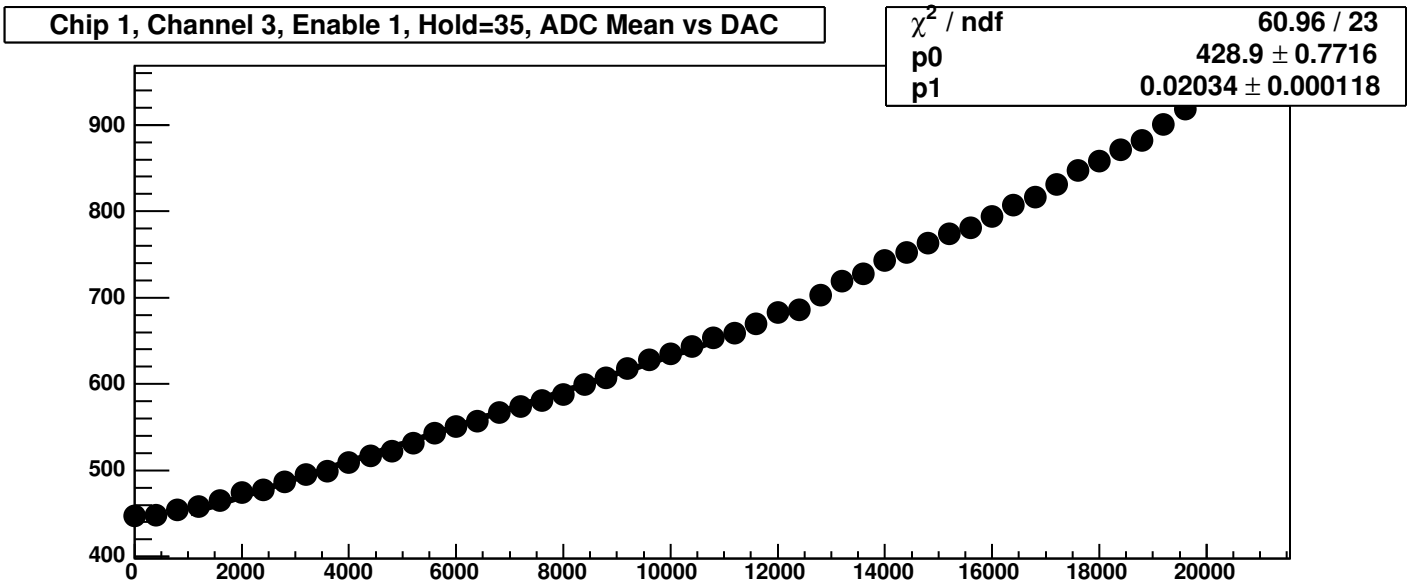


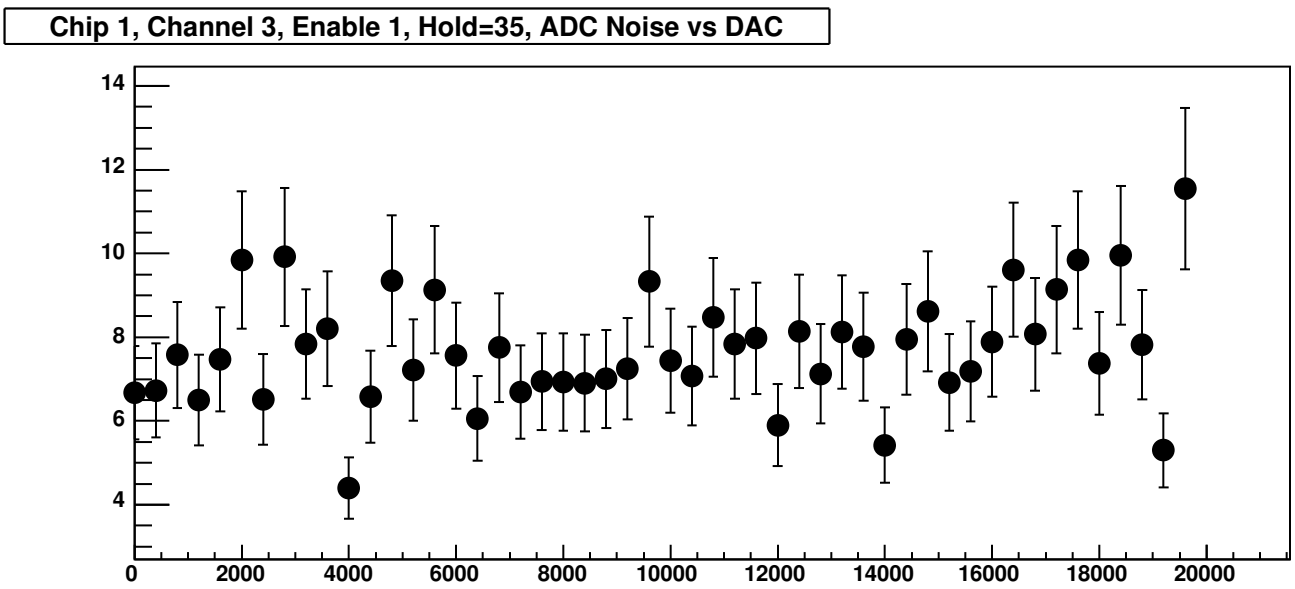
Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

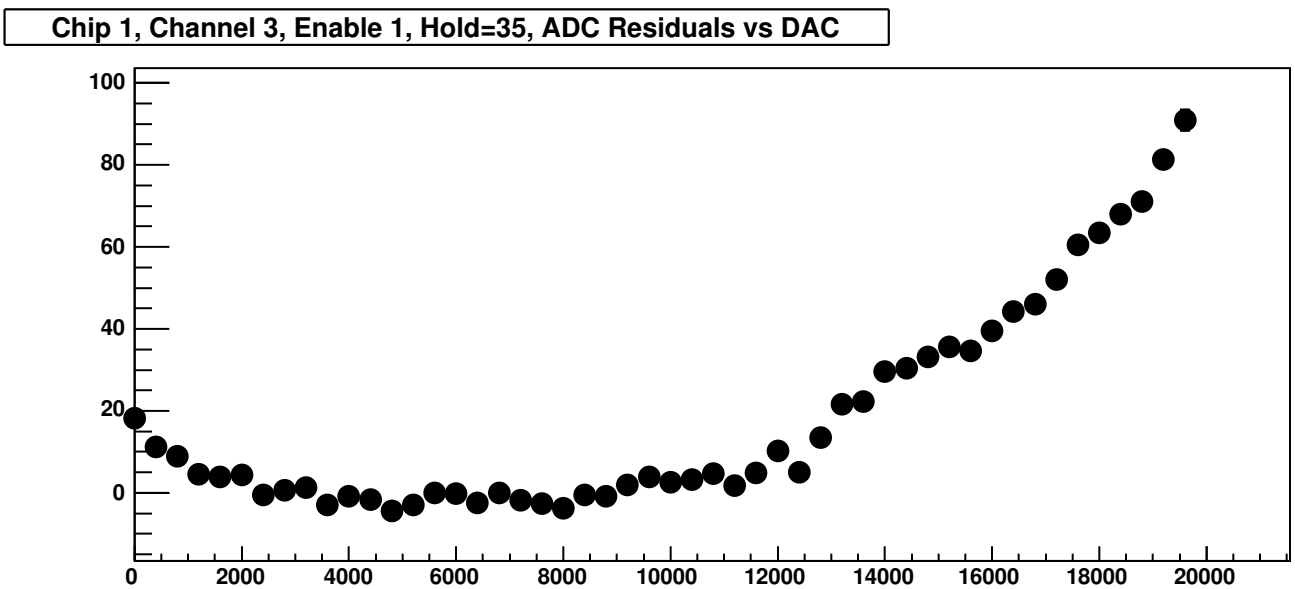


Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

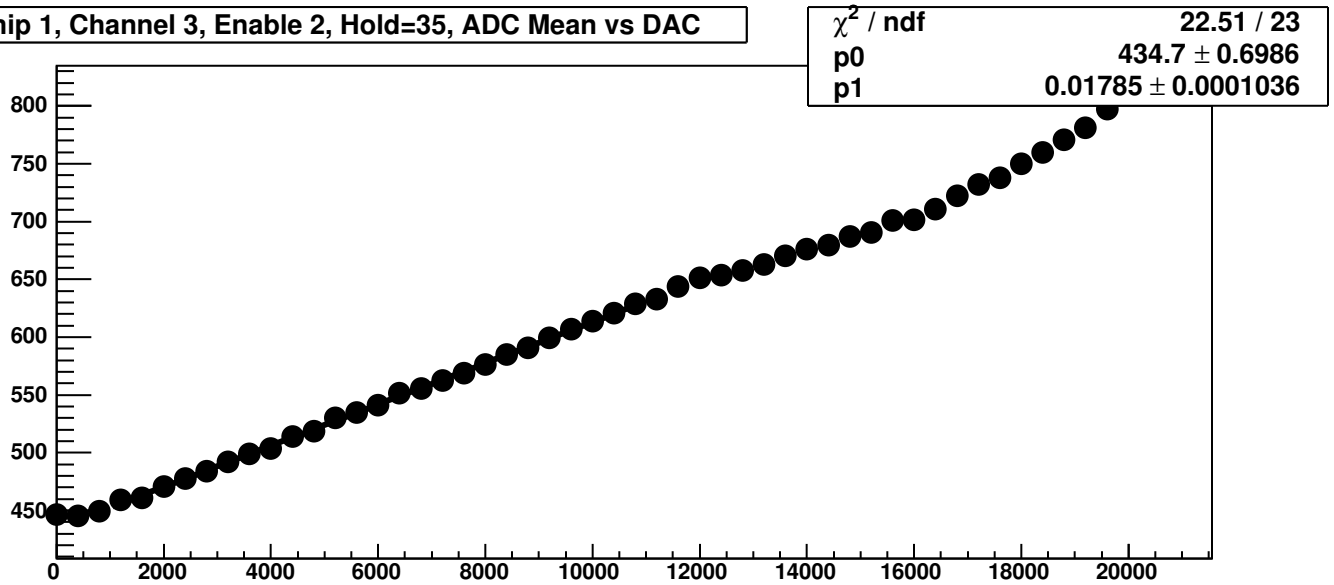



Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60.96 / 23
p0 428.9 ± 0.7716
p1 0.02034 ± 0.000118
900
800
700
600
500
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

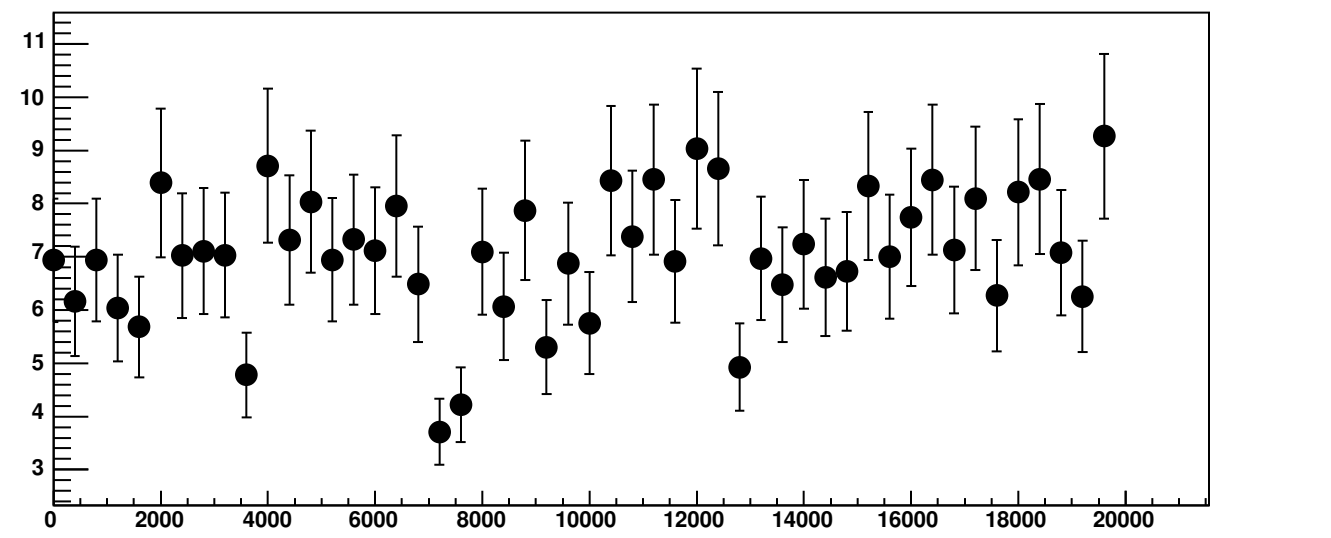

Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

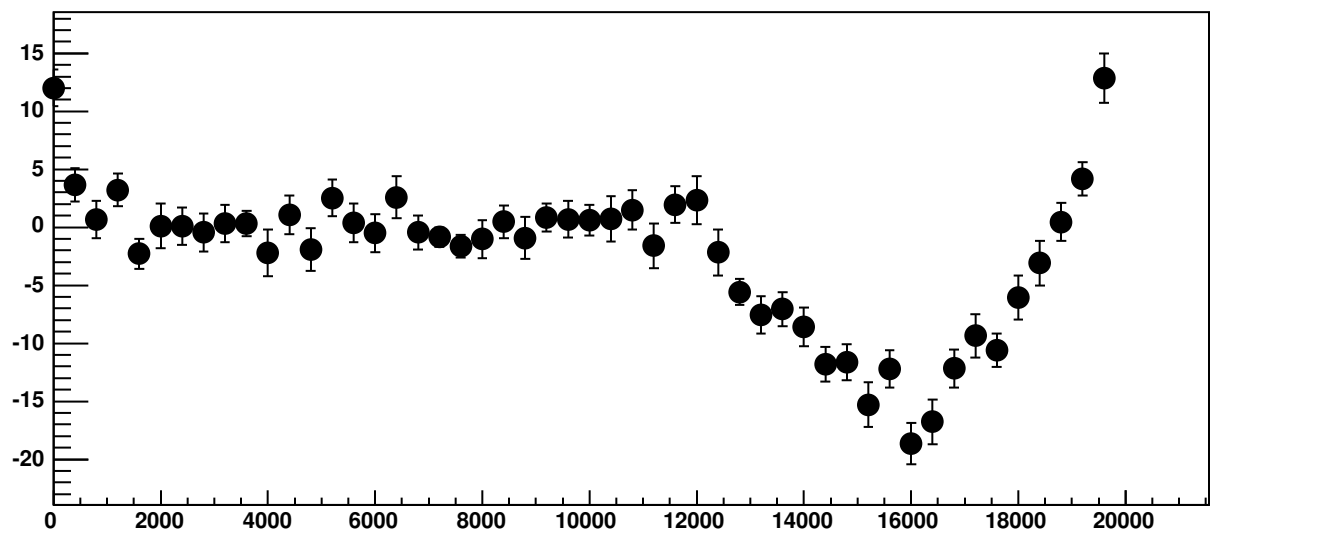
Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

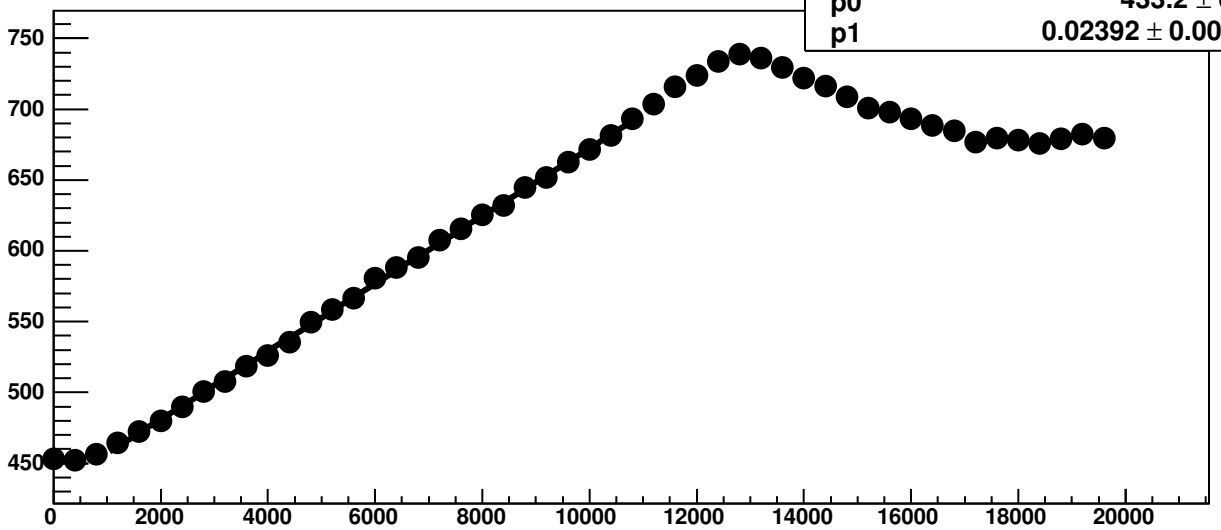


Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

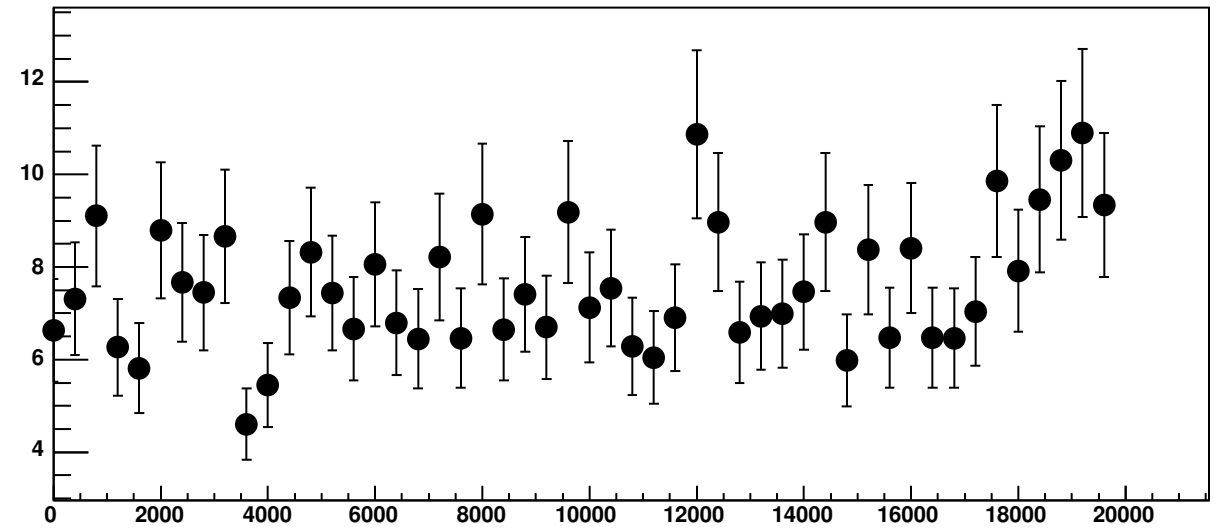


Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

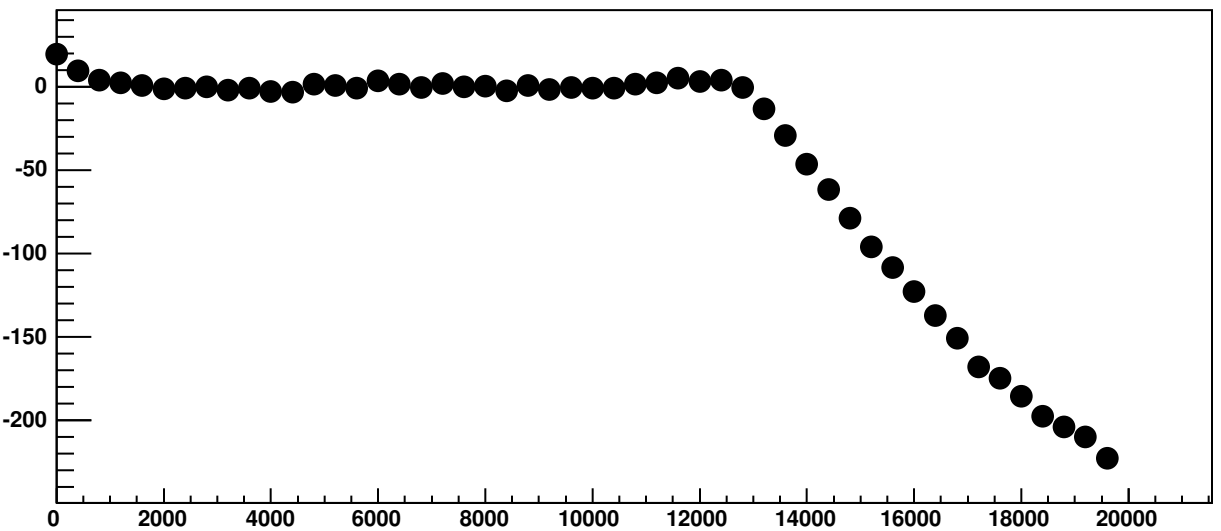
χ^2 / ndf	26.01 / 23
p0	433.2 ± 0.7101
p1	0.02392 ± 0.0001096

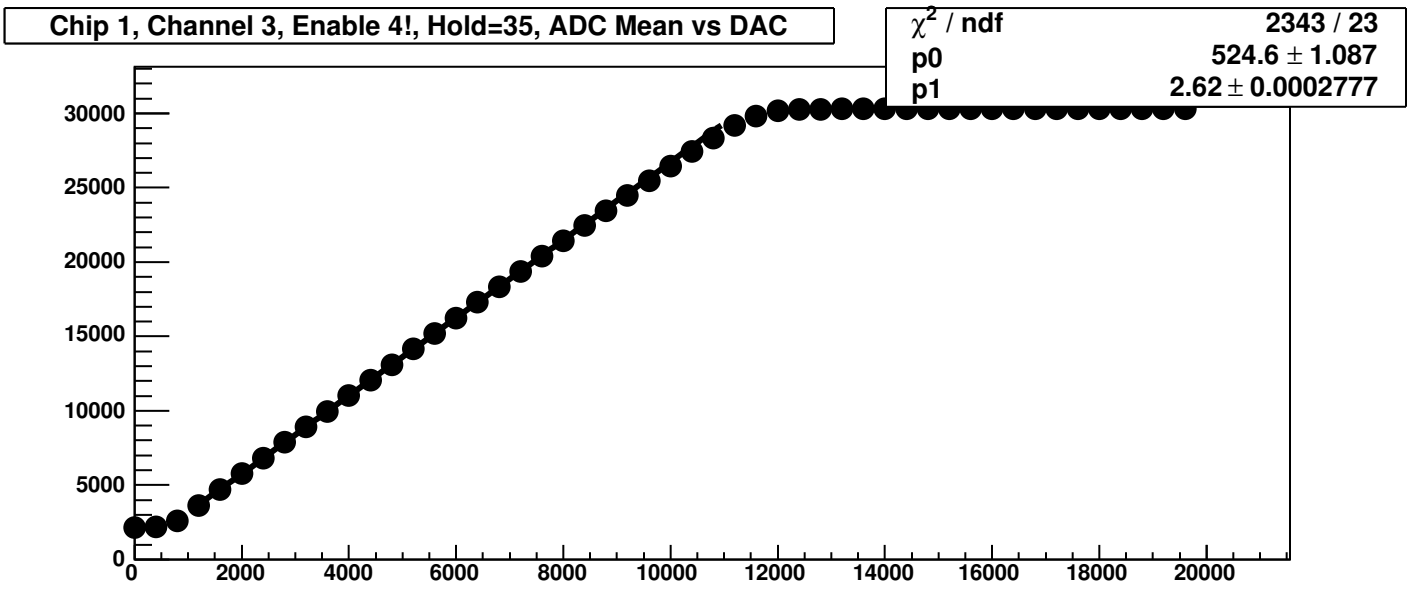


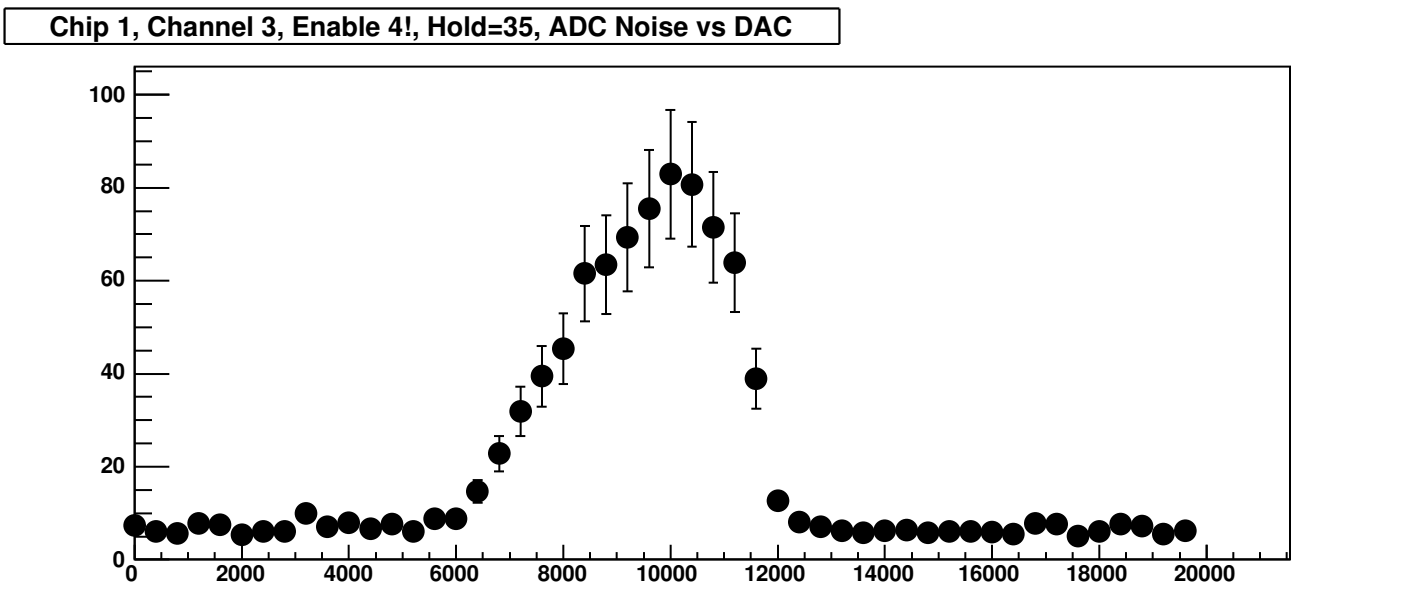
Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

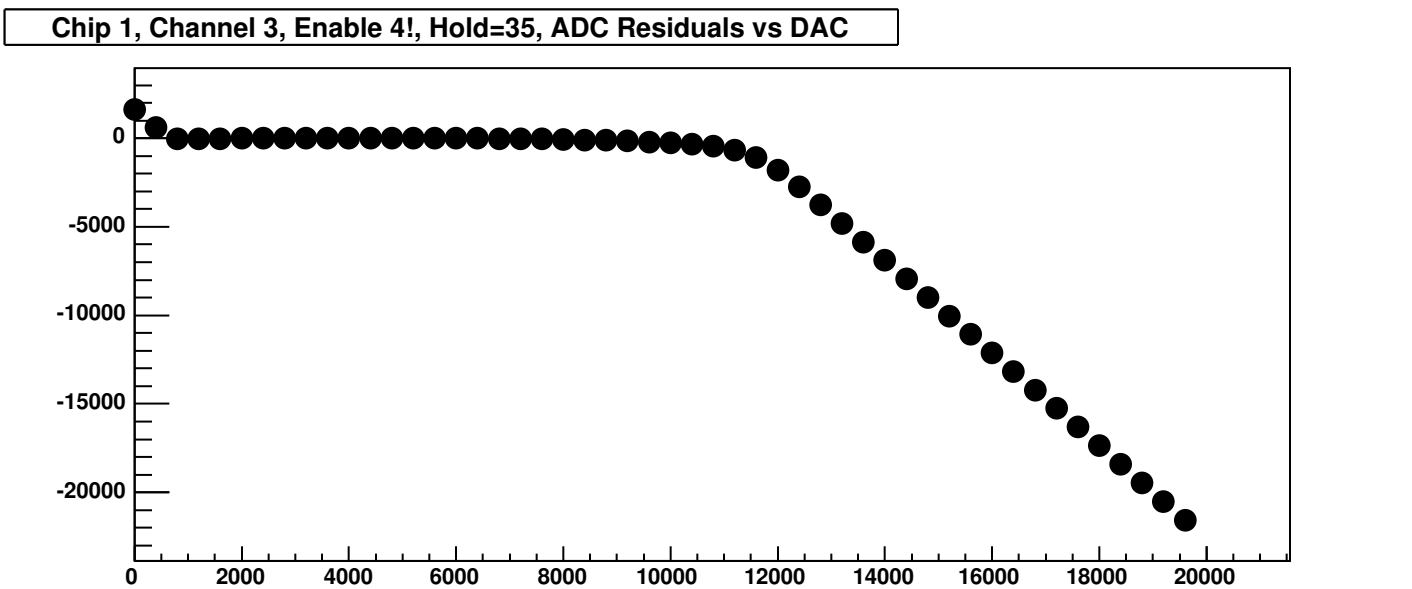


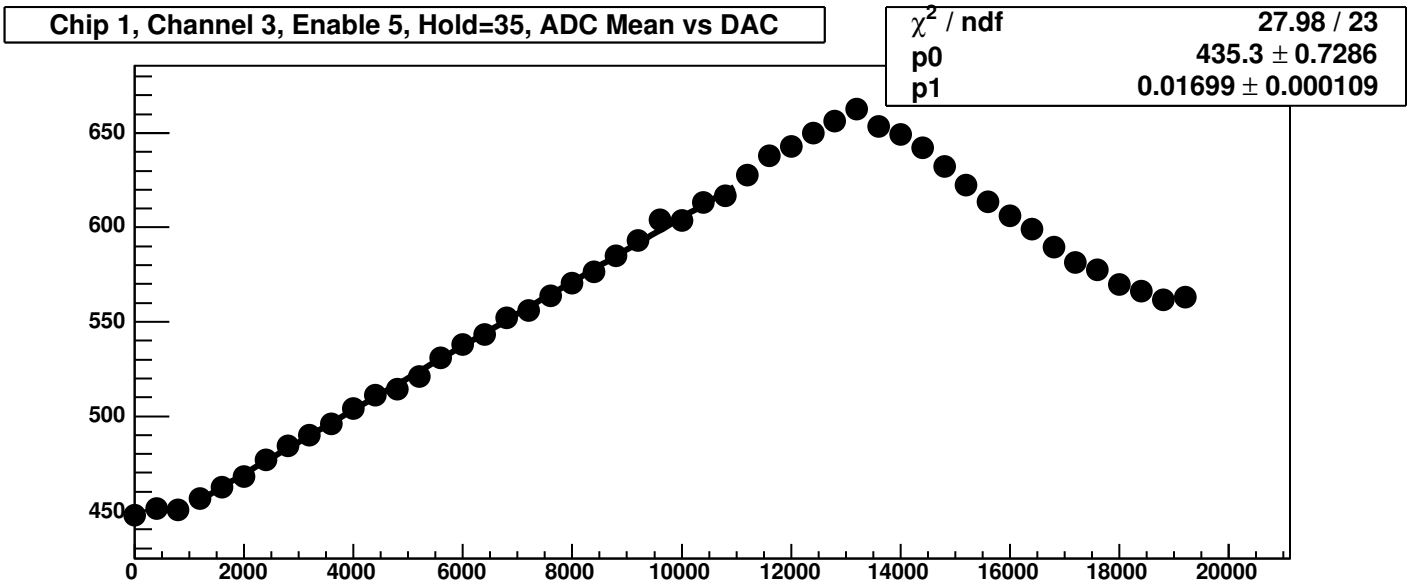
Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

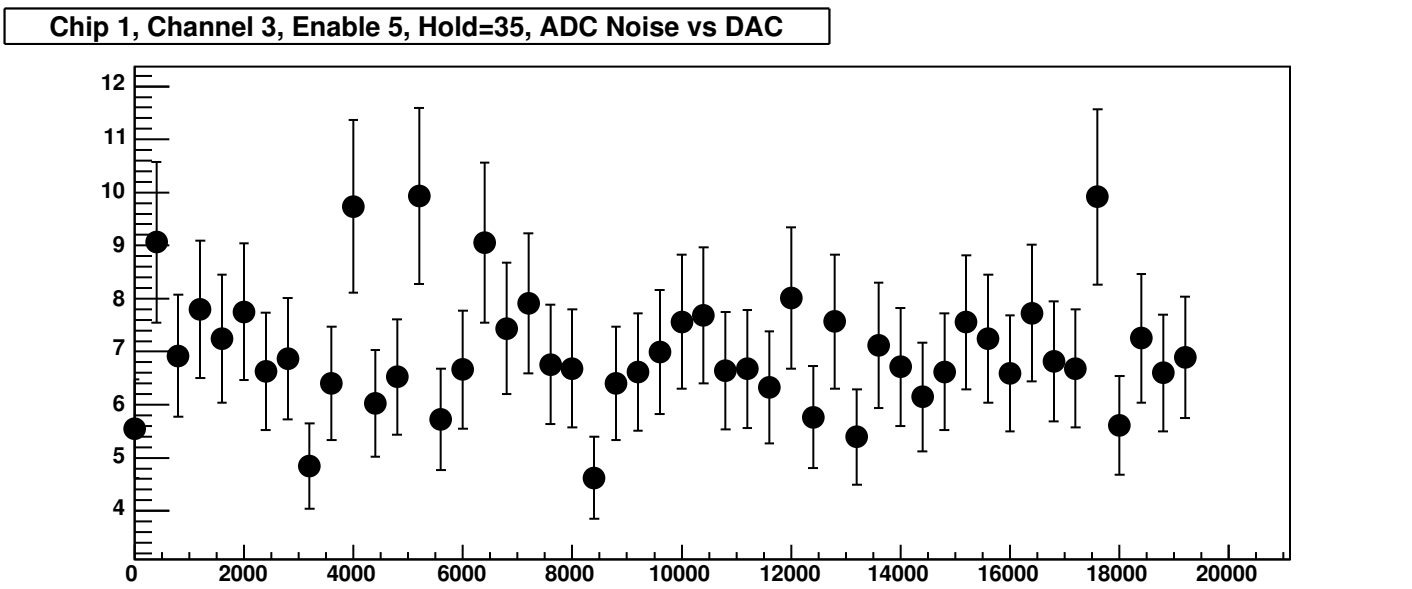


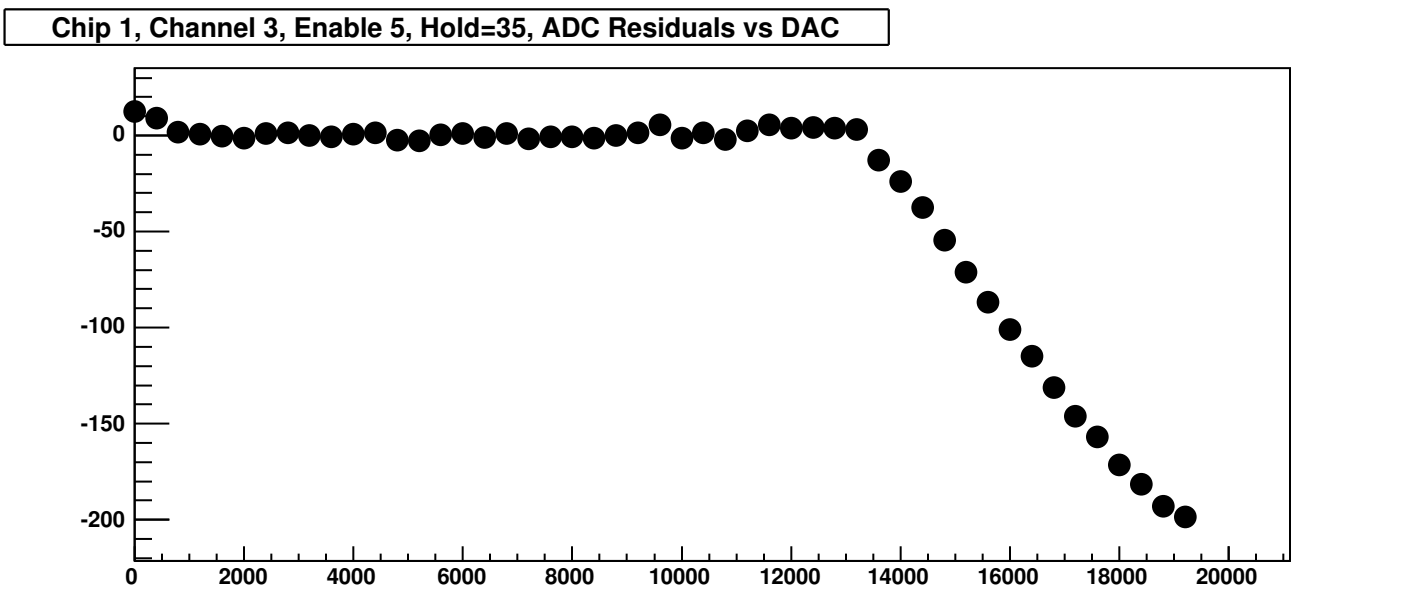

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 2343 / 23
p0 524.6 ± 1.087
p1 2.62 ± 0.0002777
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

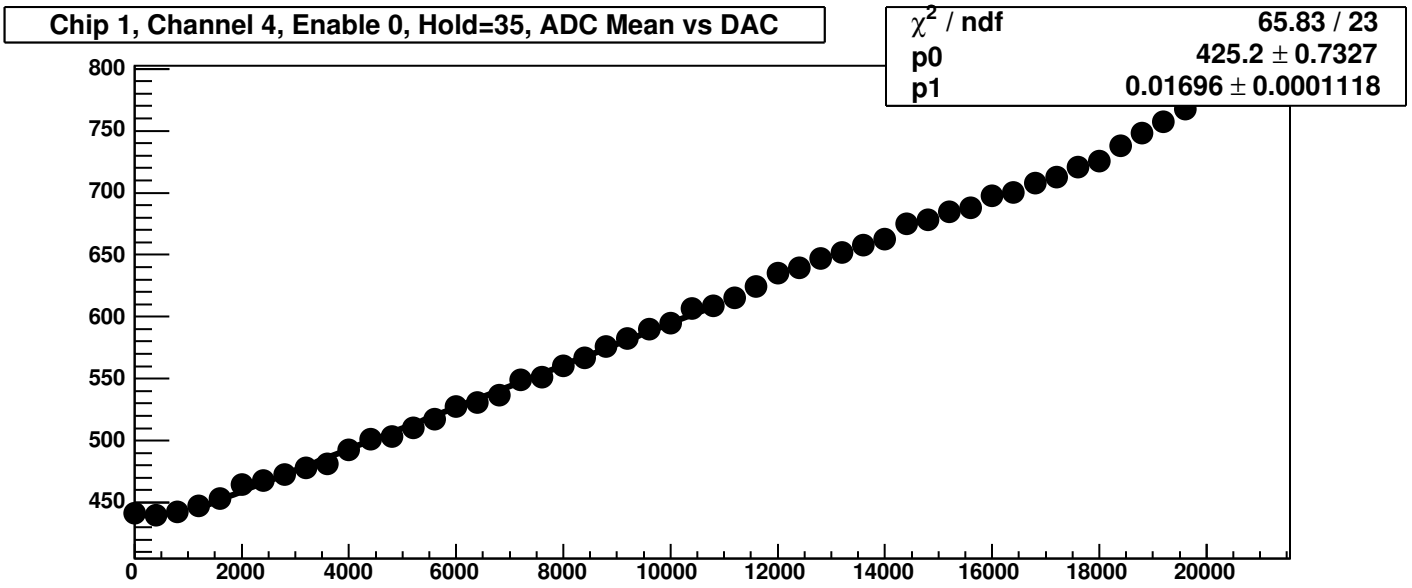

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

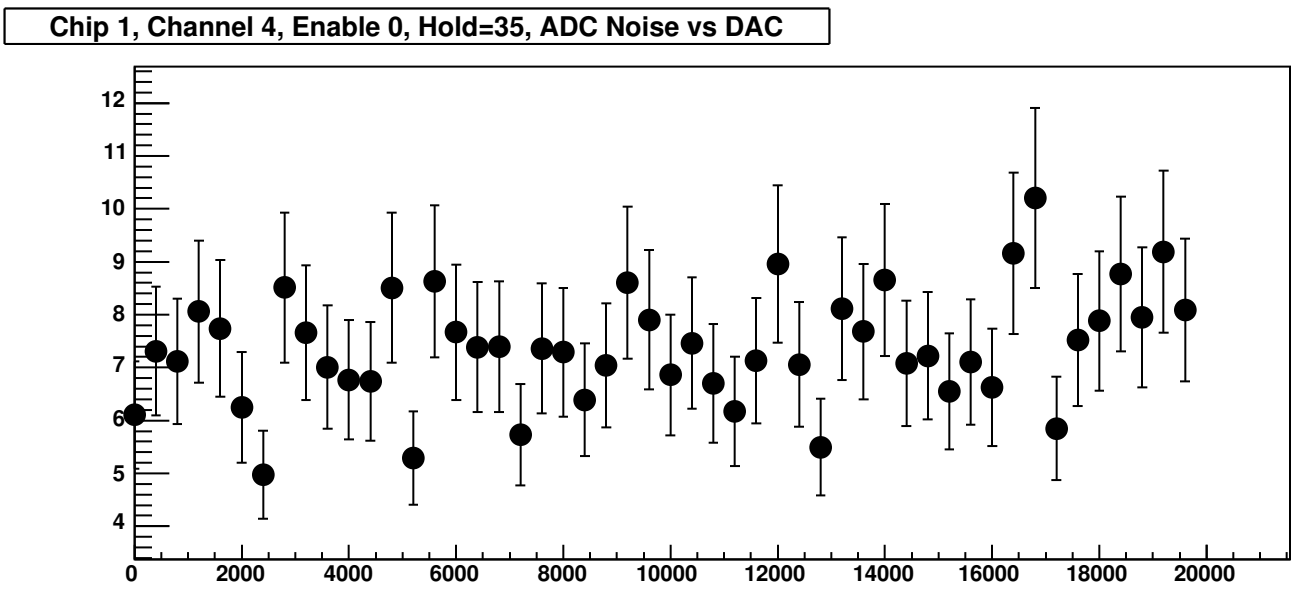

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

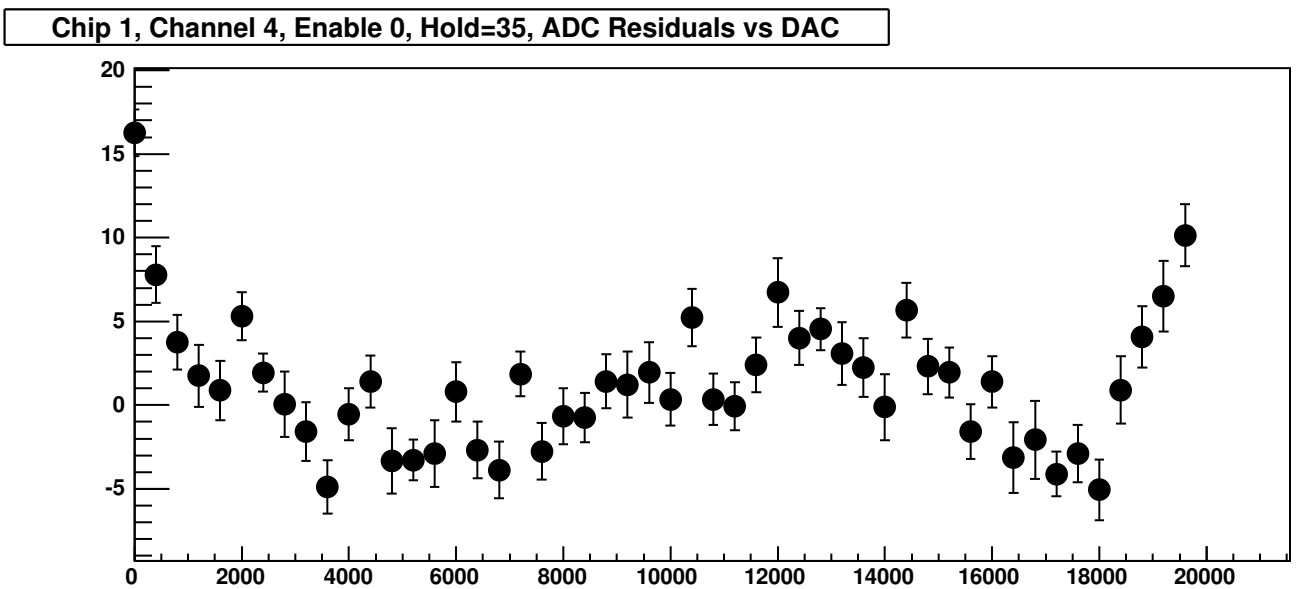

Chip 1, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 27.98 / 23
p0 435.3 ± 0.7286
p1 0.01699 ± 0.000109
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

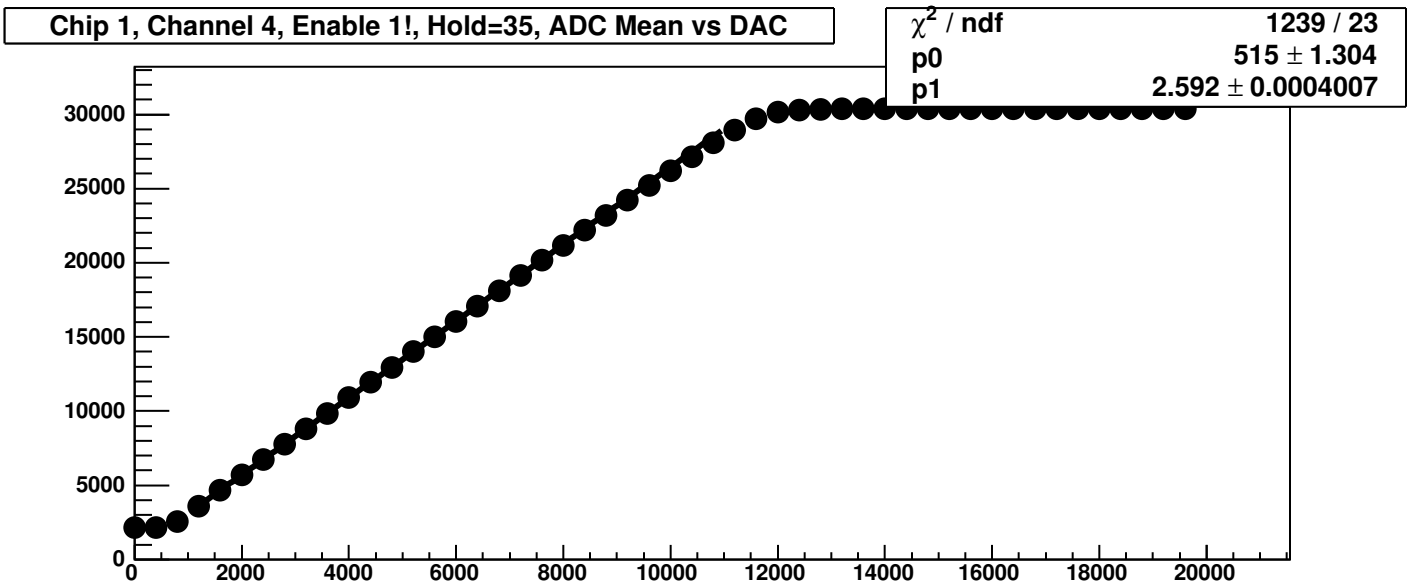

Chip 1, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

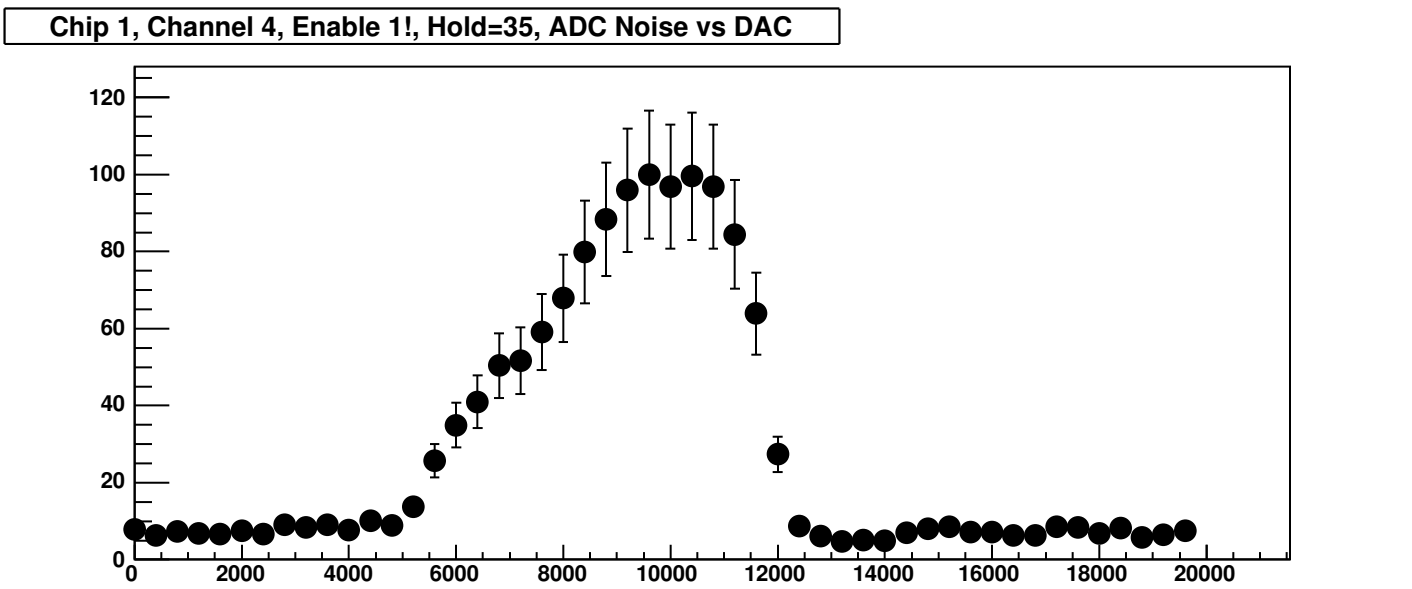

Chip 1, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

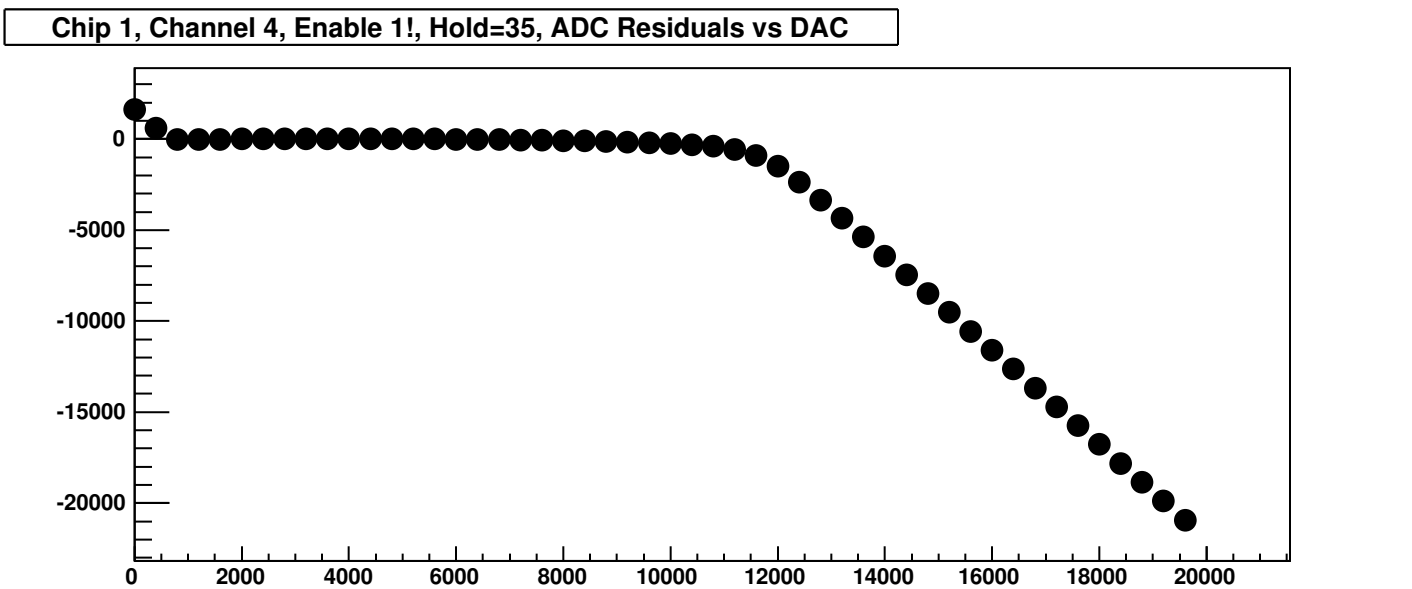

Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 65.83 / 23
p0 425.2 ± 0.7327
p1 0.01696 ± 0.0001118
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

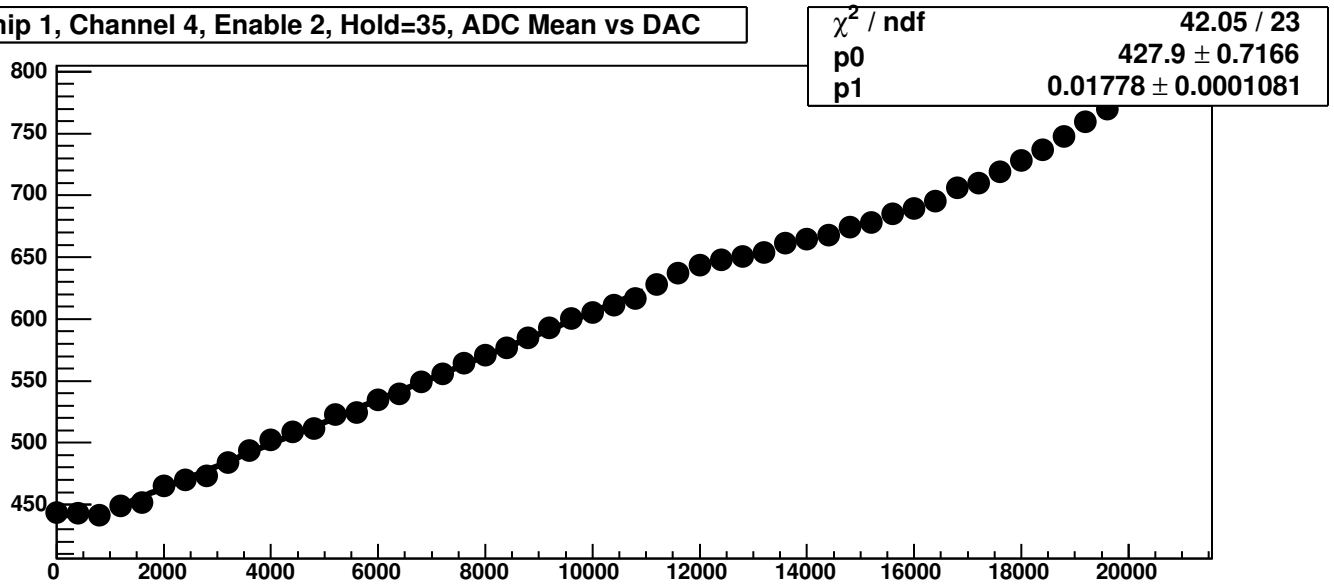

Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1239 / 23
p0 515 ± 1.304
p1 2.592 ± 0.0004007
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

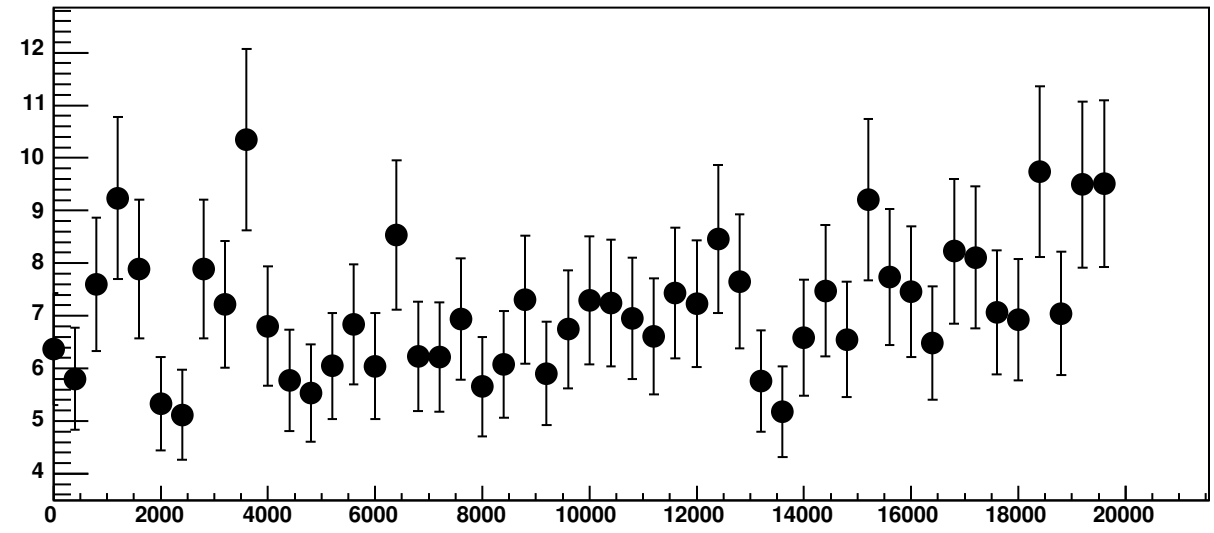

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

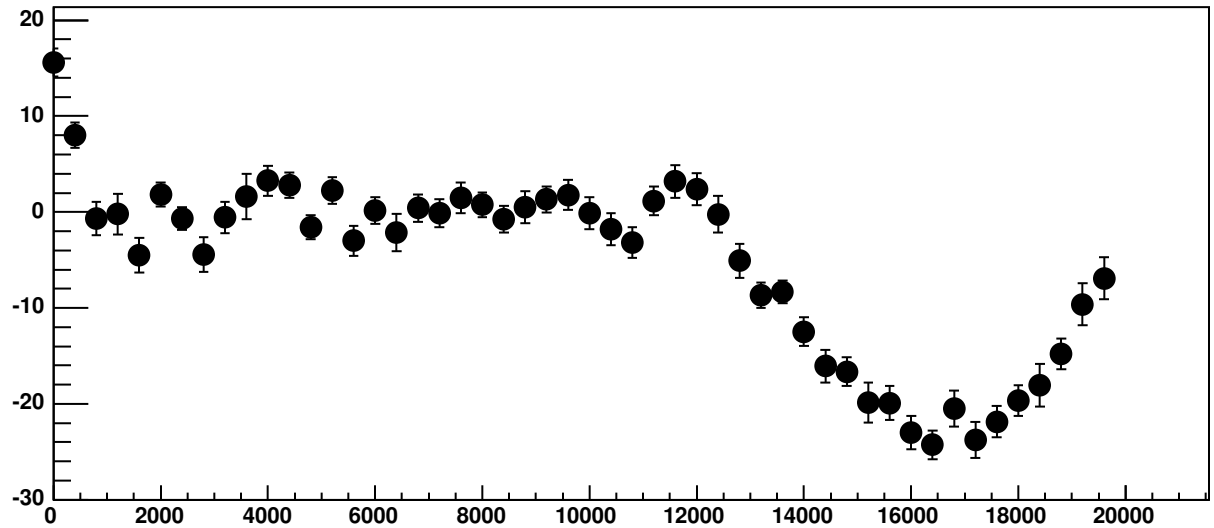
Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



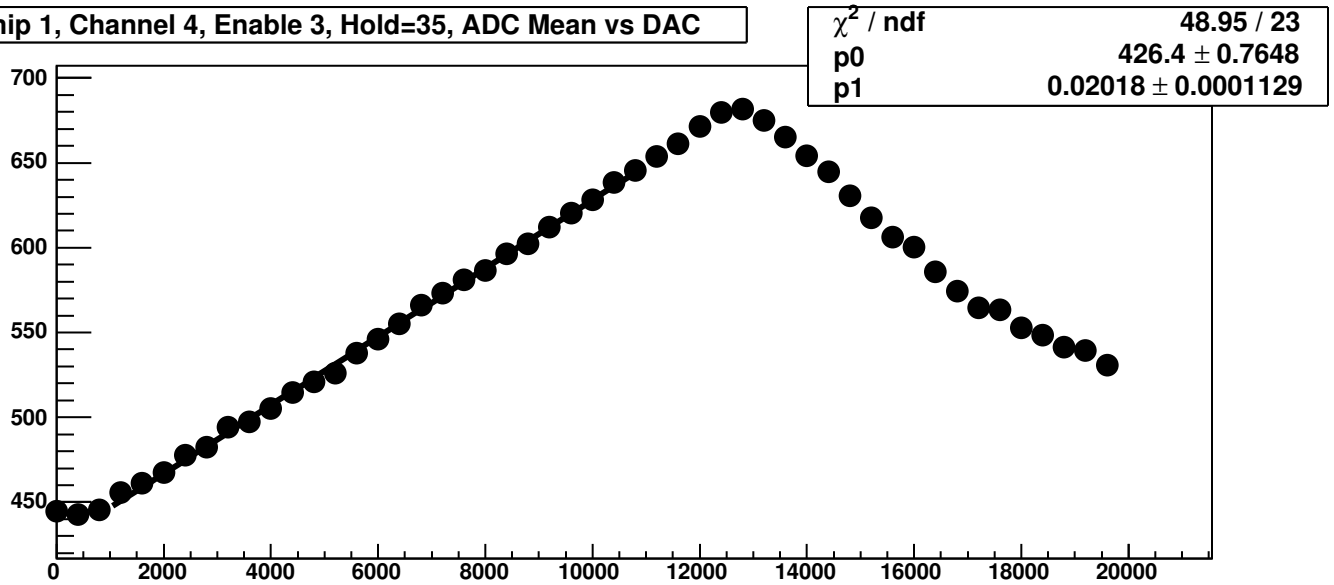
Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



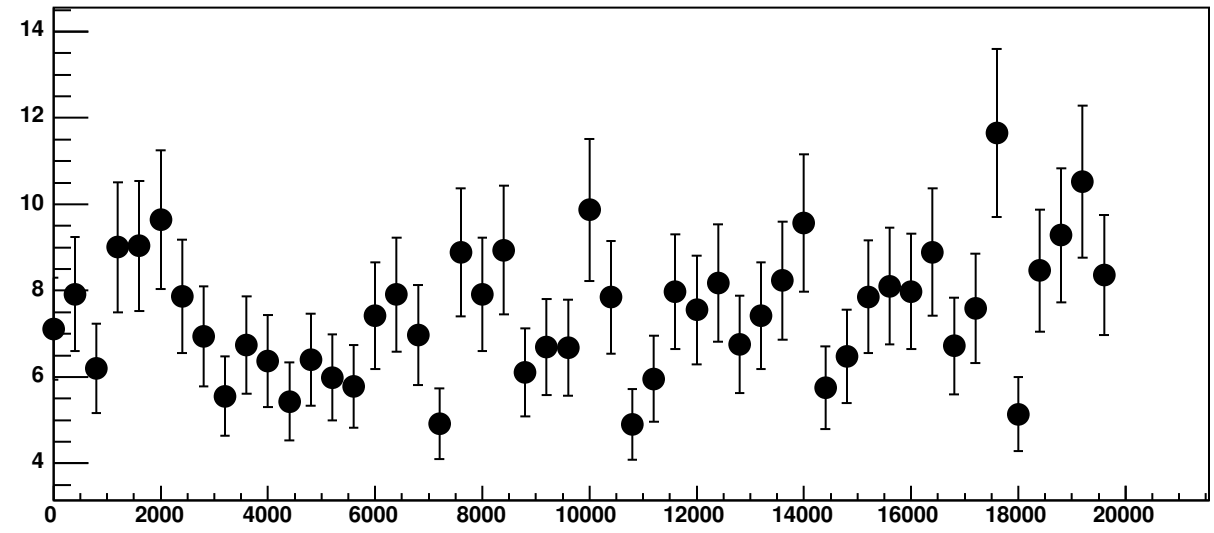
Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



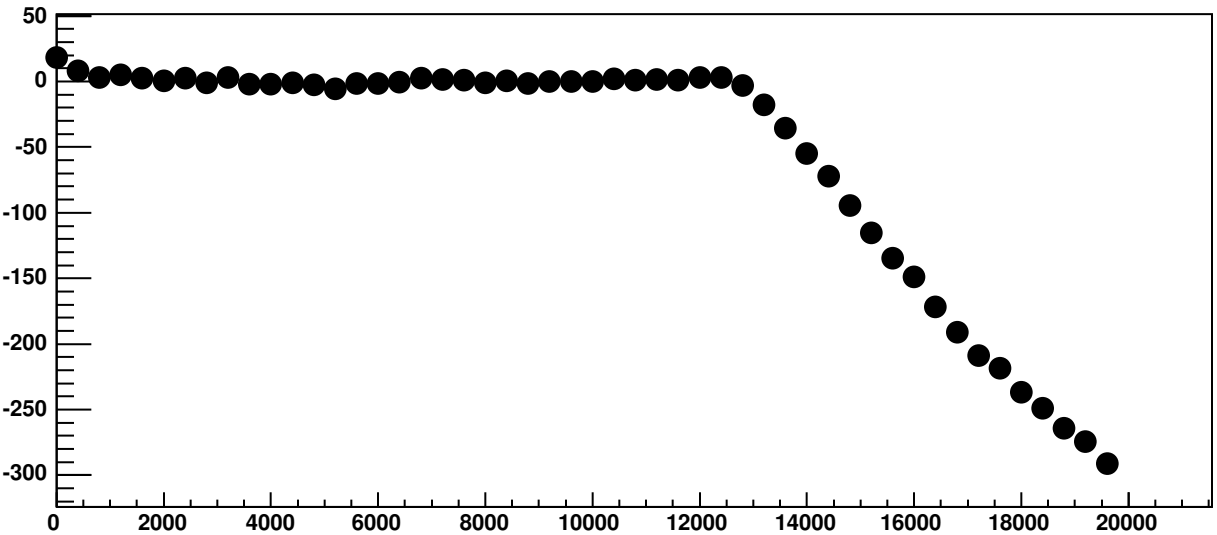
Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



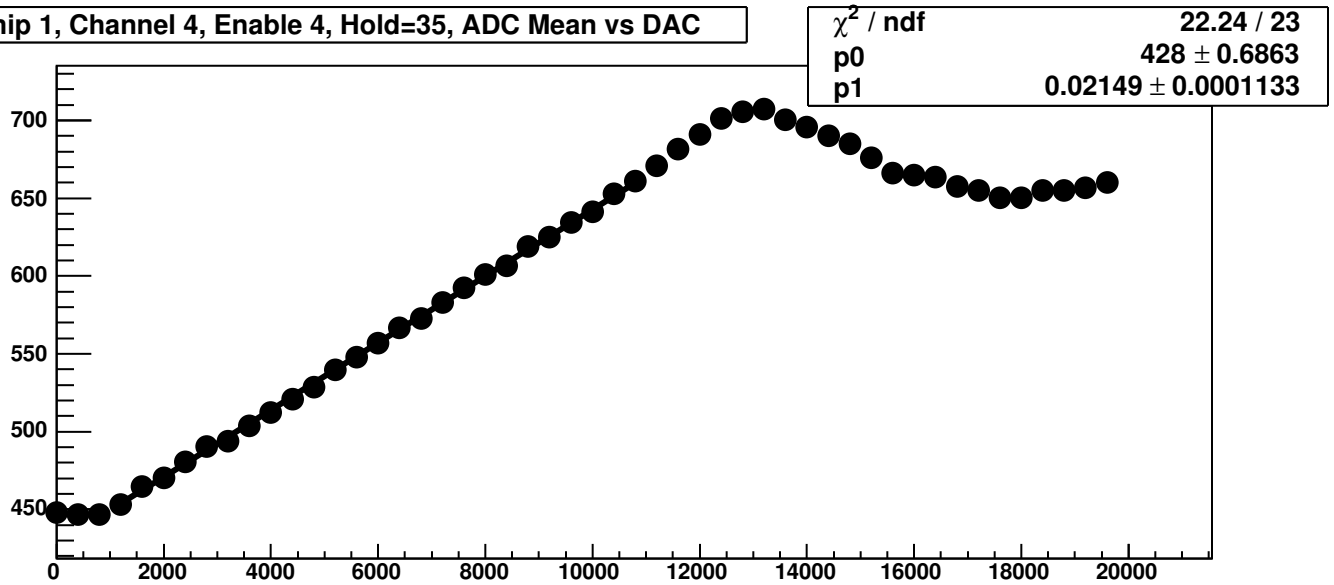
Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



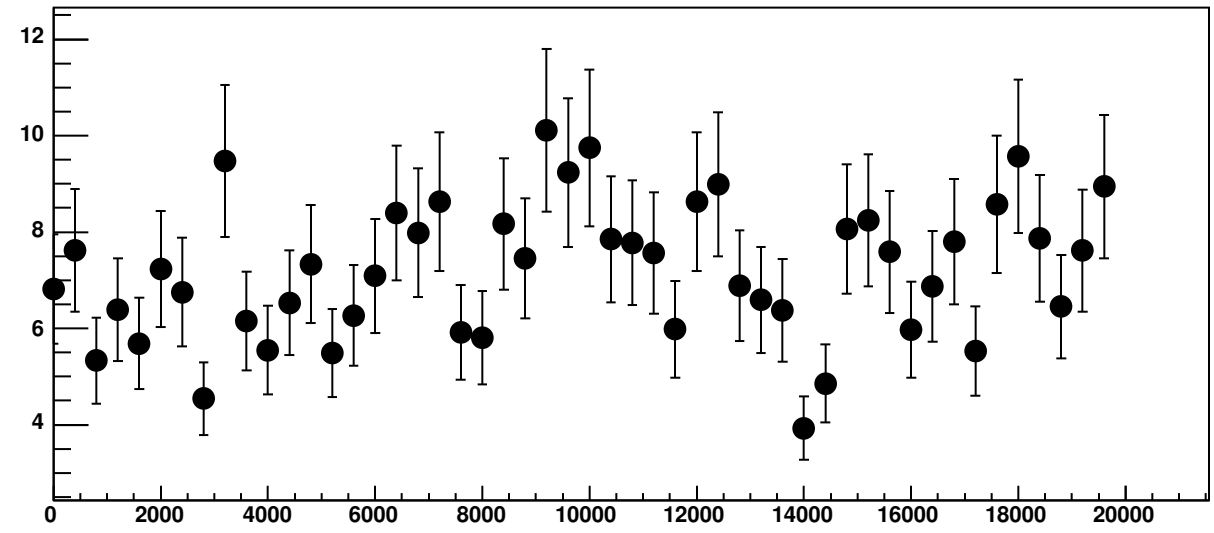
Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



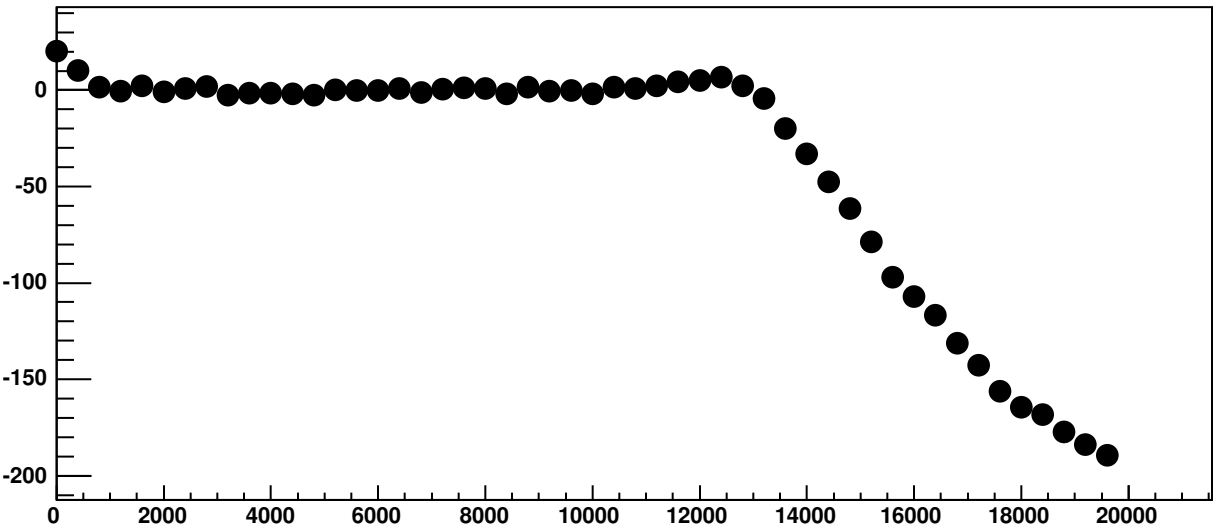
Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC

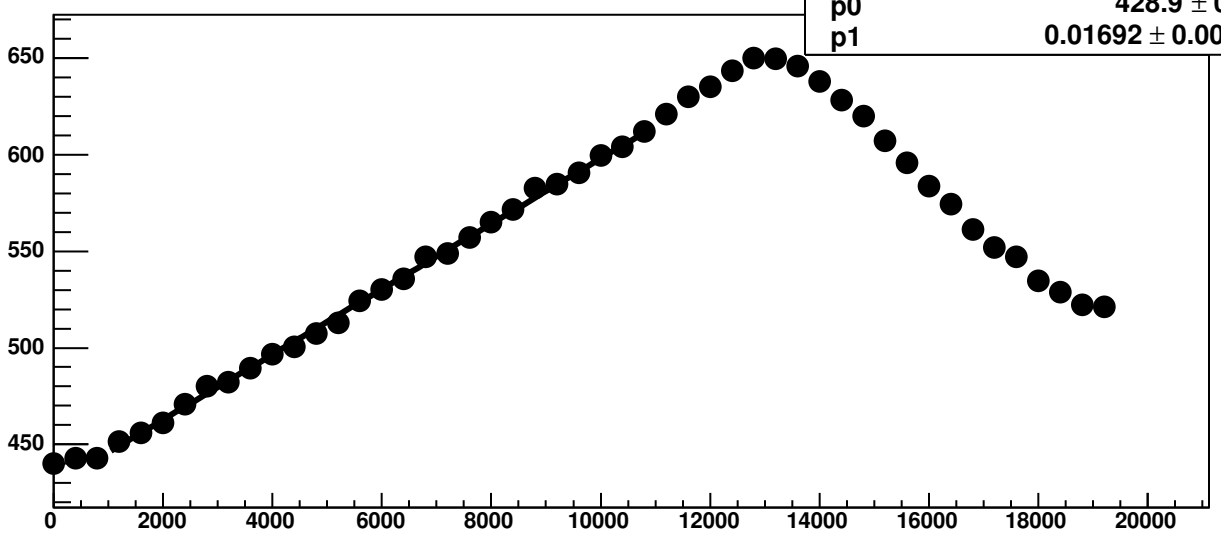


Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

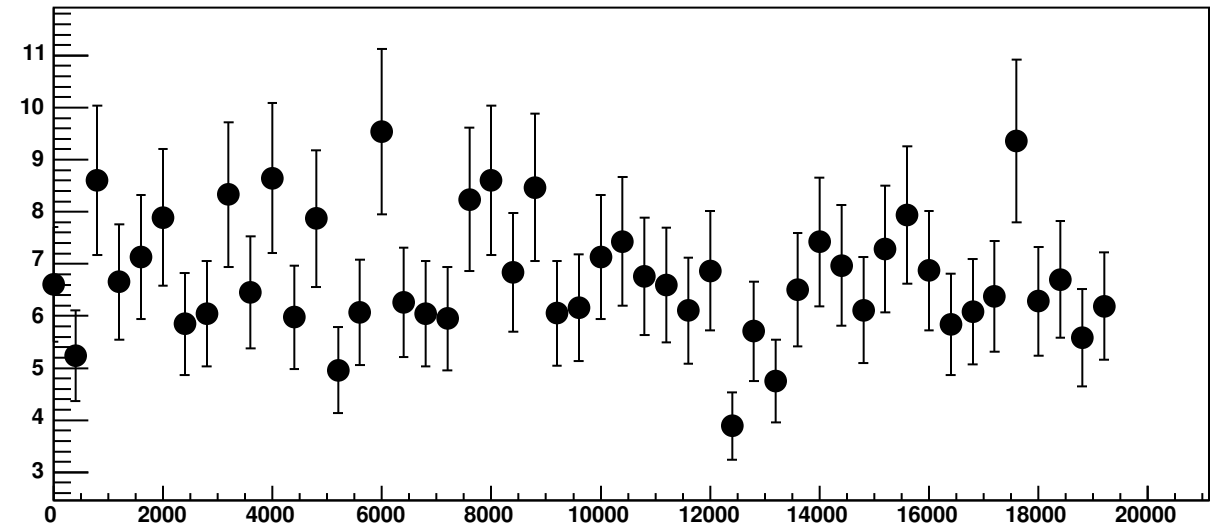


Chip 1, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

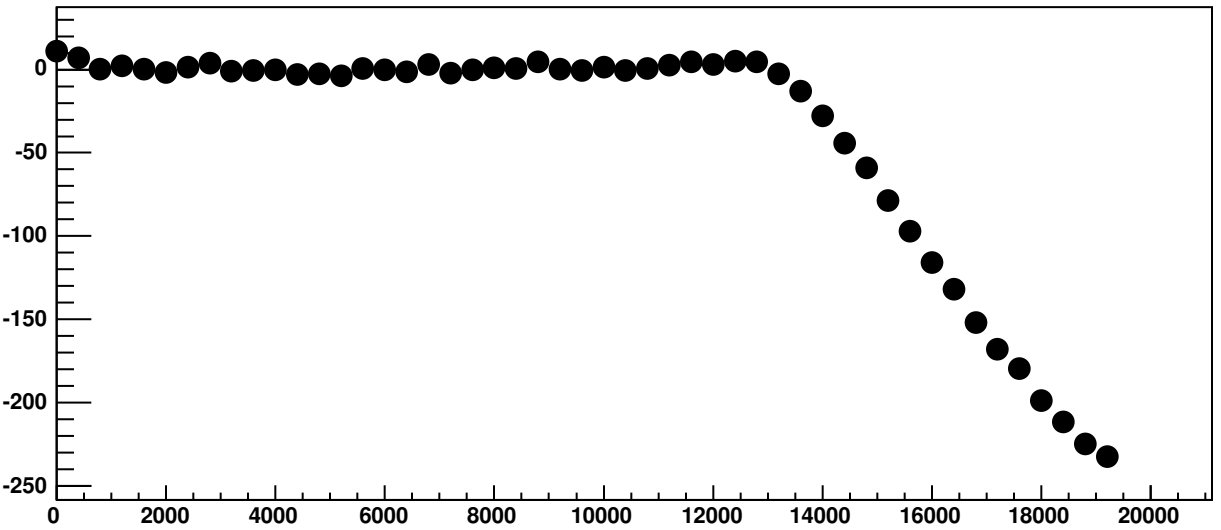
χ^2 / ndf	47.59 / 23
p0	428.9 ± 0.7184
p1	0.01692 ± 0.0001091

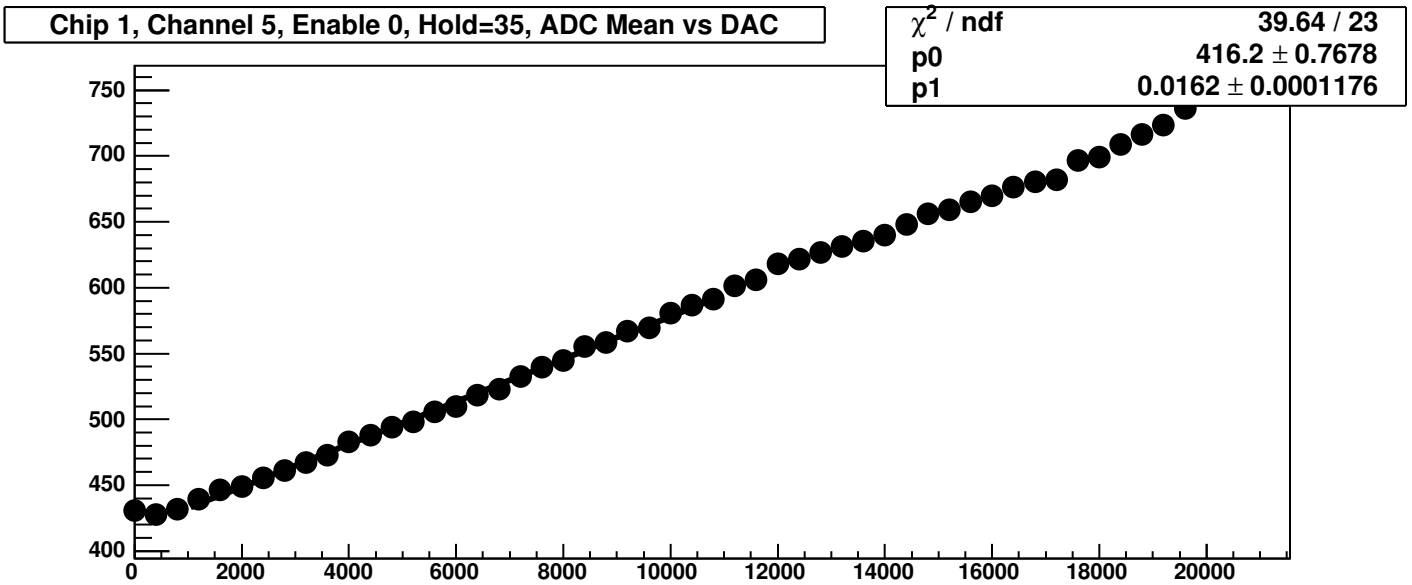


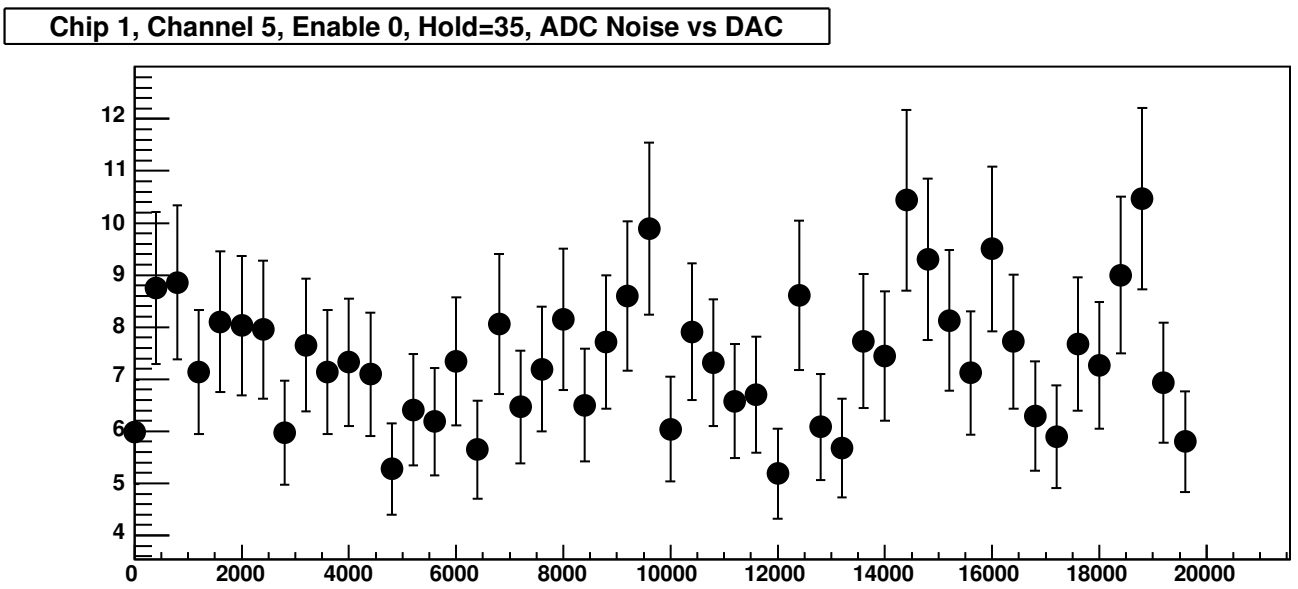
Chip 1, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

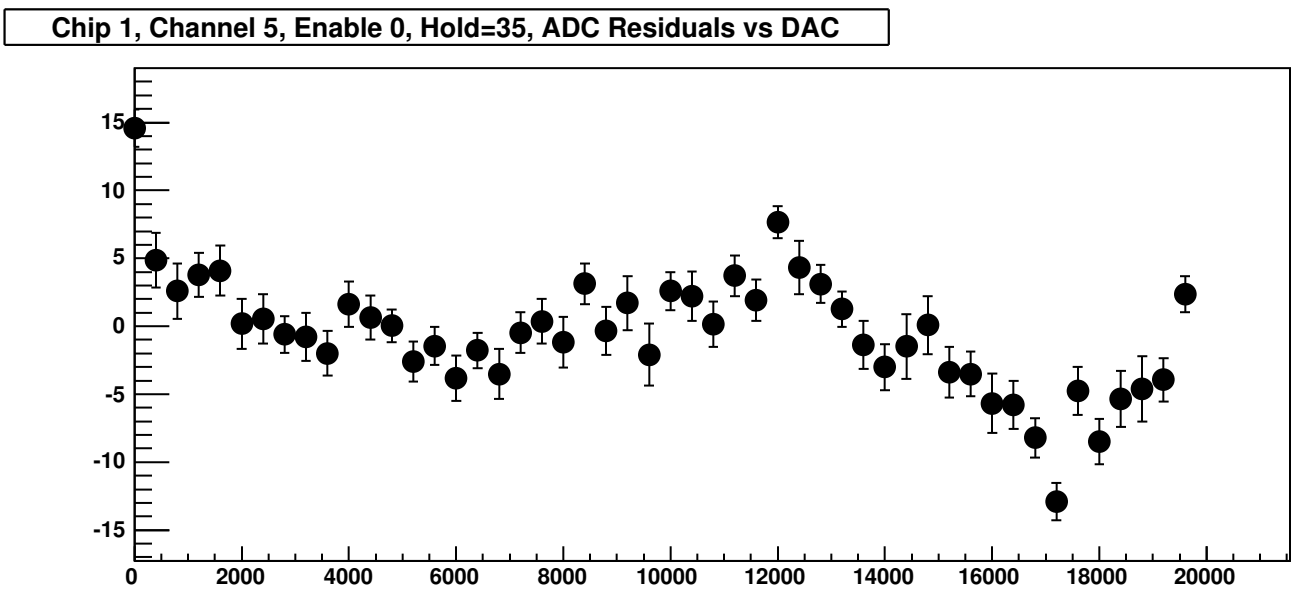


Chip 1, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

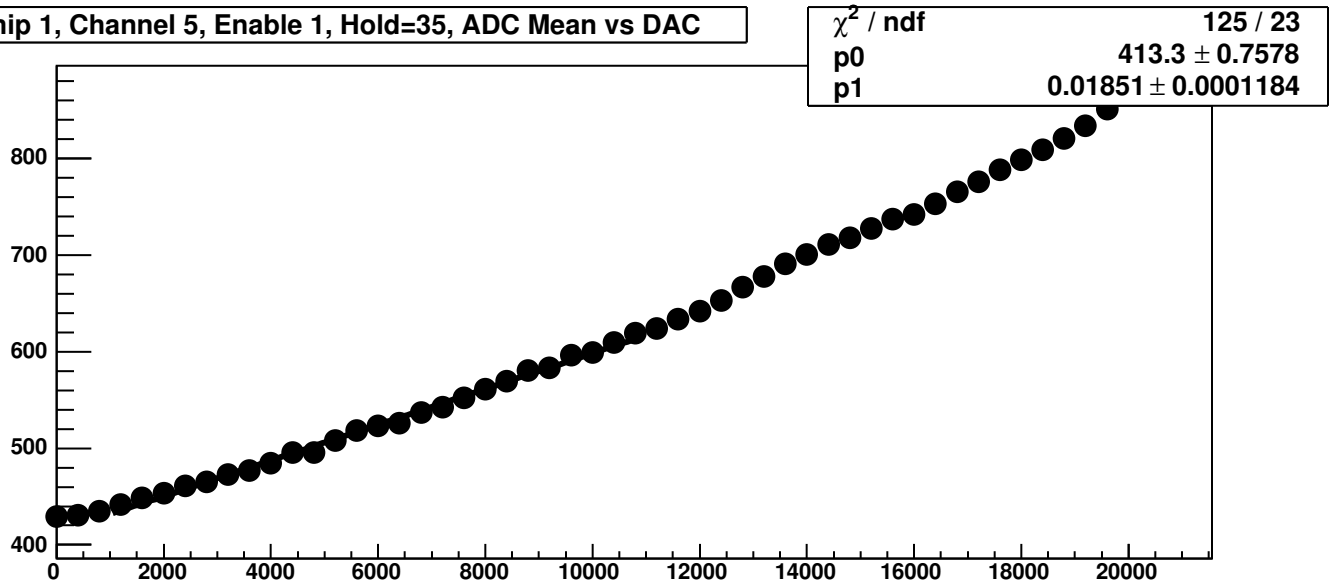



Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 39.64 / 23
p0 416.2 ± 0.7678
p1 0.0162 ± 0.0001176
750
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

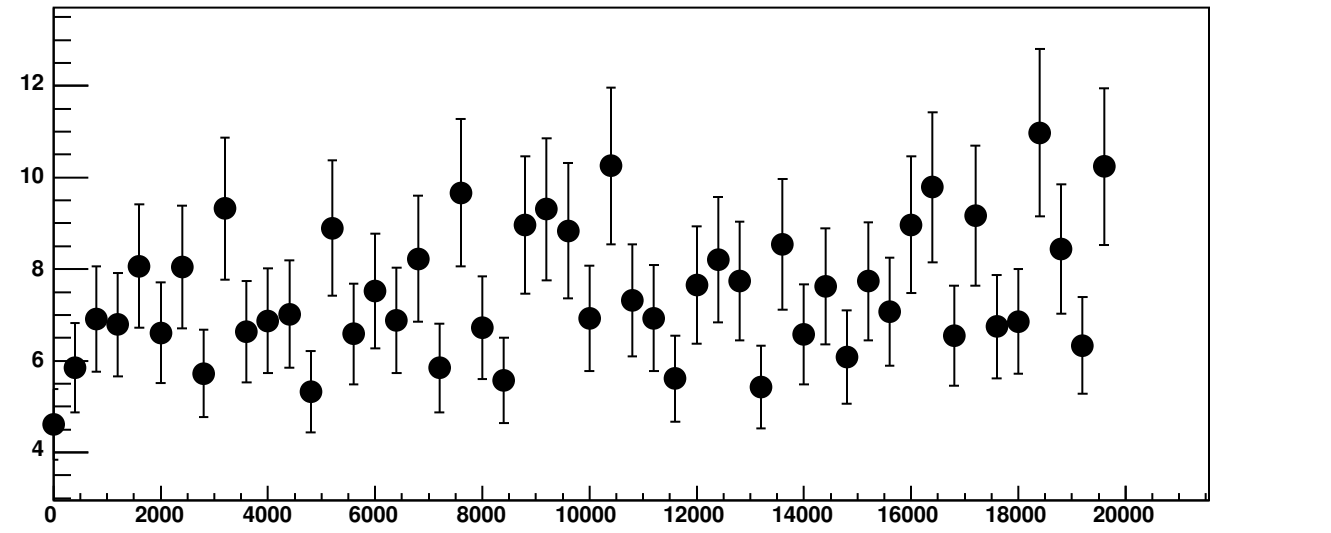

Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

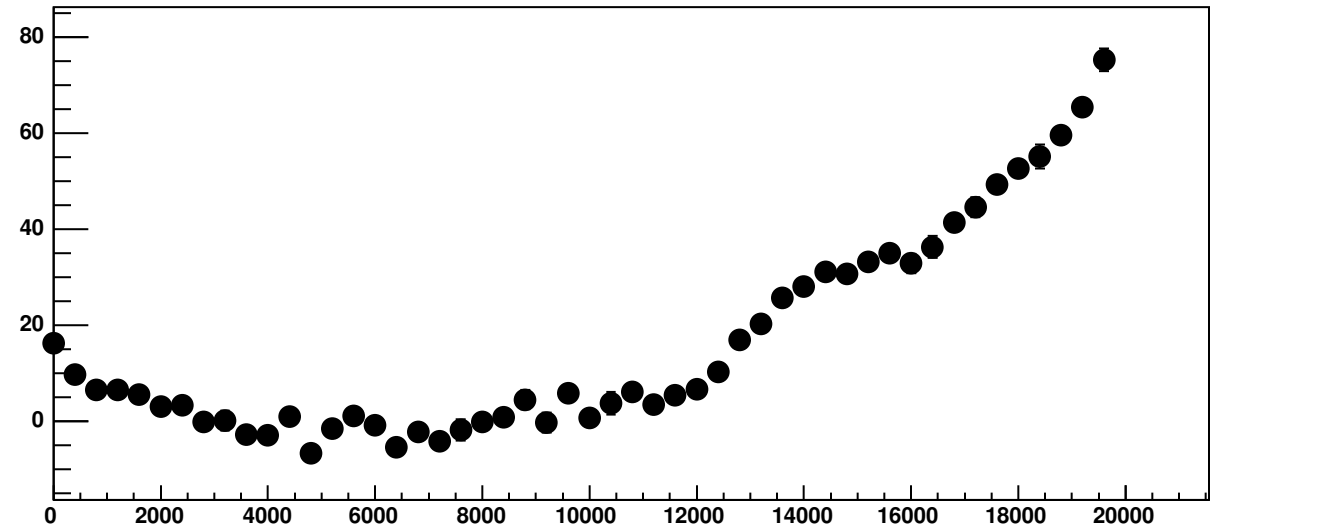
Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

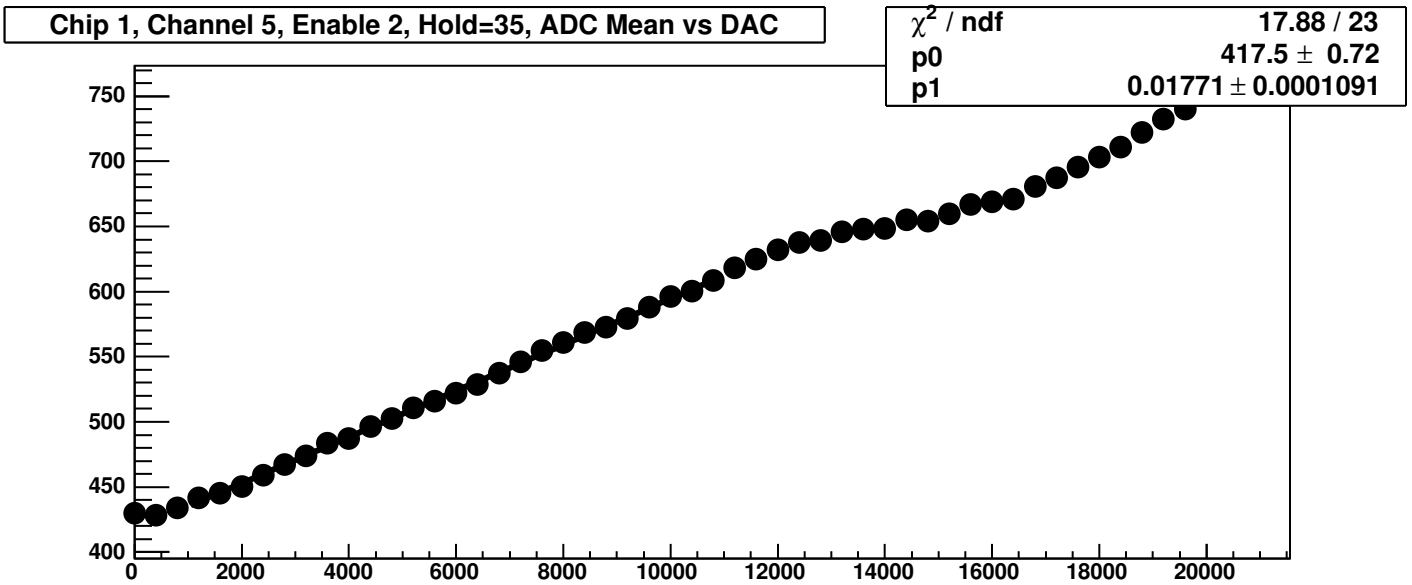


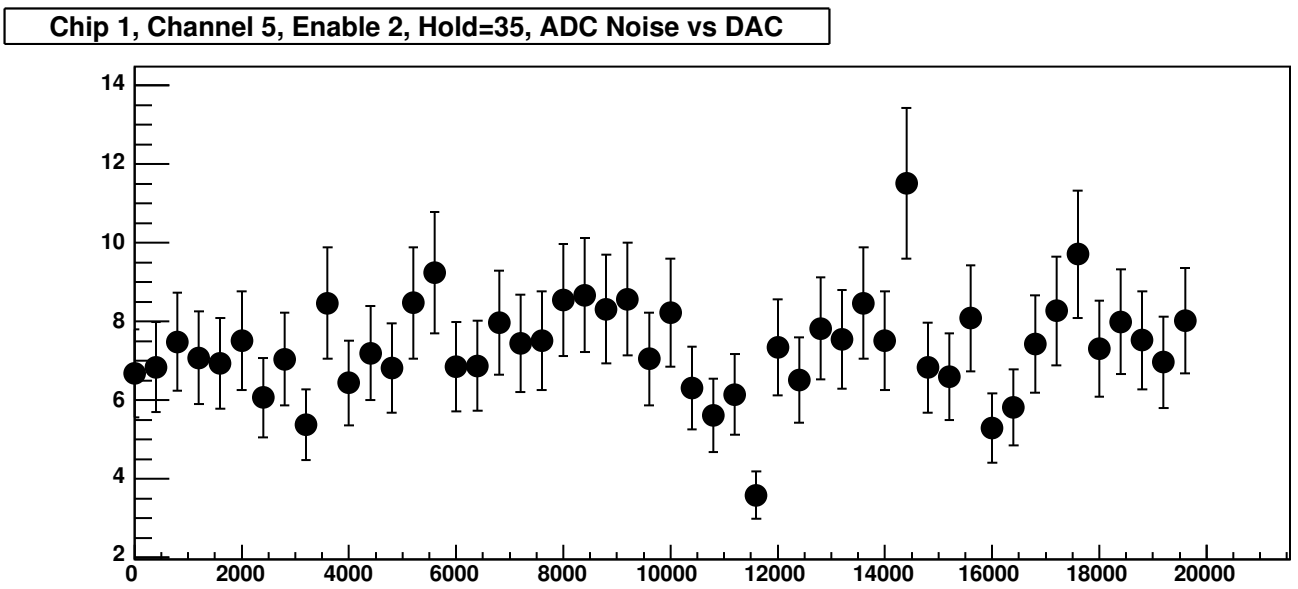
Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC

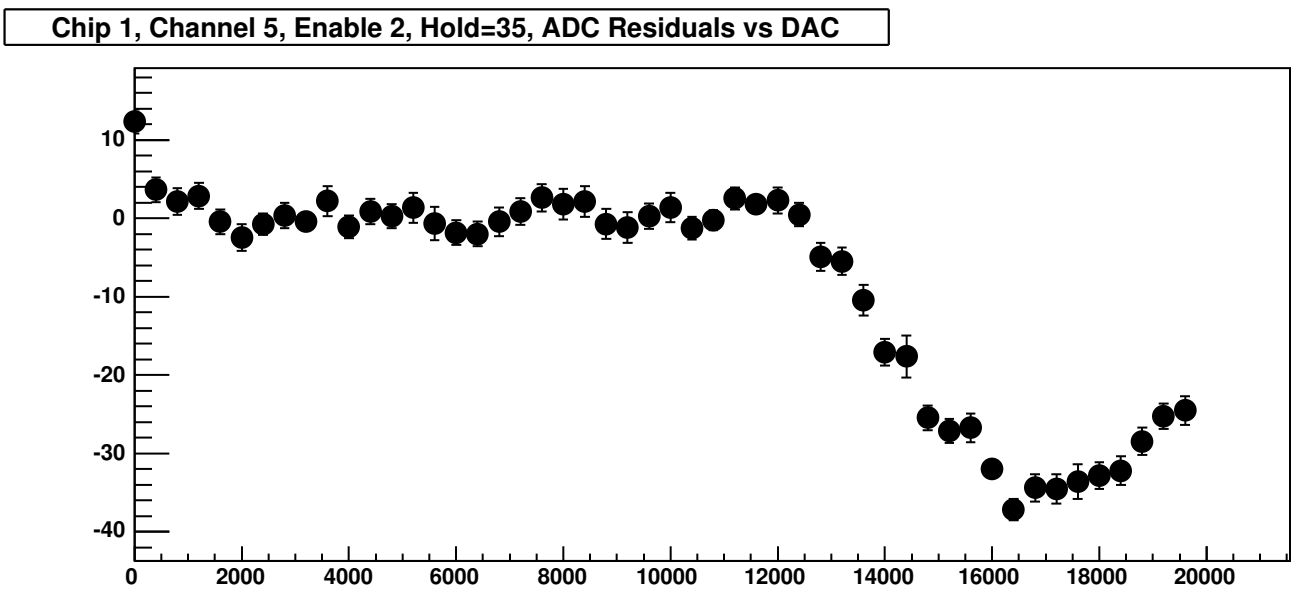


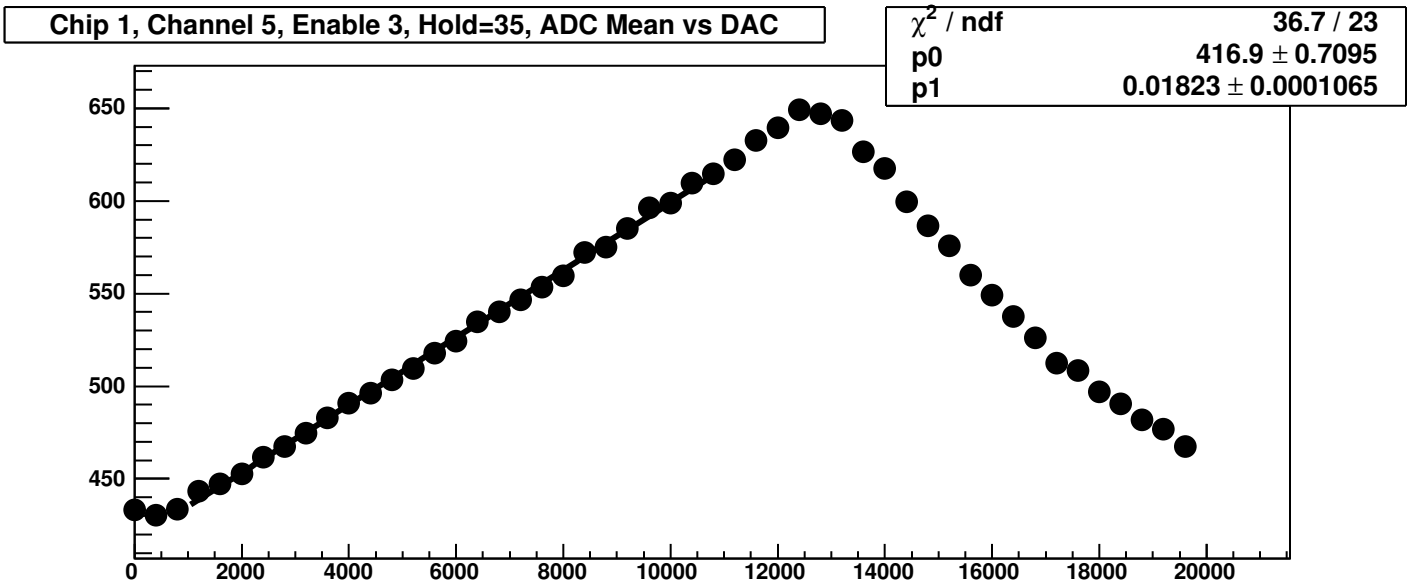
Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

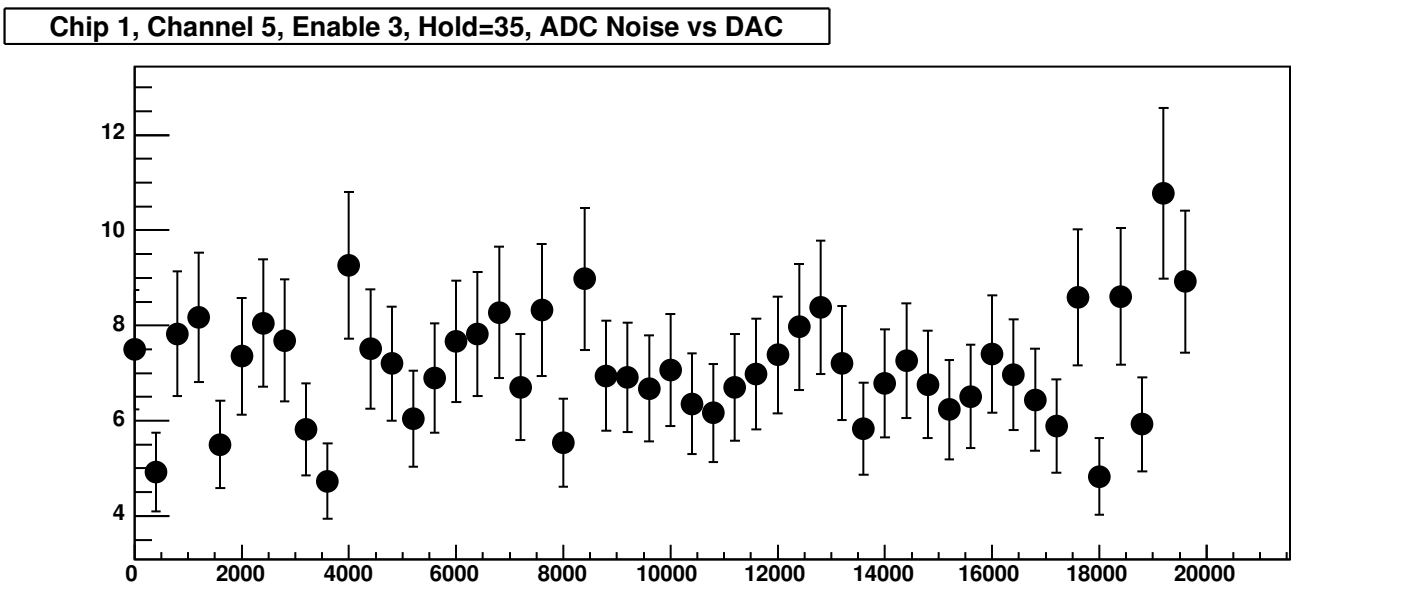


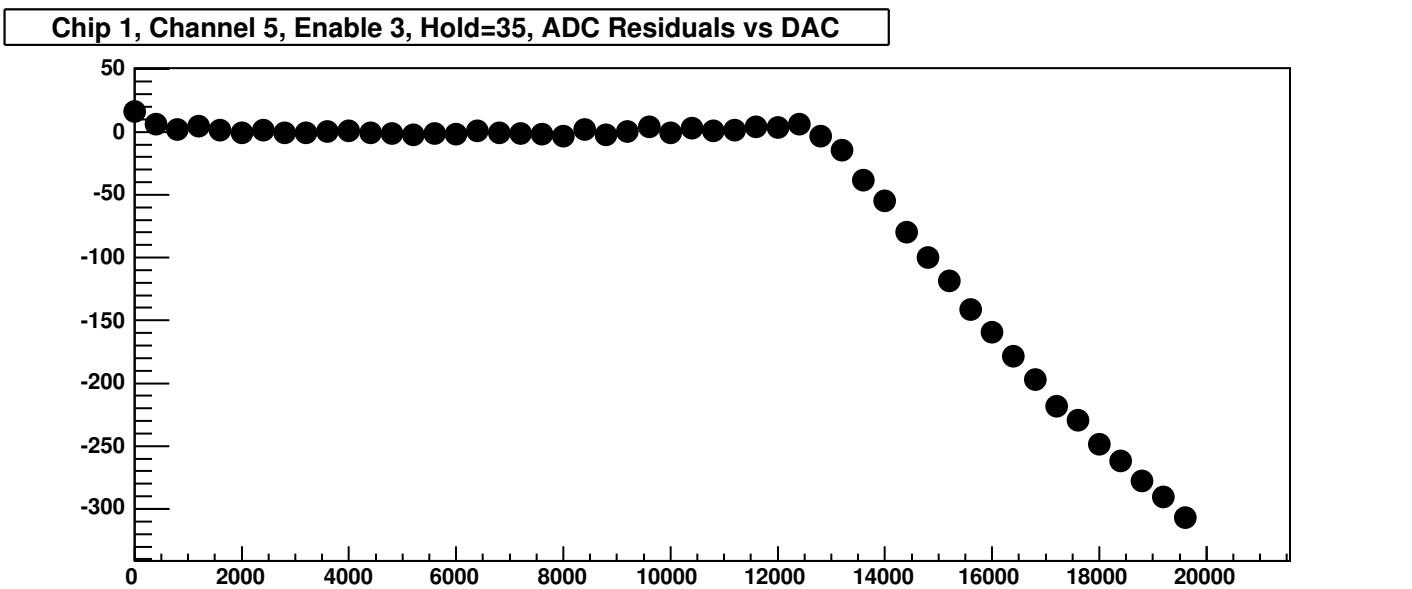

Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 17.88 / 23
p0 417.5 ± 0.72
p1 0.01771 ± 0.0001091
750
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

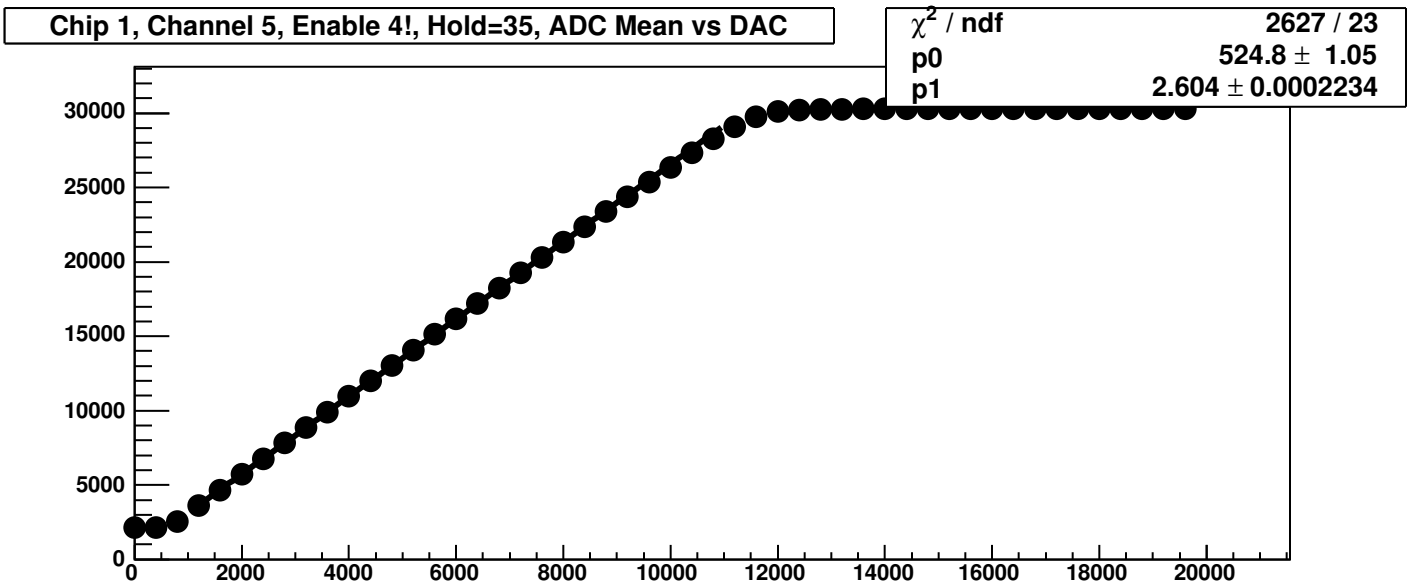

Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

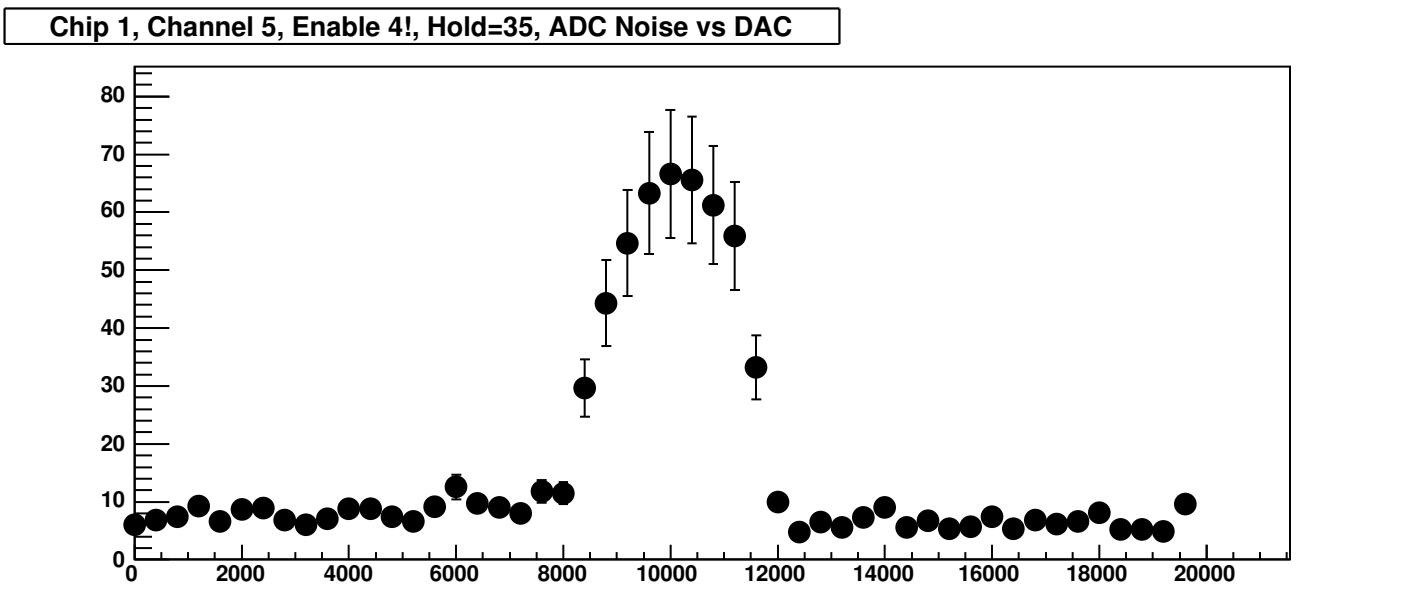

Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

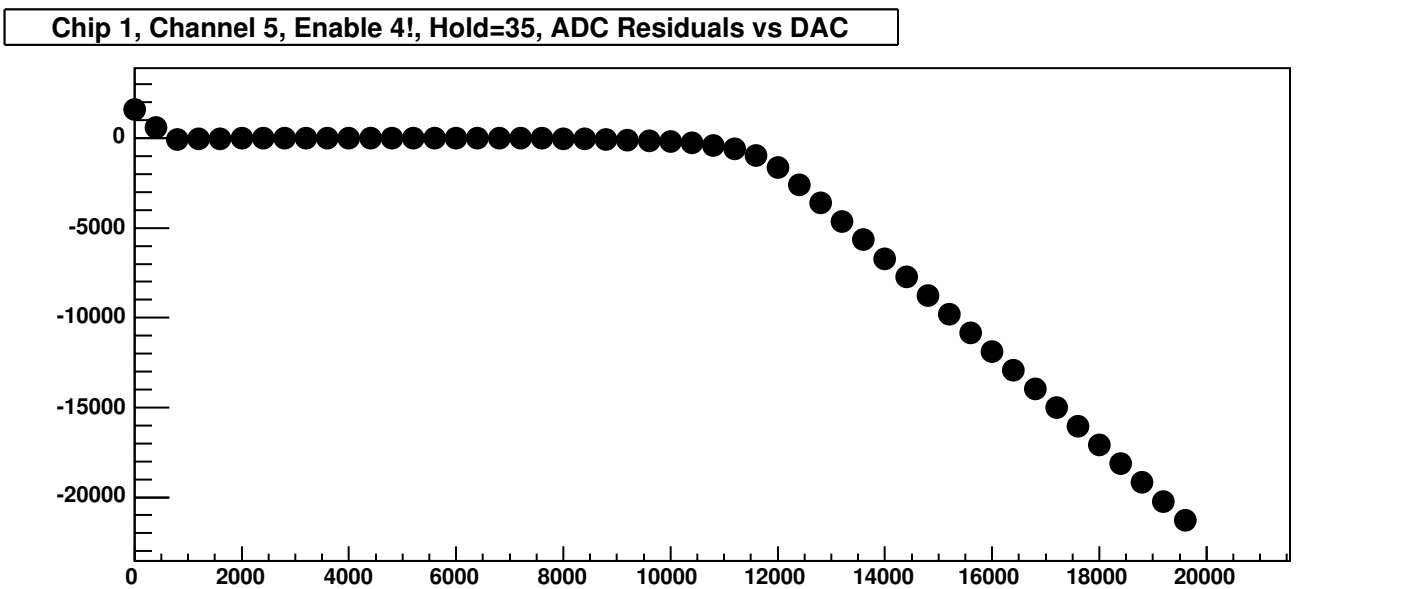

Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.7 / 23
p0 416.9 ± 0.7095
p1 0.01823 ± 0.0001065
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

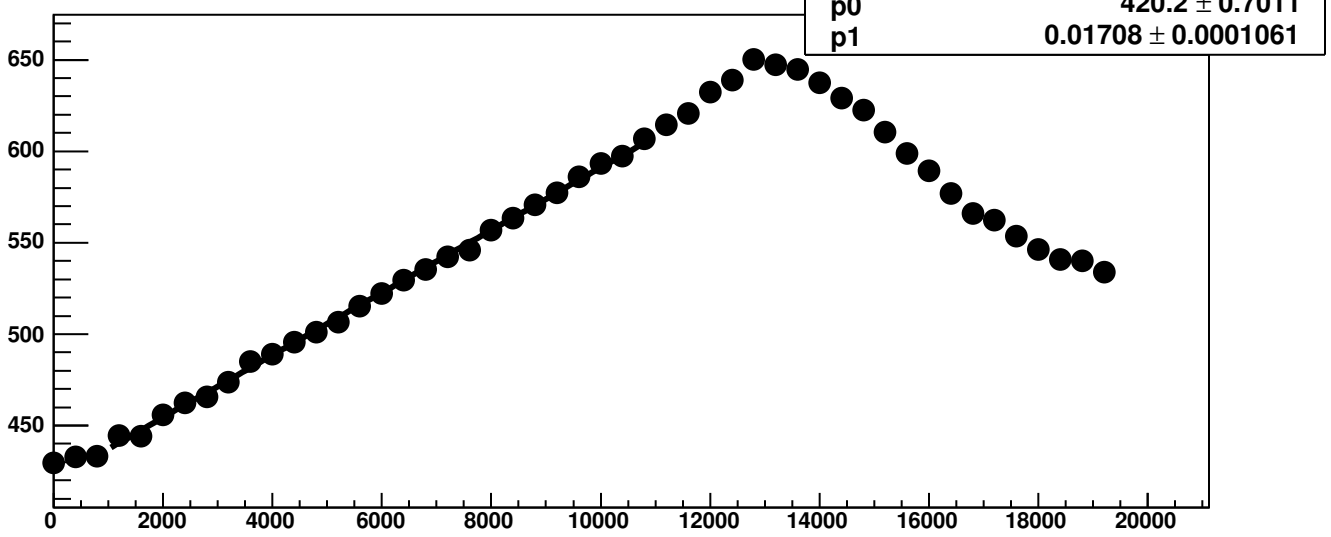

Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 2627 / 23
p0 524.8 ± 1.05
p1 2.604 ± 0.0002234
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

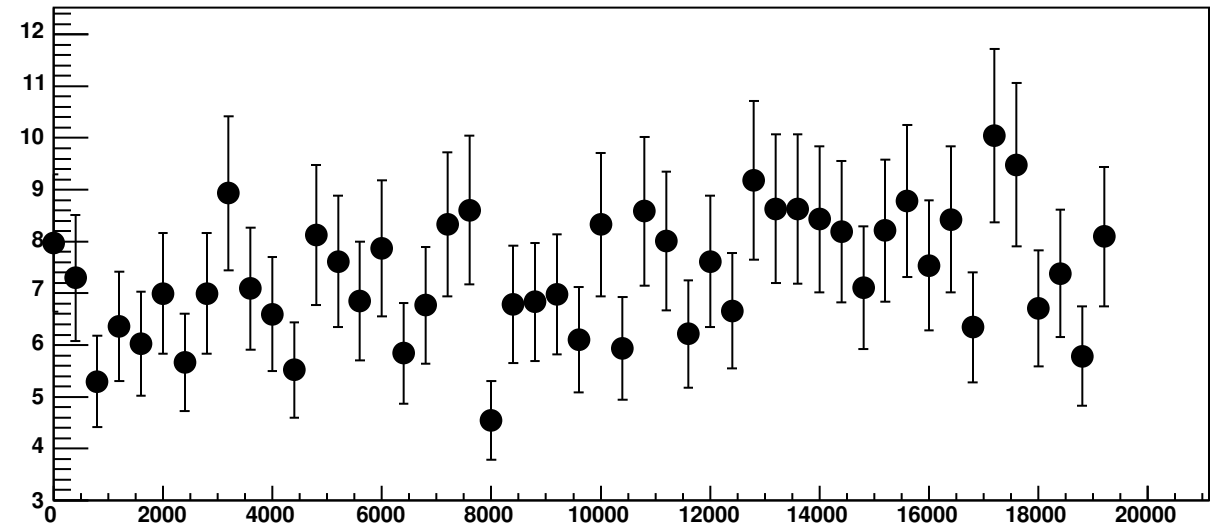

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

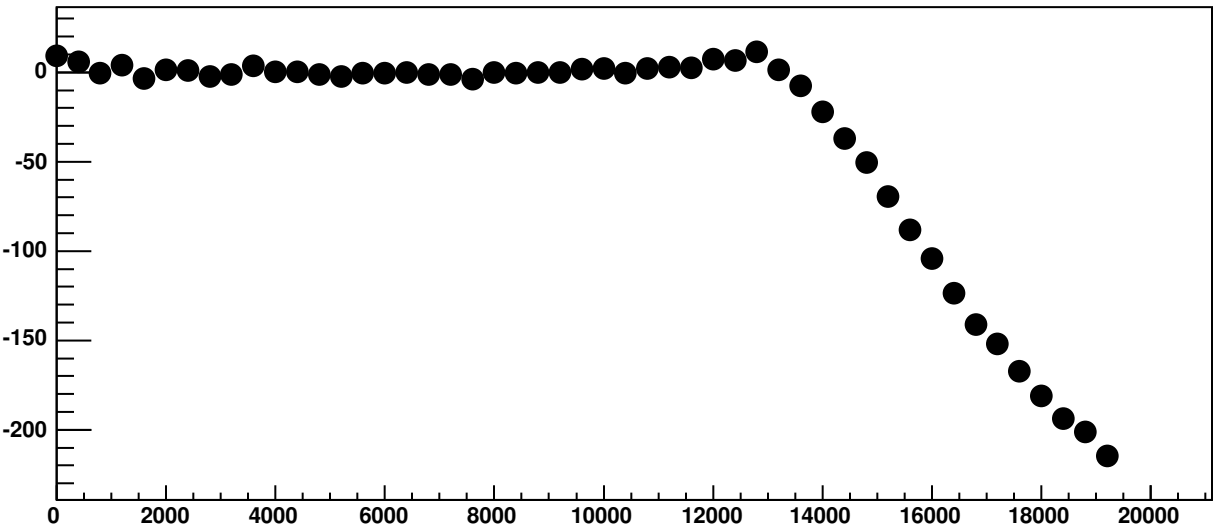
Chip 1, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



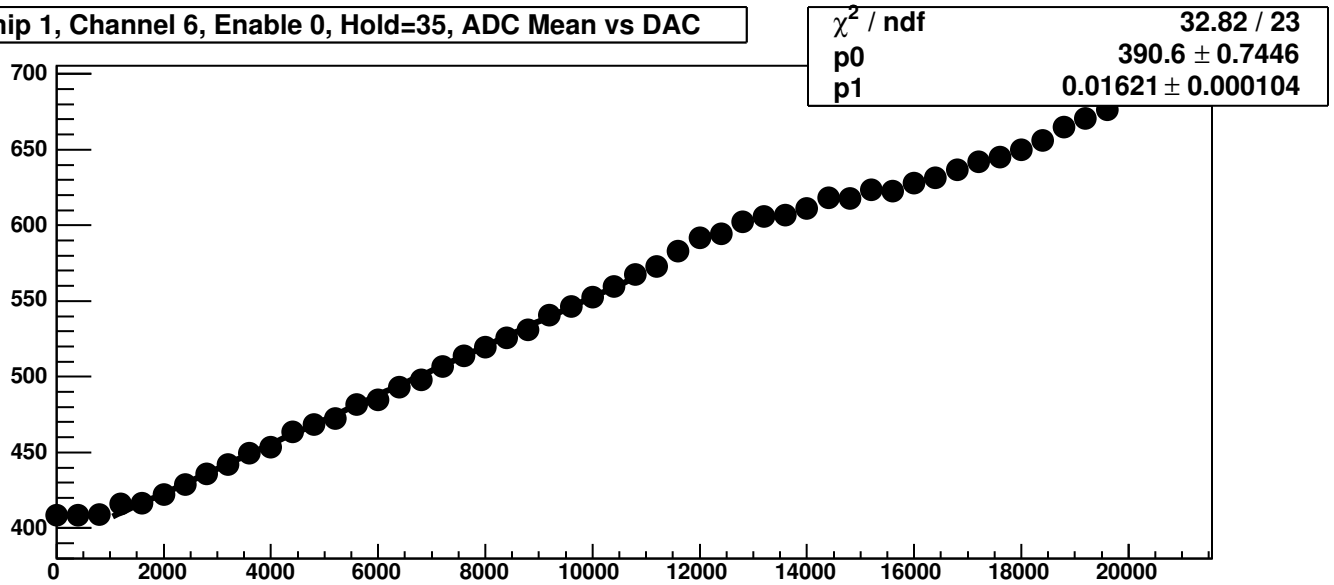
Chip 1, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



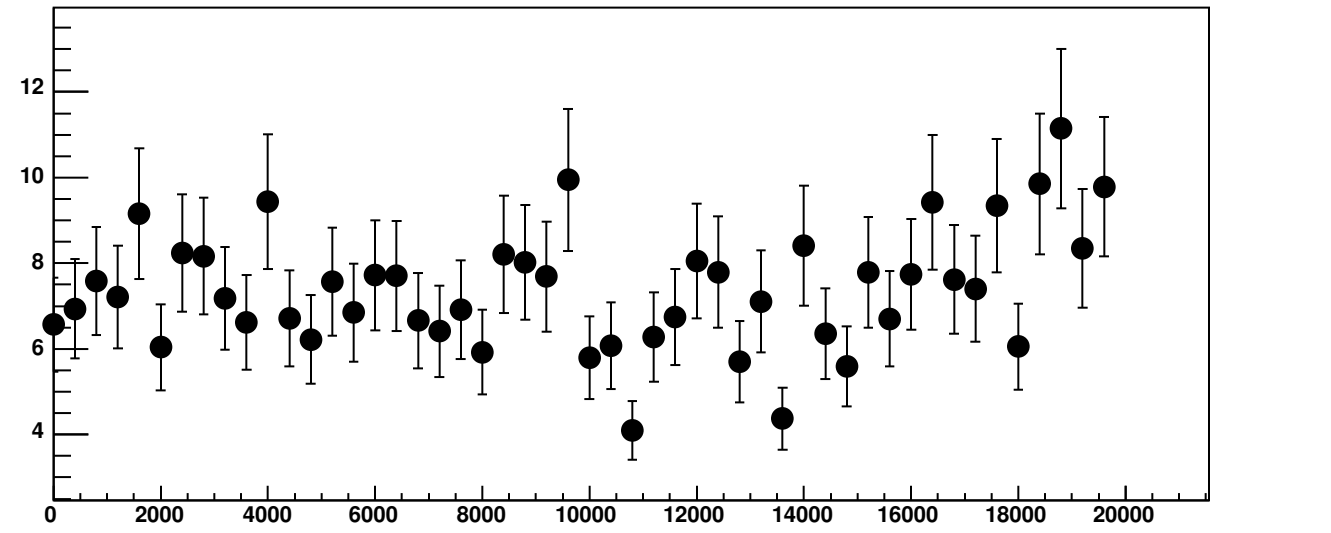
Chip 1, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



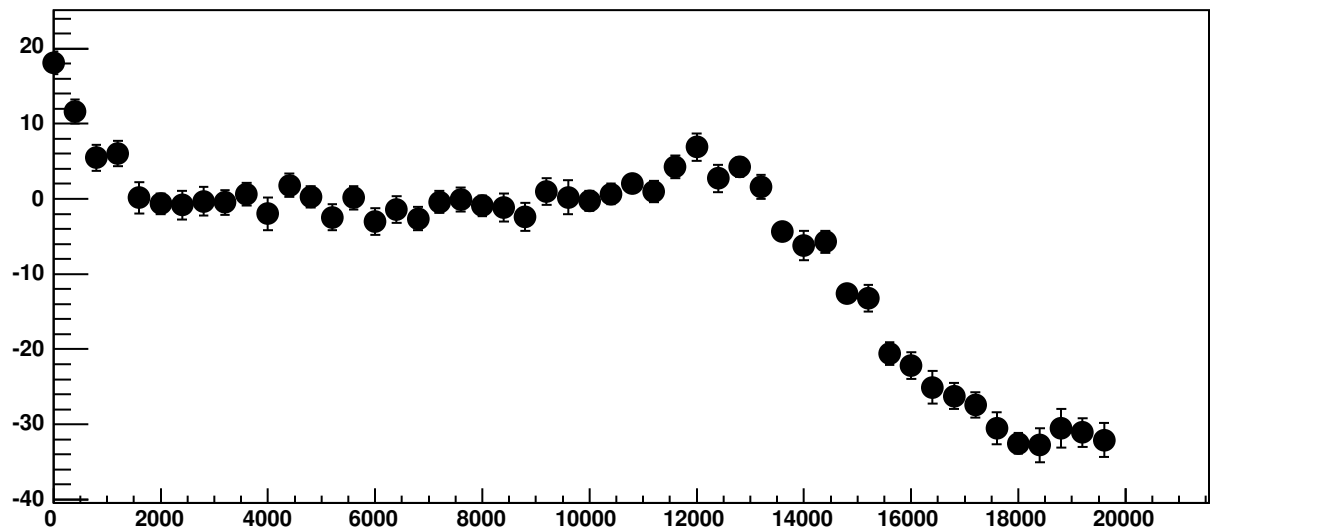
Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

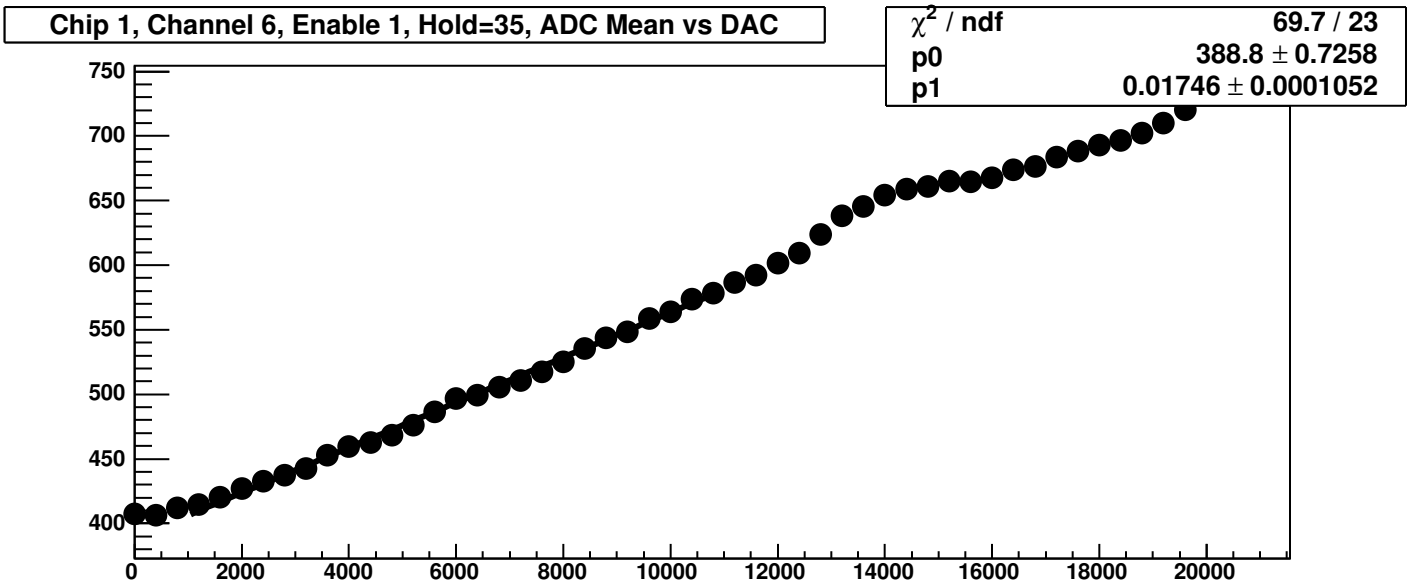


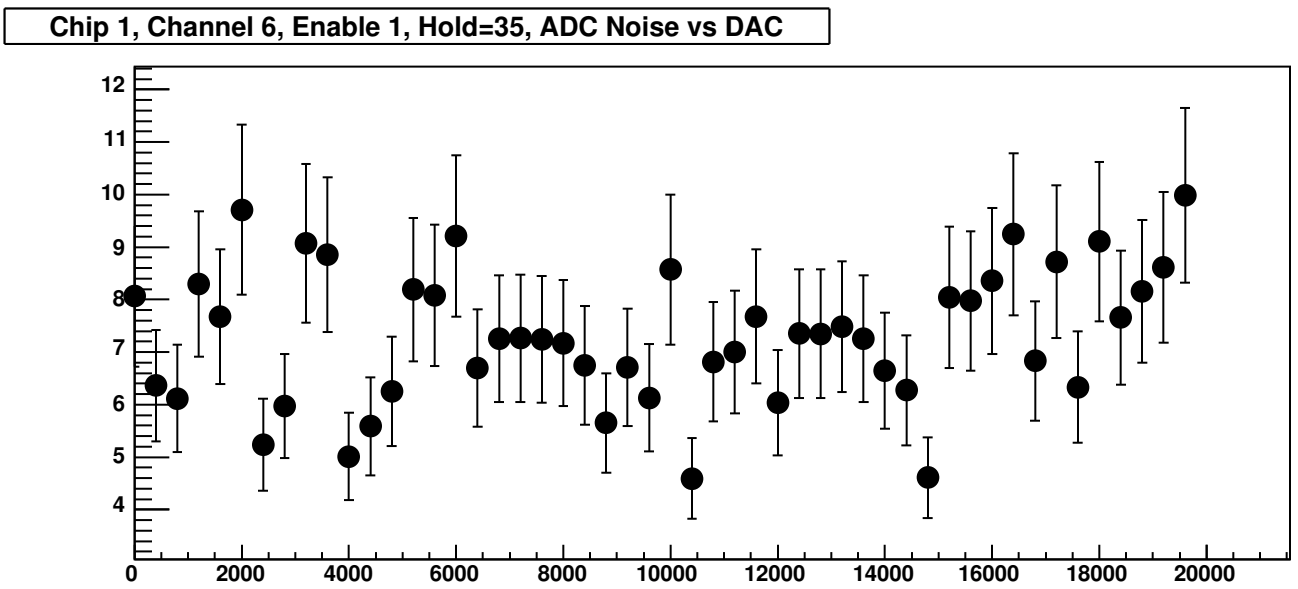
Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC

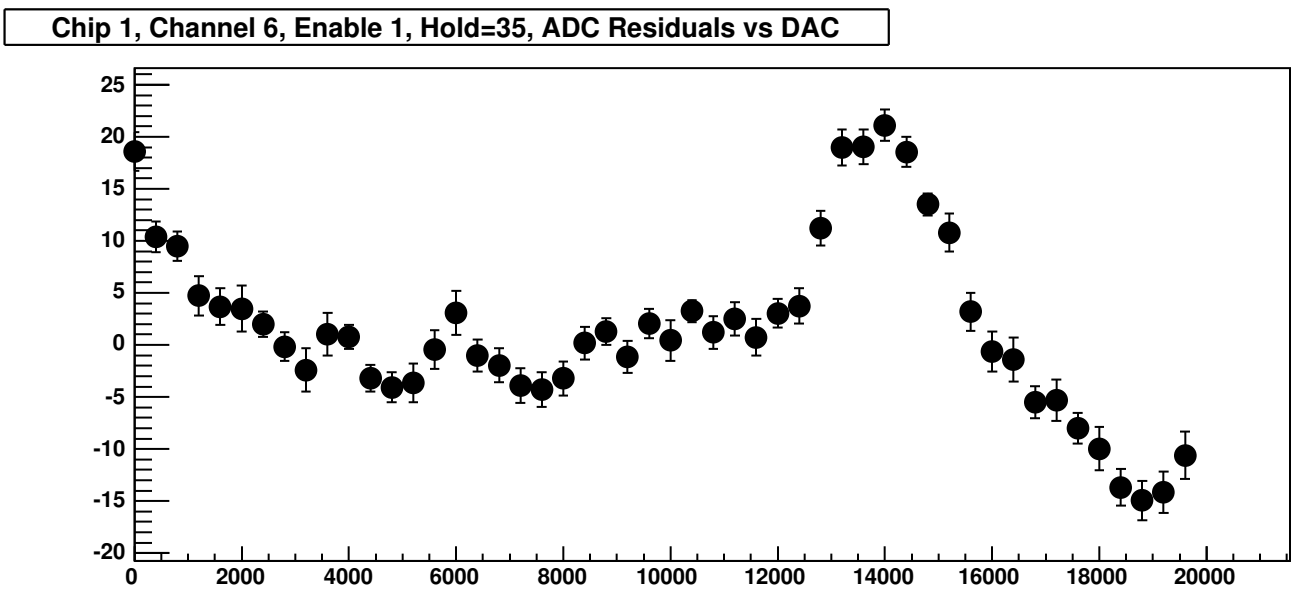


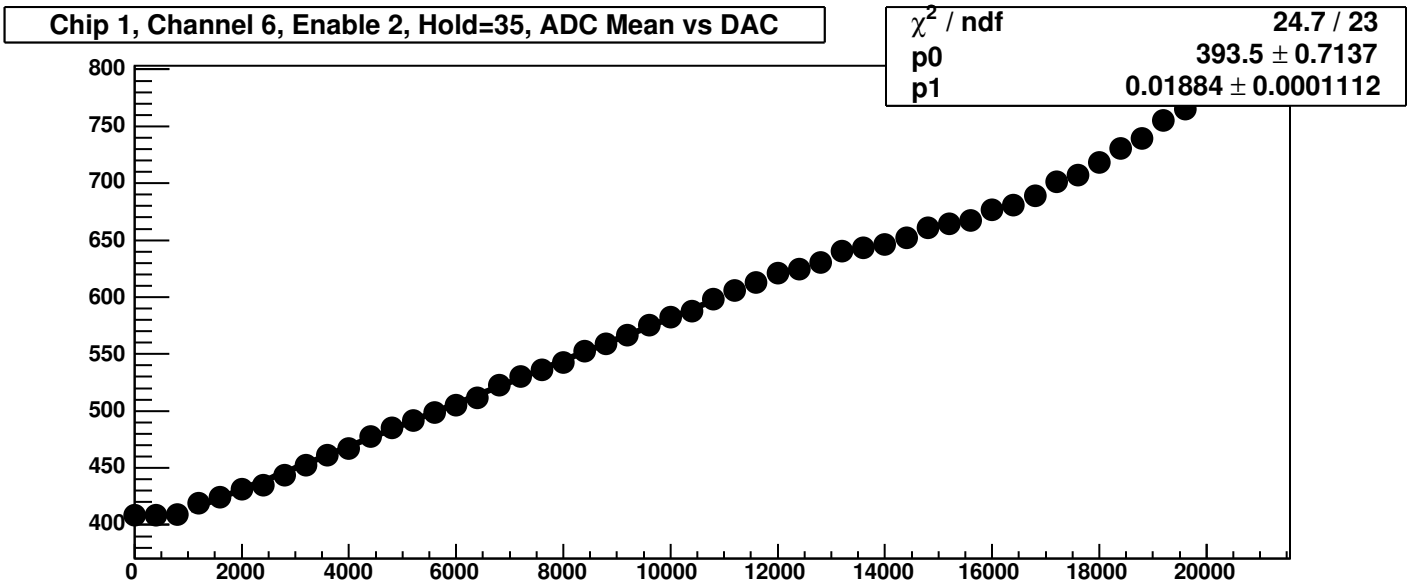
Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

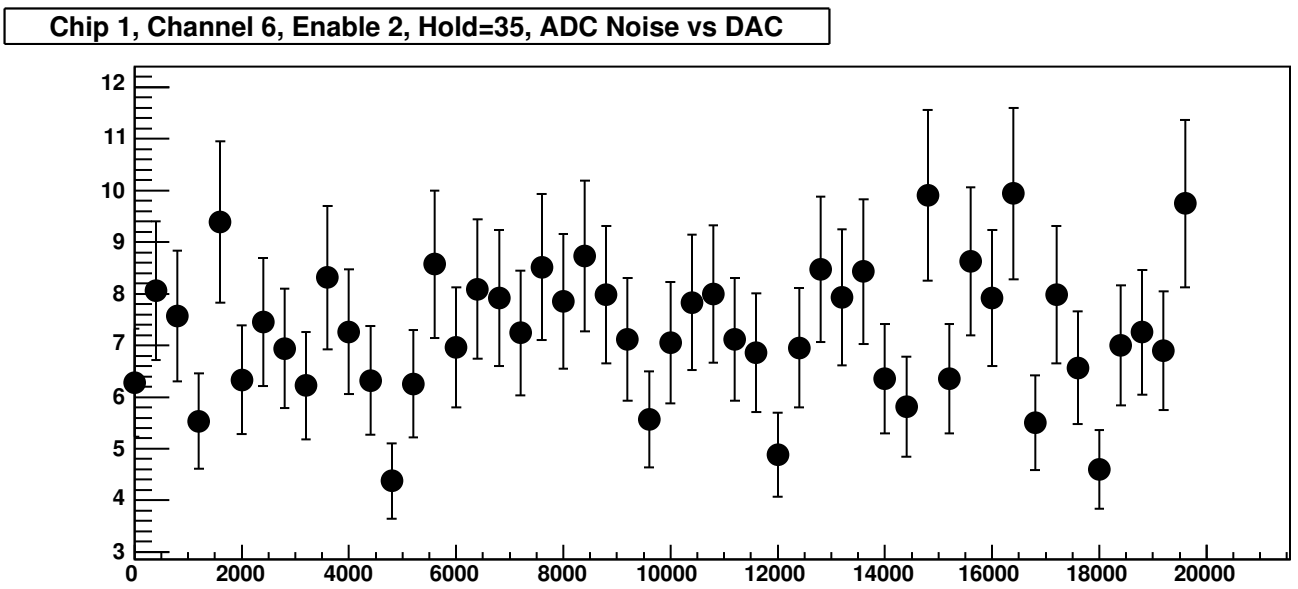


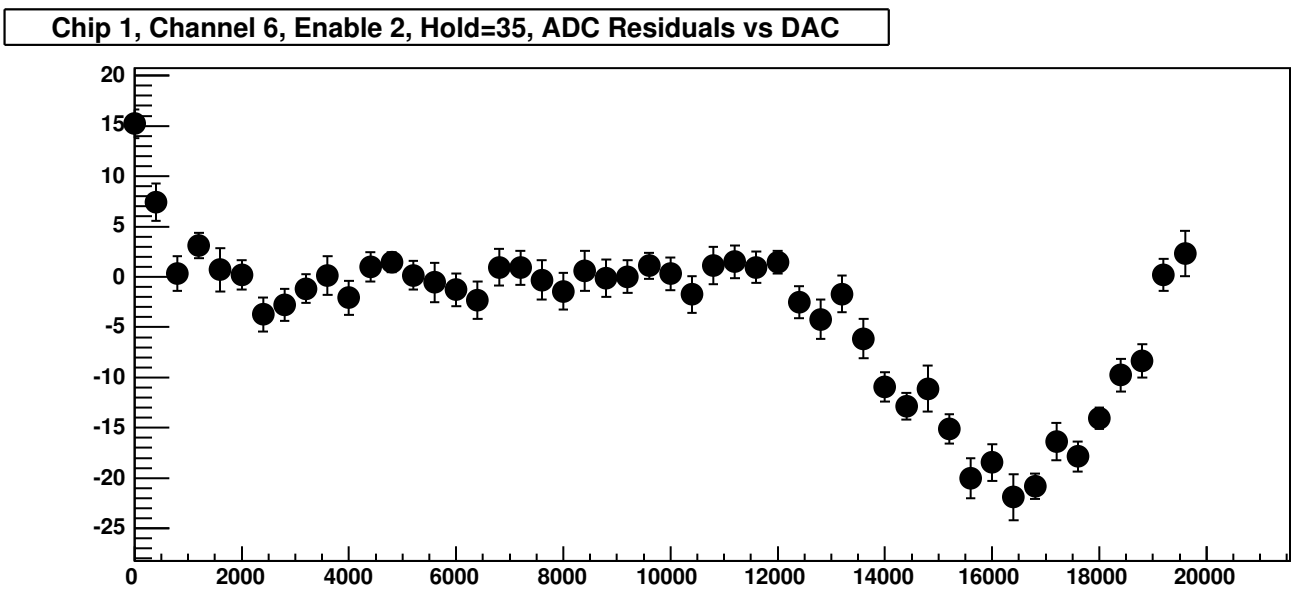

Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 69.7 / 23
p0 388.8 ± 0.7258
p1 0.01746 ± 0.0001052
750
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

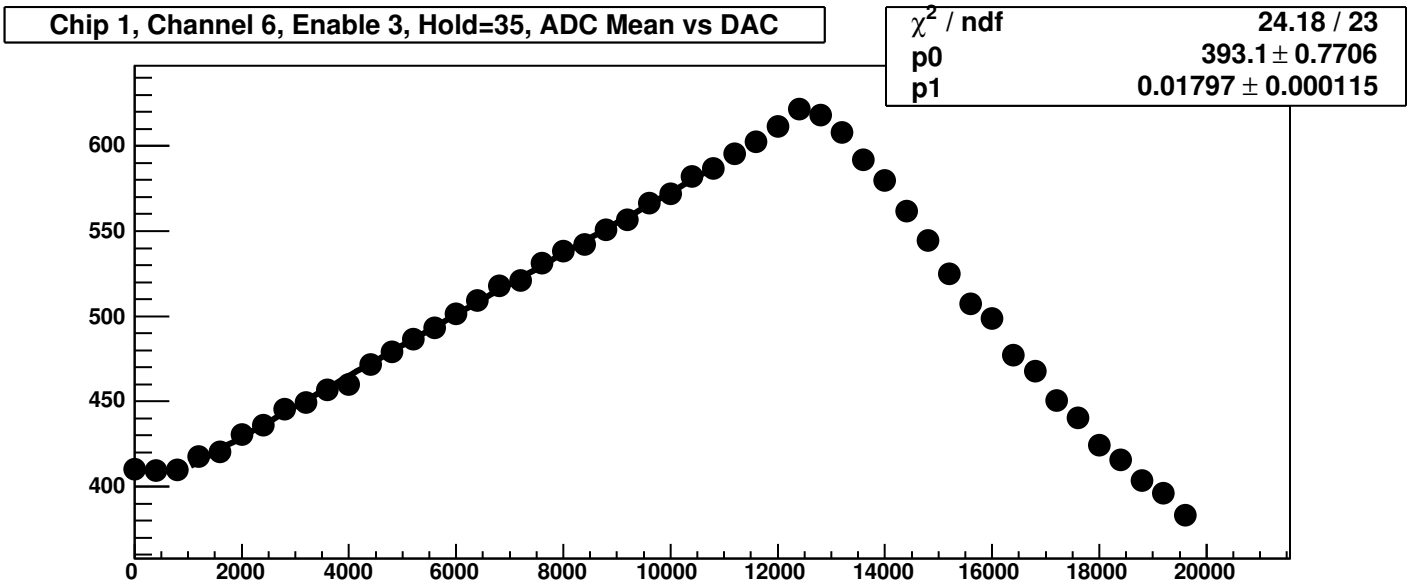

Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

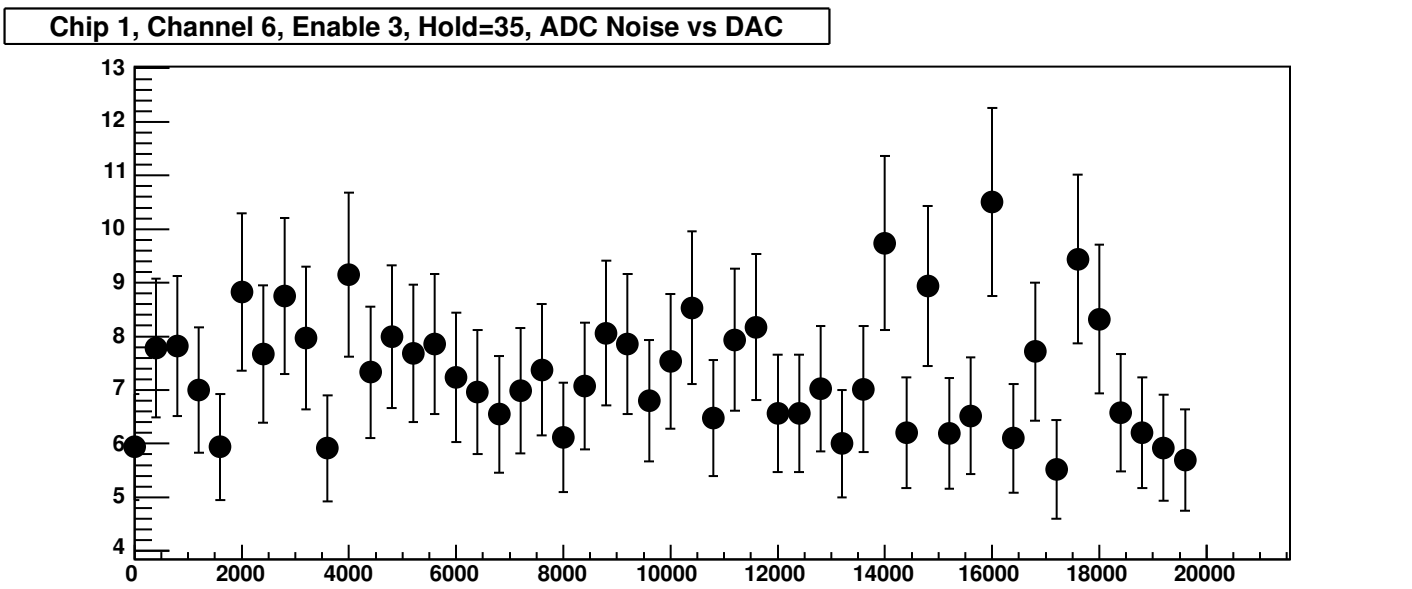

Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

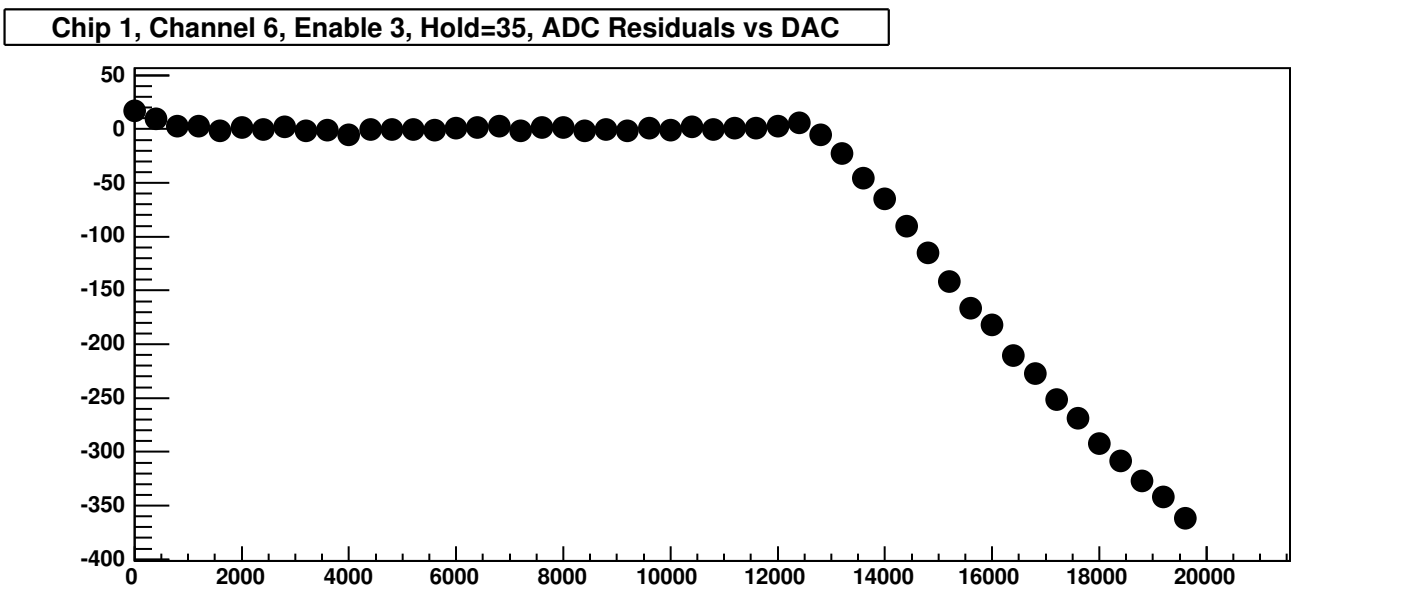

Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 24.7 / 23
p0 393.5 ± 0.7137
p1 0.01884 ± 0.0001112
800
750
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

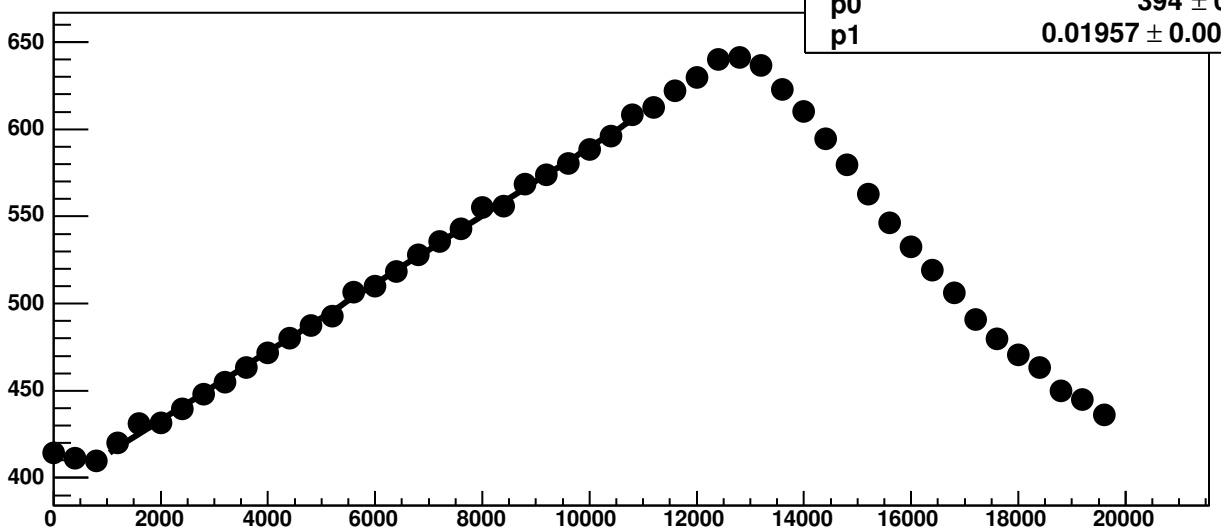

Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.18 / 23
p0 393.1 ± 0.7706
p1 0.01797 ± 0.000115
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

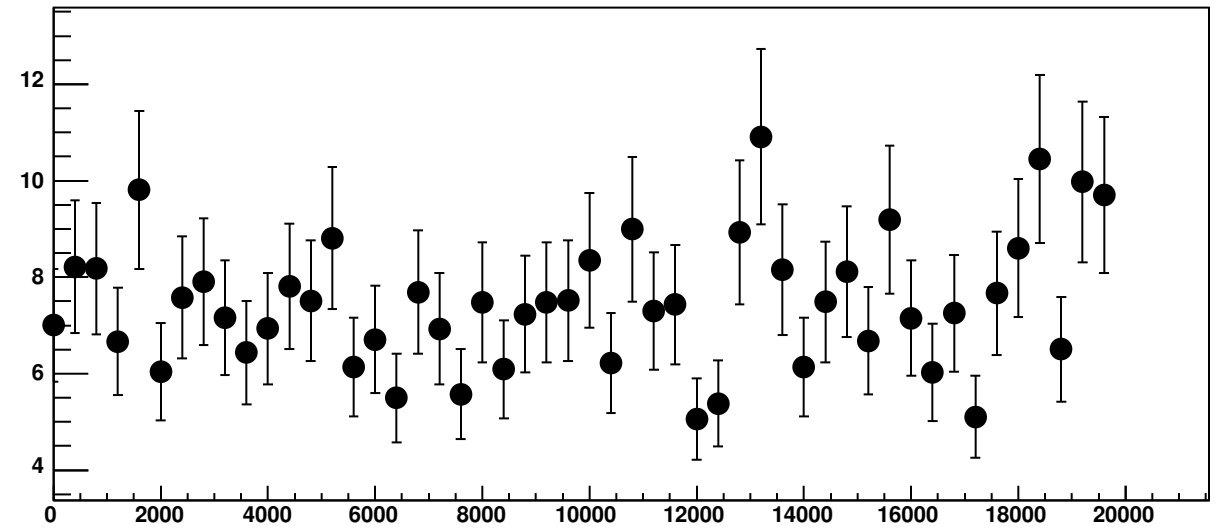

Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

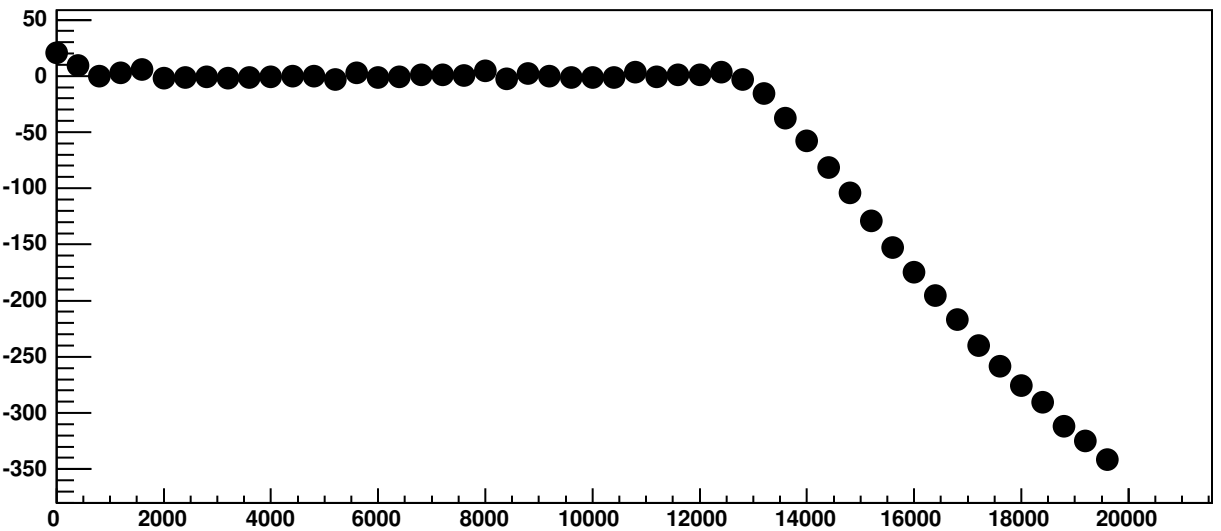
χ^2 / ndf	38.72 / 23
p0	394 ± 0.7667
p1	0.01957 ± 0.0001157

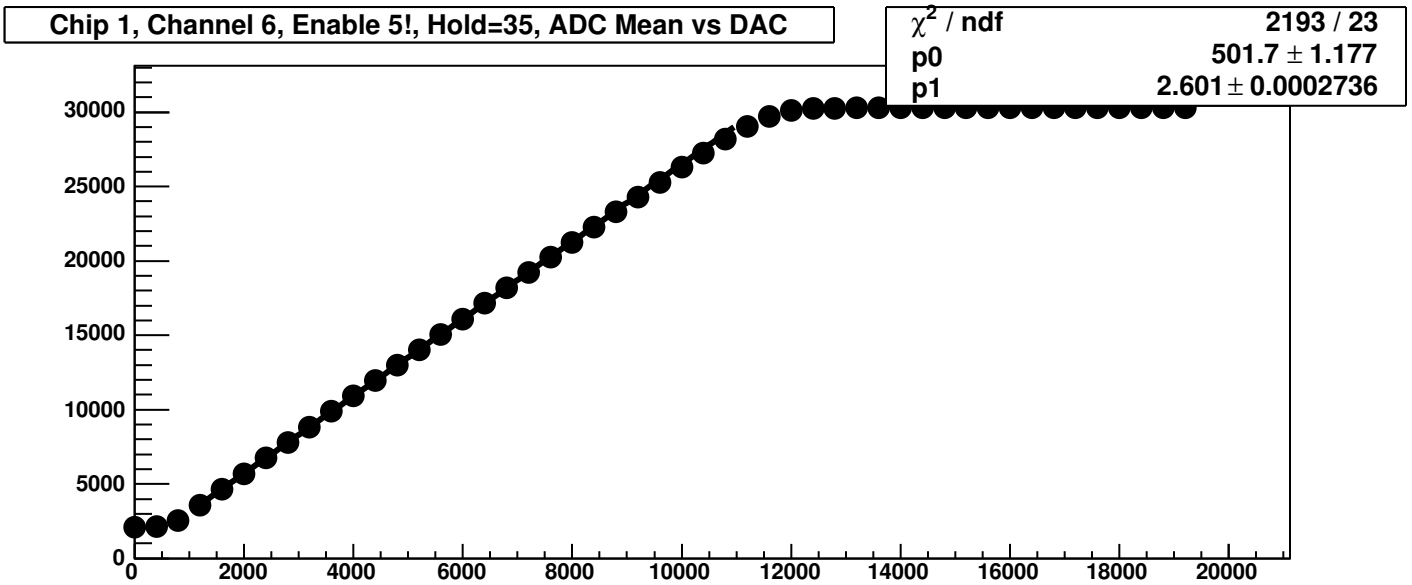


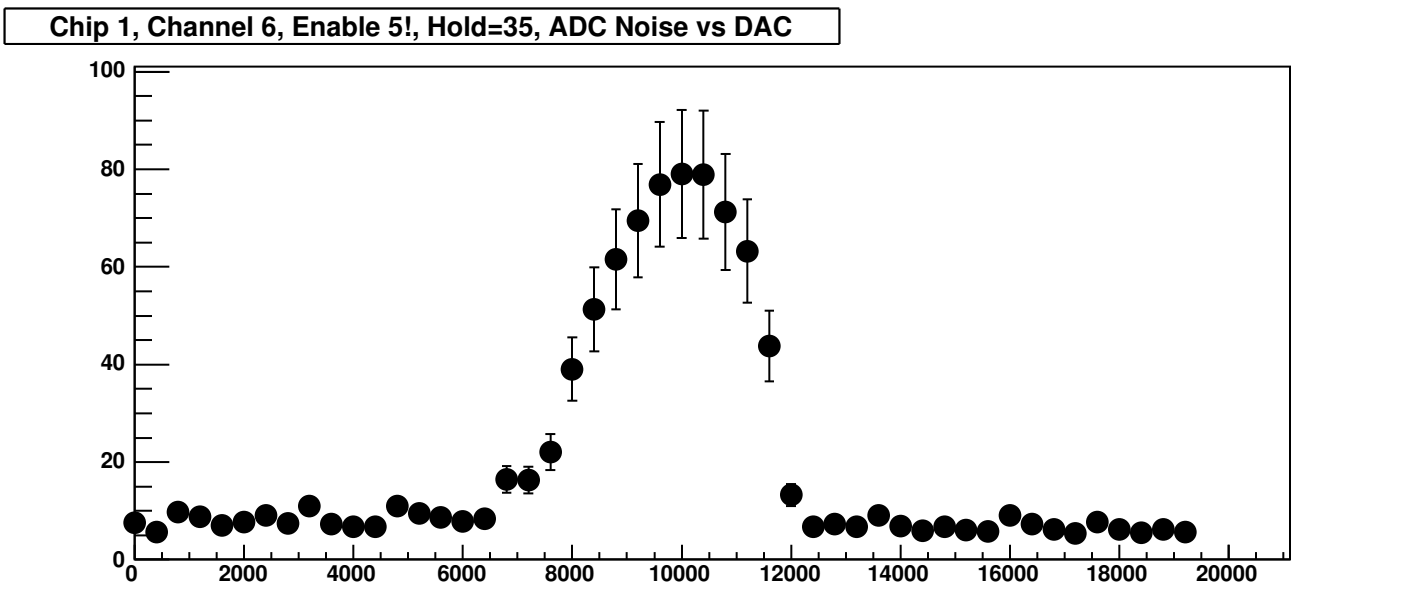
Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

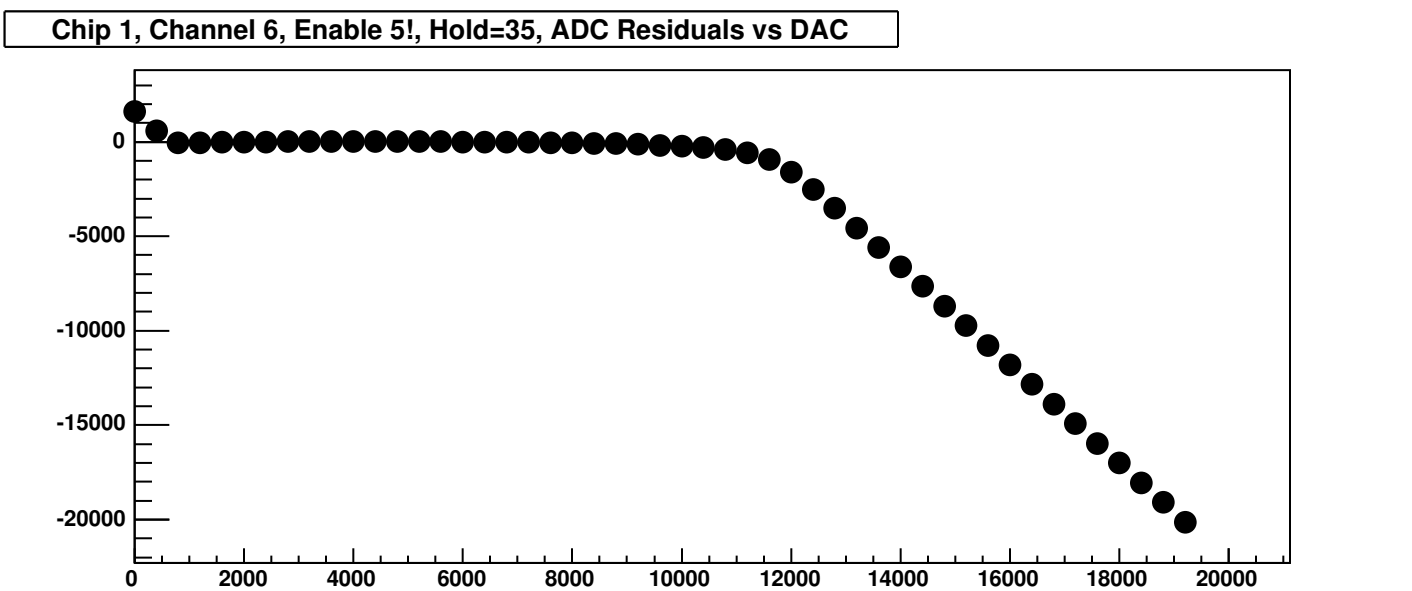


Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

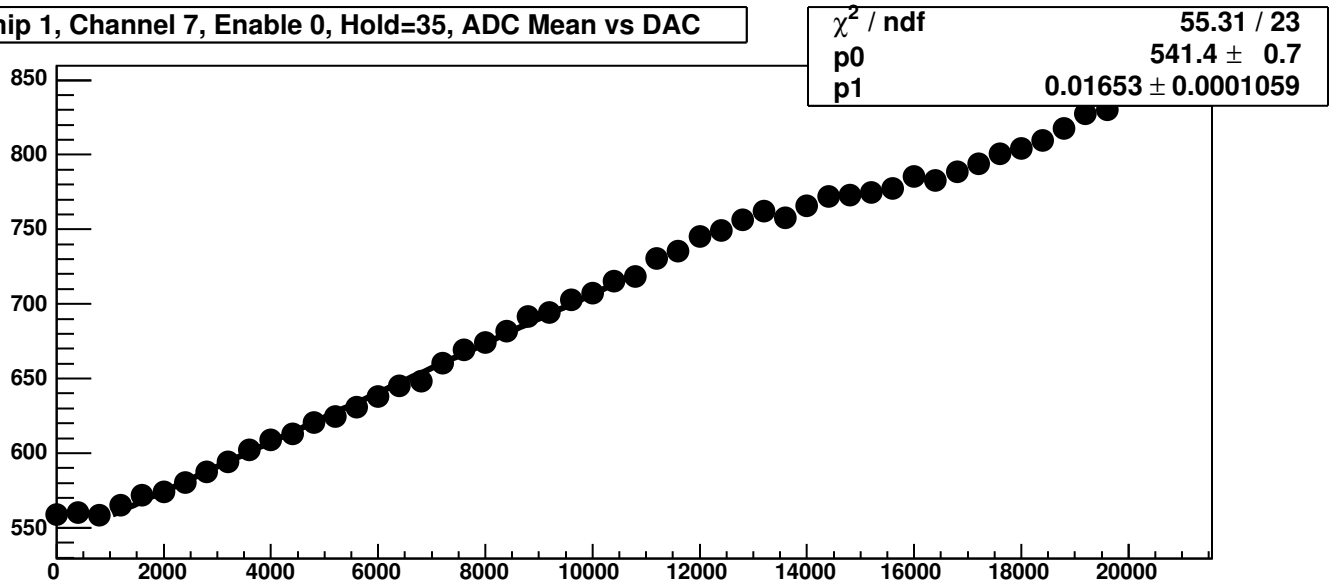



Chip 1, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ^2 / ndf 2193 / 23
p0 501.7 ± 1.177
p1 2.601 ± 0.0002736
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

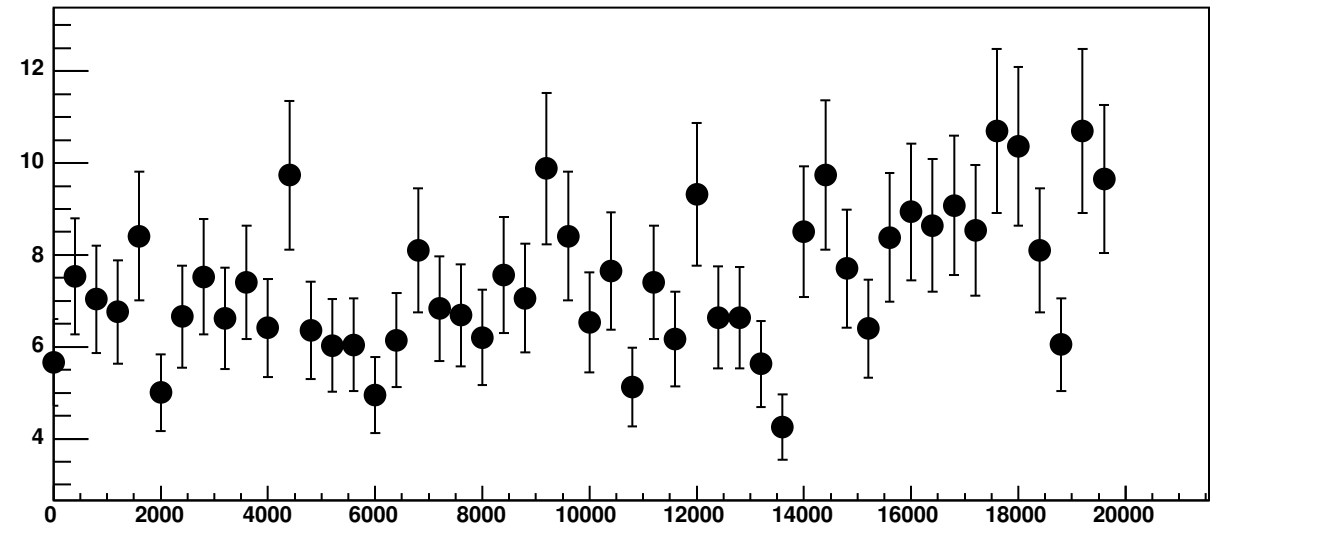

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

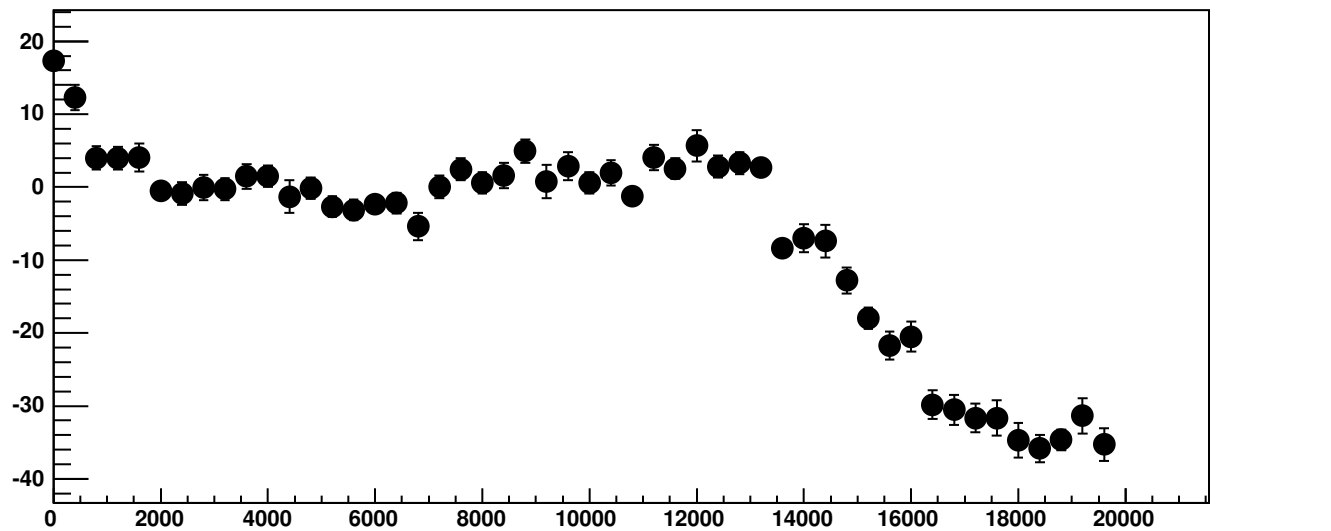
Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



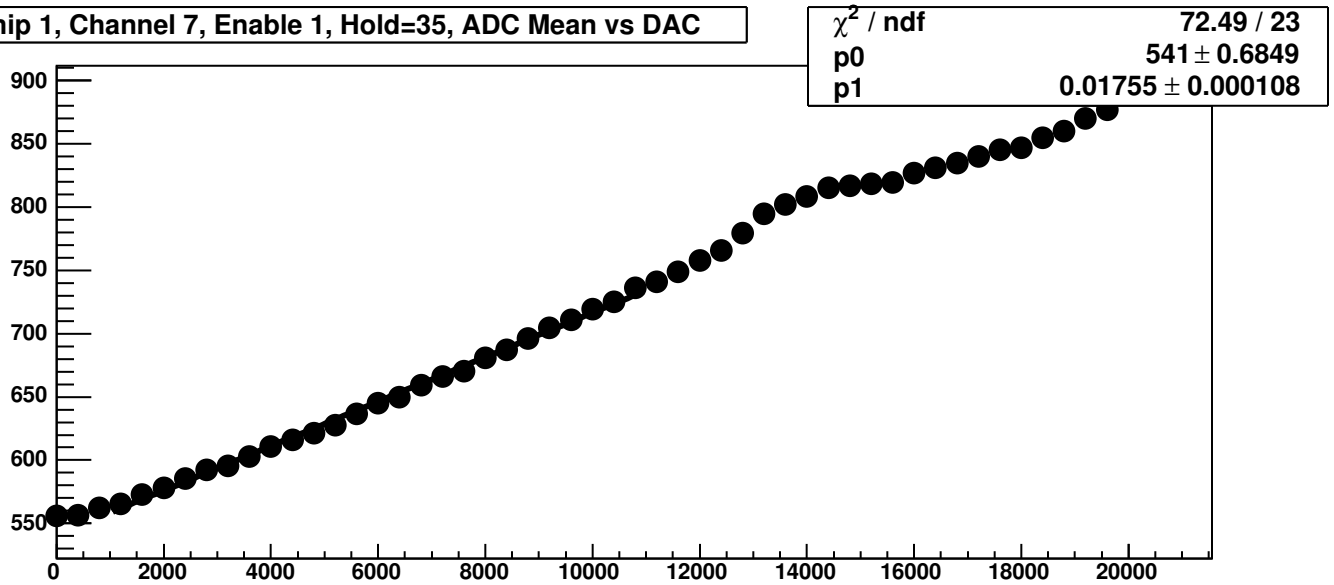
Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



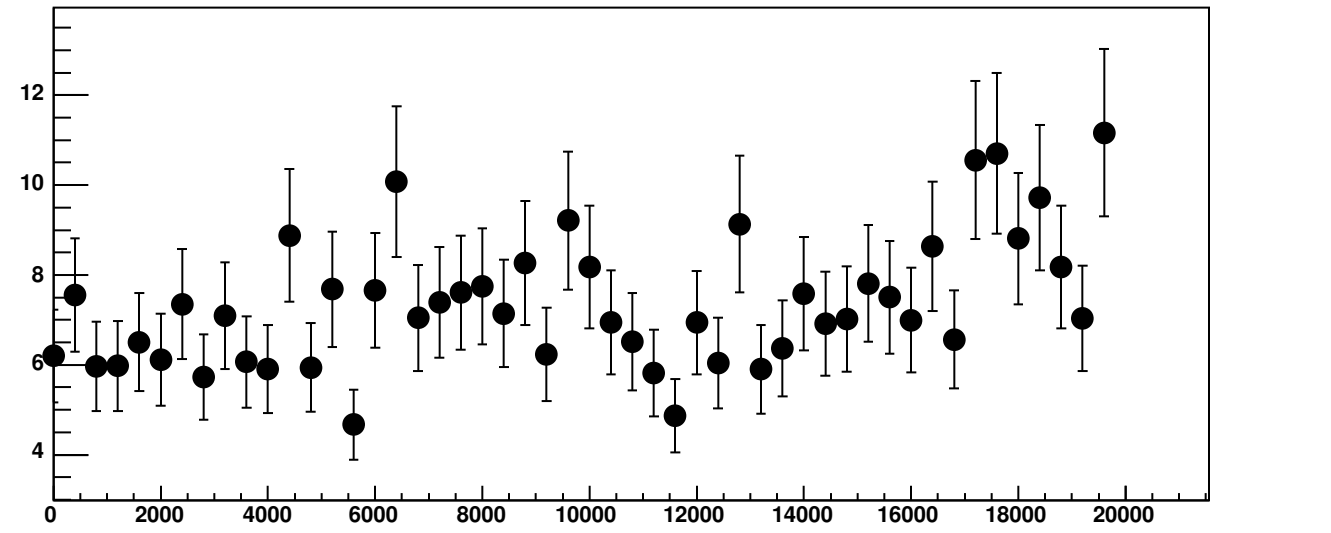
Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



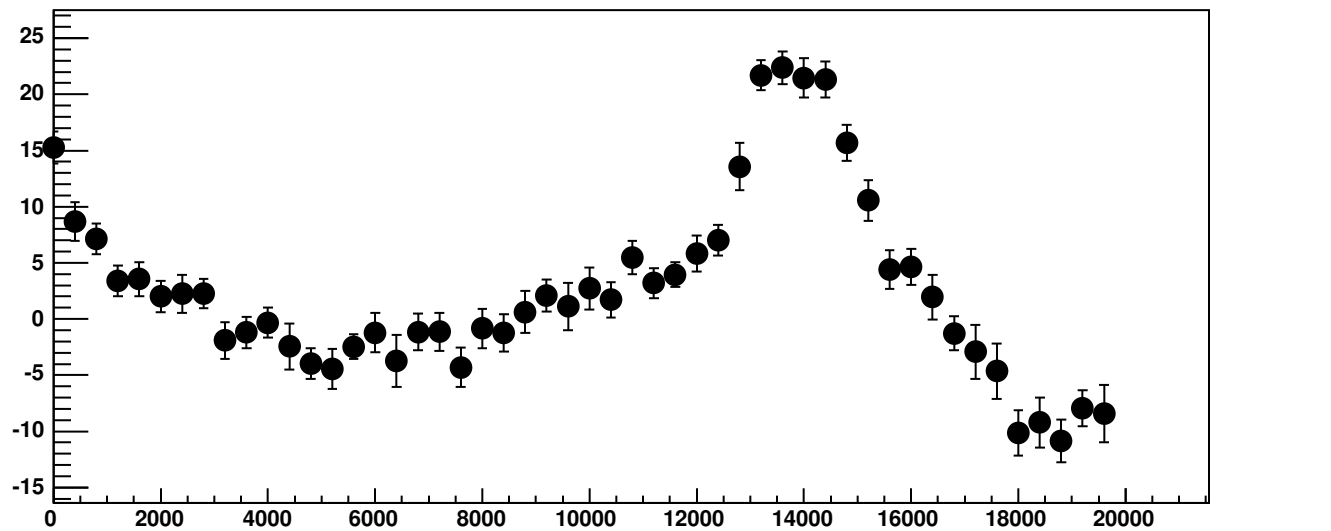
Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



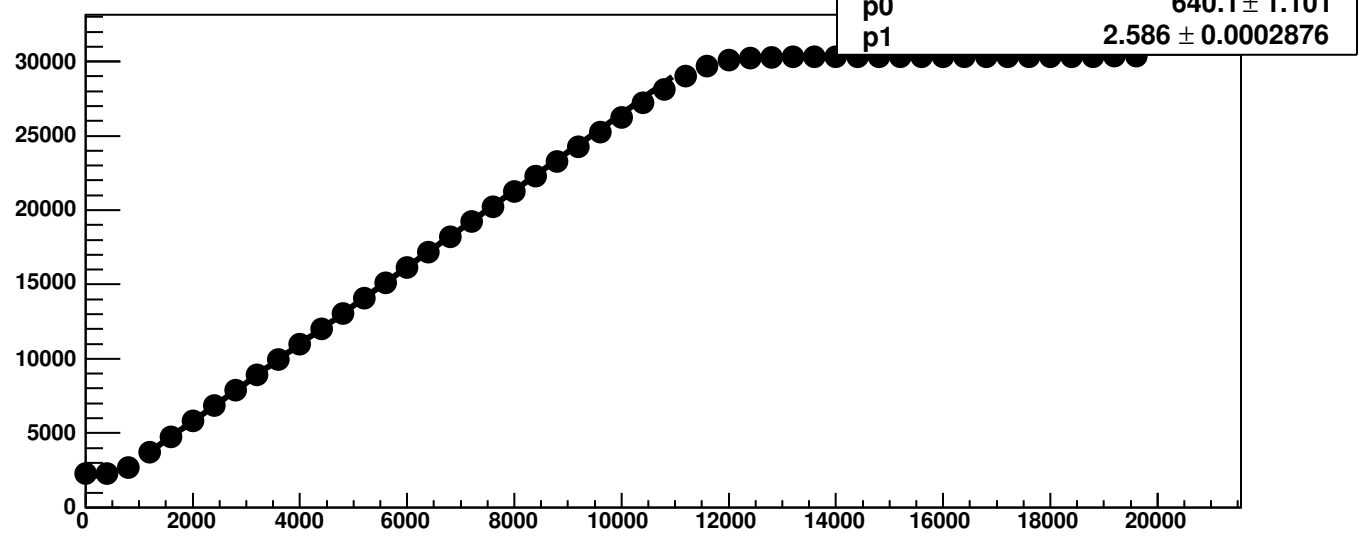
Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



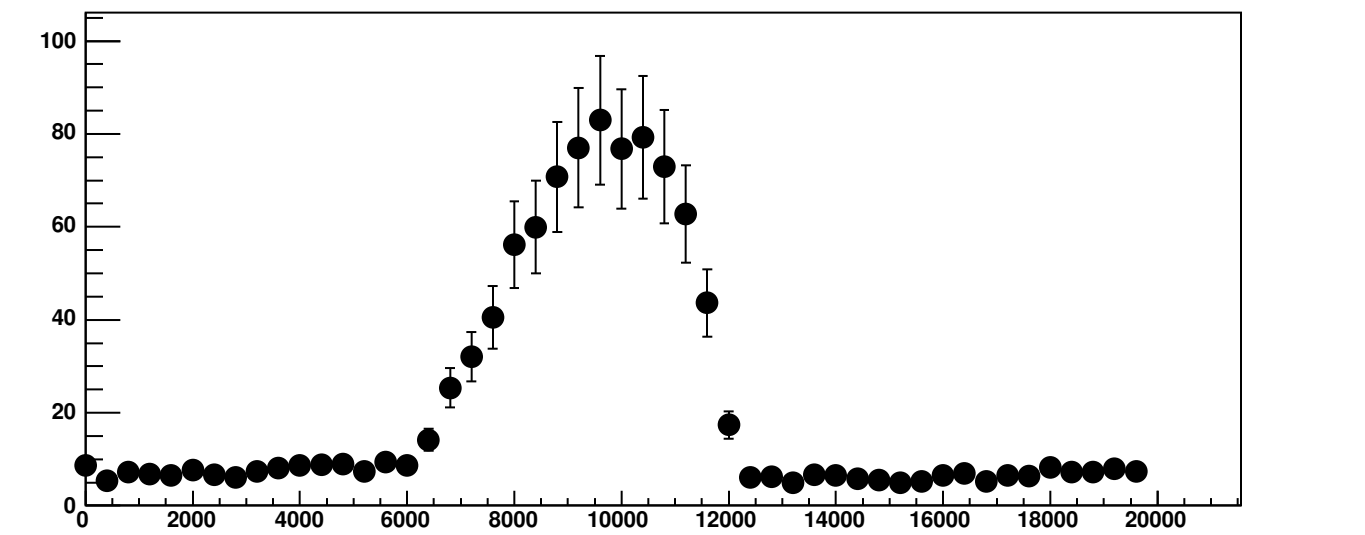
Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



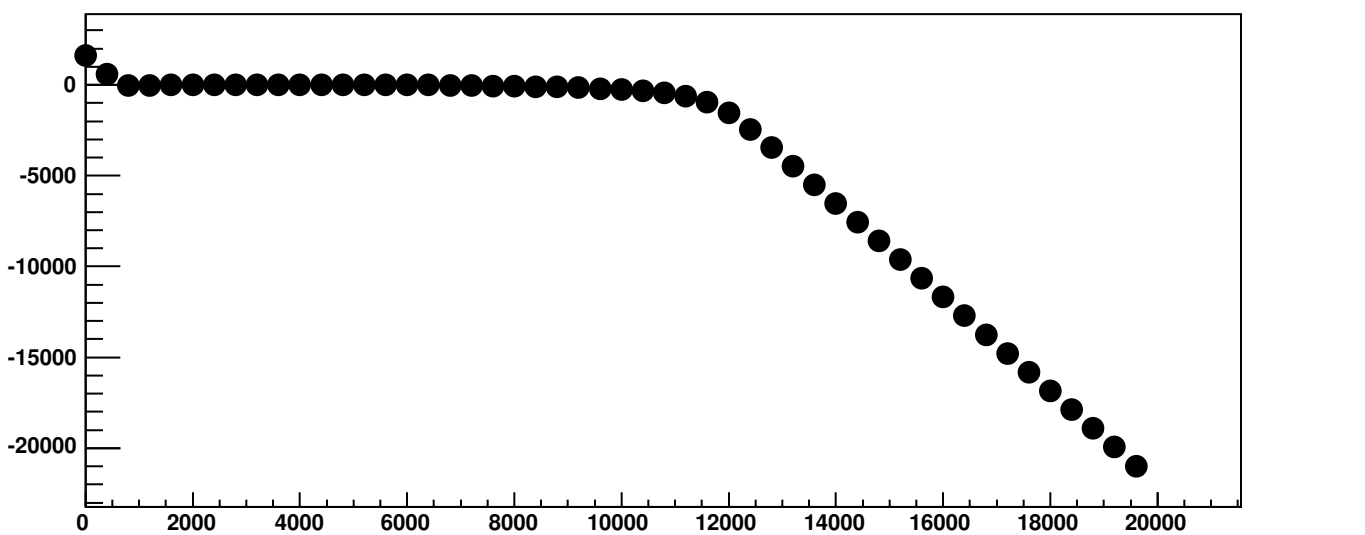
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



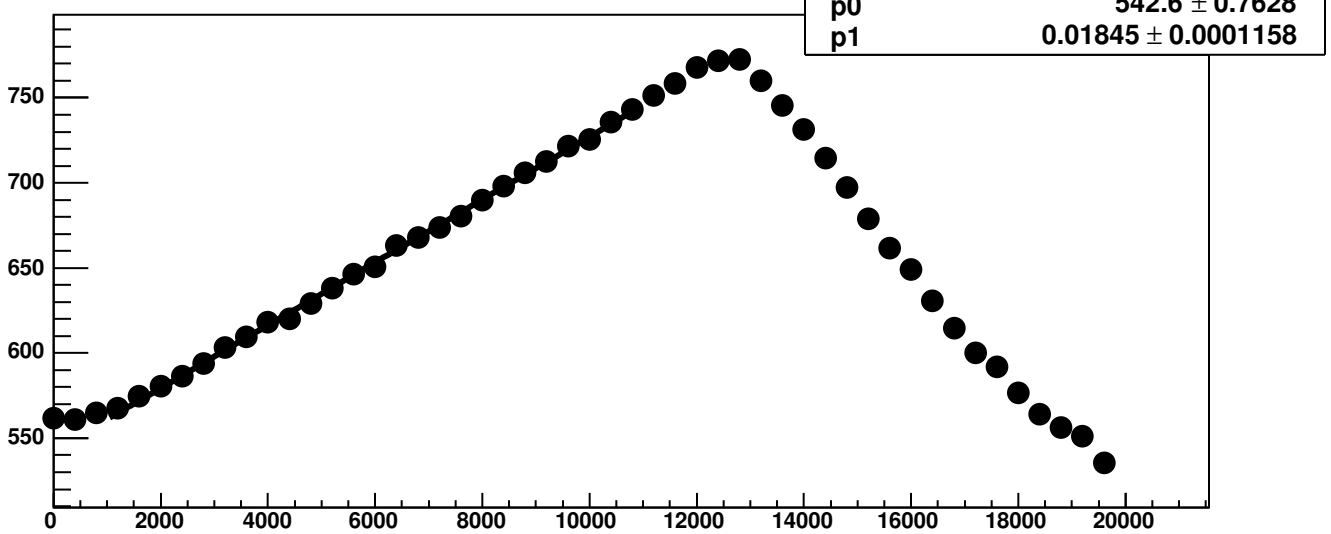
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



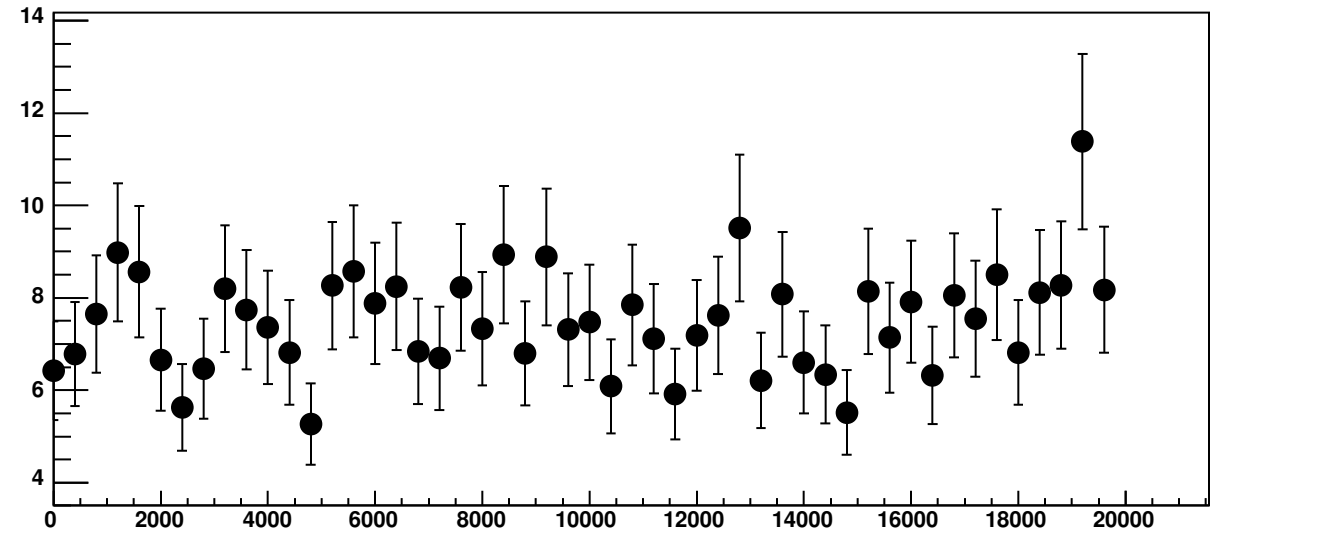
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC



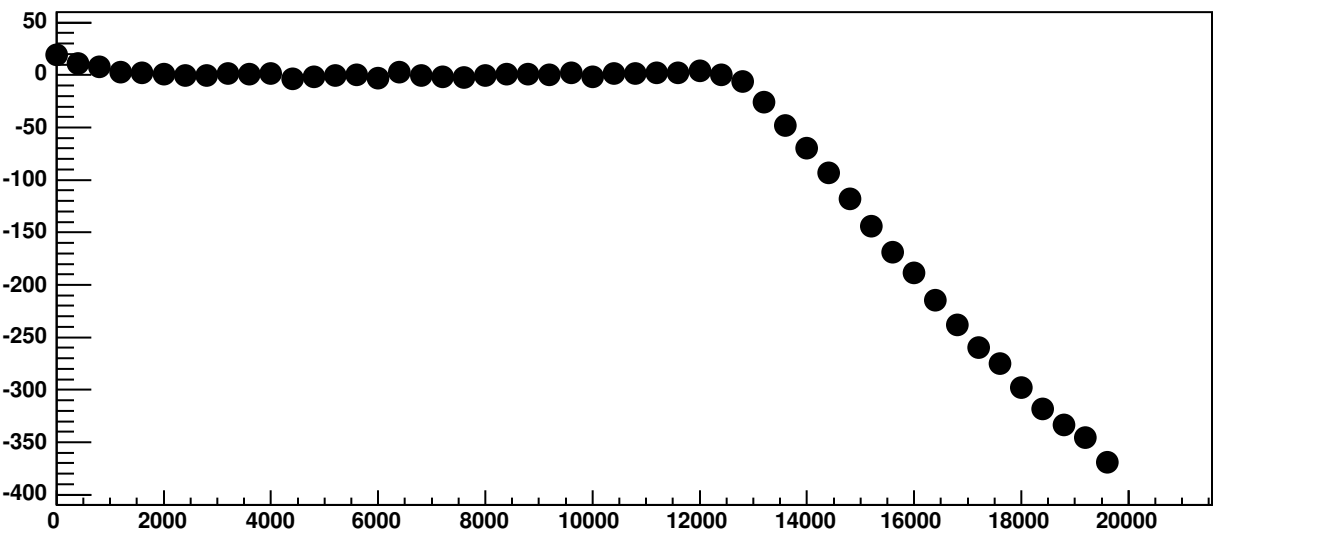
Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

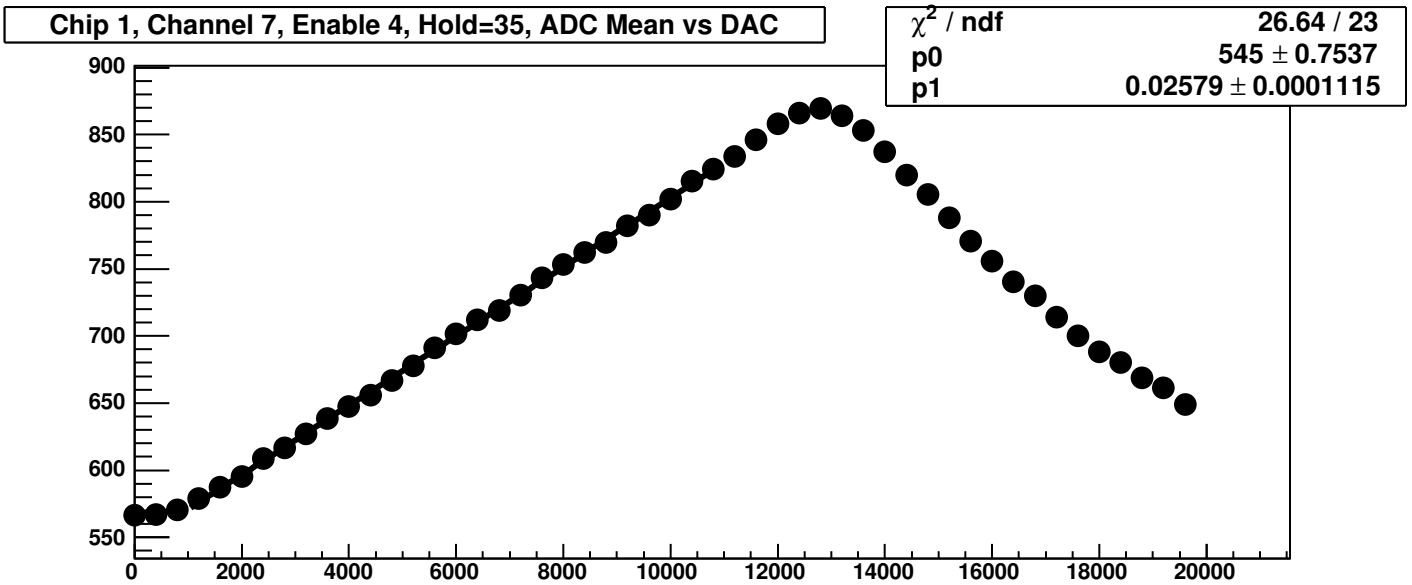


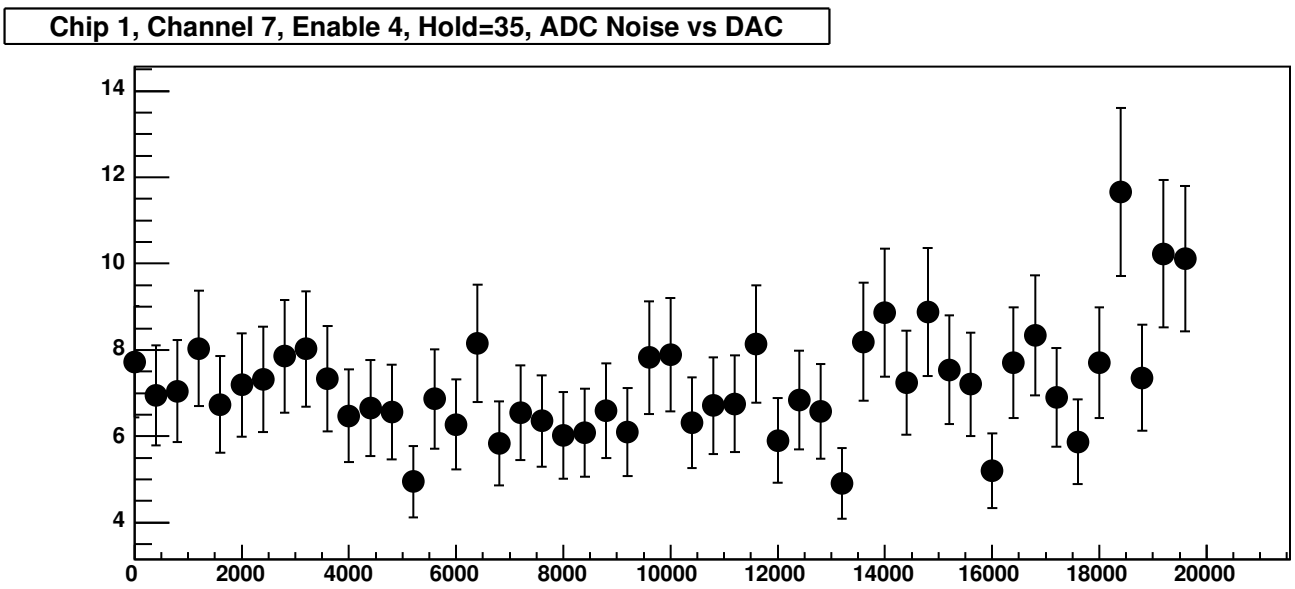
Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC

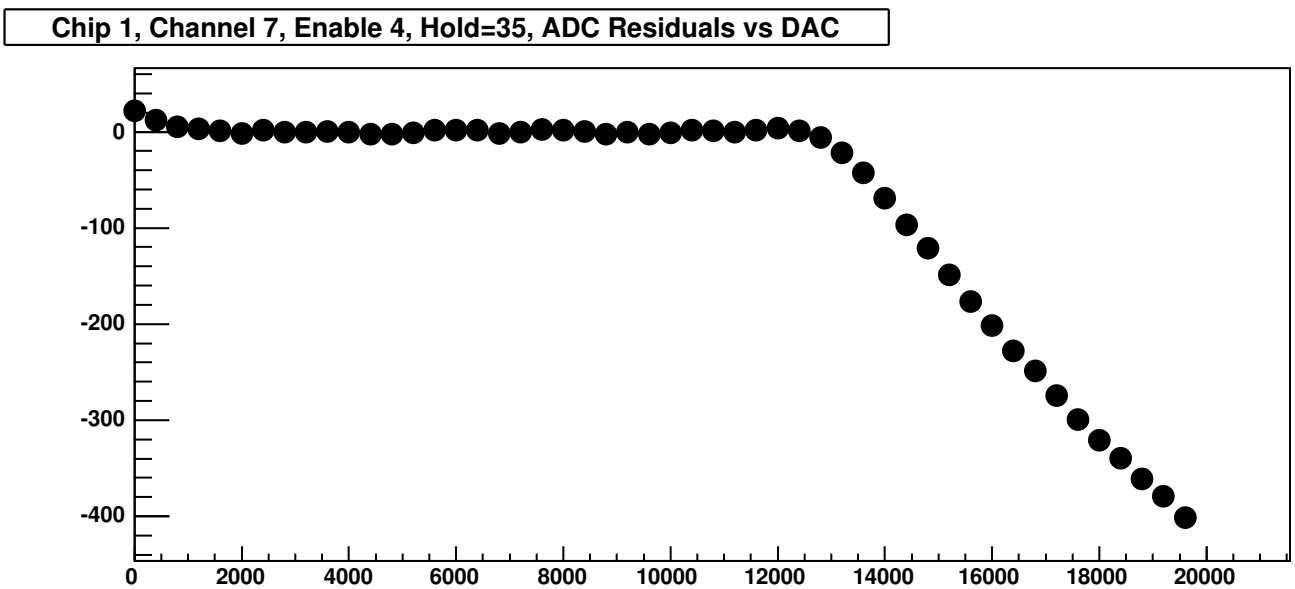


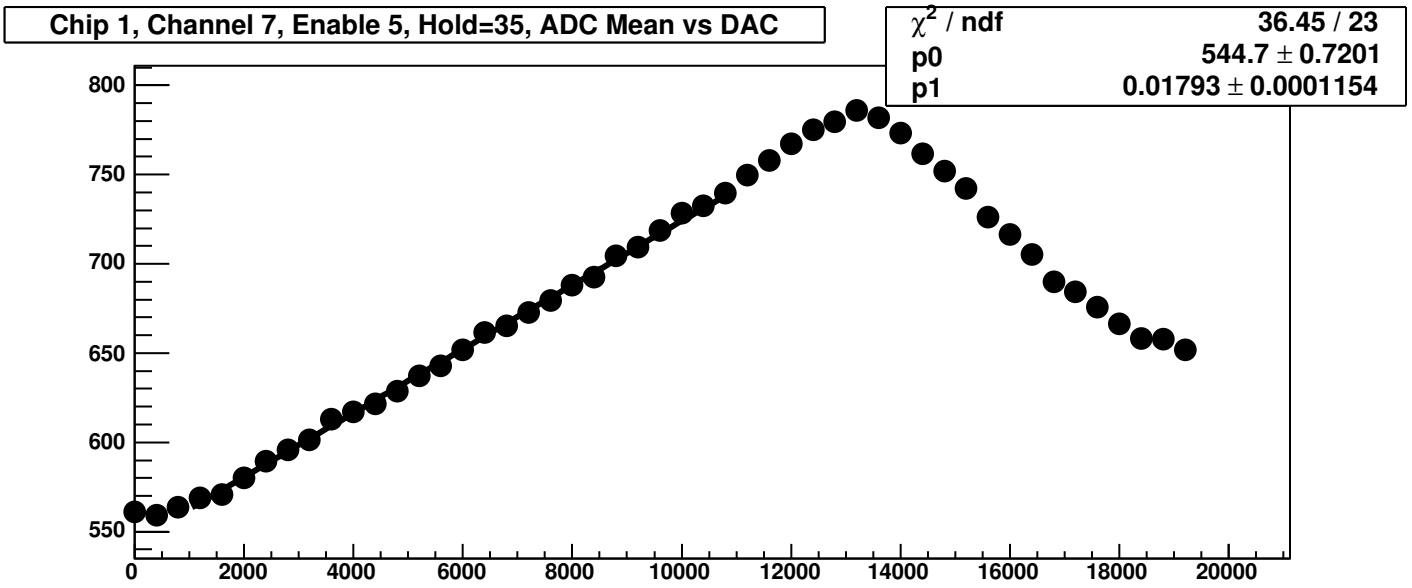
Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

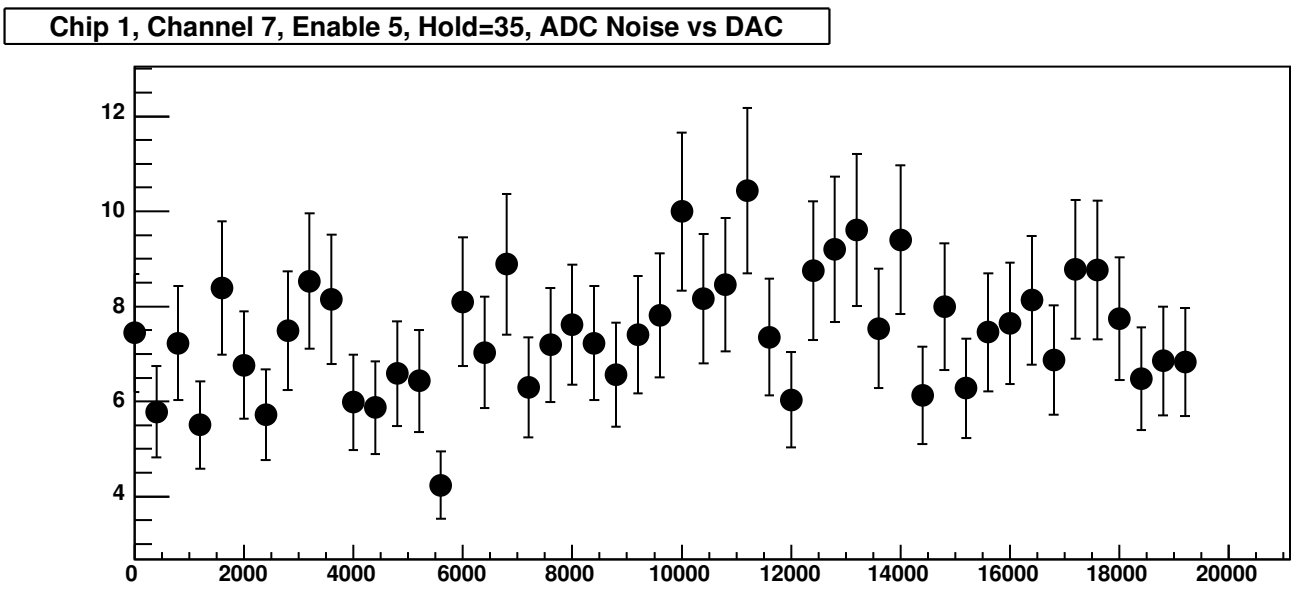


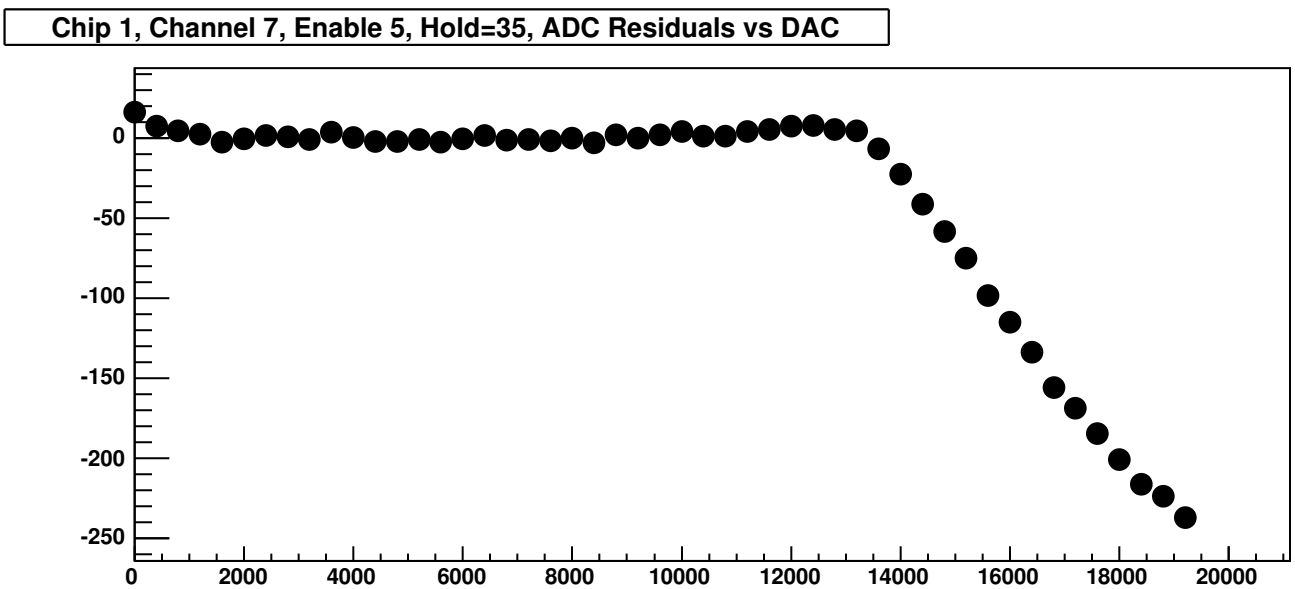

Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.64 / 23
p0 545 ± 0.7537
p1 0.02579 ± 0.0001115
900
850
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

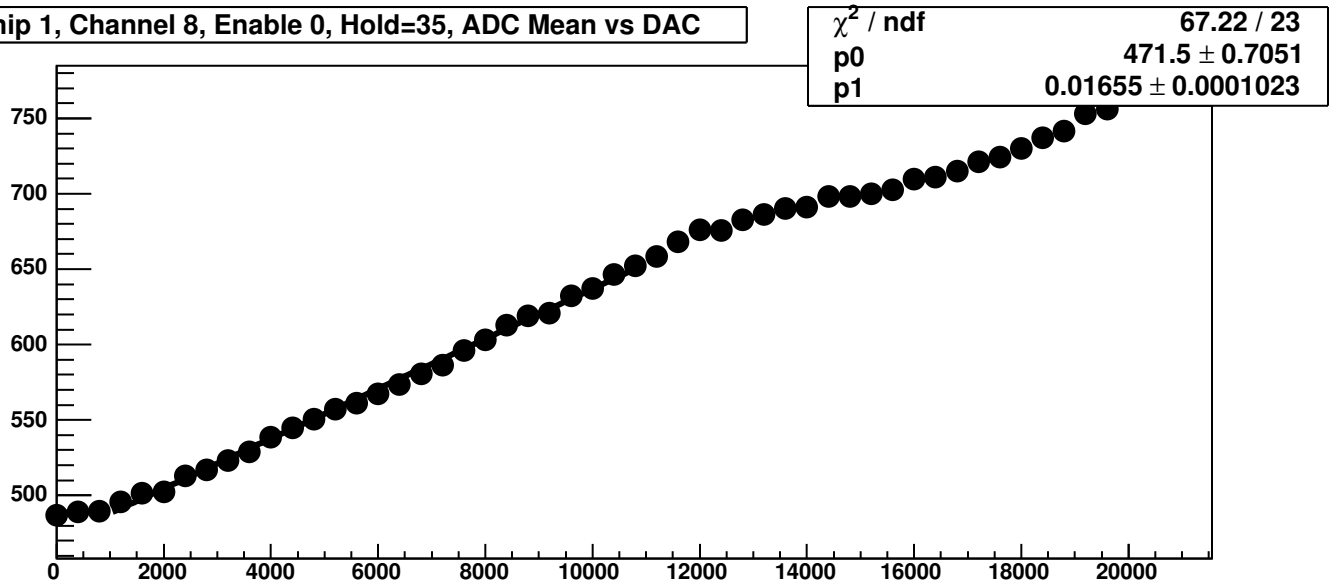

Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 36.45 / 23
p0 544.7 ± 0.7201
p1 0.01793 ± 0.0001154
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

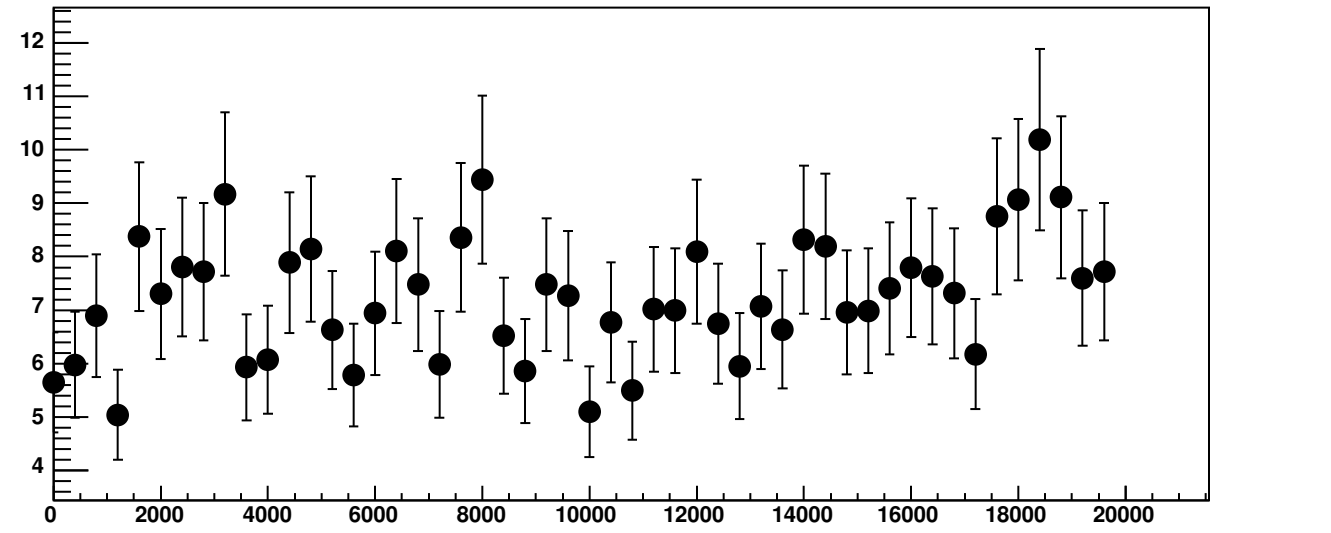

Chip 1, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

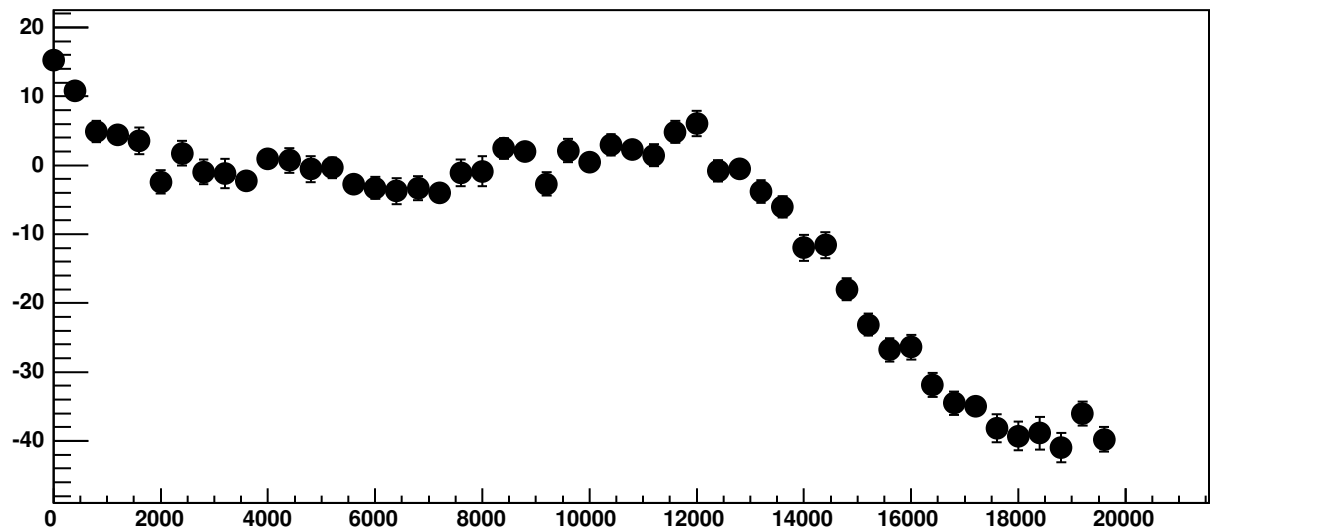
Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



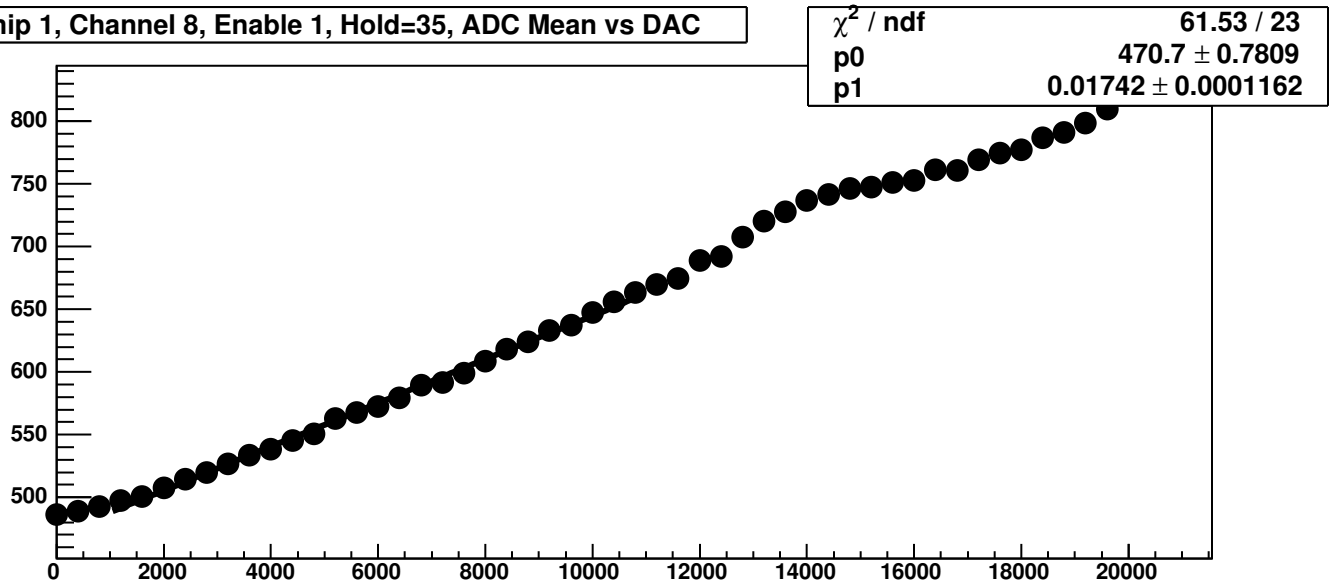
Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



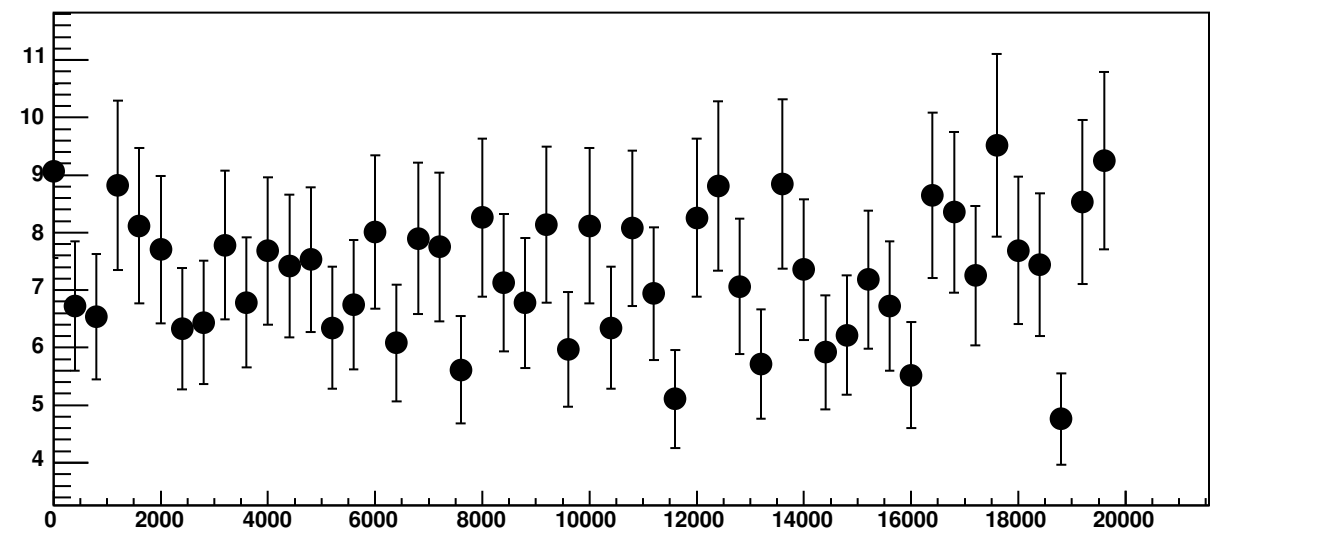
Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



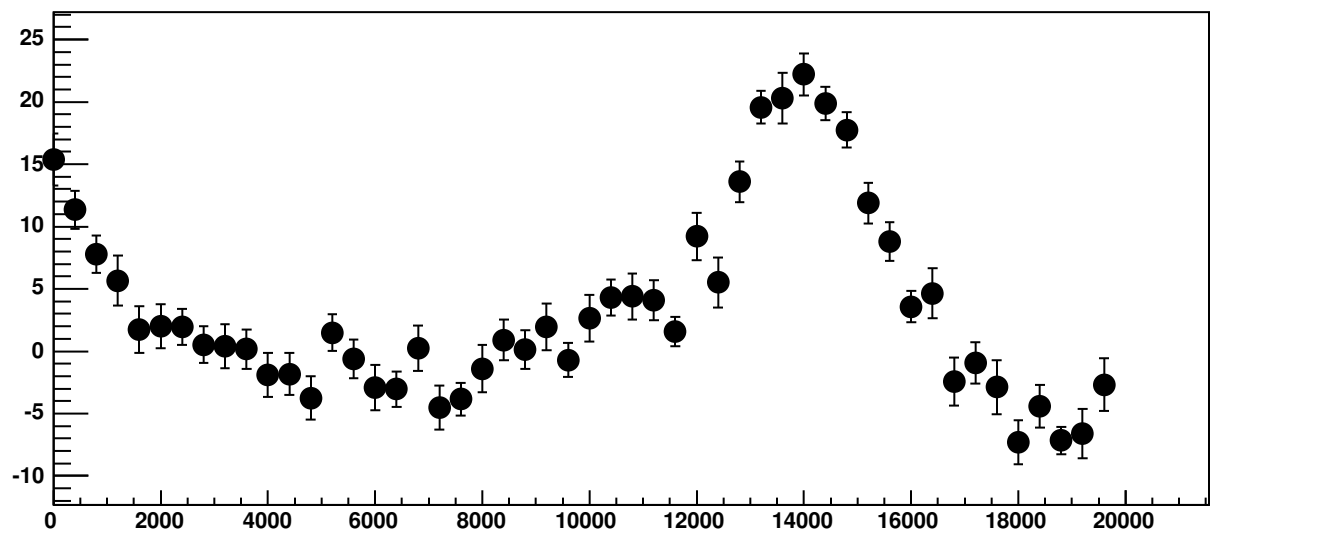
Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

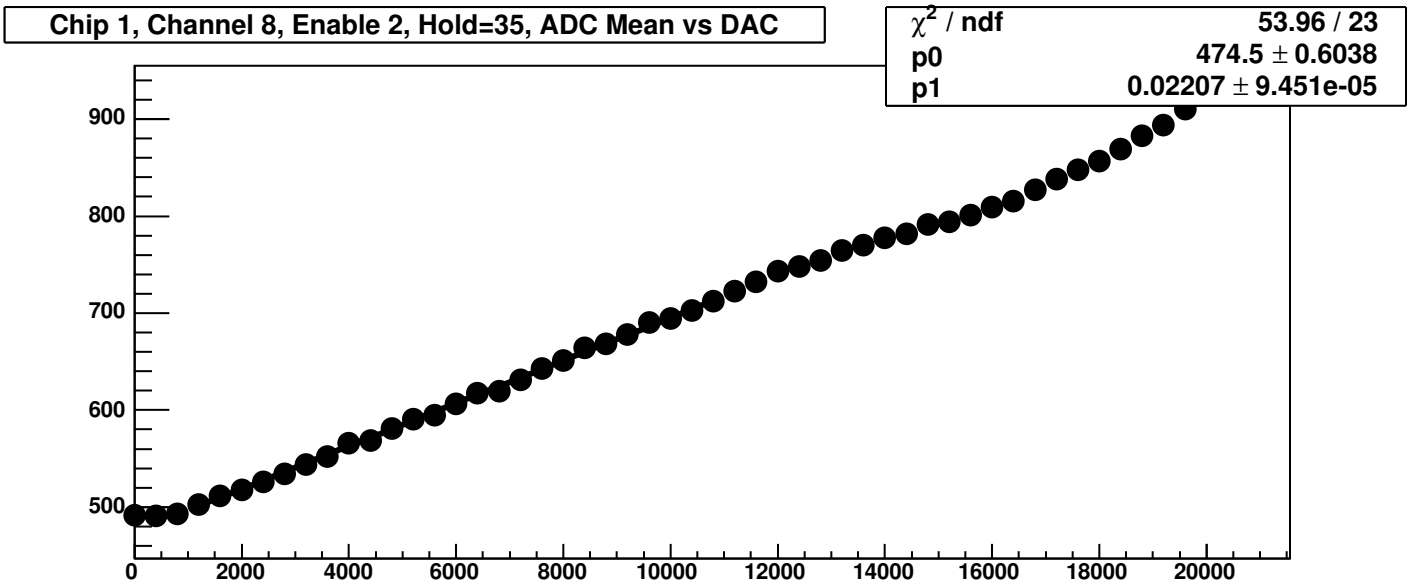


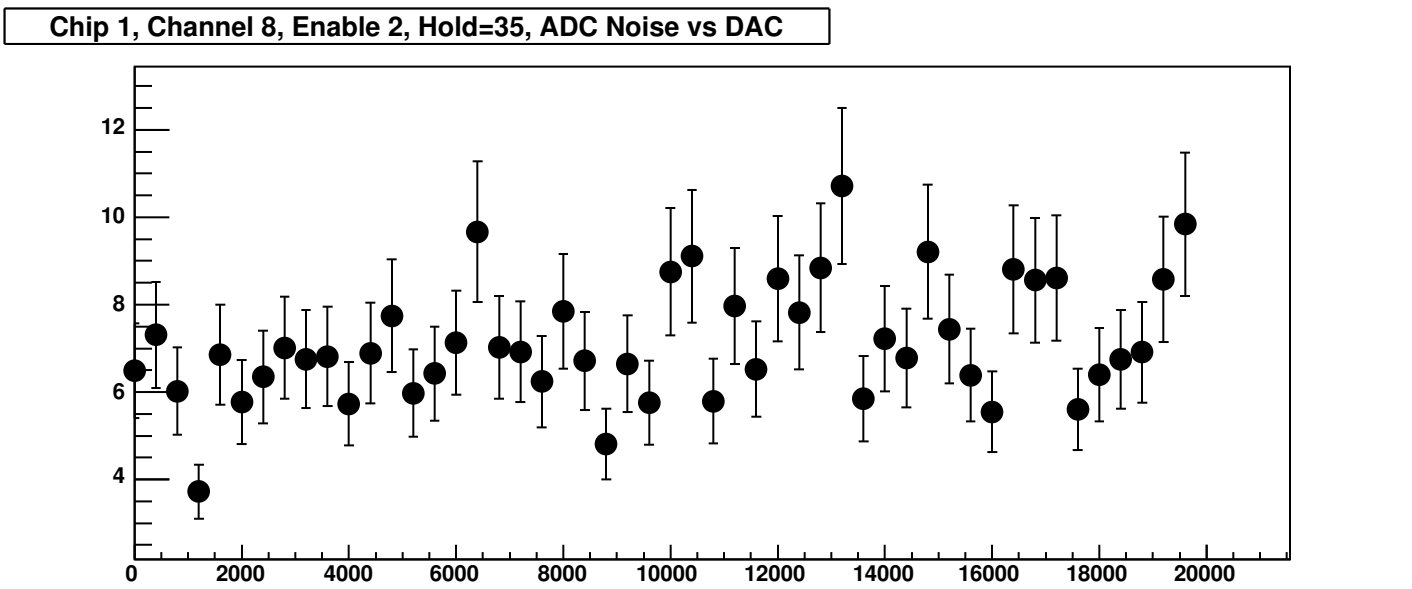
Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

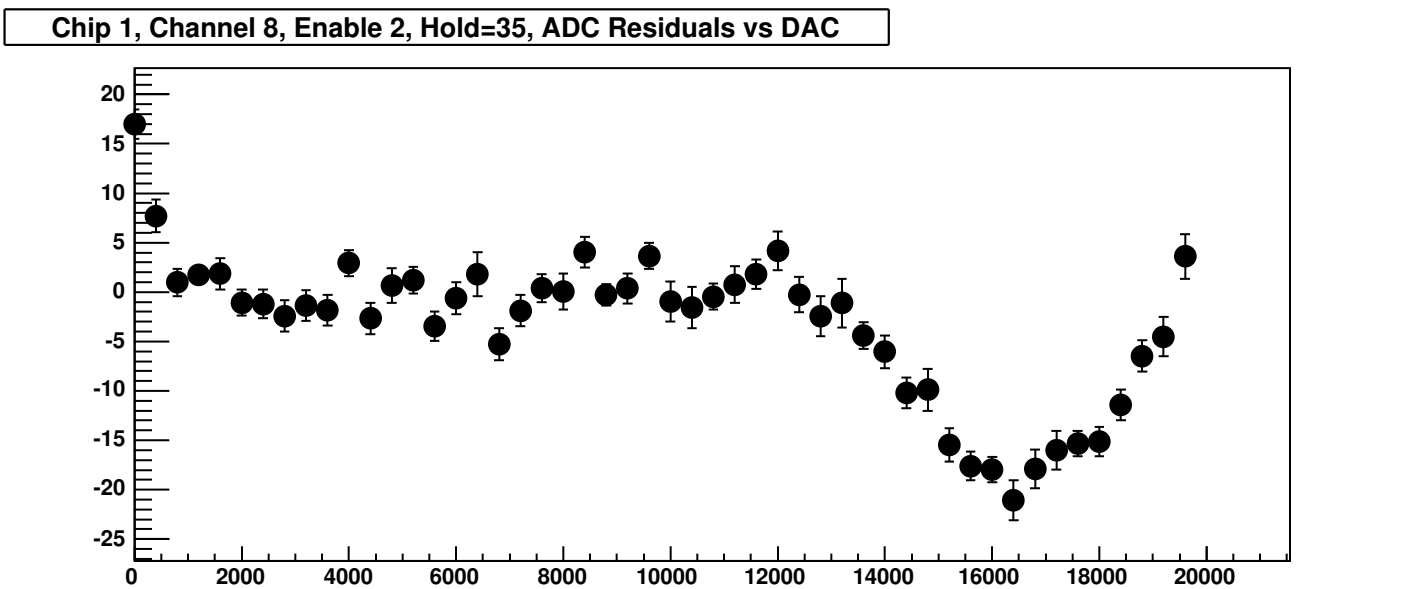


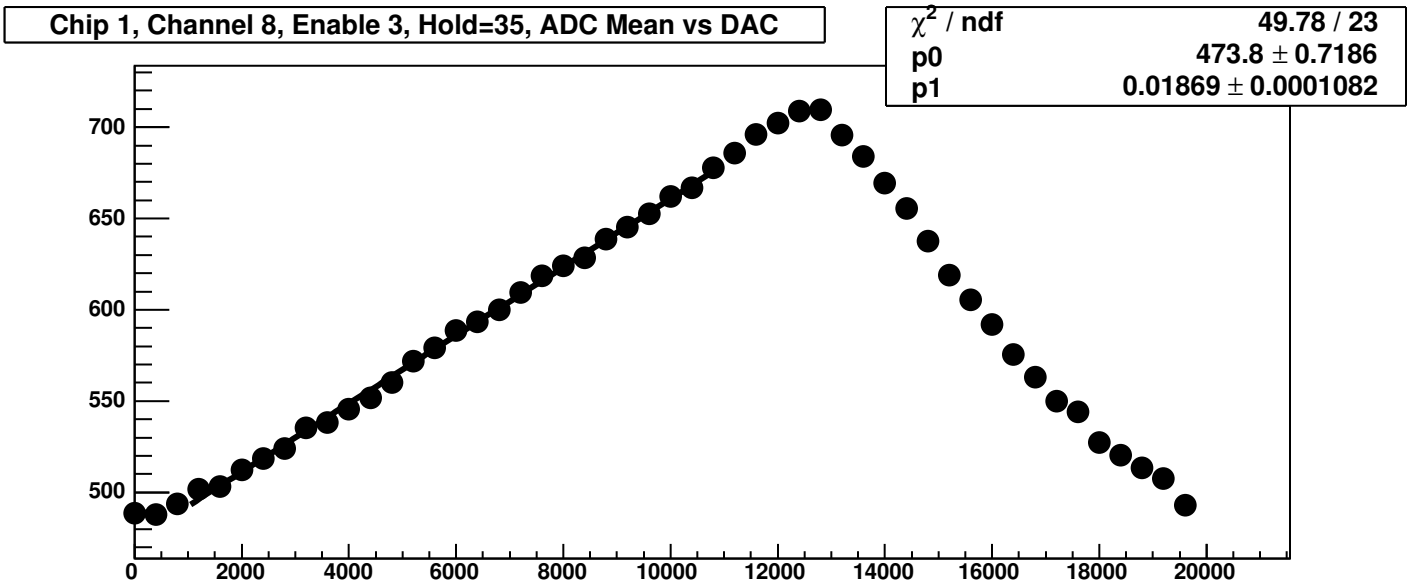
Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

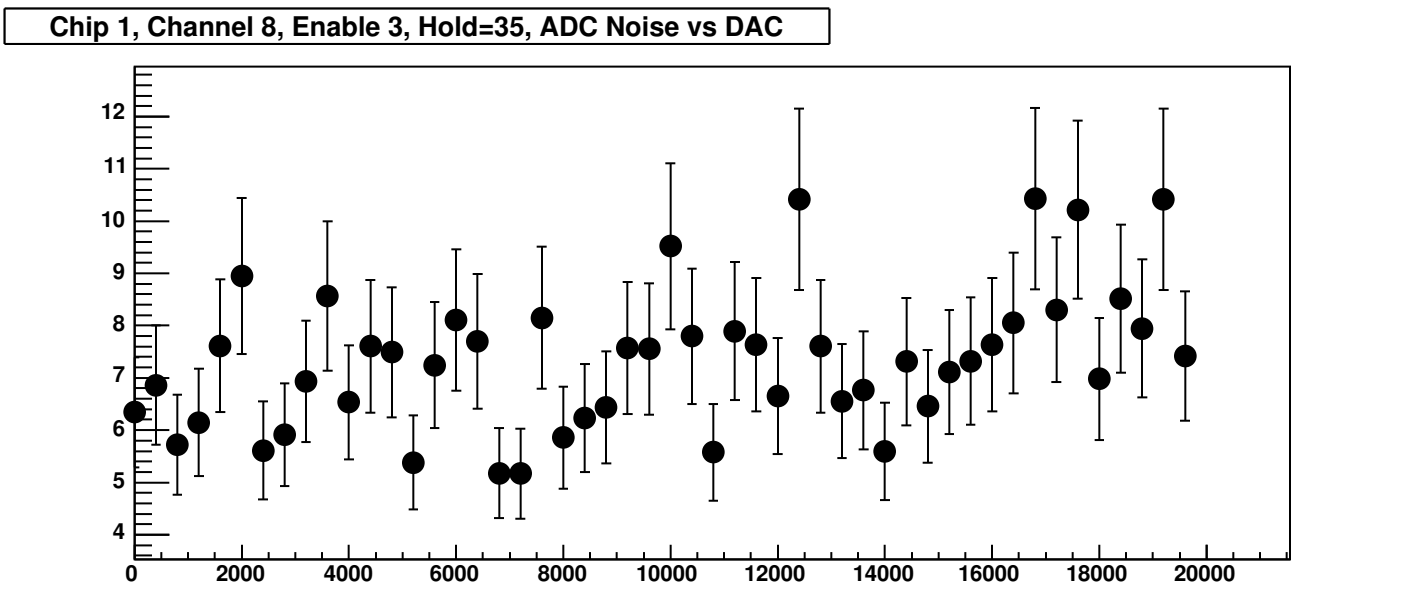


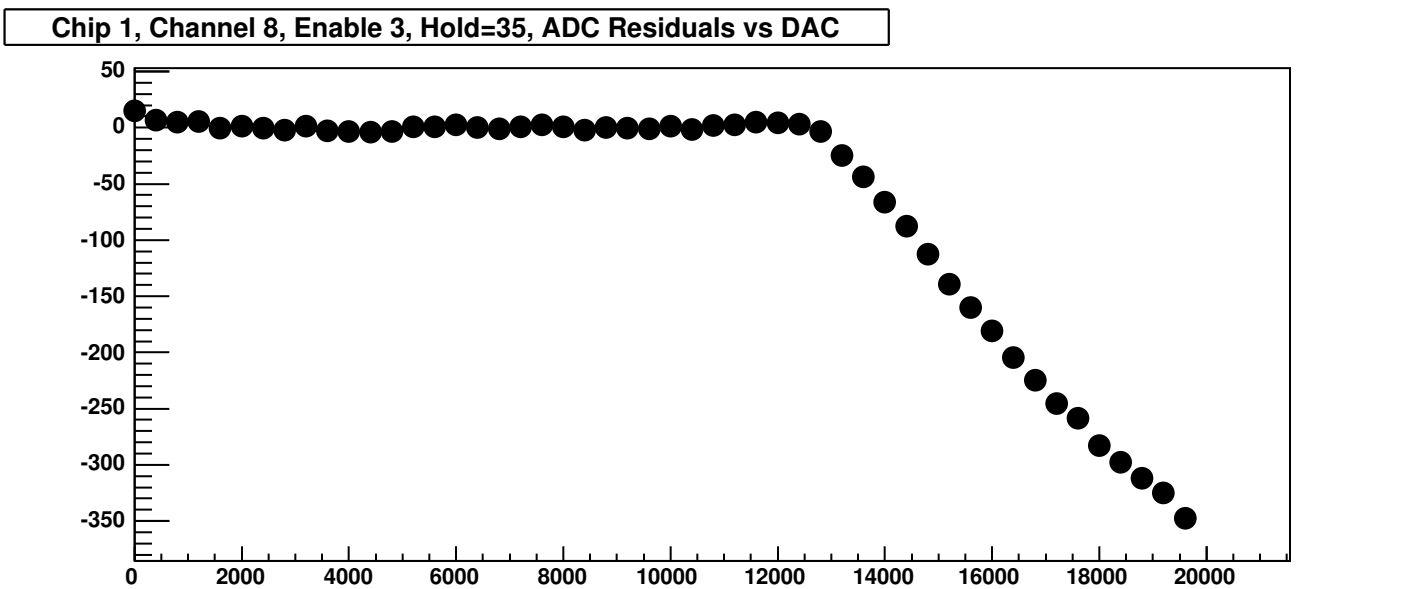

Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 53.96 / 23
p0 474.5 ± 0.6038
p1 0.02207 ± 9.451e-05
900
800
700
600
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

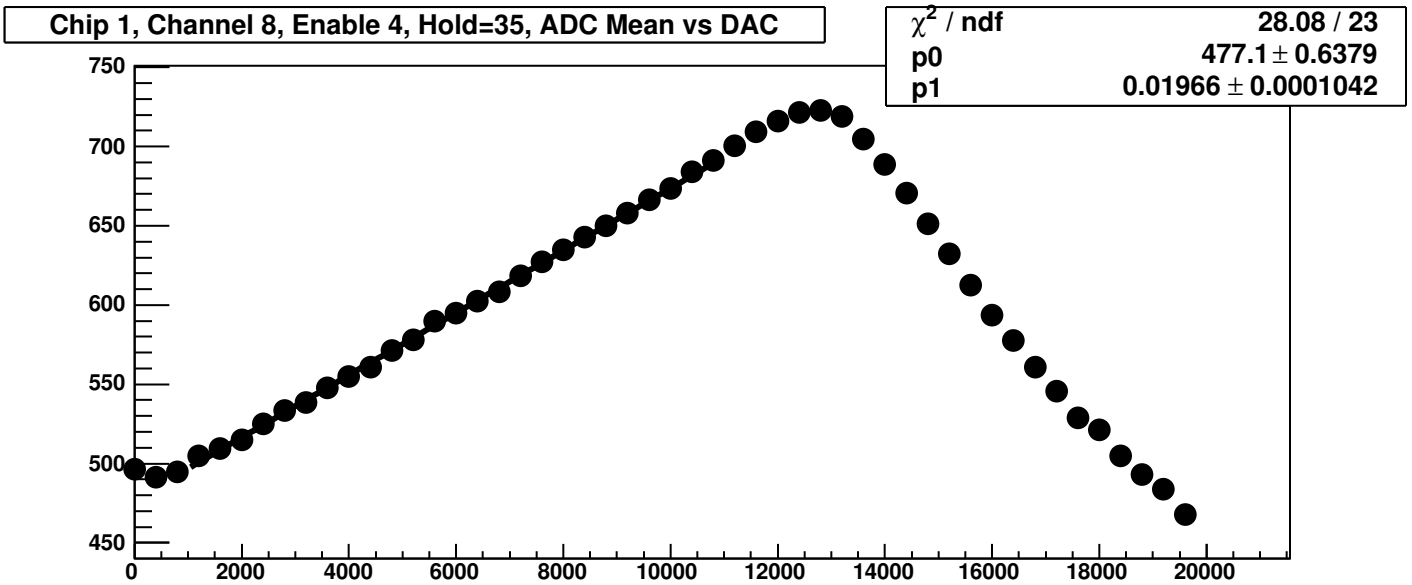

Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

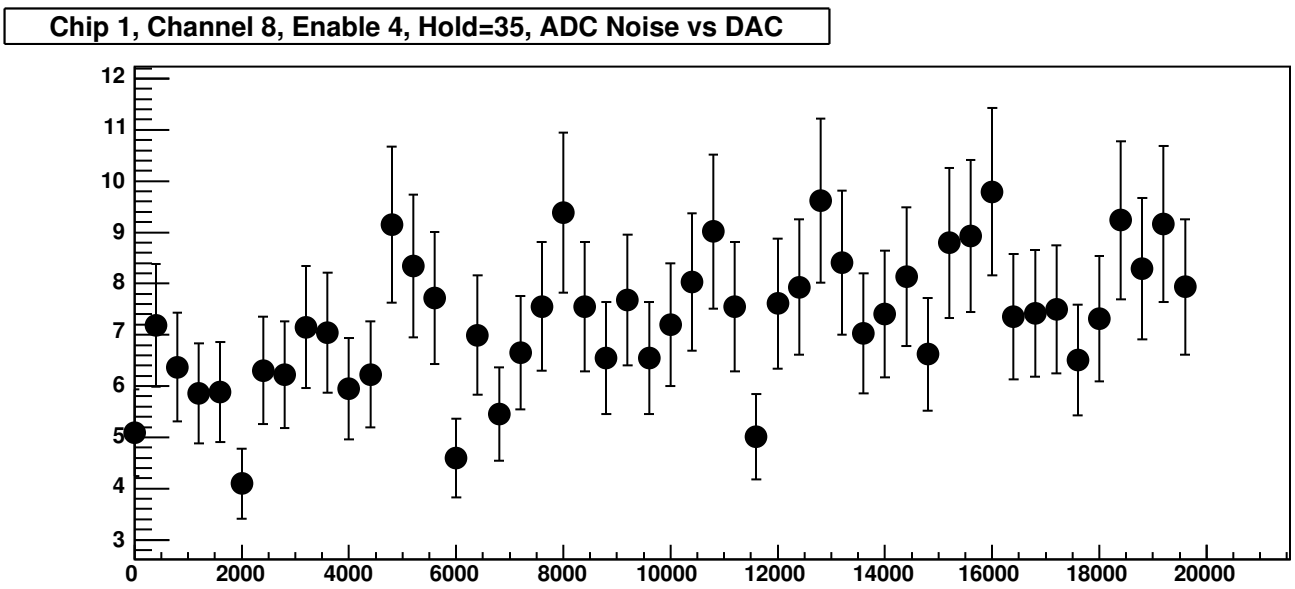

Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

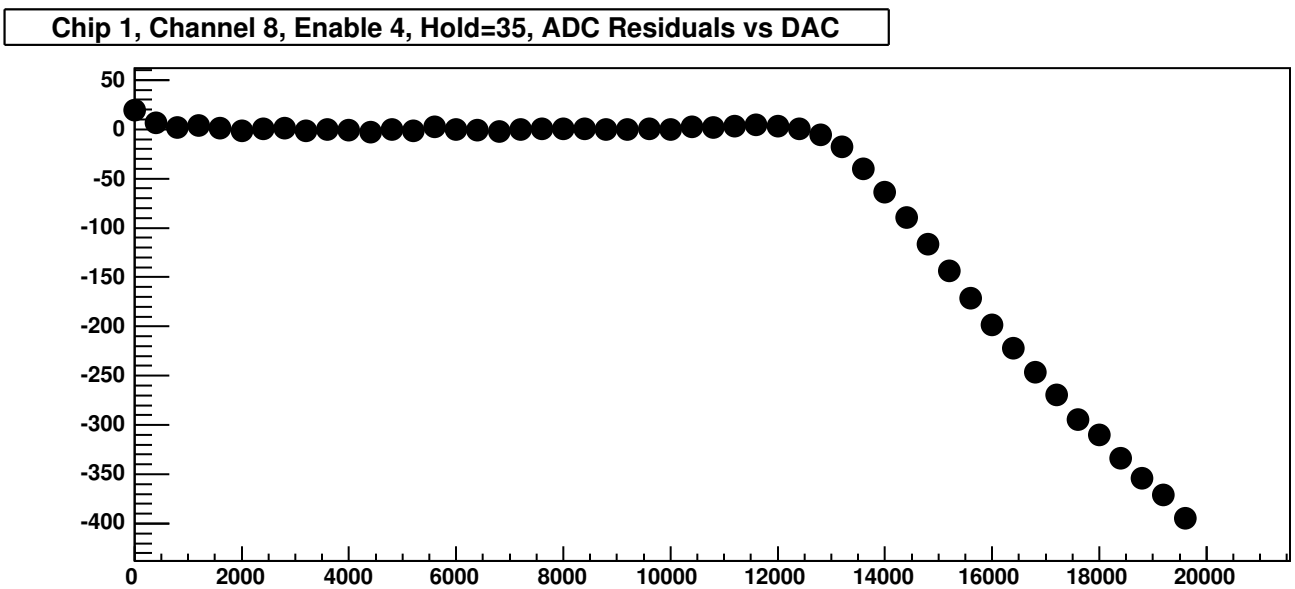

Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 49.78 / 23
p0 473.8 ± 0.7186
p1 0.01869 ± 0.0001082
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

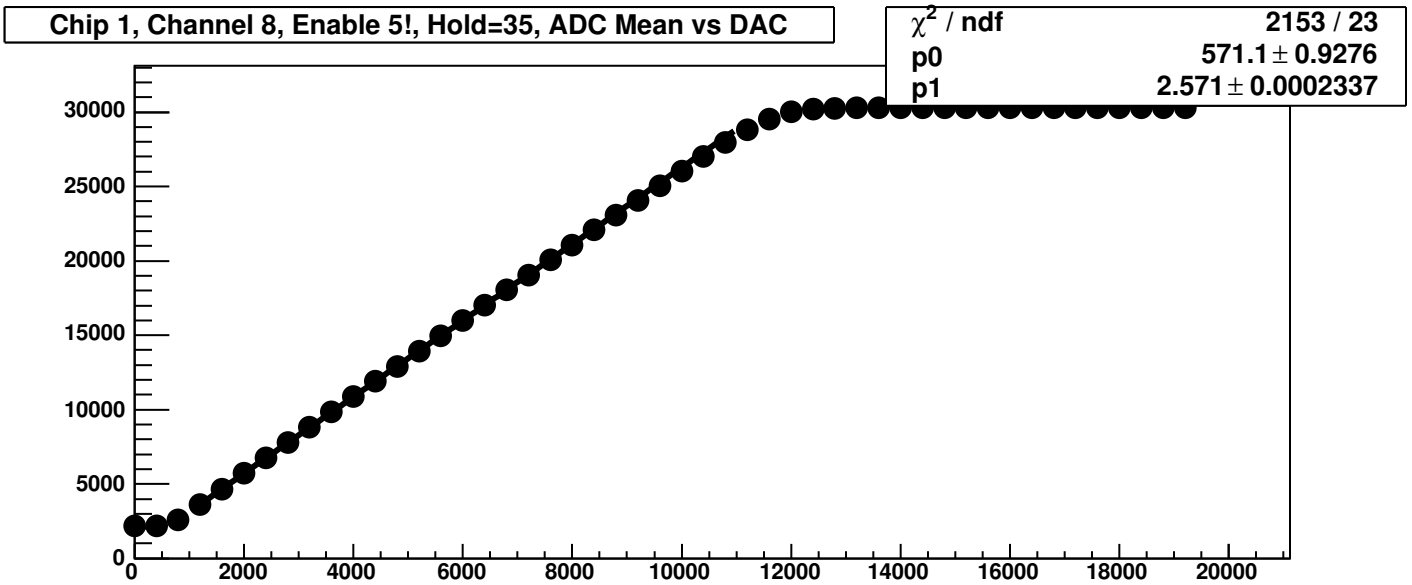

Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

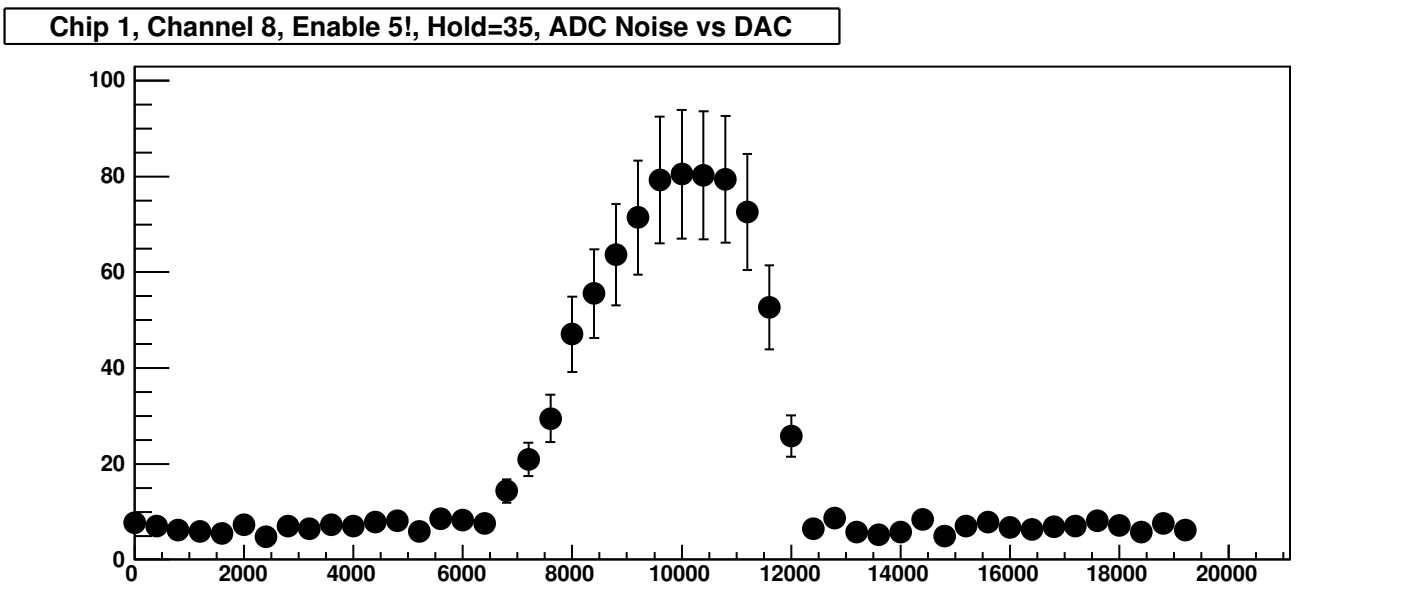

Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

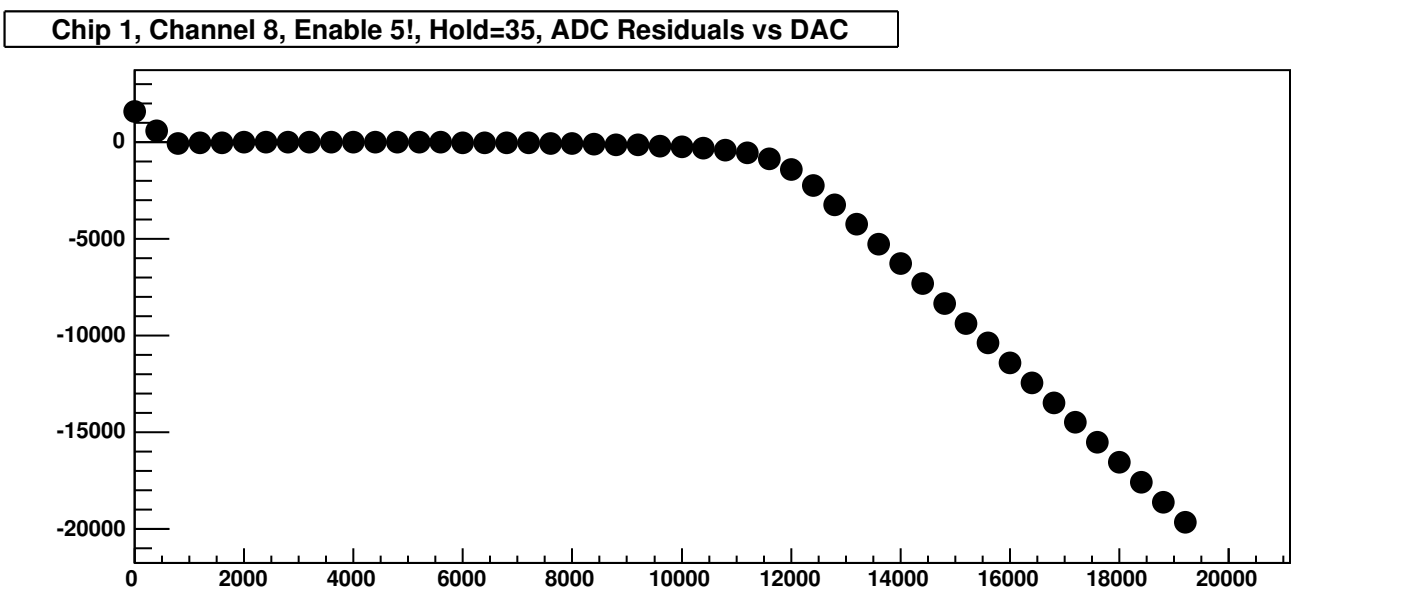

Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.08 / 23
p0 477.1 ± 0.6379
p1 0.01966 ± 0.0001042
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

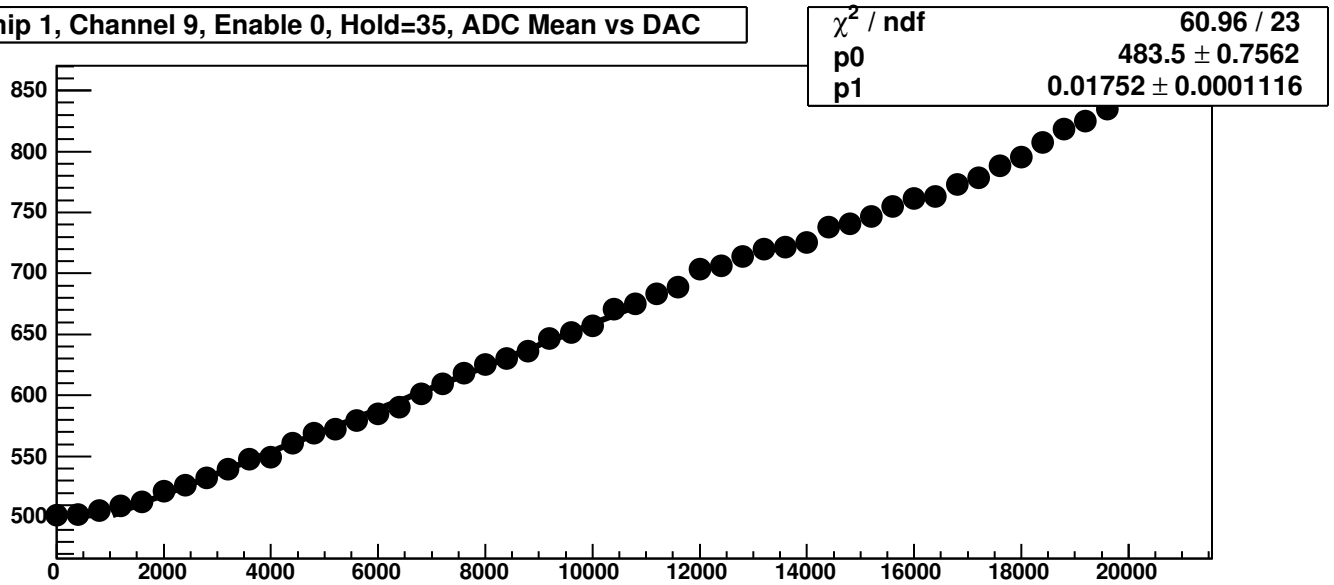

Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2153 / 23
p0 571.1 ± 0.9276
p1 2.571 ± 0.0002337
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

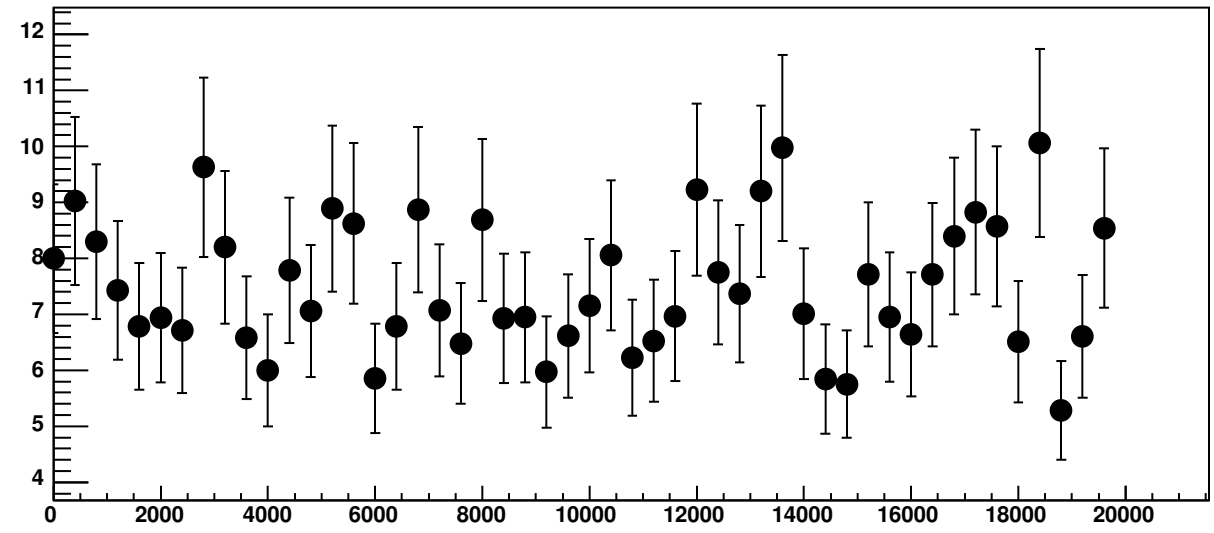

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

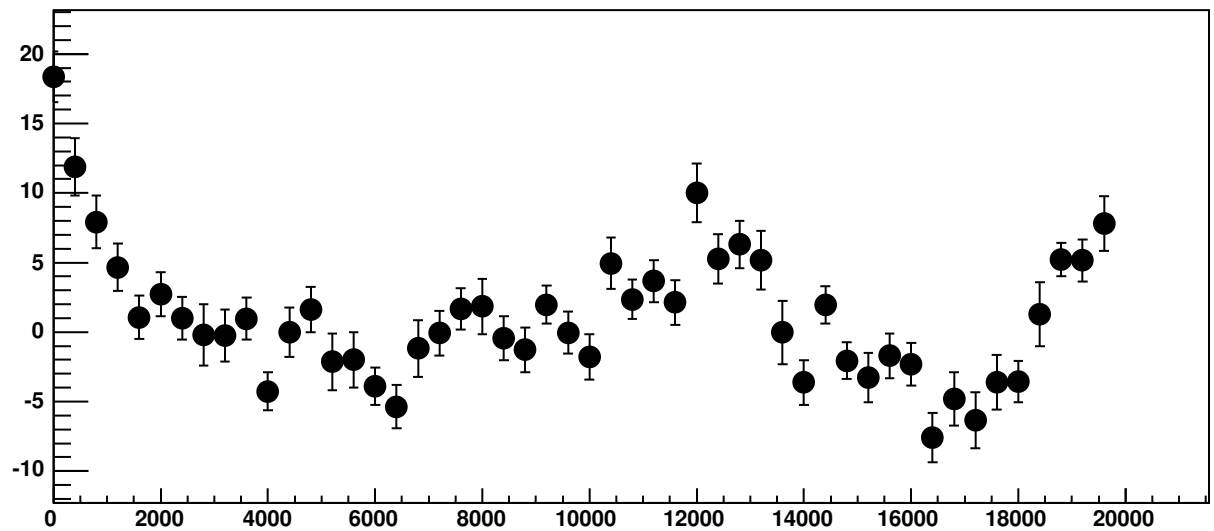
Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



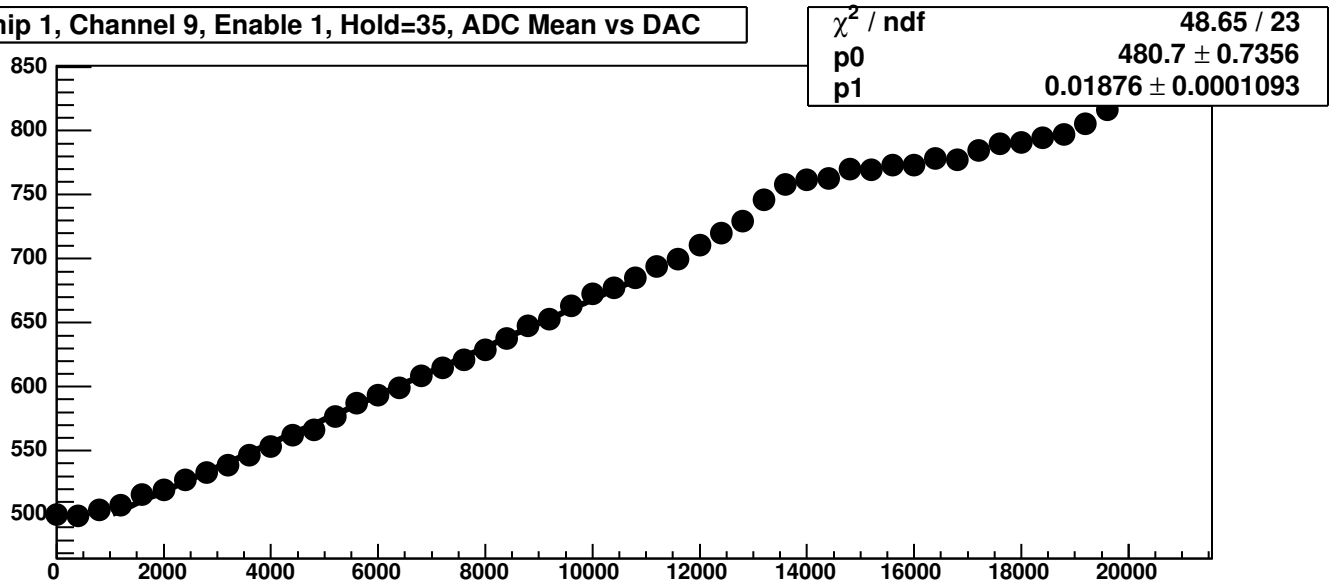
Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



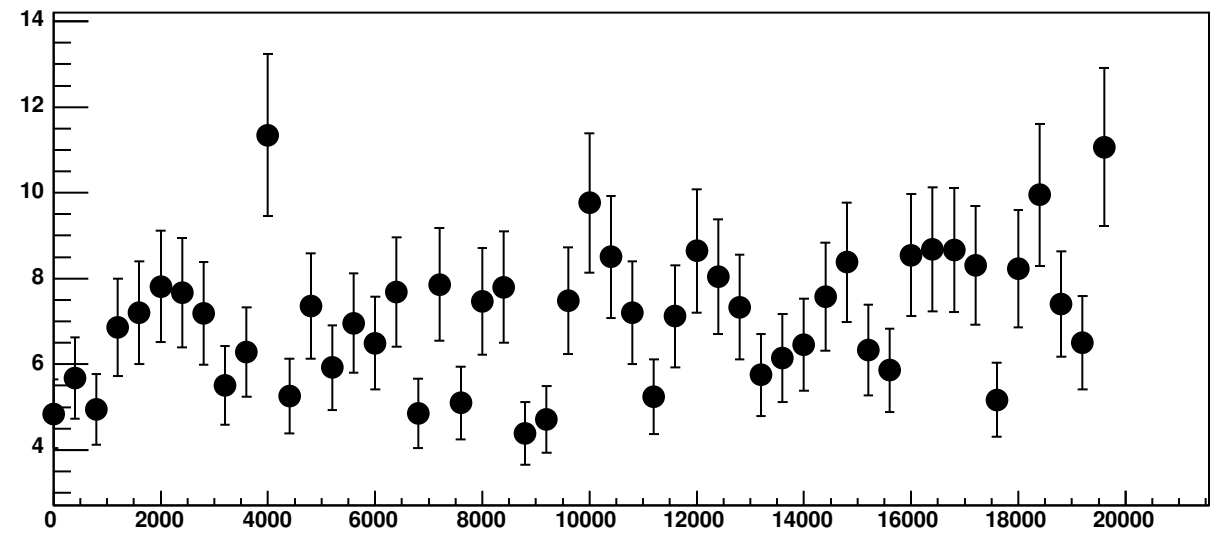
Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC



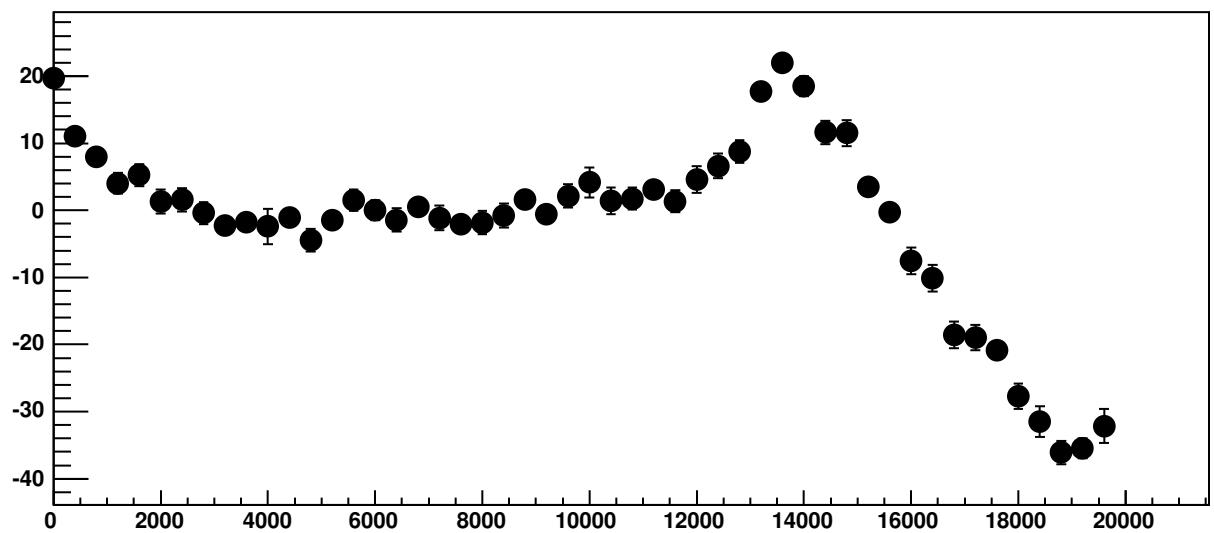
Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

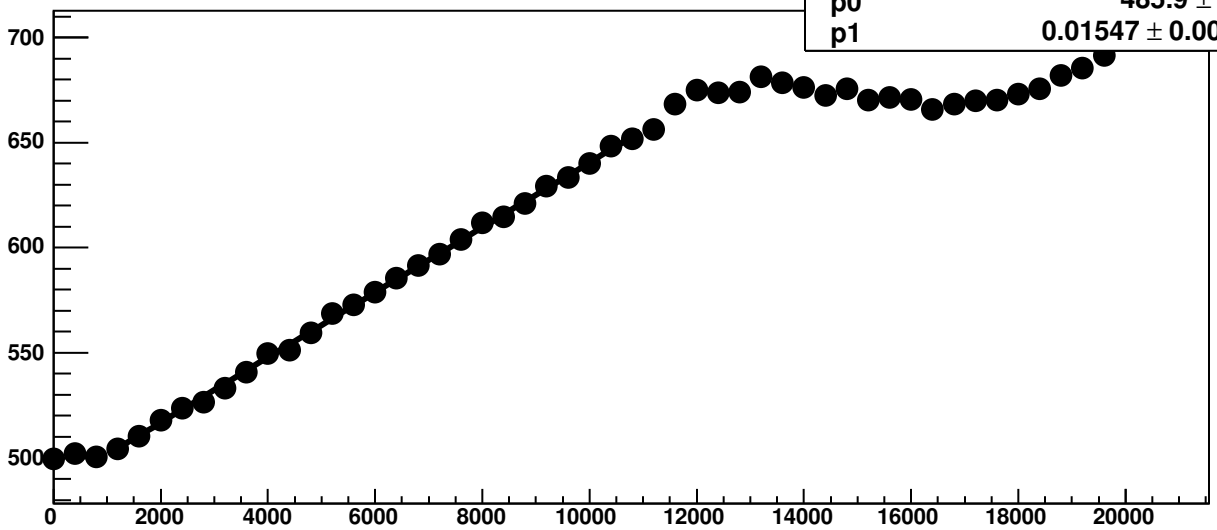


Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

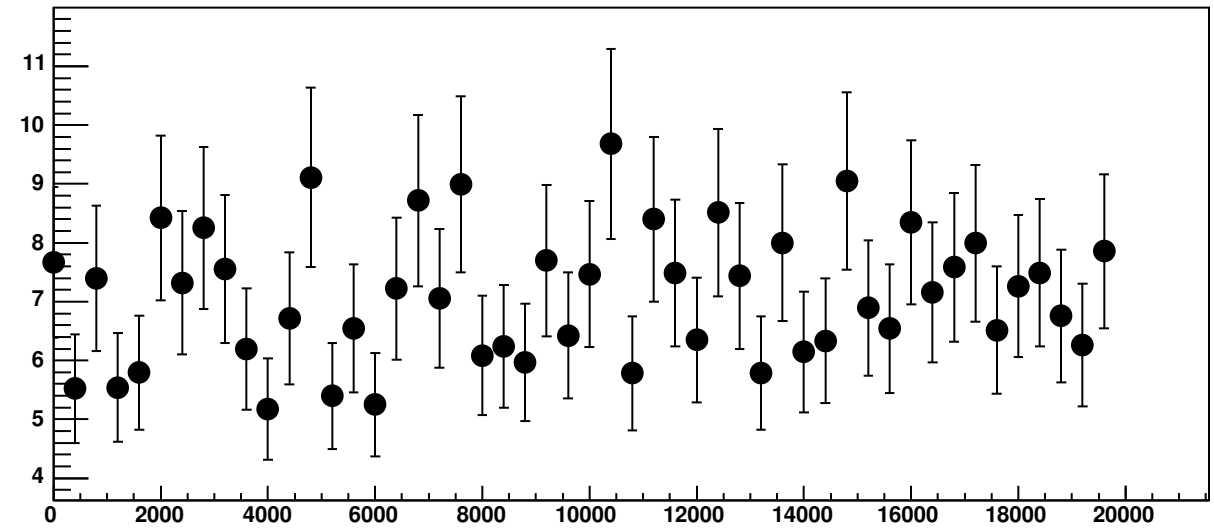


Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

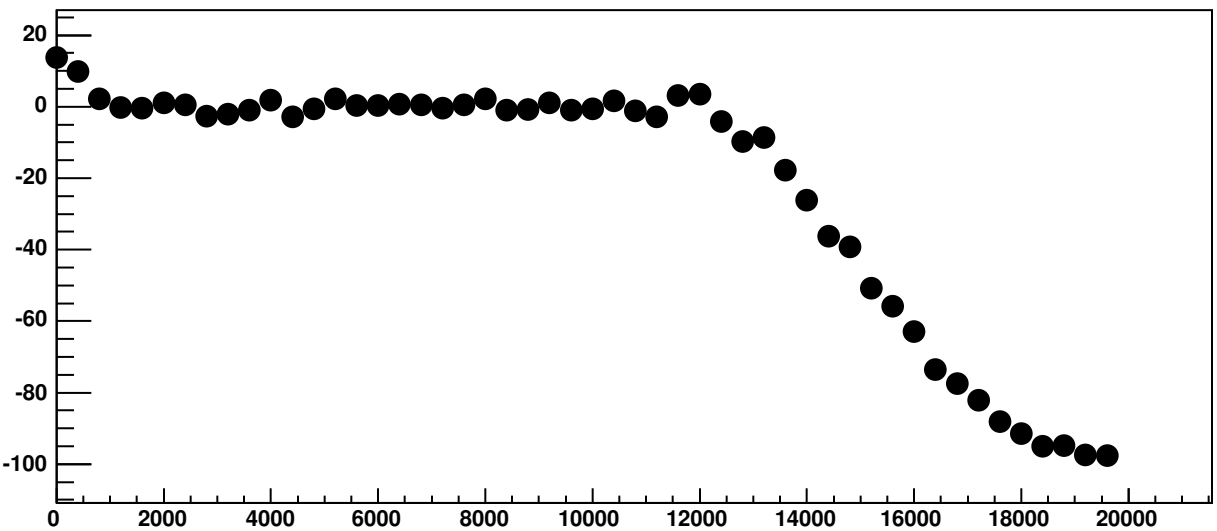
χ^2 / ndf	19.44 / 23
p0	485.9 ± 0.6931
p1	0.01547 ± 0.0001059

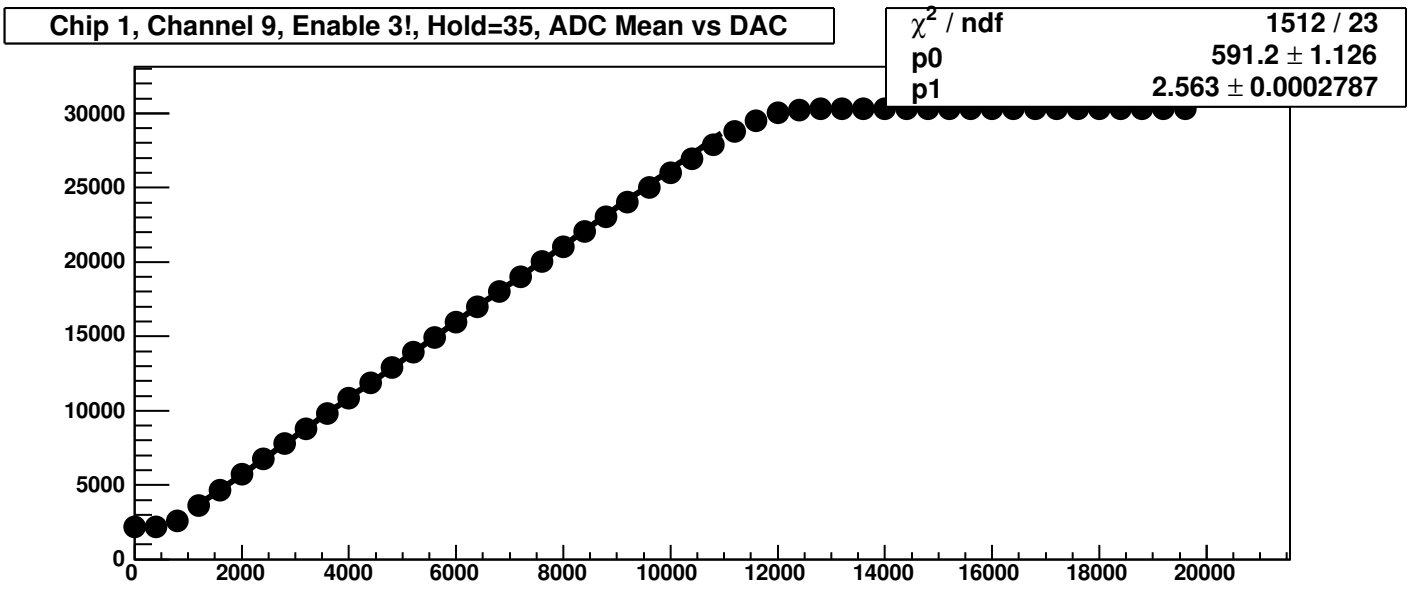


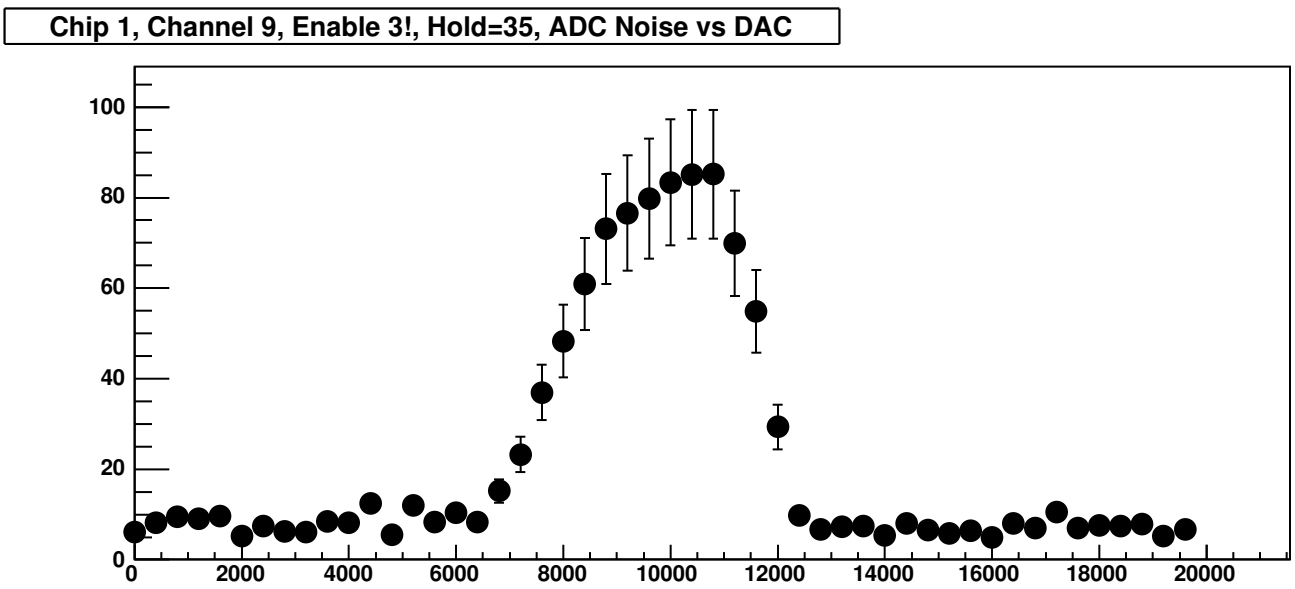
Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

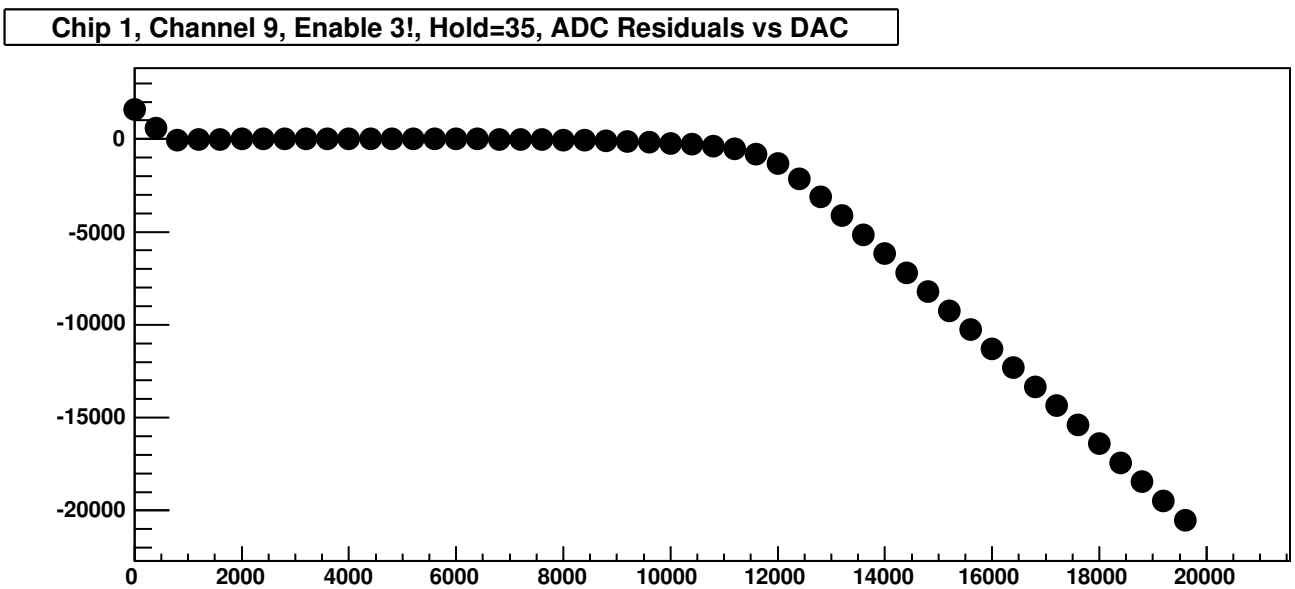


Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

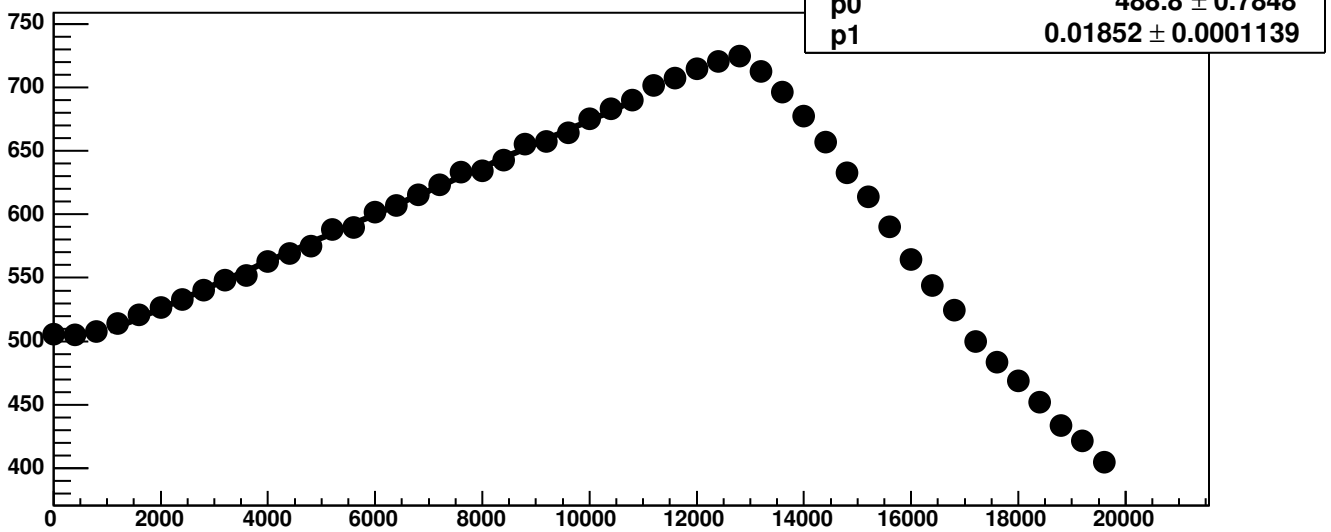



Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1512 / 23
p0 591.2 ± 1.126
p1 2.563 ± 0.0002787
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

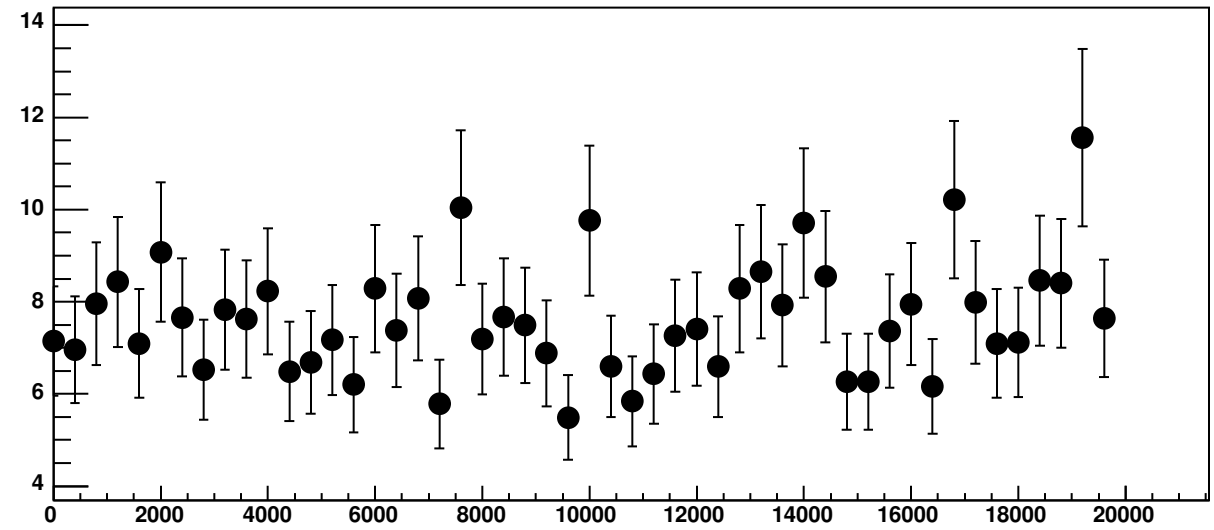

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

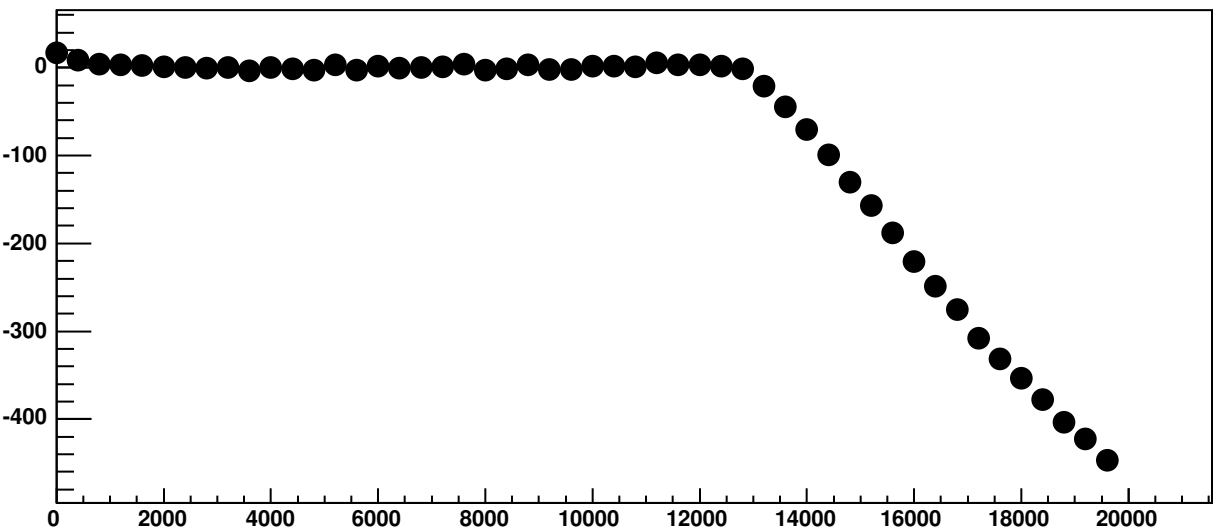
Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

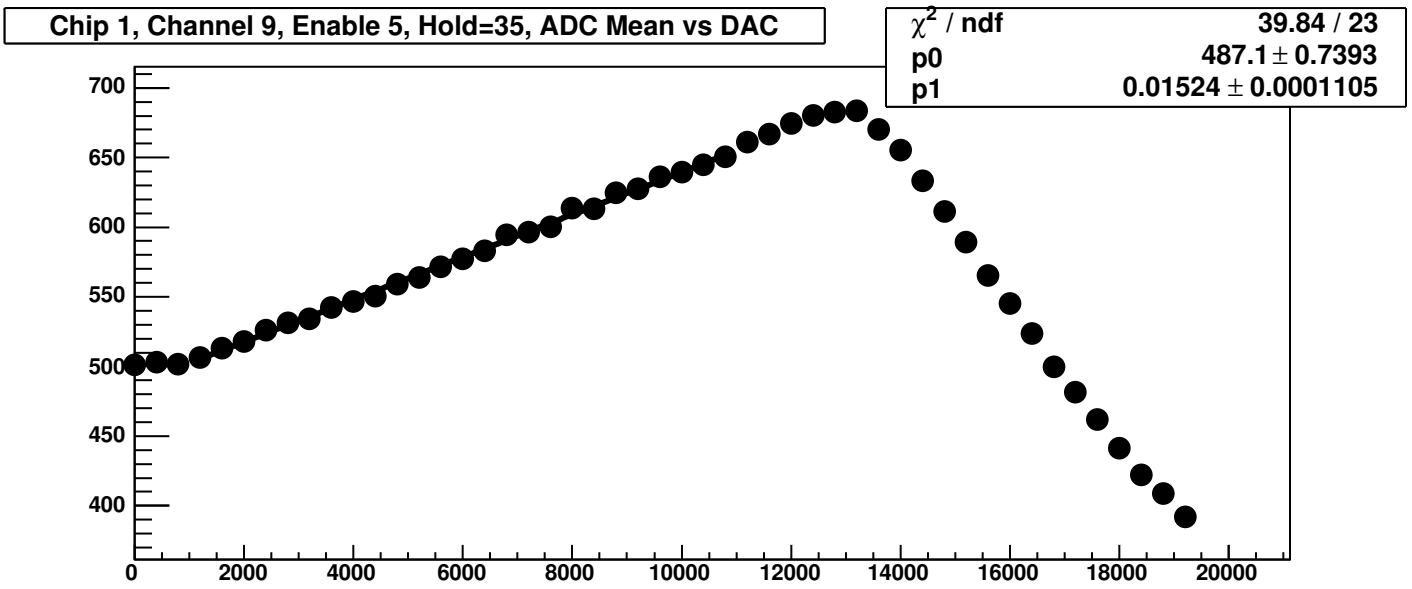


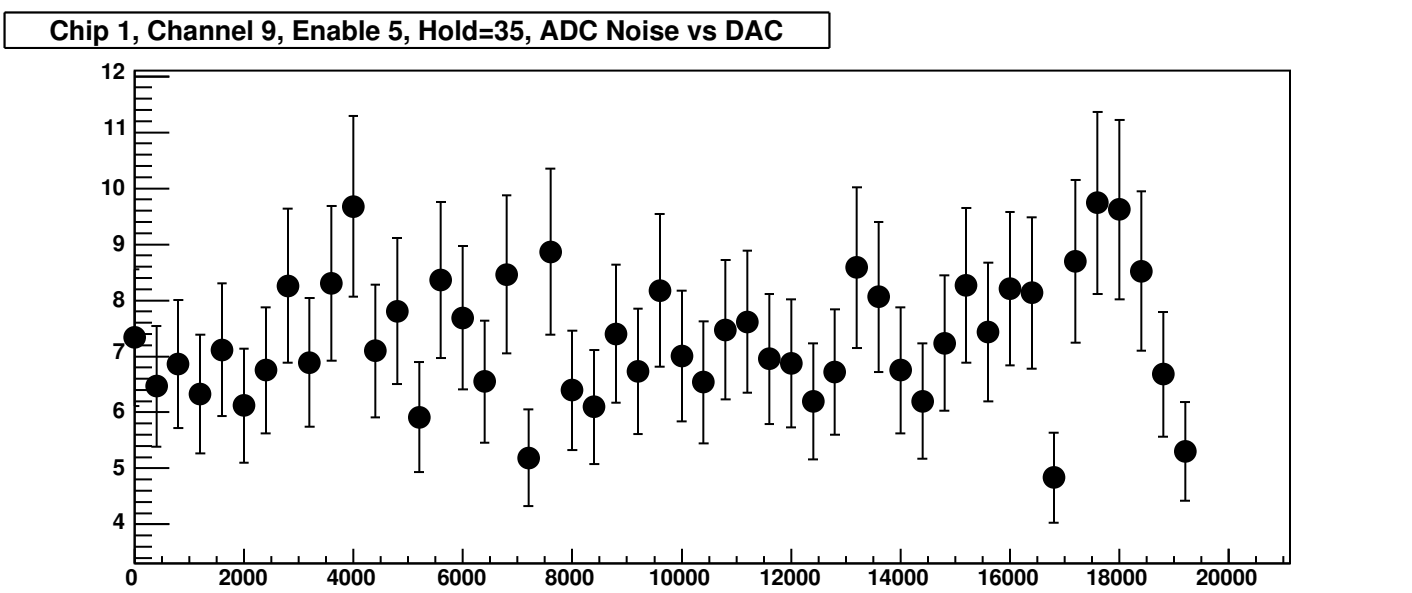
Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

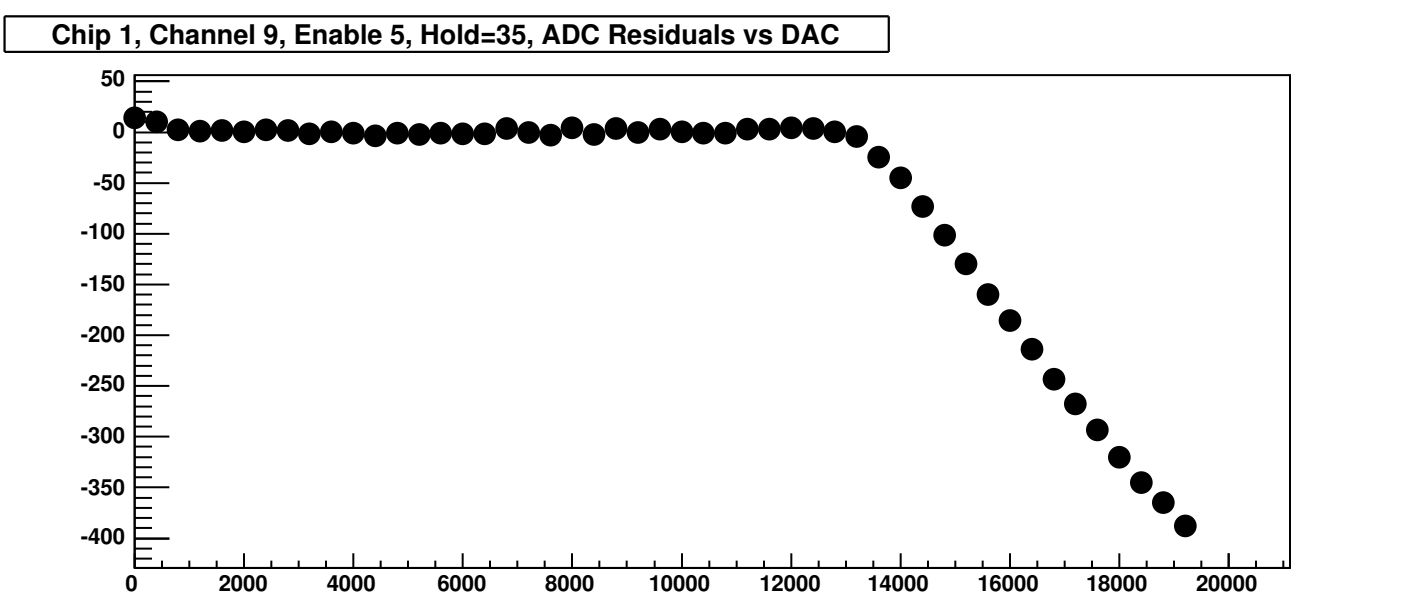


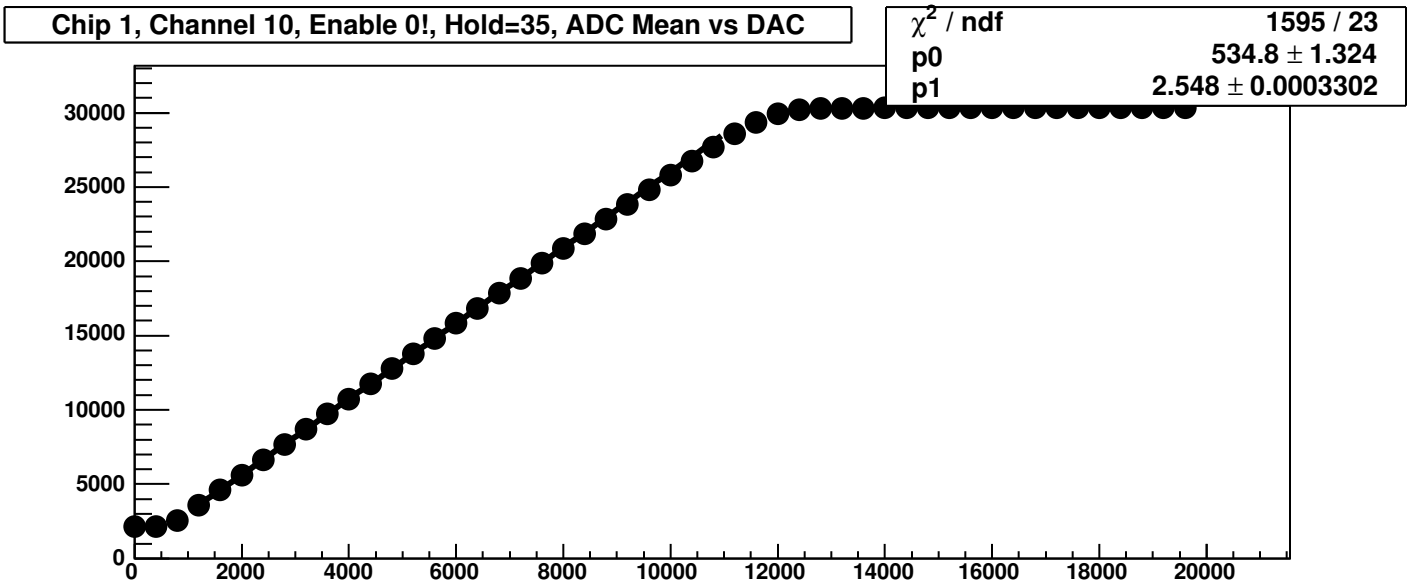
Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

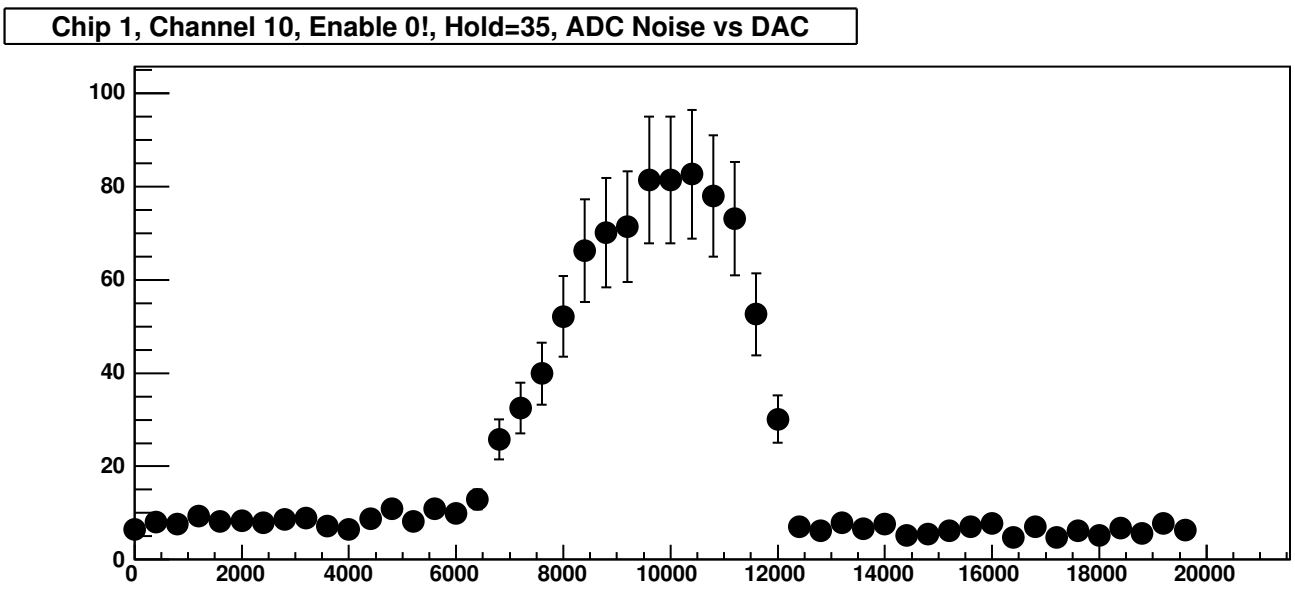


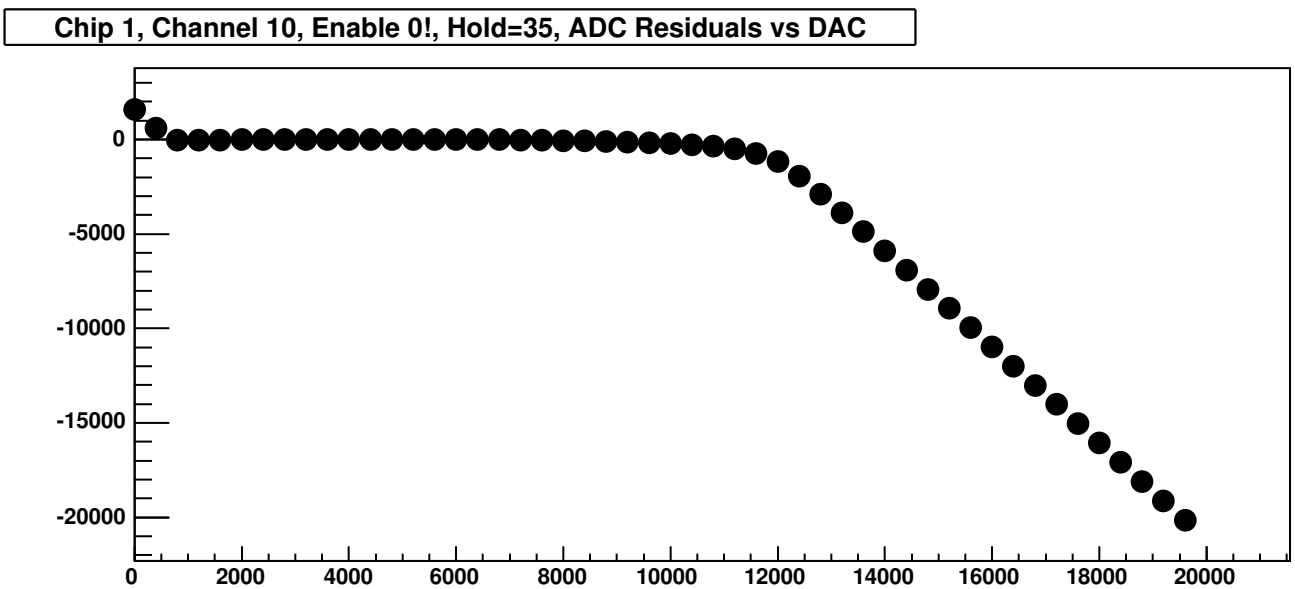

Chip 1, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 39.84 / 23
p0 487.1 ± 0.7393
p1 0.01524 ± 0.0001105
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

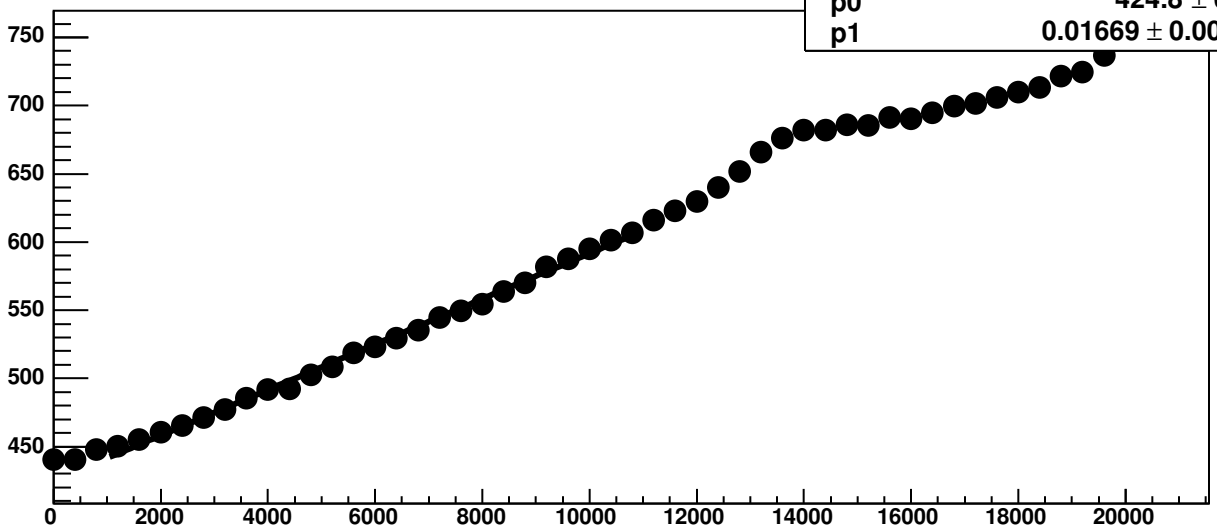

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1595 / 23
p0 534.8 ± 1.324
p1 2.548 ± 0.0003302
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

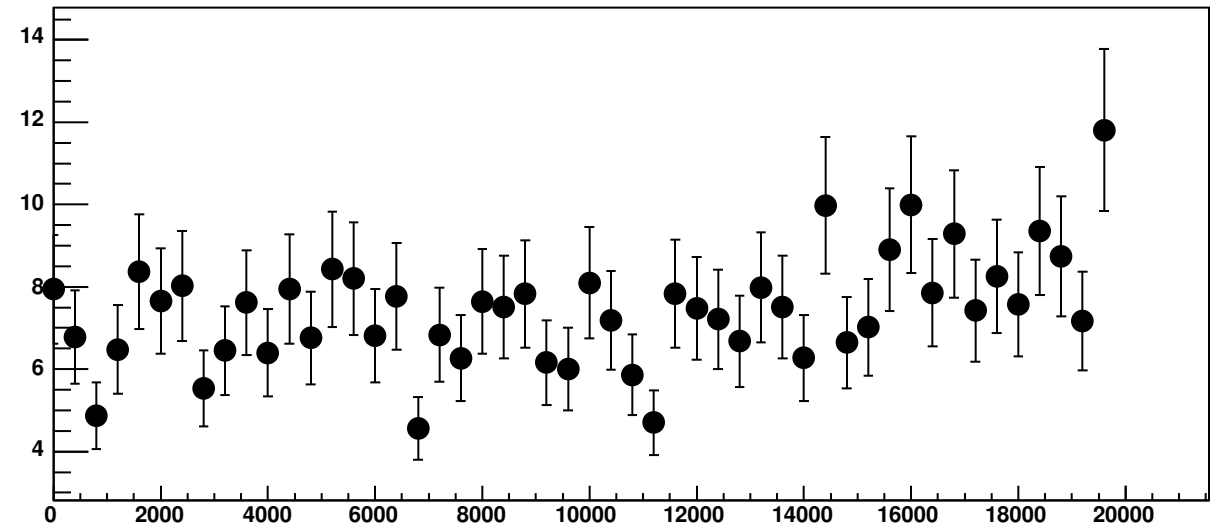

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

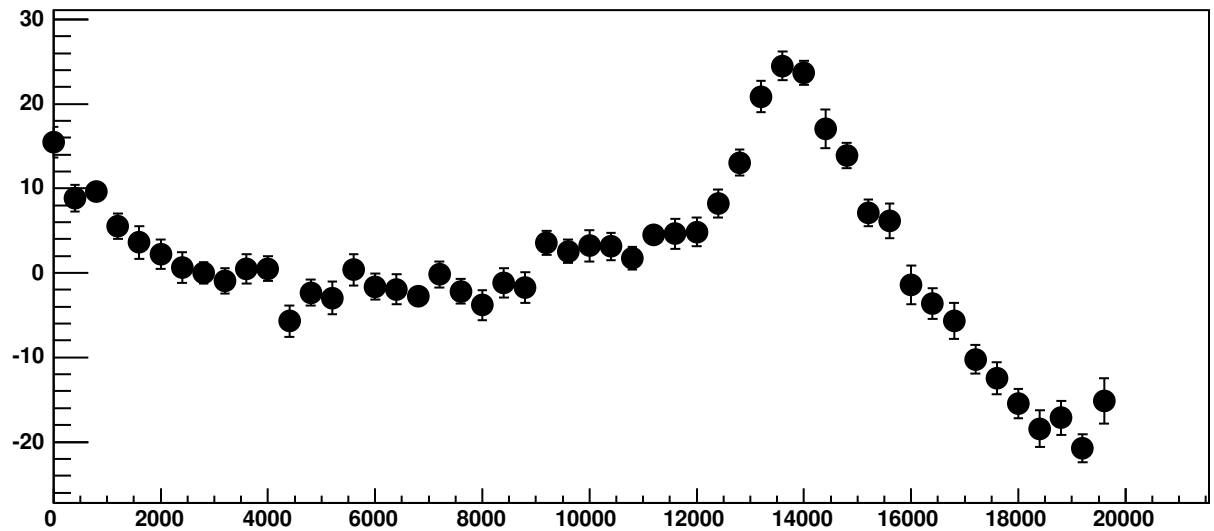
χ^2 / ndf	69.73 / 23
p0	424.8 ± 0.7377
p1	0.01669 ± 0.0001089

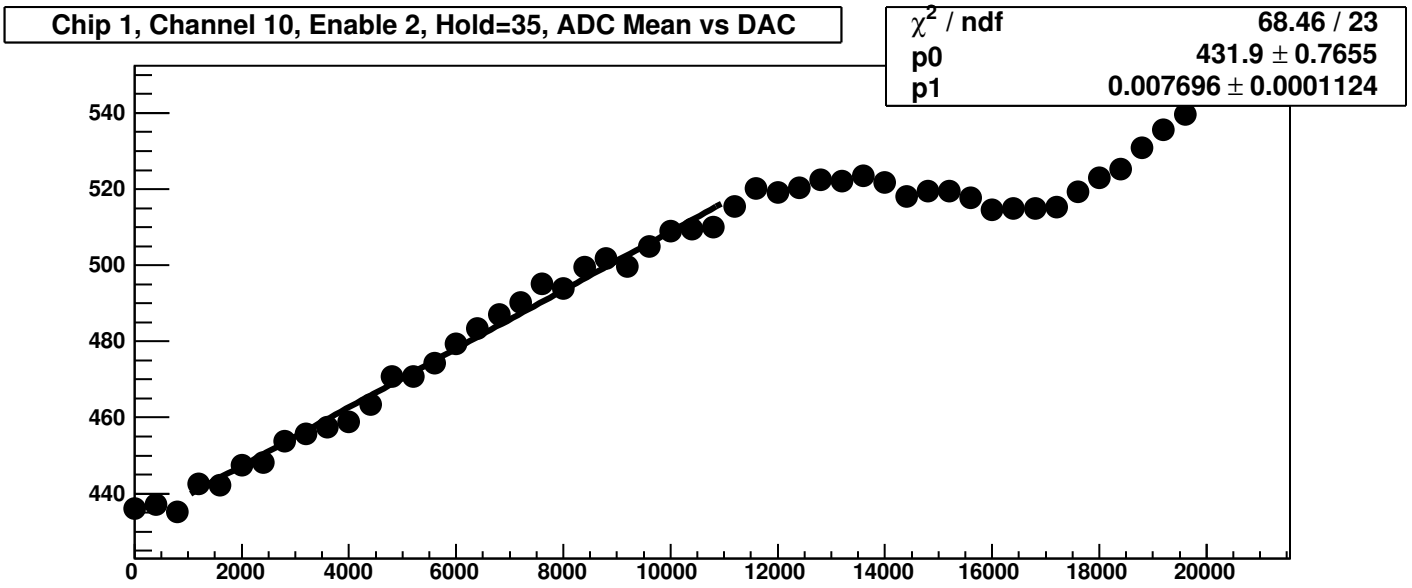


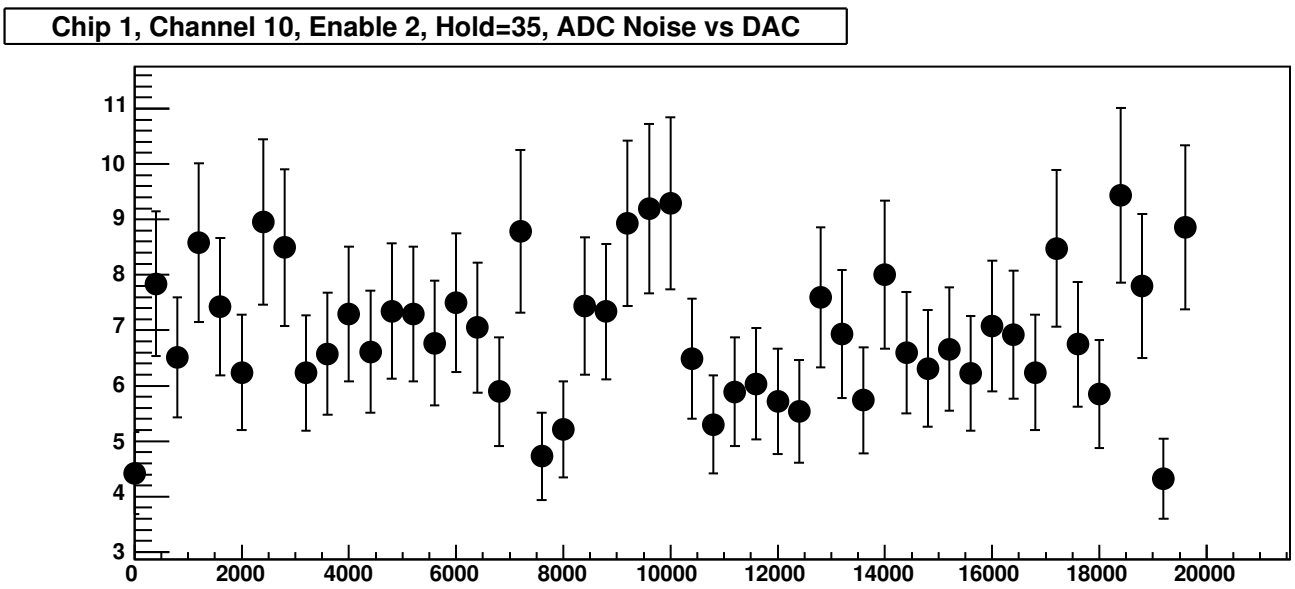
Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC

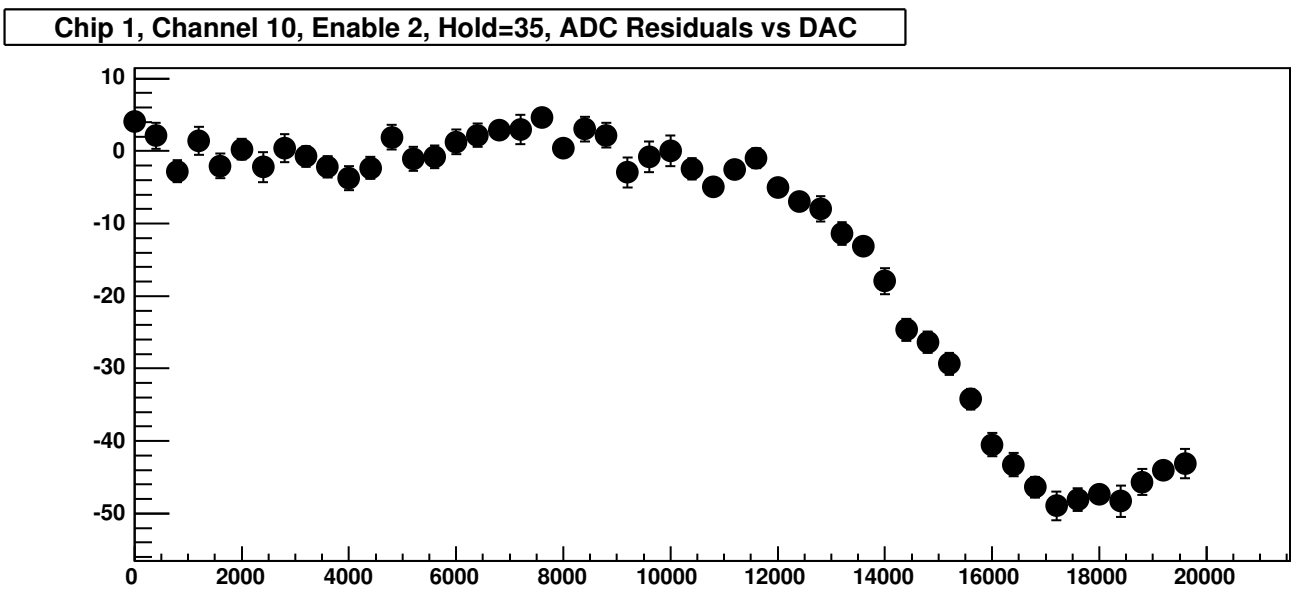


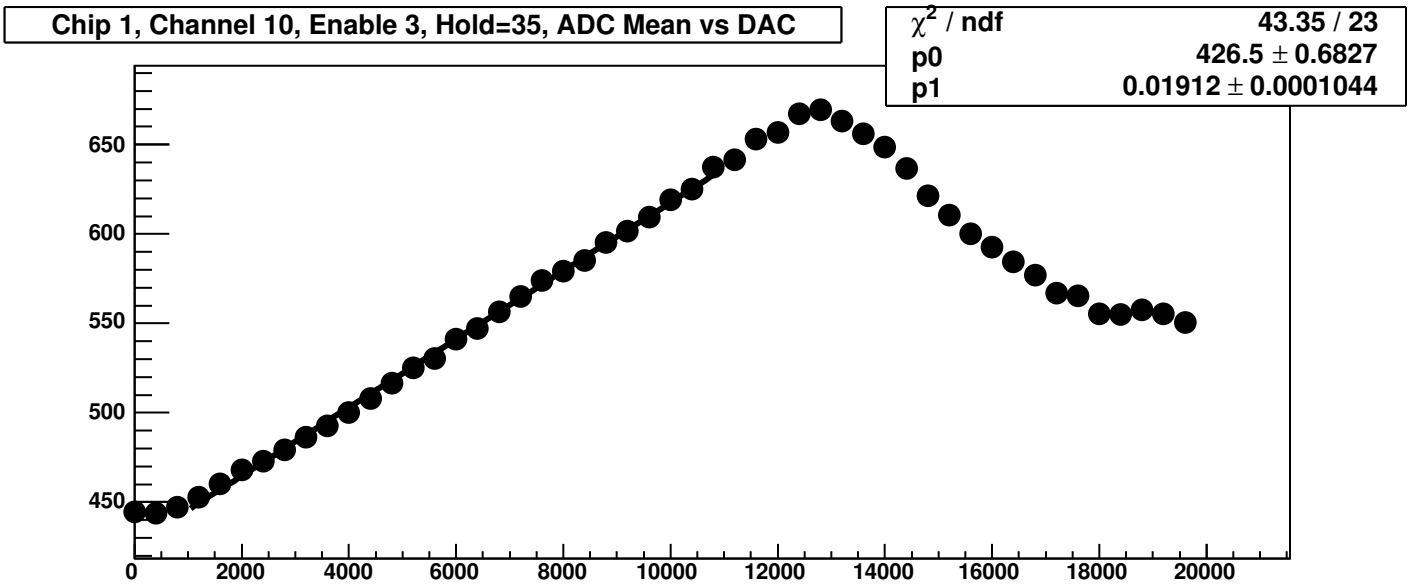
Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

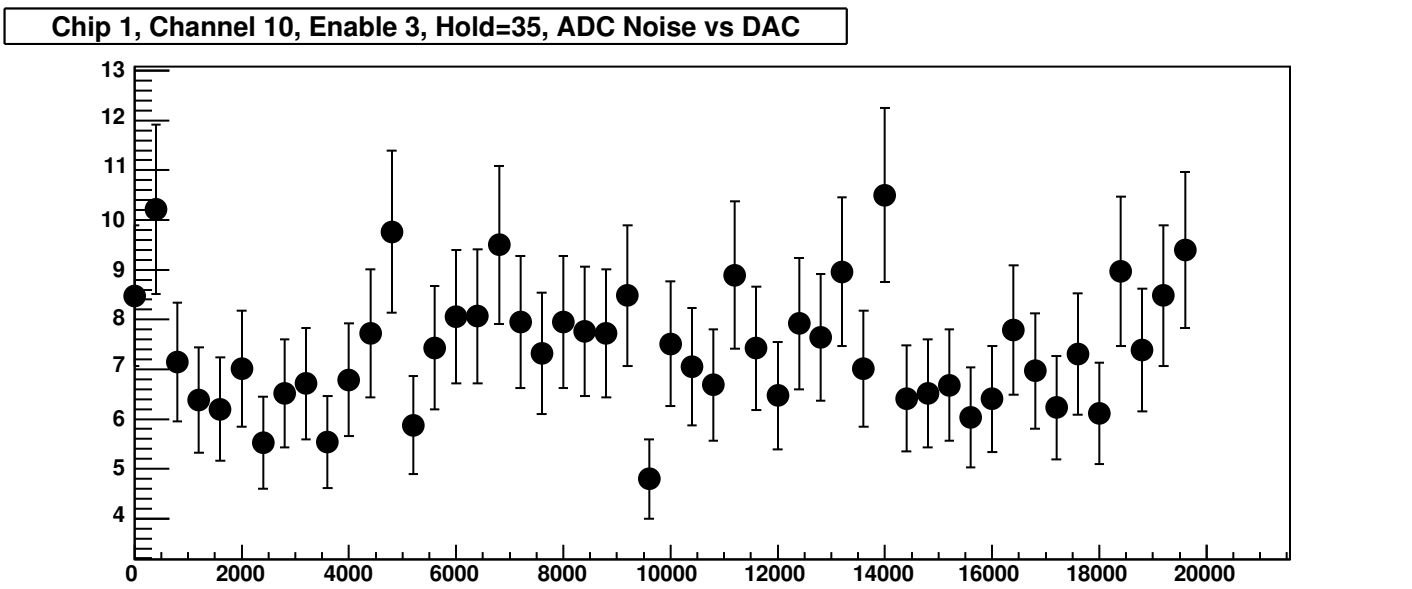


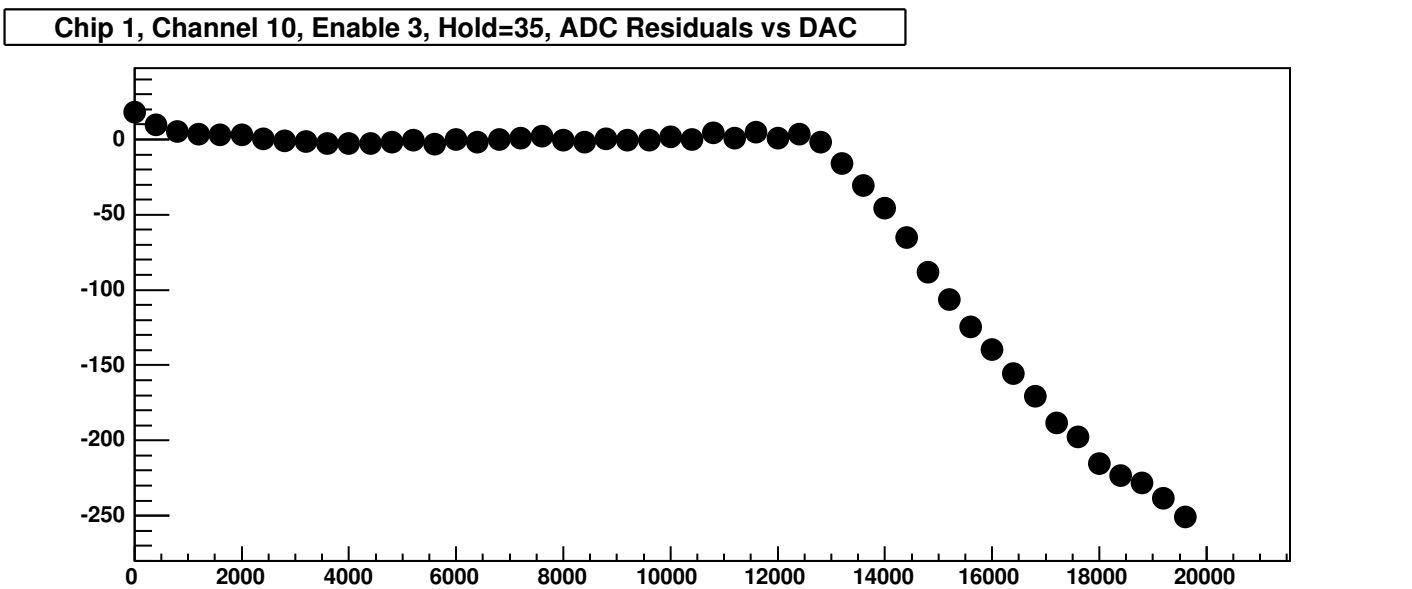

Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 68.46 / 23
p0 431.9 ± 0.7655
p1 0.007696 ± 0.0001124
540
520
500
480
460
440
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

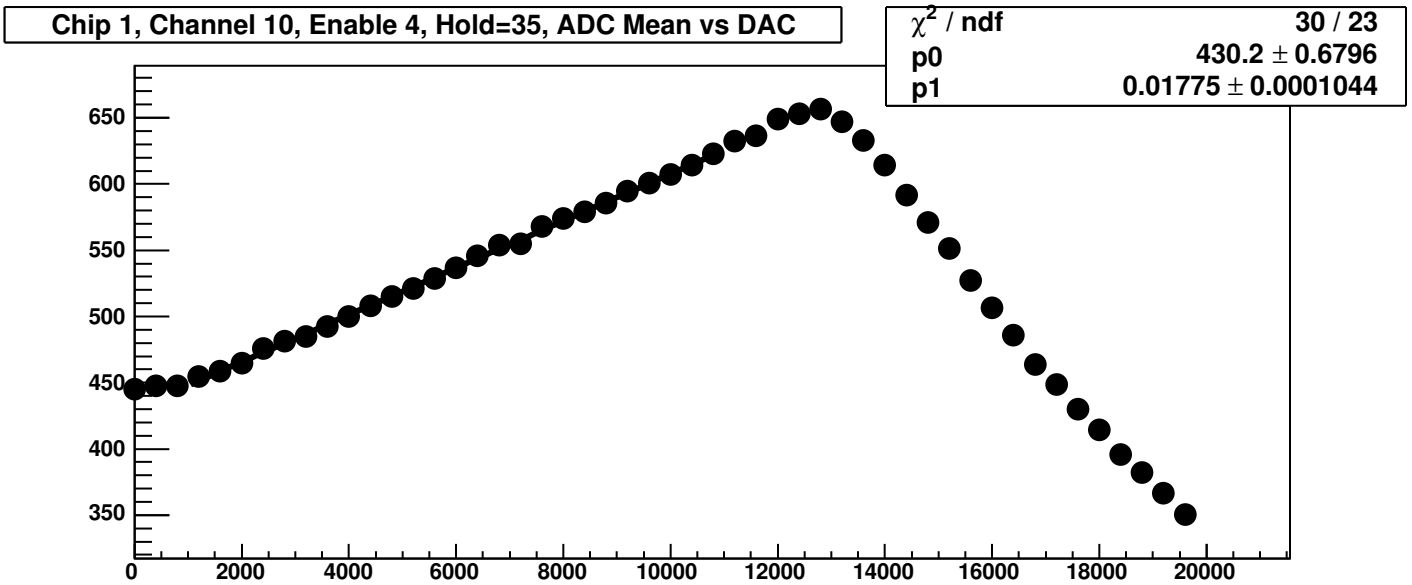

Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

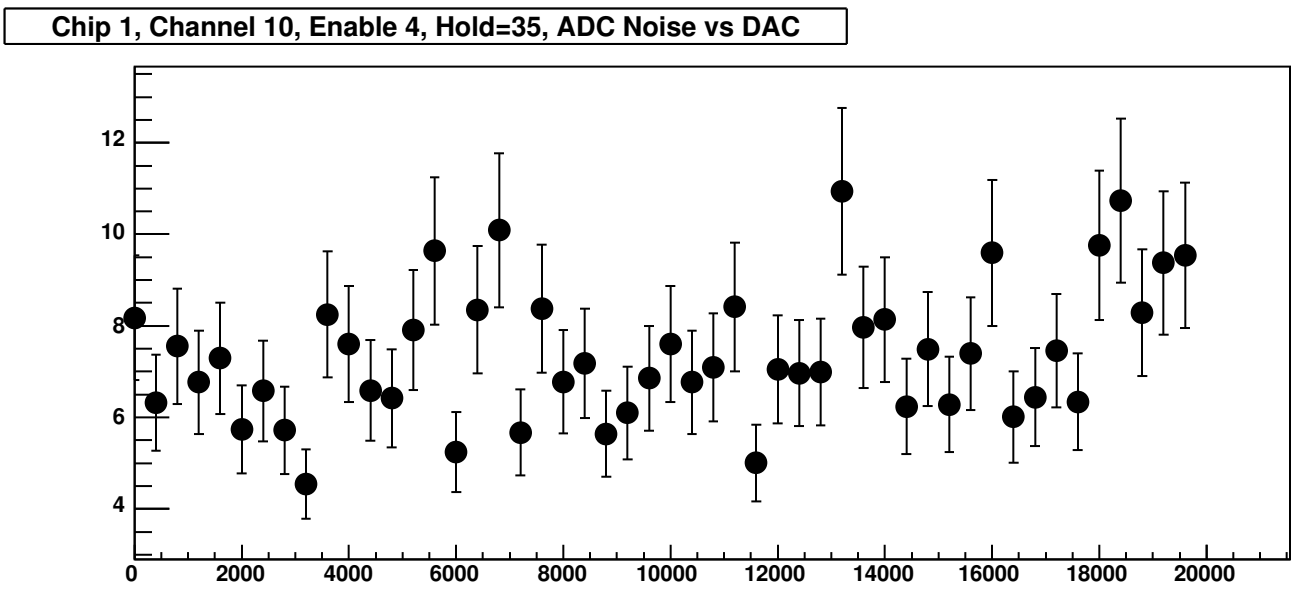

Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

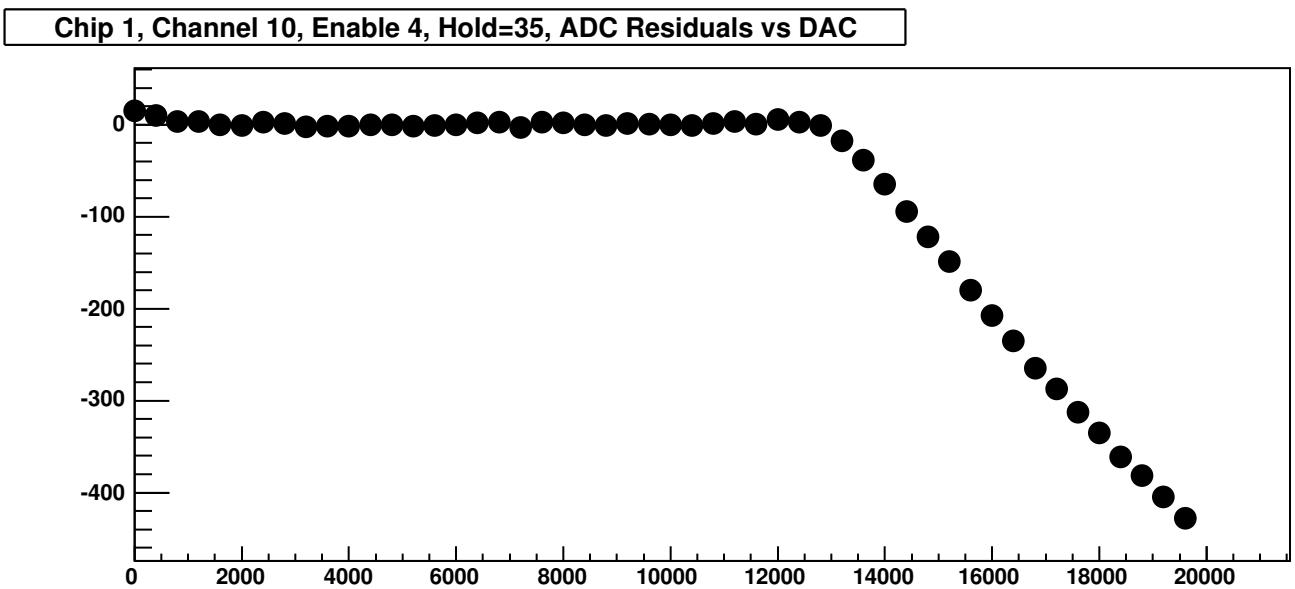

Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 43.35 / 23
p0 426.5 ± 0.6827
p1 0.01912 ± 0.0001044
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

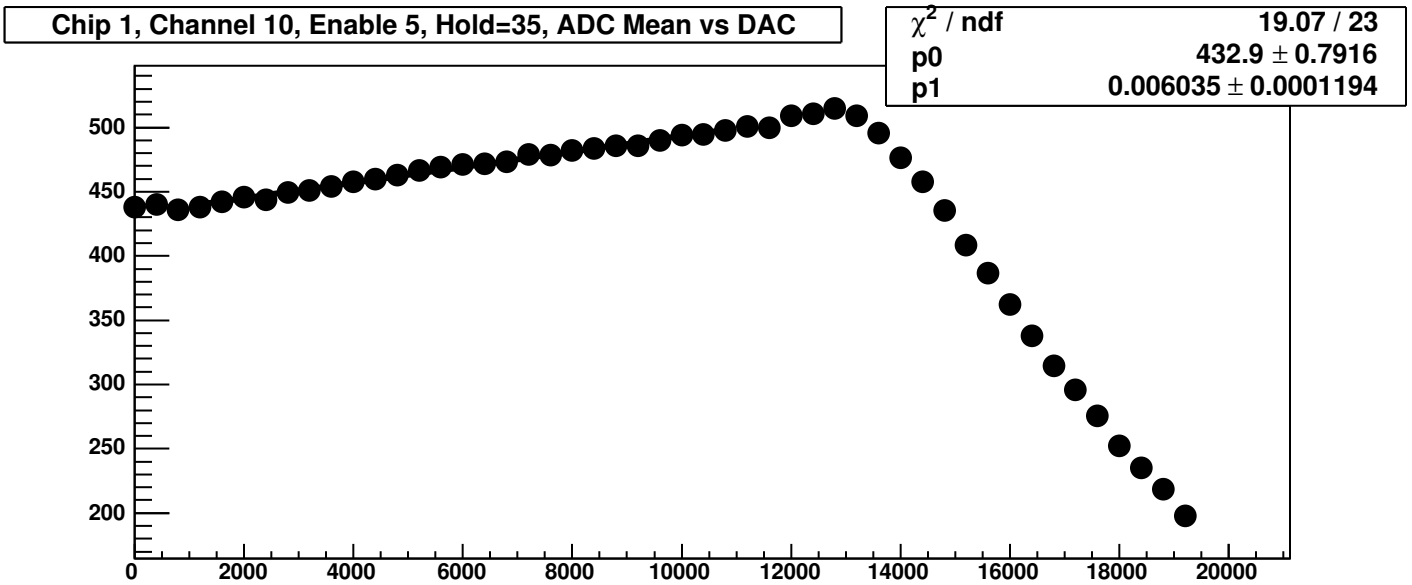

Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

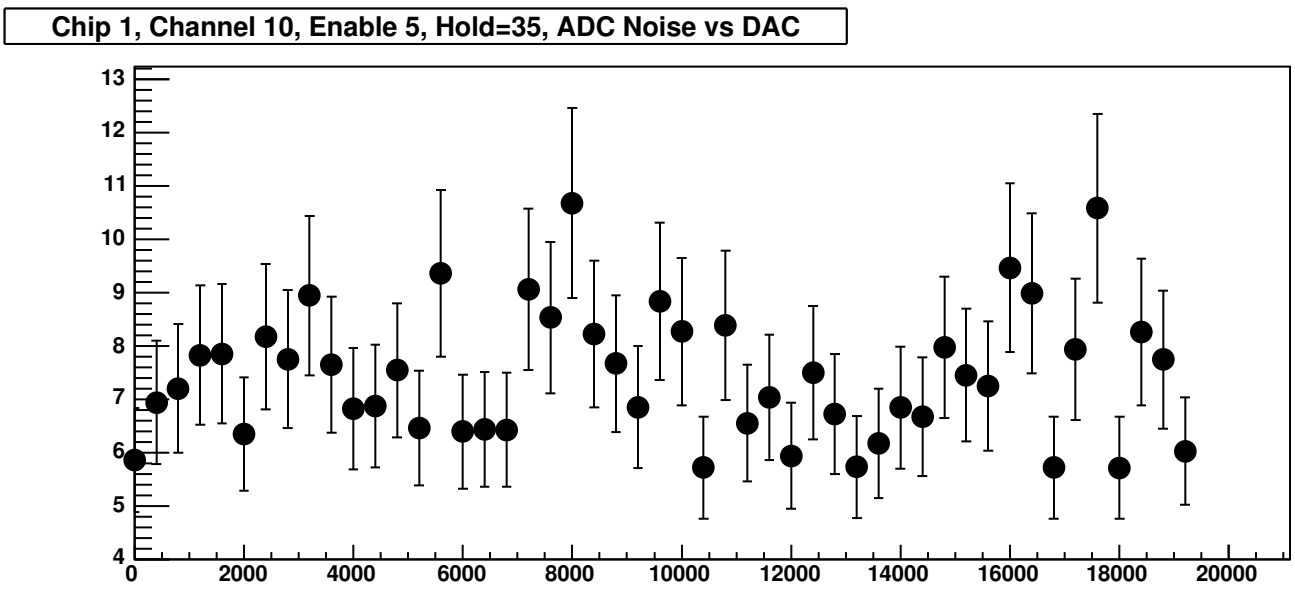

Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

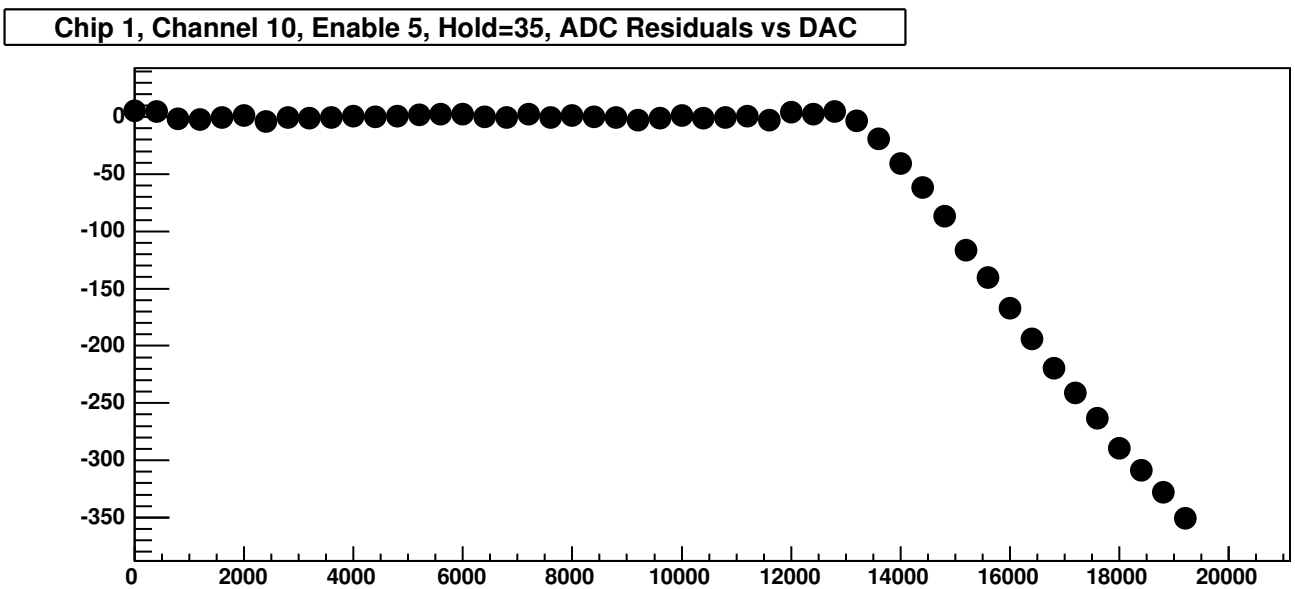

Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30 / 23
p0 430.2 ± 0.6796
p1 0.01775 ± 0.0001044
650
600
550
500
450
400
350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

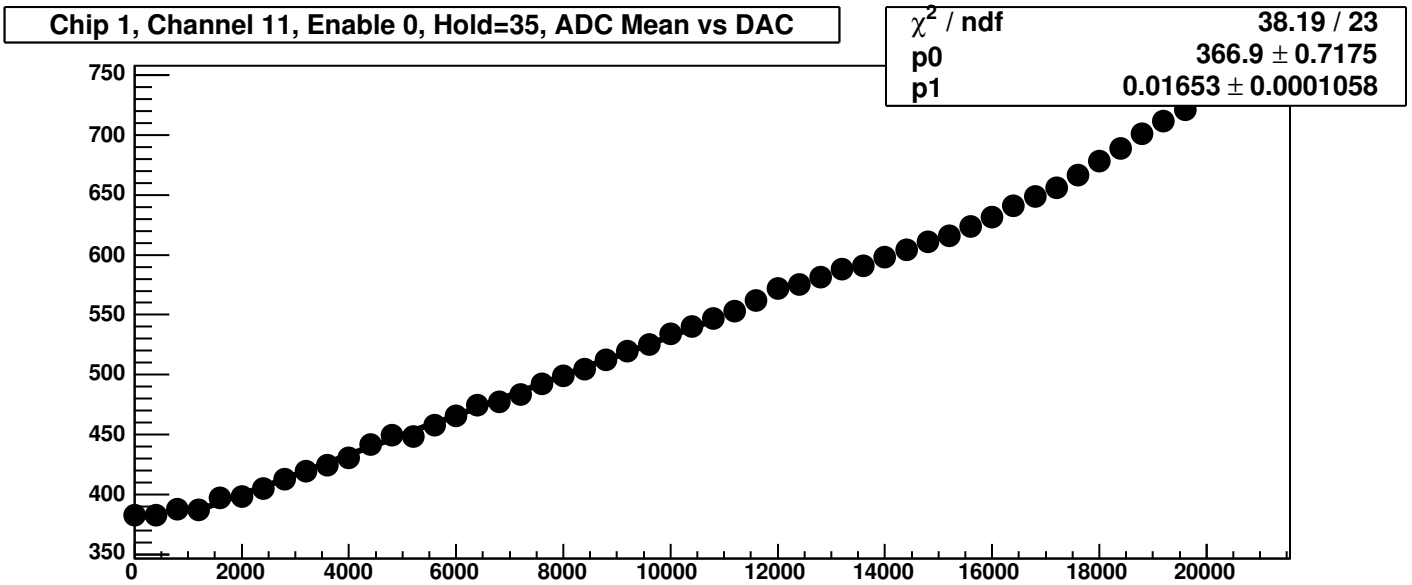

Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

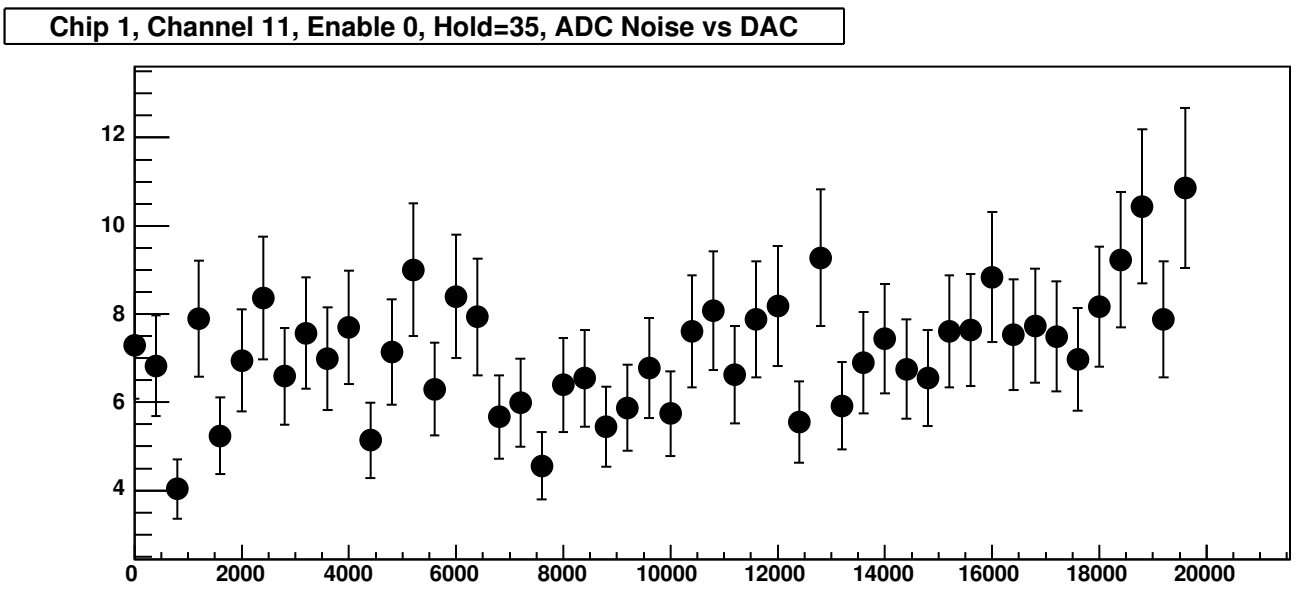

Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

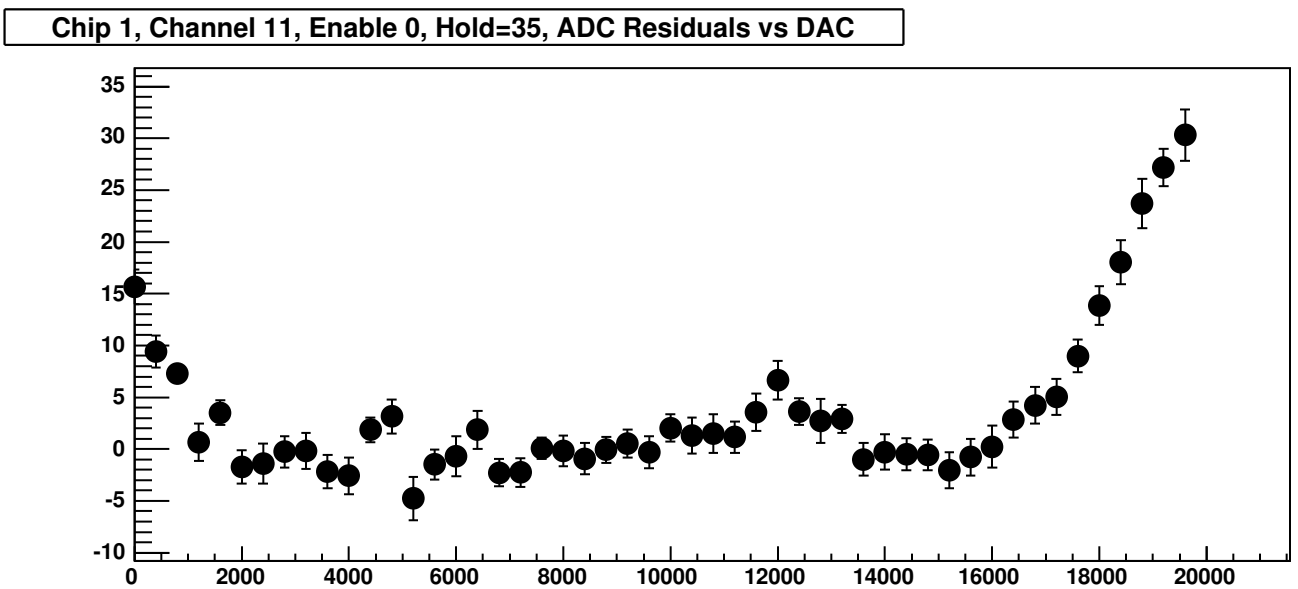

Chip 1, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 19.07 / 23
p0 432.9 ± 0.7916
p1 0.006035 ± 0.0001194
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

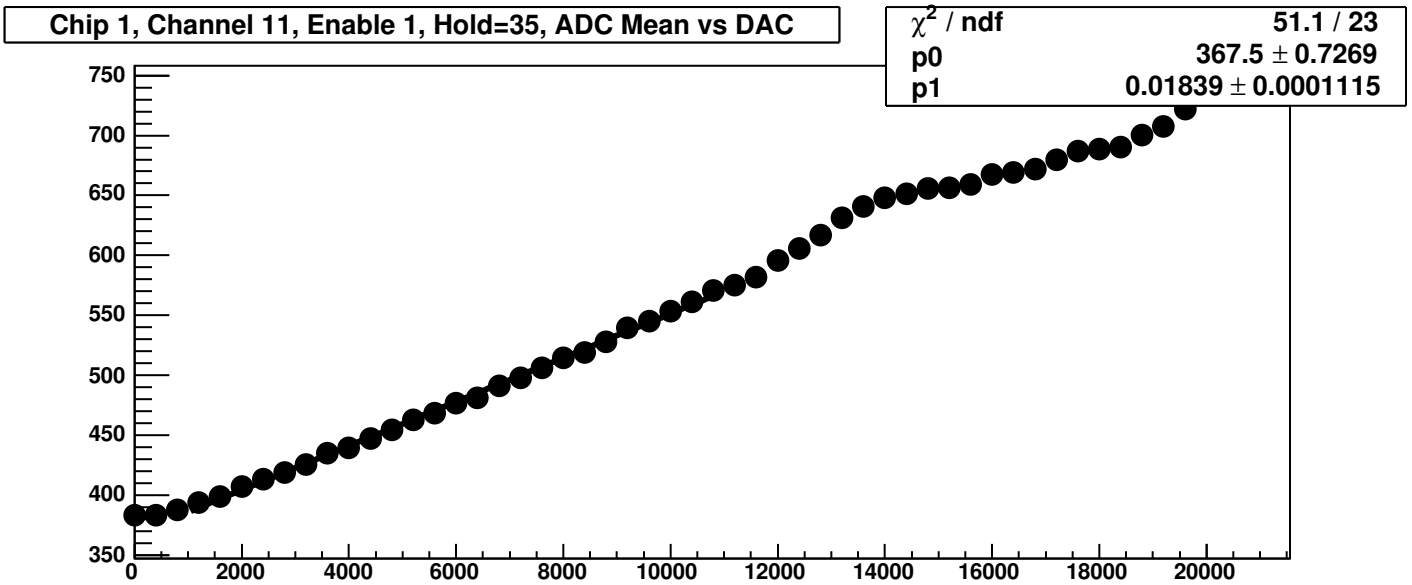

Chip 1, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

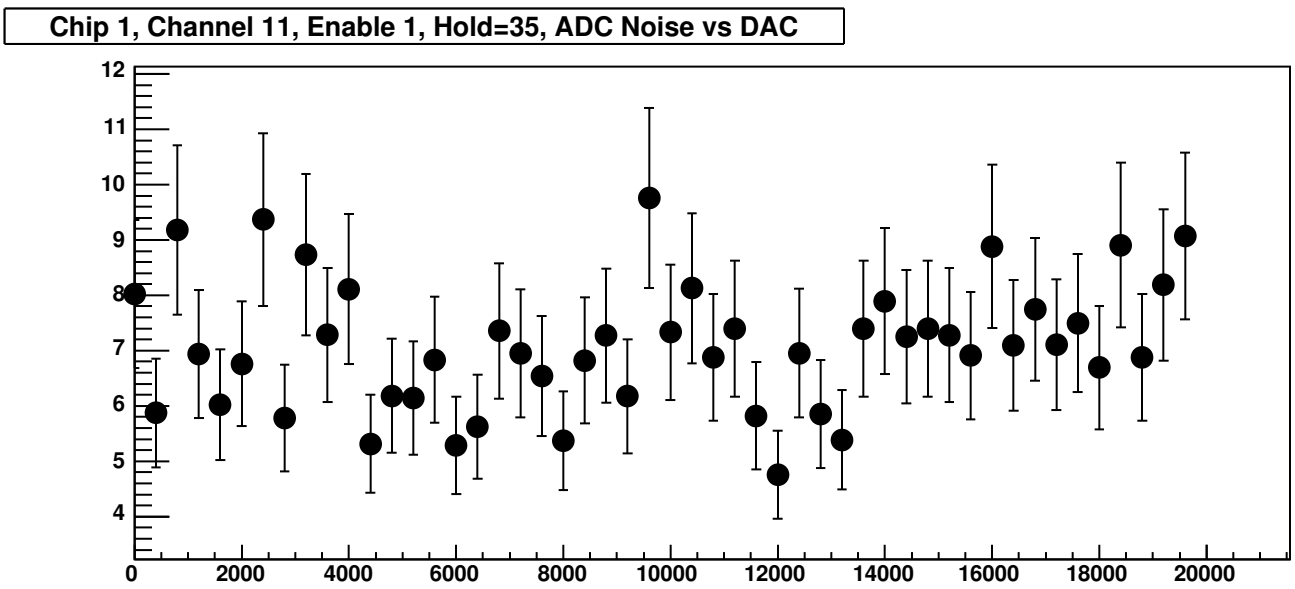

Chip 1, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

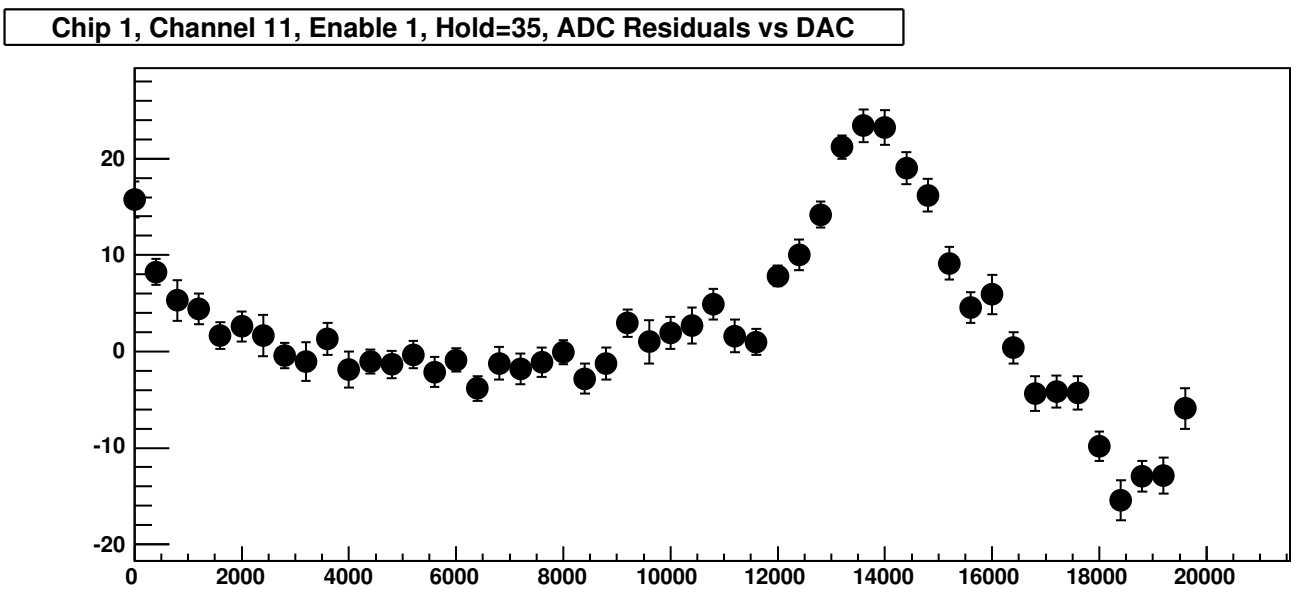

Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 38.19 / 23
p0 366.9 ± 0.7175
p1 0.01653 ± 0.0001058
750
700
650
600
550
500
450
400
350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

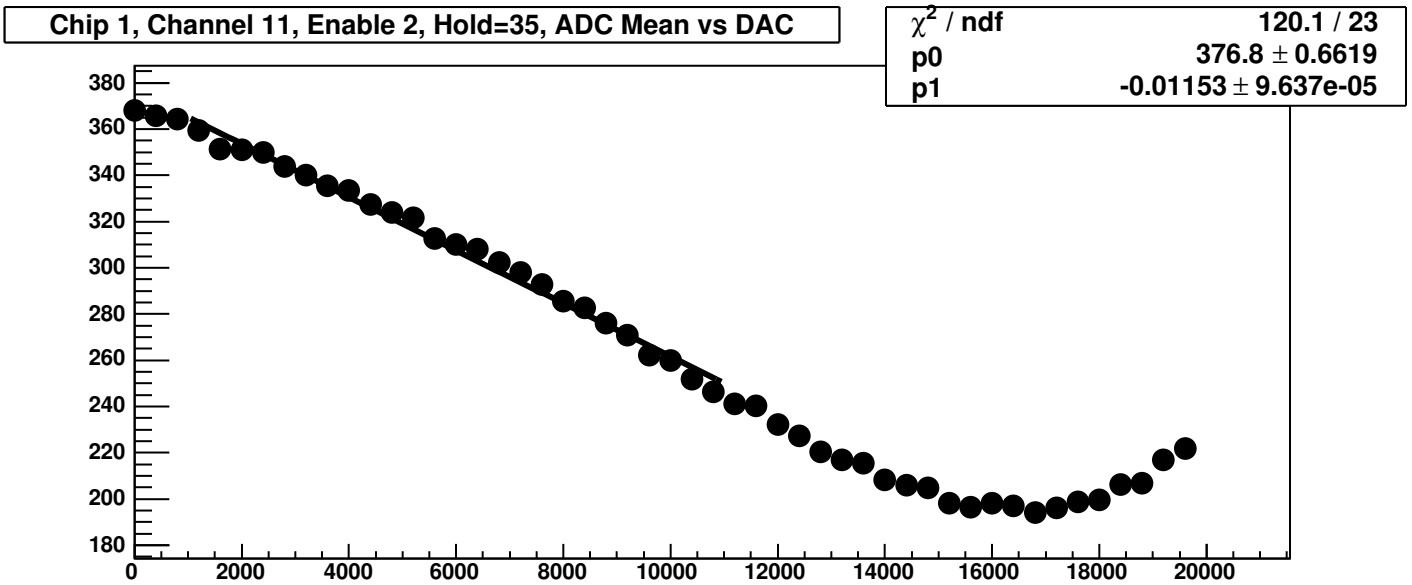

Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

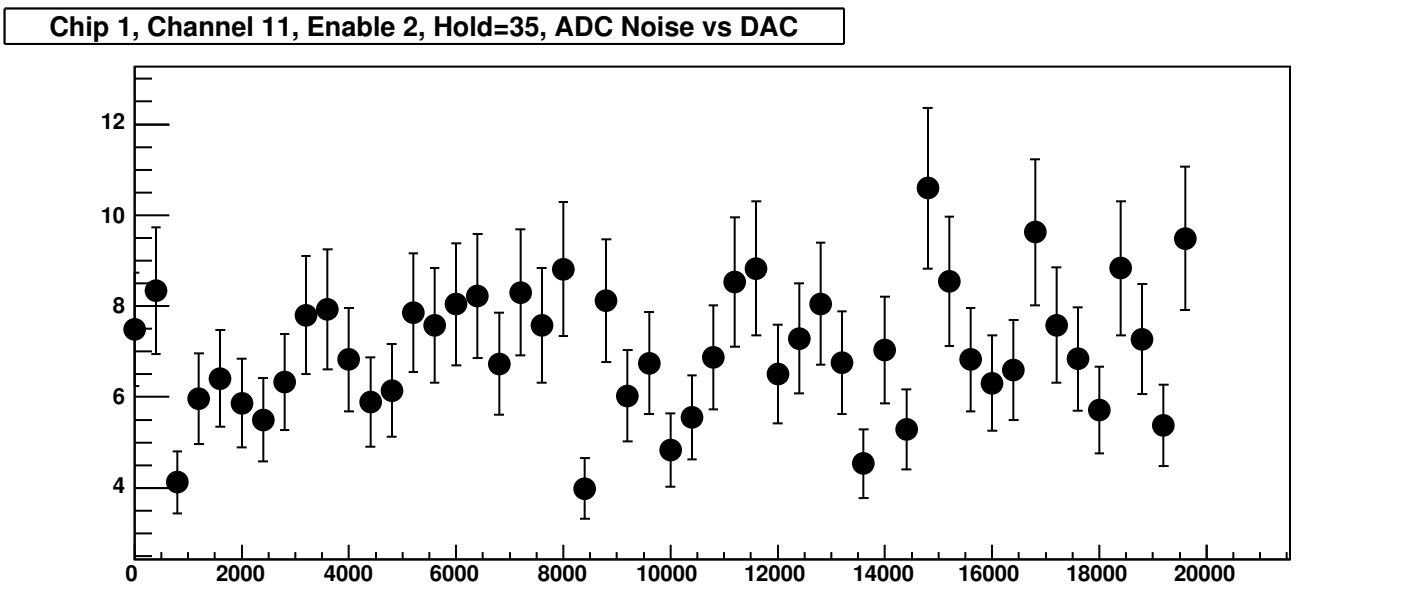

Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

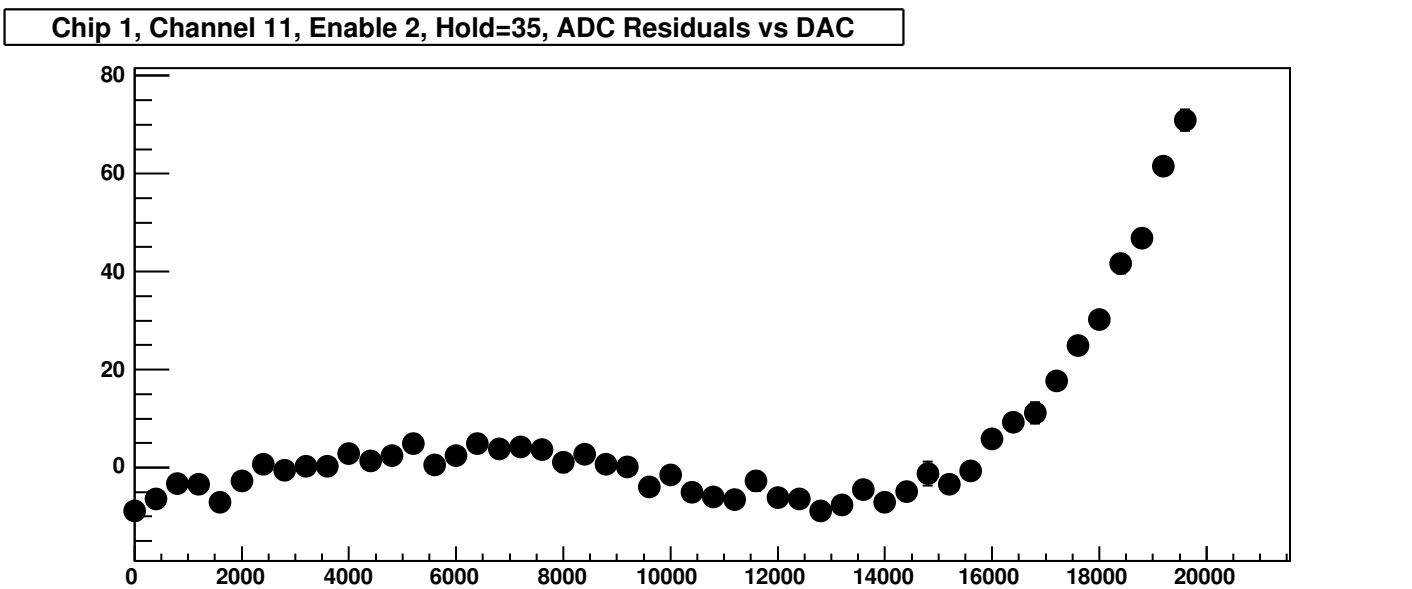

Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 51.1 / 23
p0 367.5 ± 0.7269
p1 0.01839 ± 0.0001115
750
700
650
600
550
500
450
400
350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

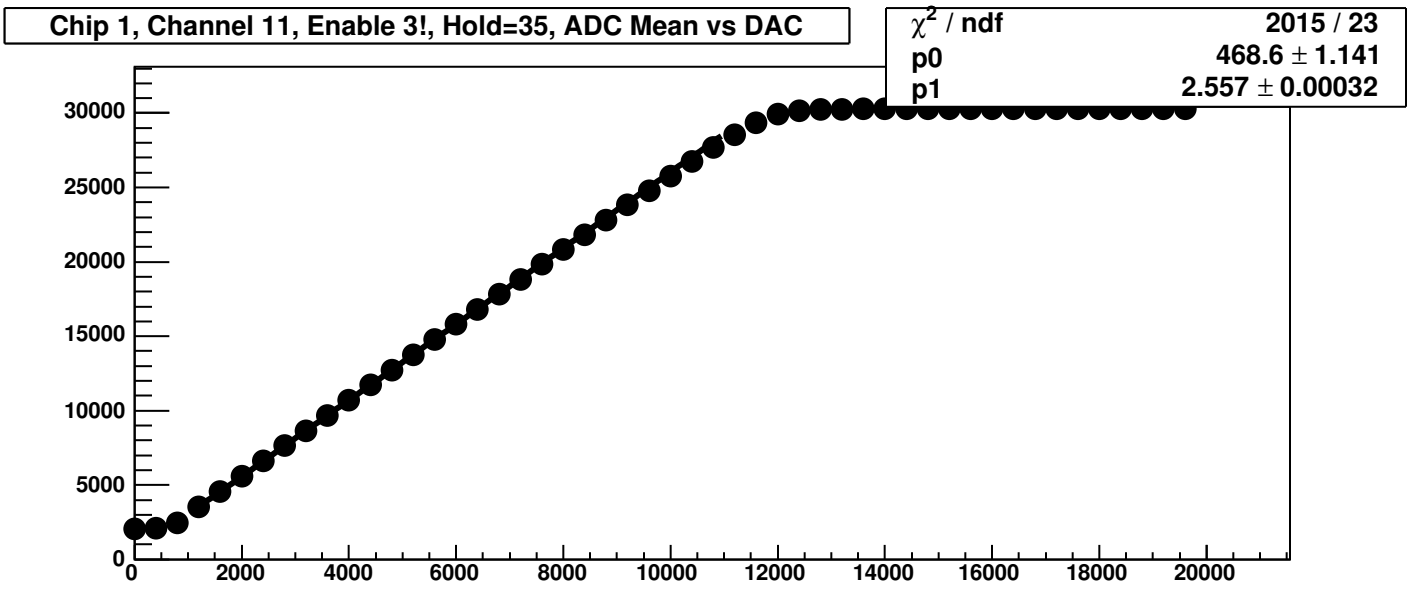

Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

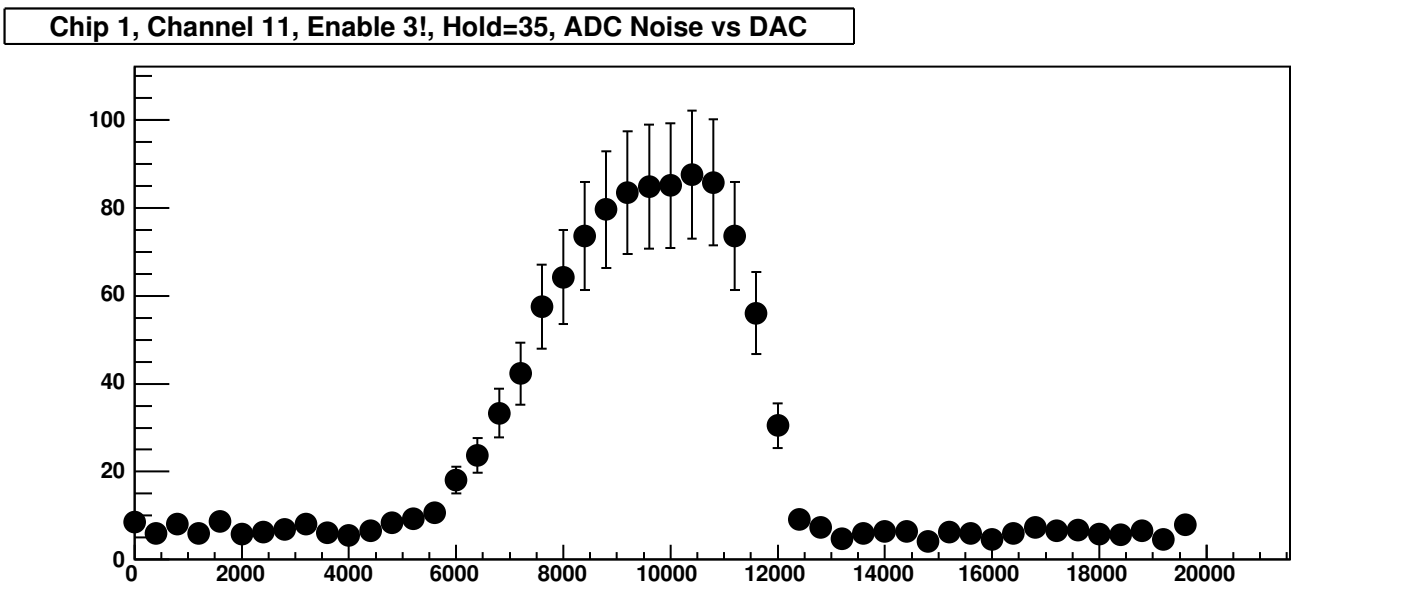

Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

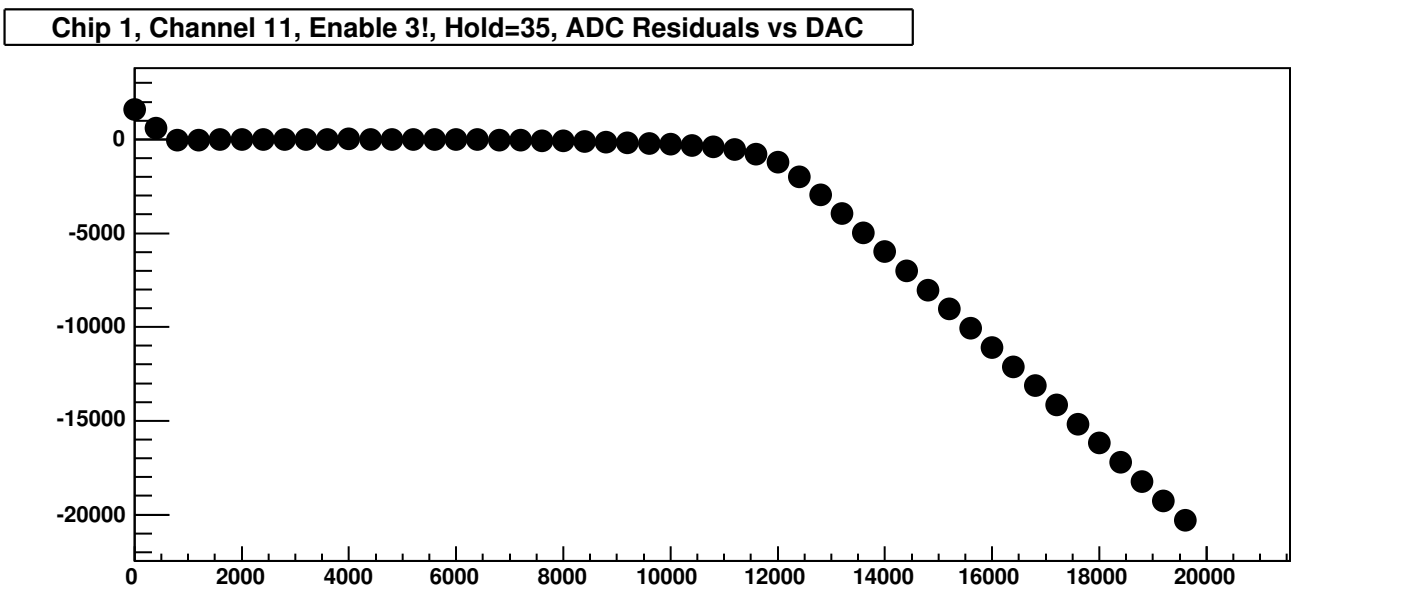

Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 120.1 / 23
p0 376.8 ± 0.6619
p1 -0.01153 ± 9.637e-05
380
360
340
320
300
280
260
240
220
200
180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

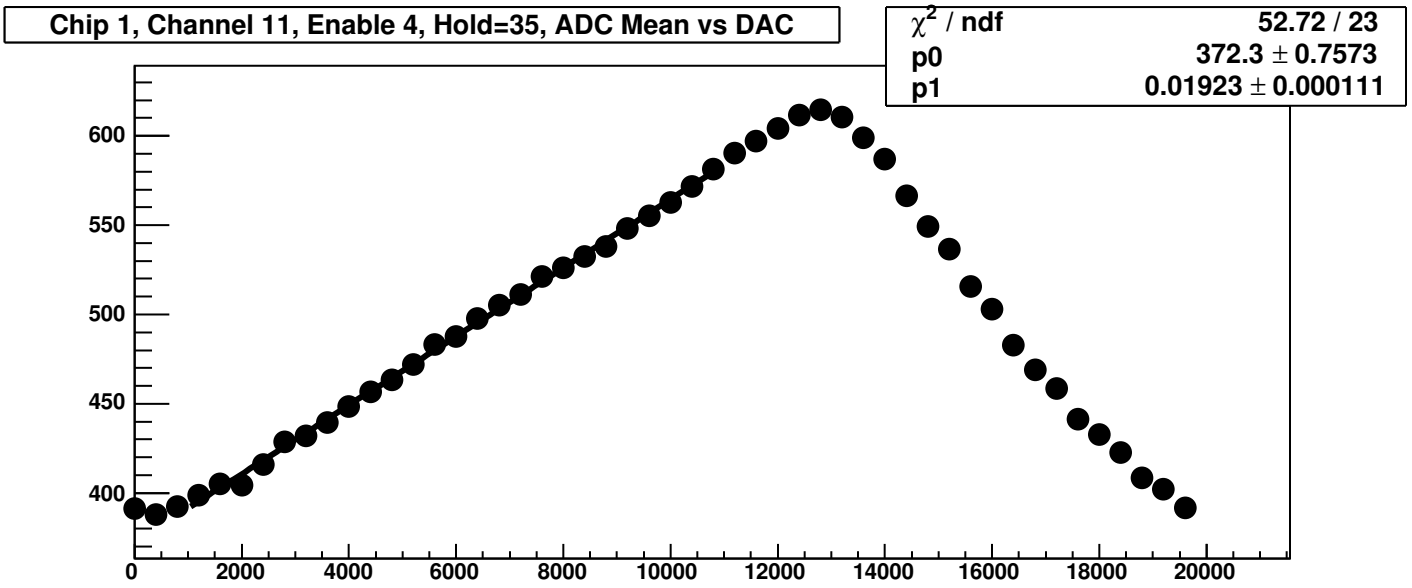

Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

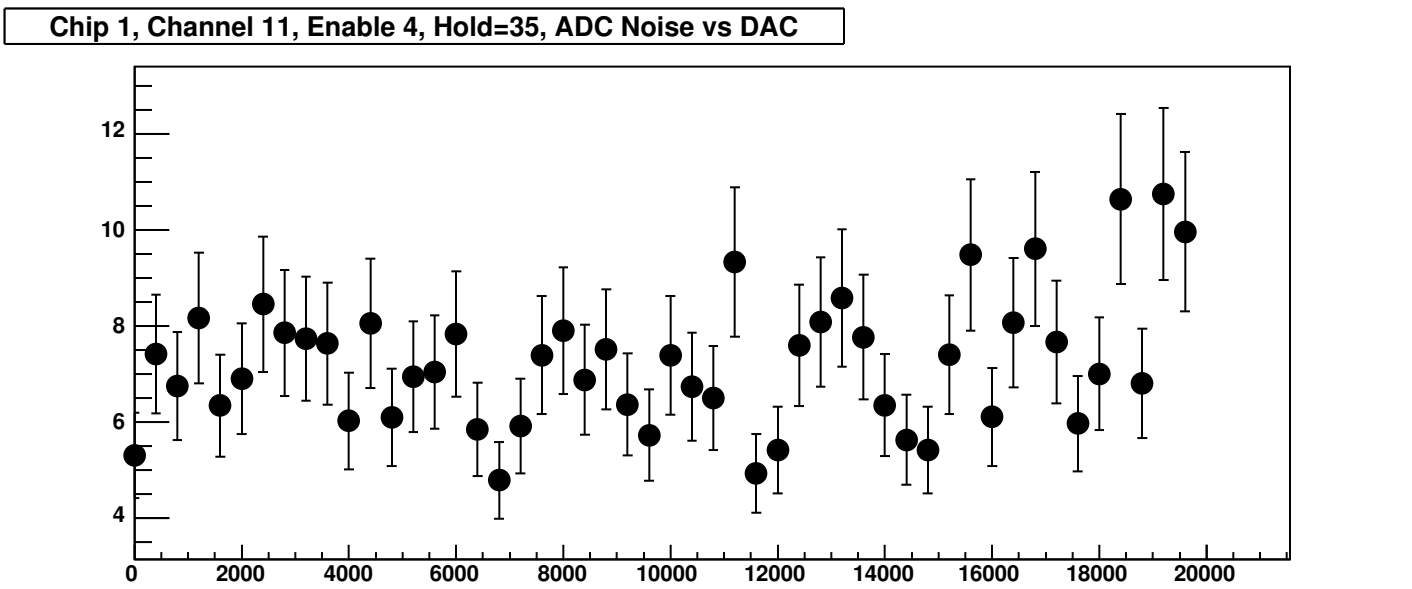

Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

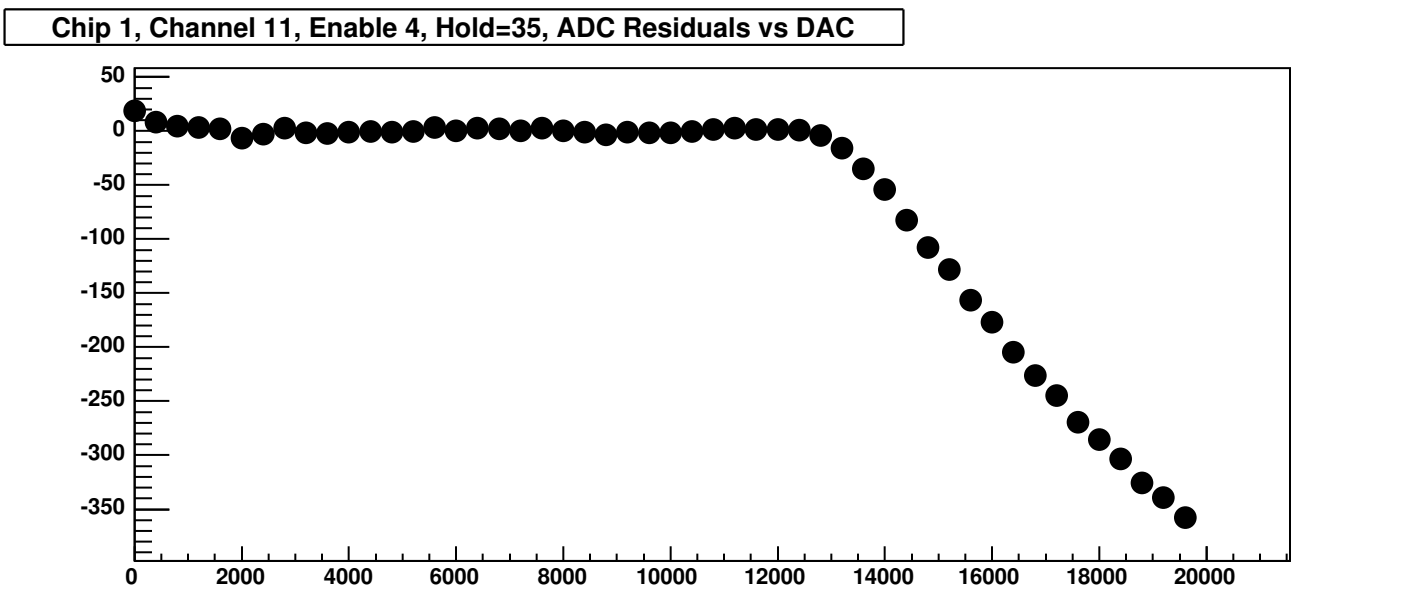

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 2015 / 23
p0 468.6 ± 1.141
p1 2.557 ± 0.00032
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

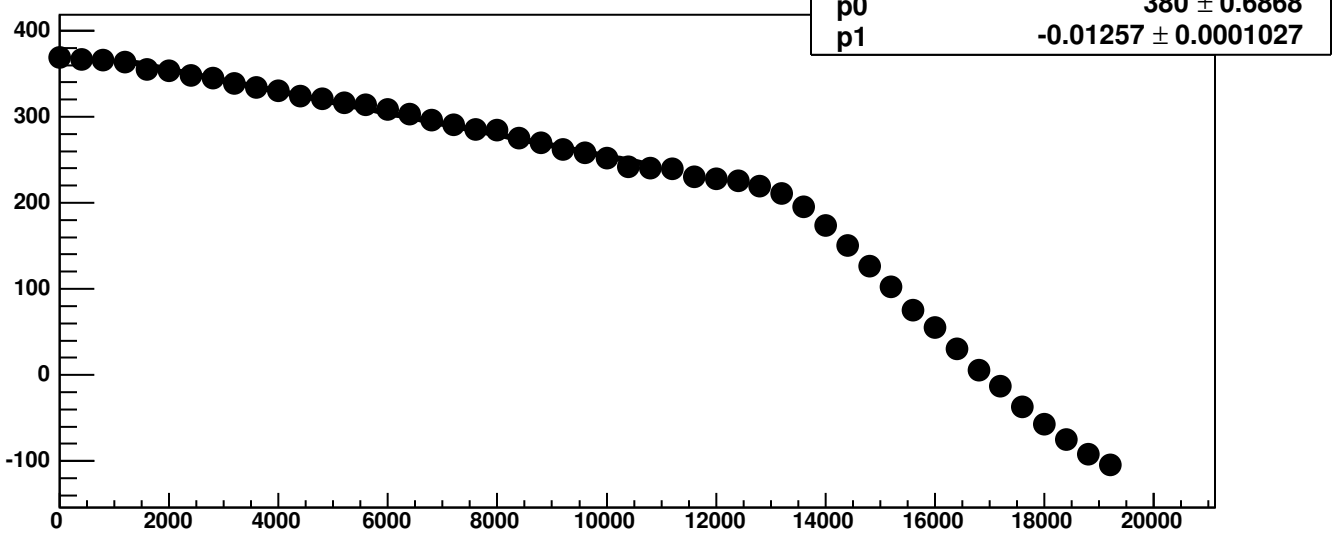

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 52.72 / 23
p0 372.3 ± 0.7573
p1 0.01923 ± 0.000111
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

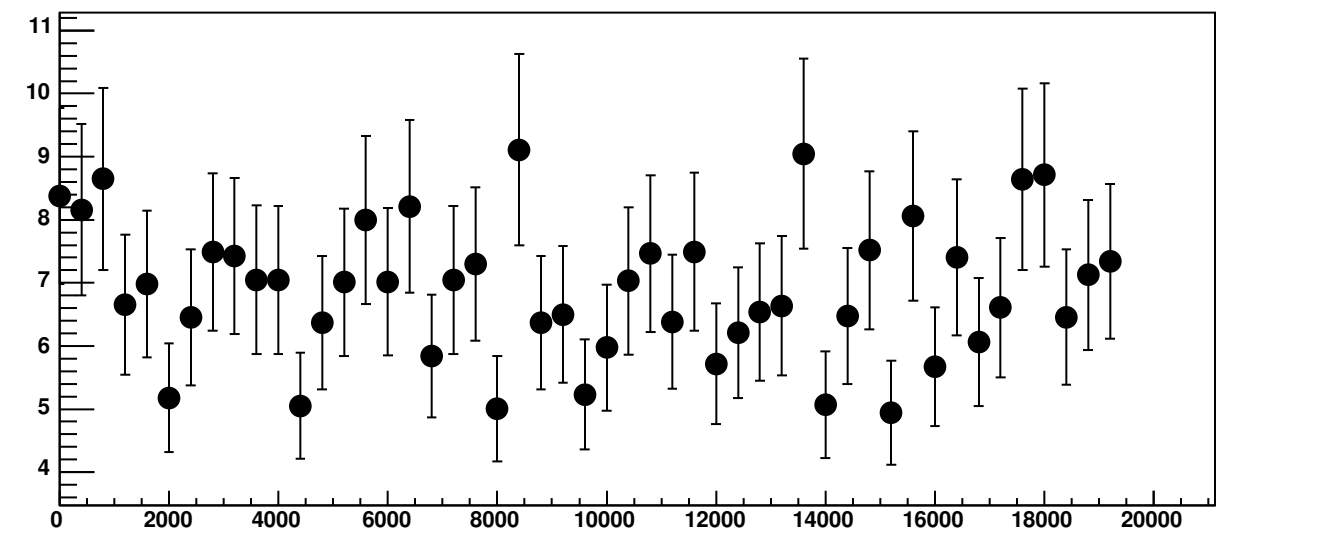

Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

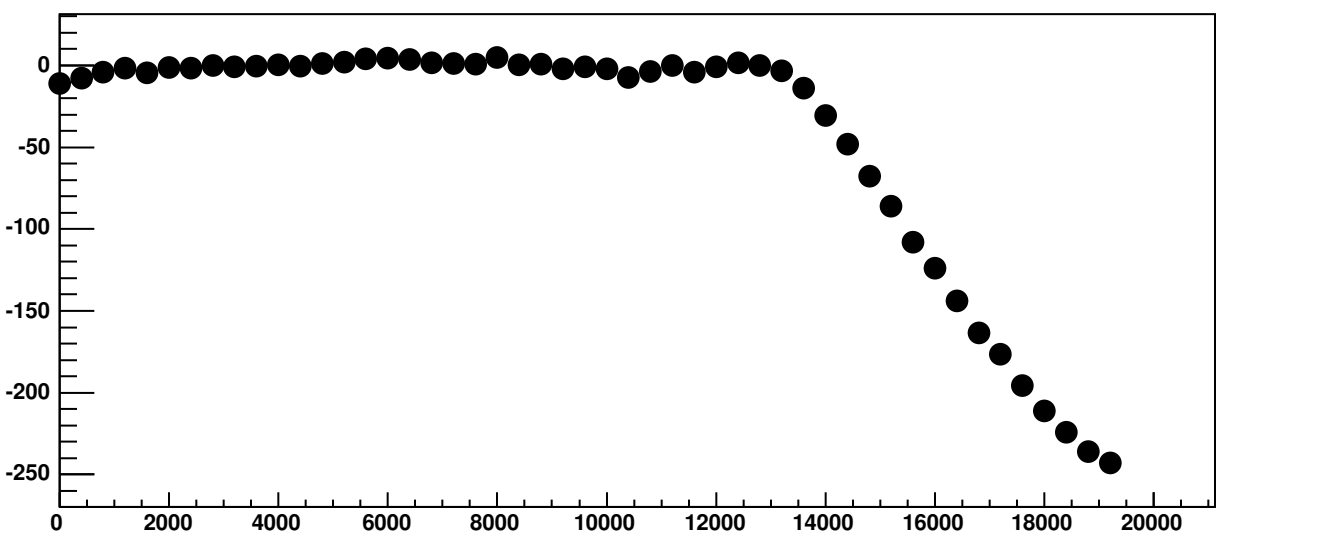
Chip 1, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

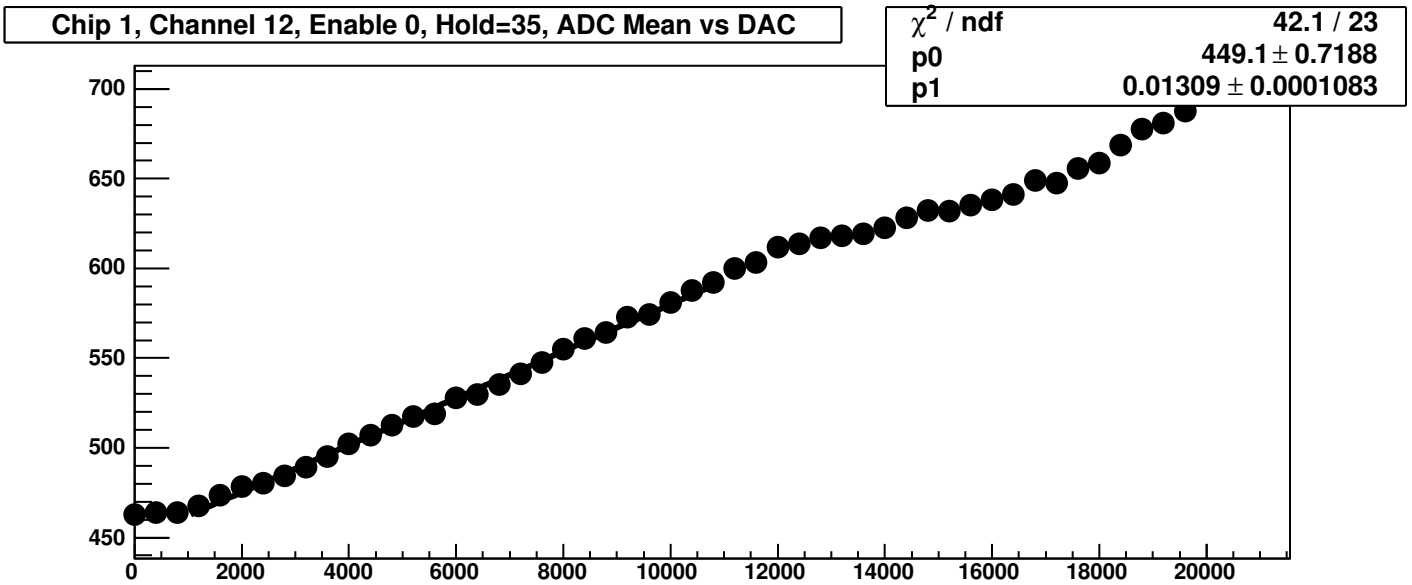


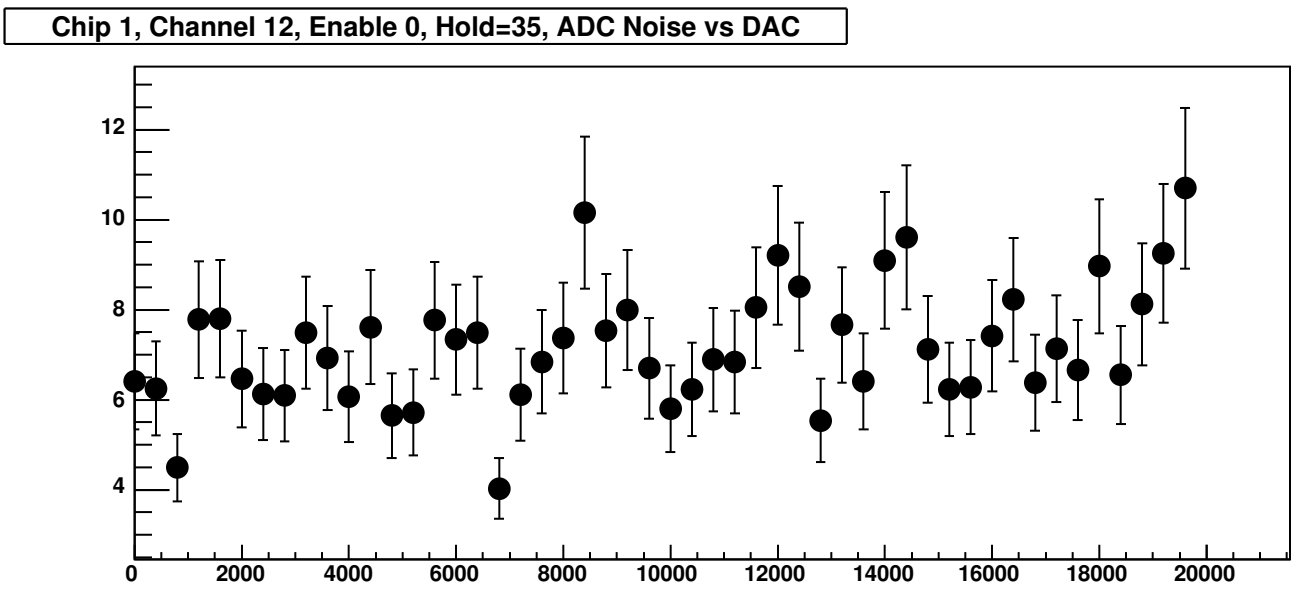
Chip 1, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

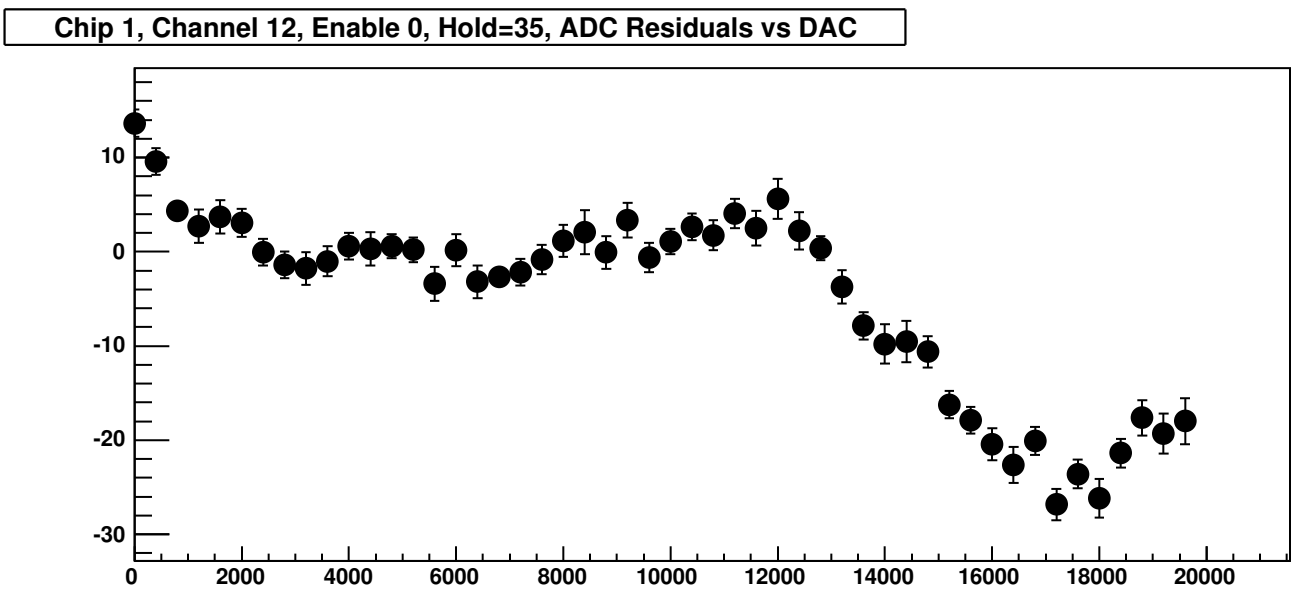


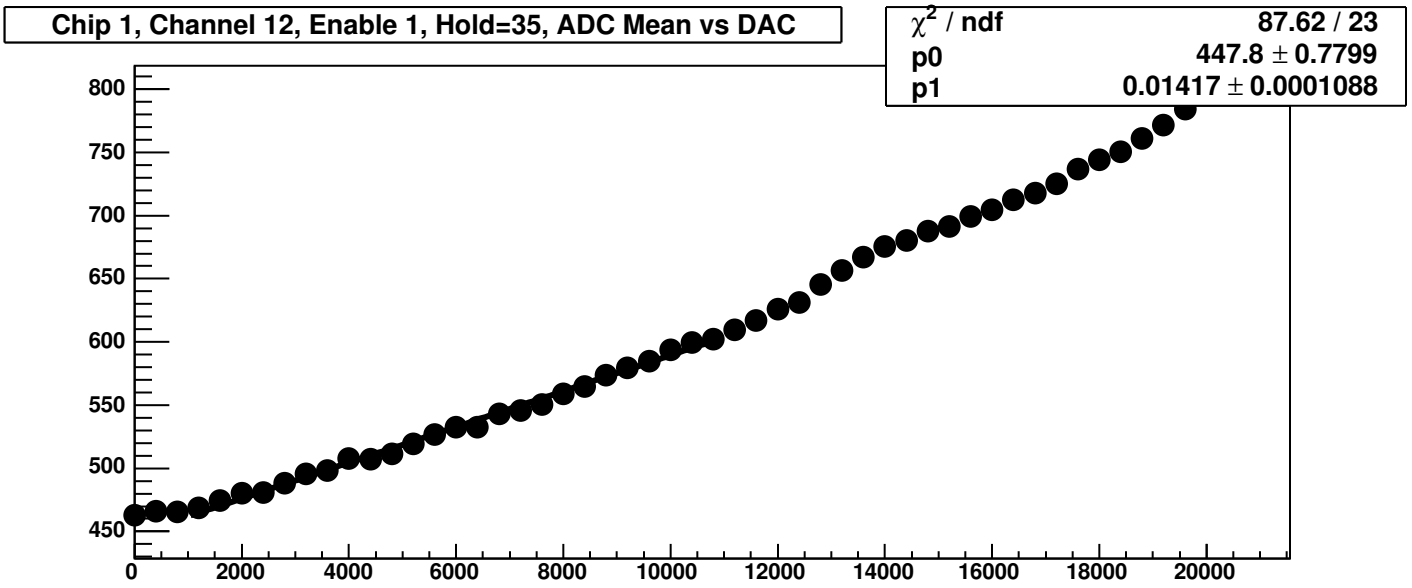
Chip 1, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

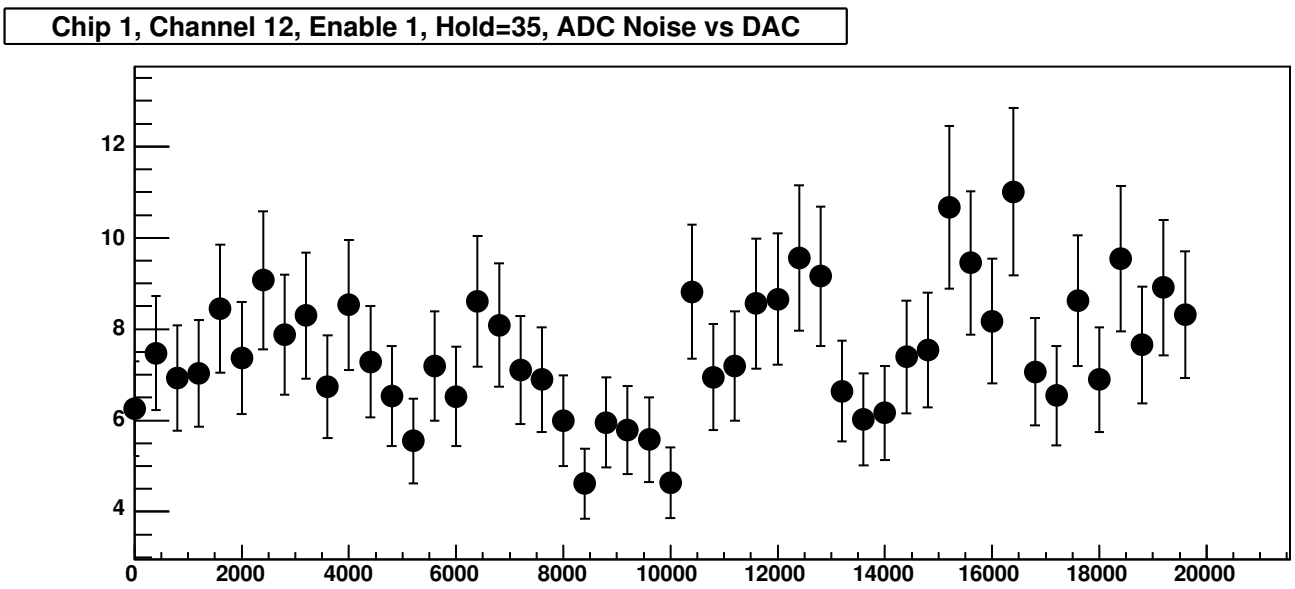


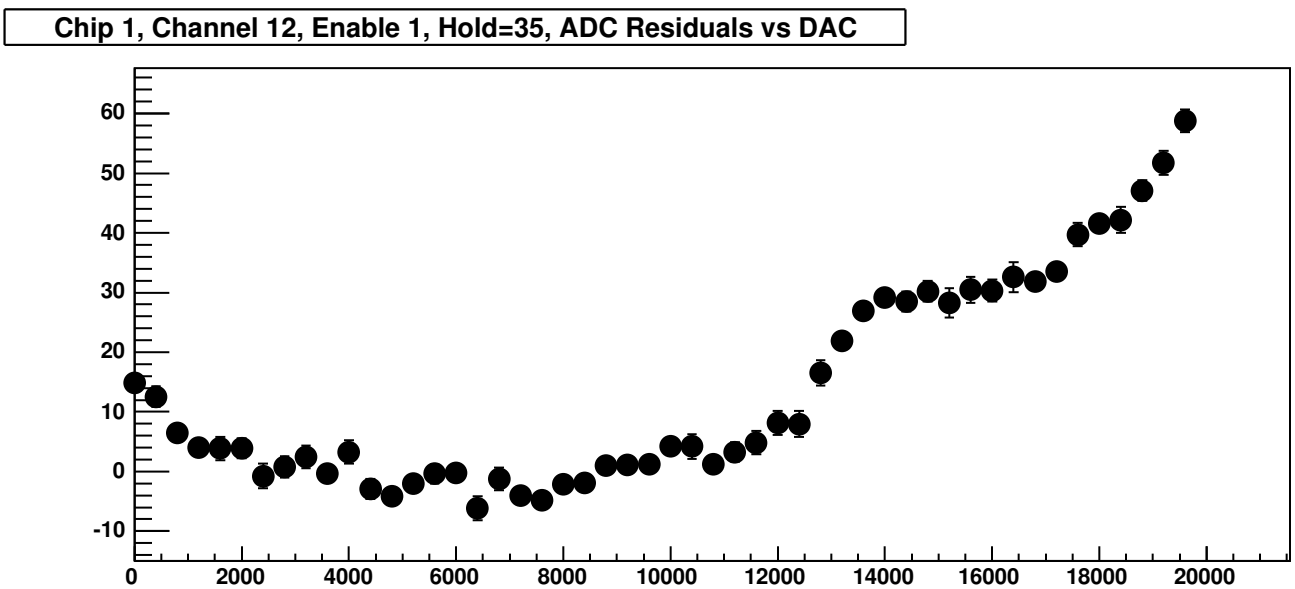

Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 42.1 / 23
p0 449.1 ± 0.7188
p1 0.01309 ± 0.0001083
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

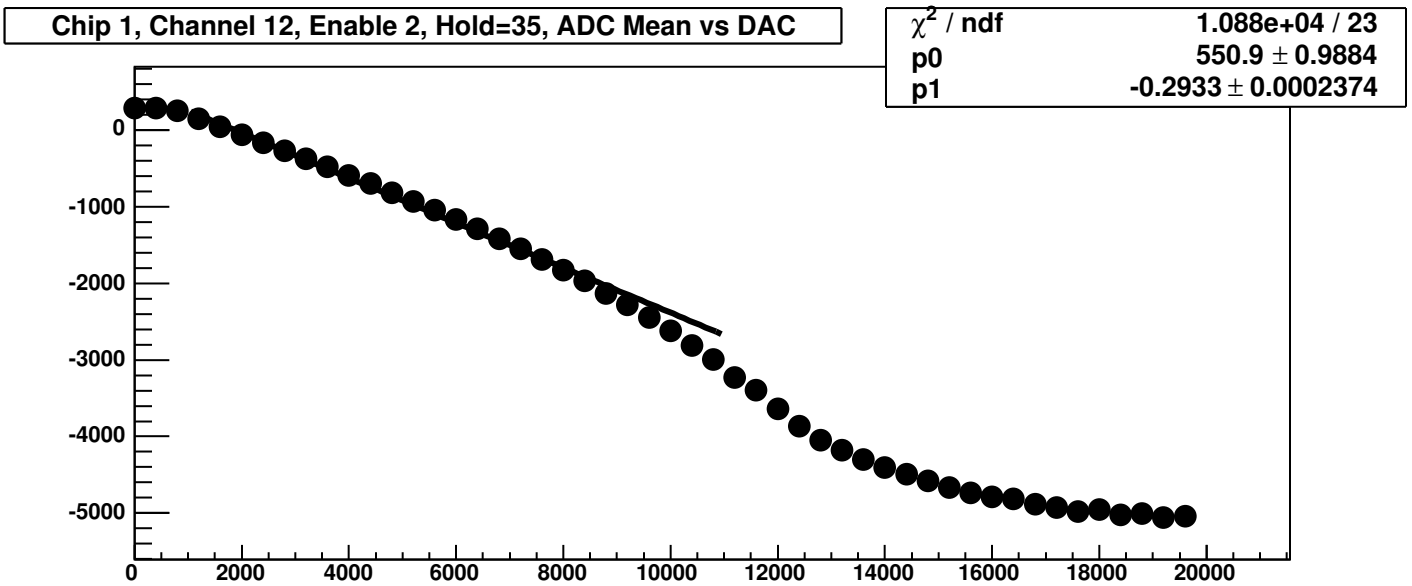

Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

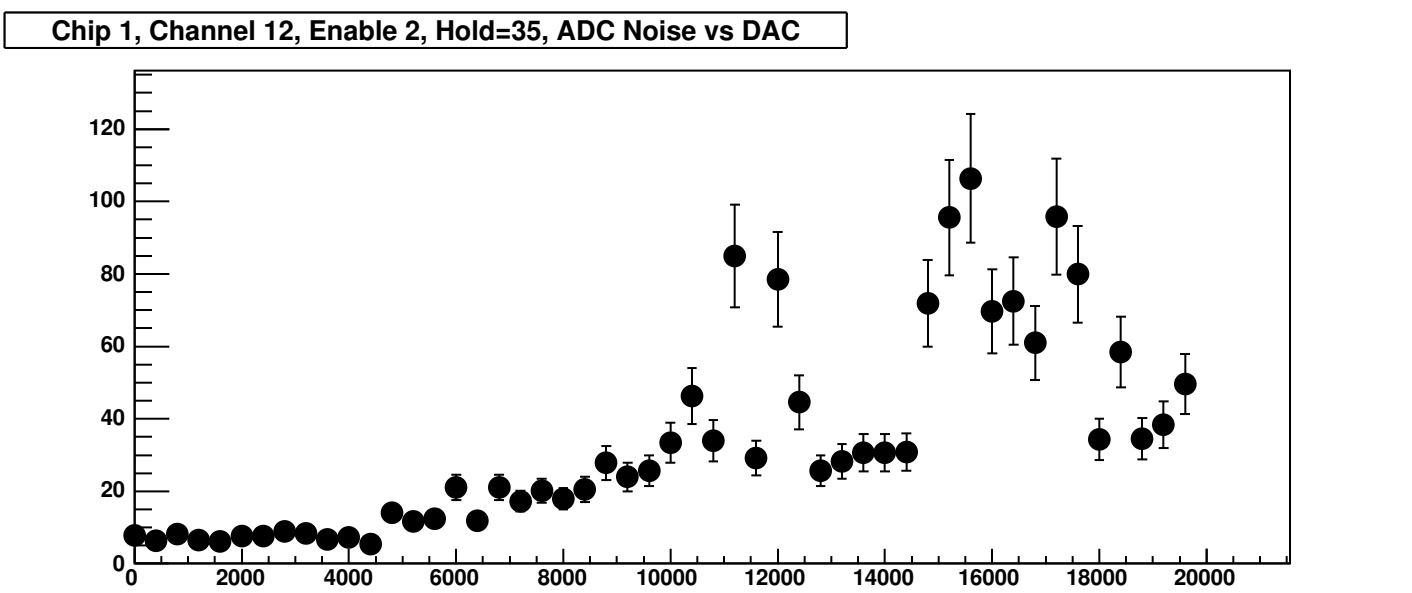

Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

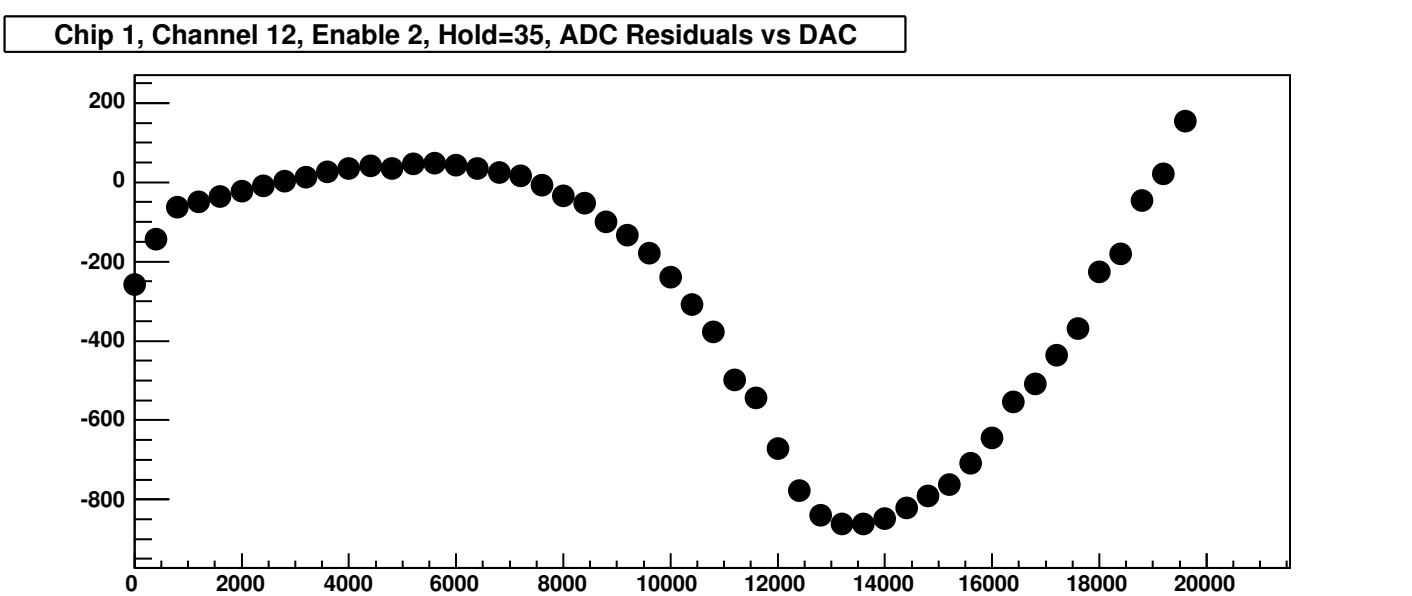

Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 87.62 / 23
p0 447.8 ± 0.7799
p1 0.01417 ± 0.0001088
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

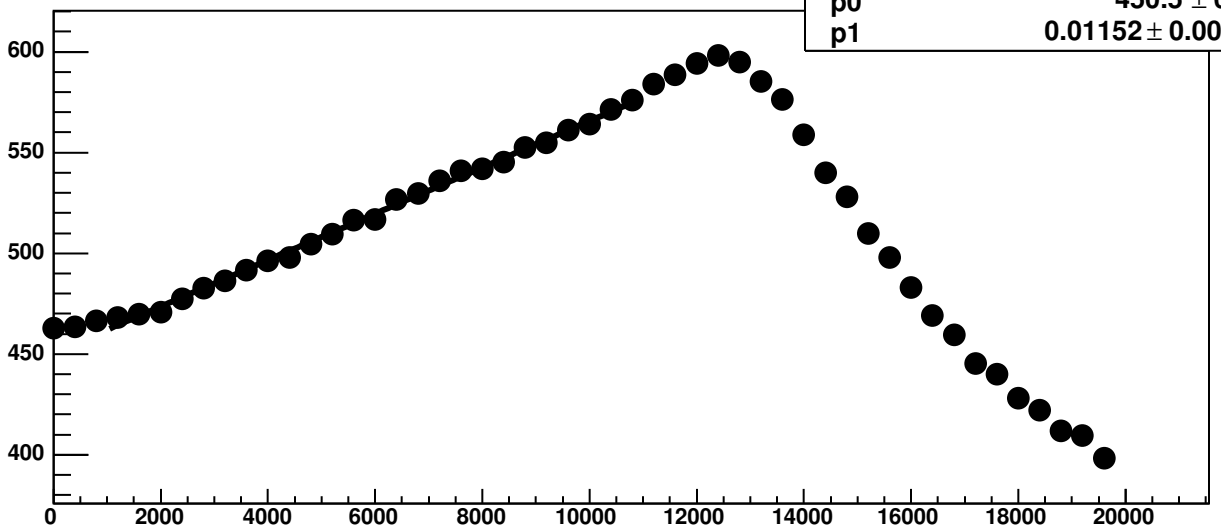

Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.088e+04 / 23
p0 550.9 ± 0.9884
p1 -0.2933 ± 0.0002374
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

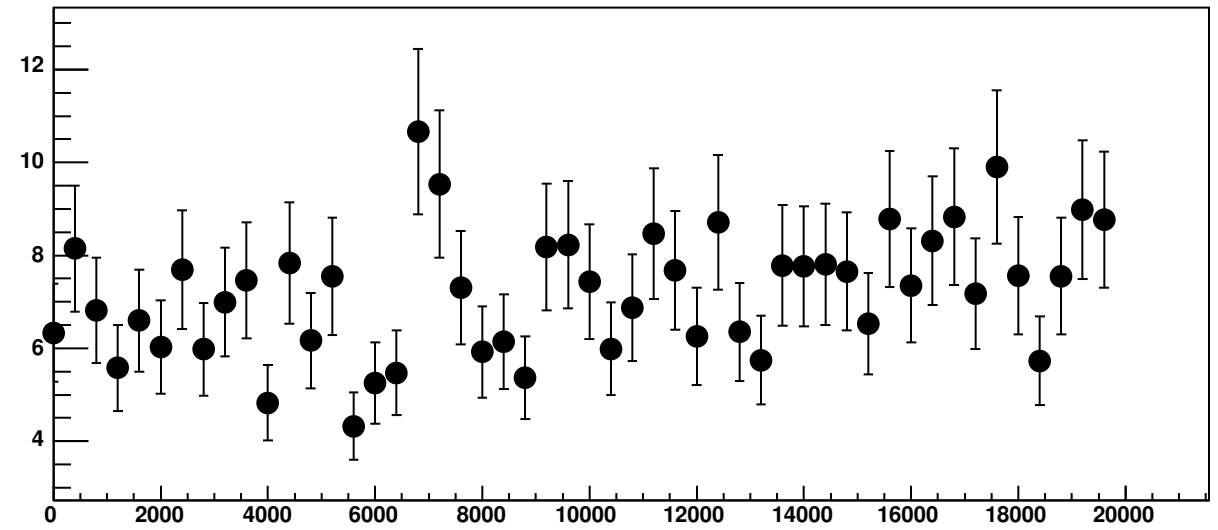

Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

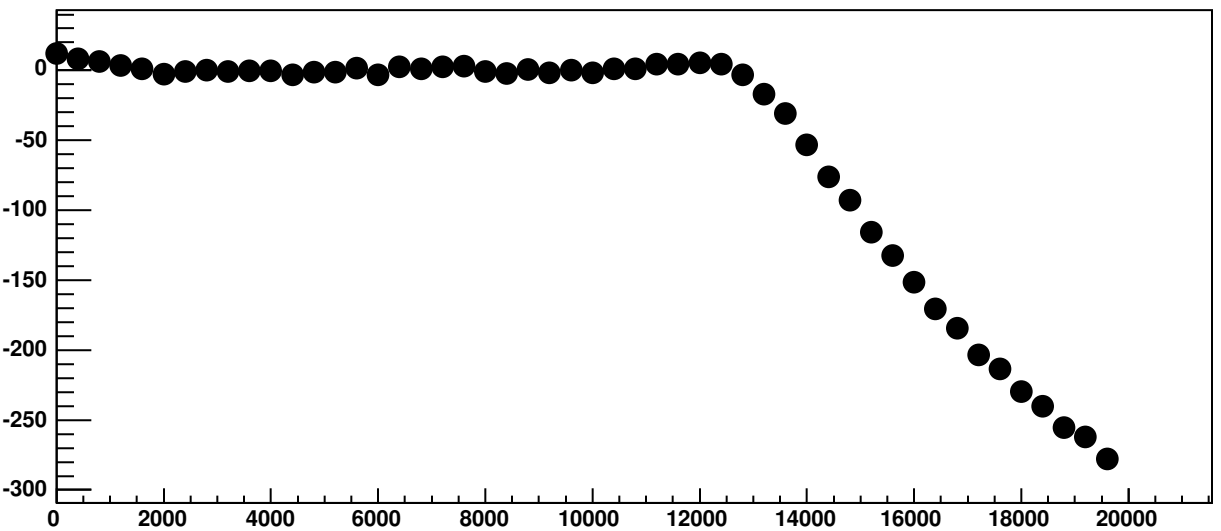
χ^2 / ndf	40.58 / 23
p0	450.5 ± 0.6684
p1	0.01152 ± 0.0001034

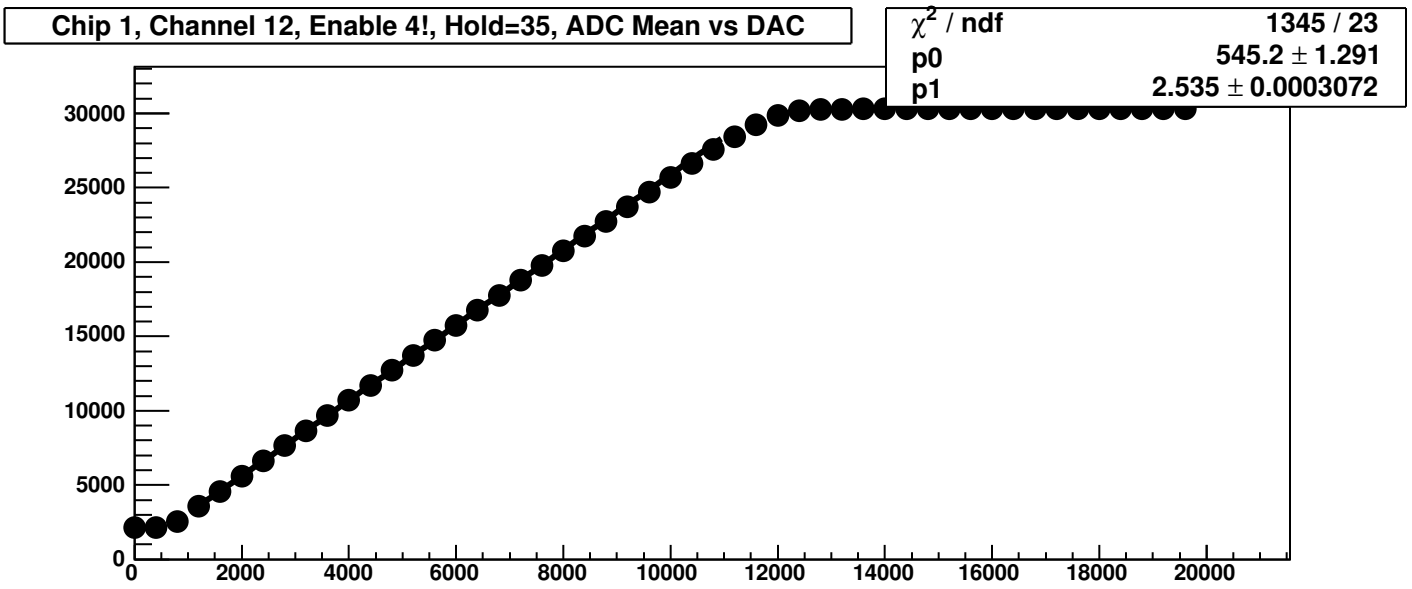


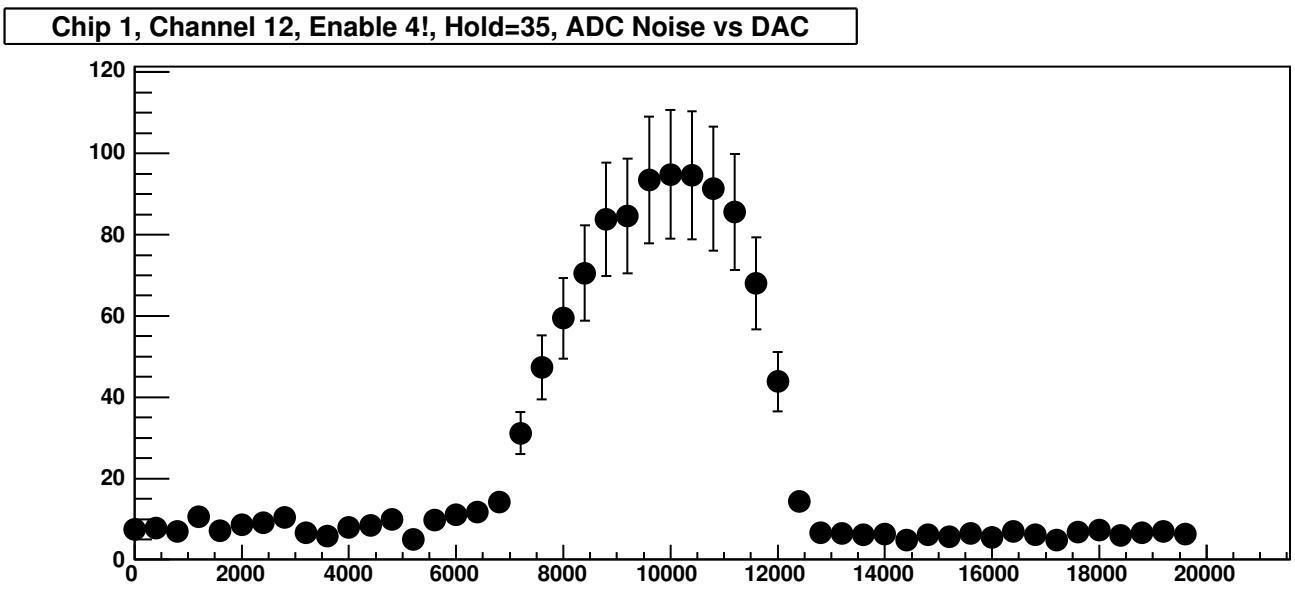
Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

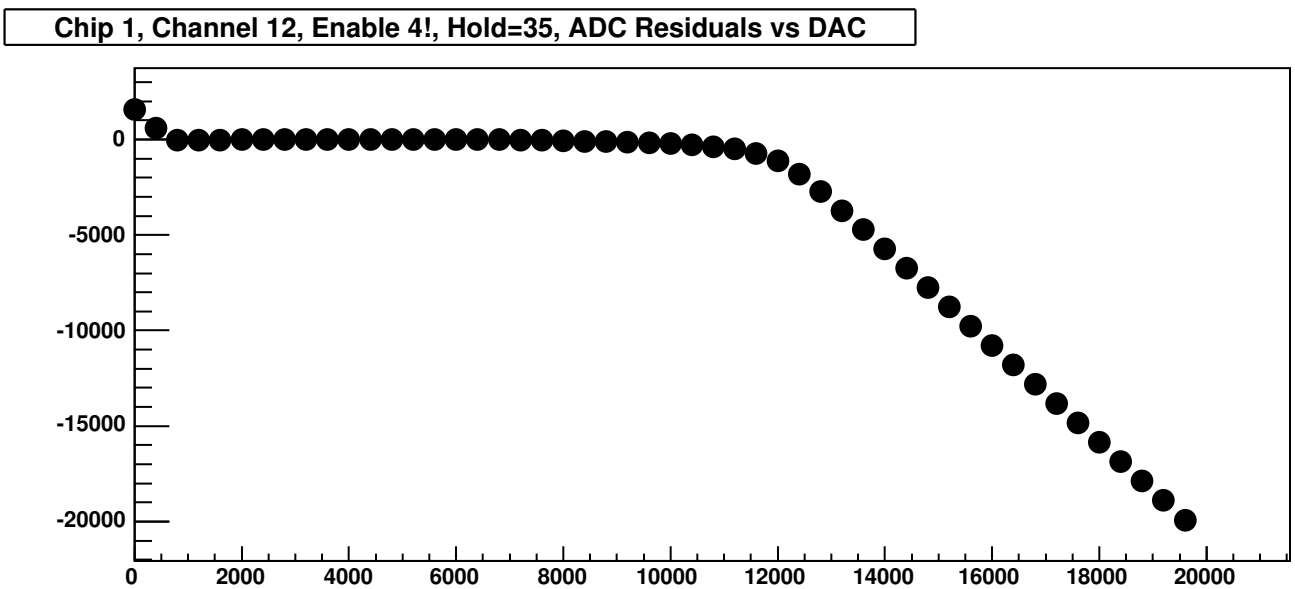


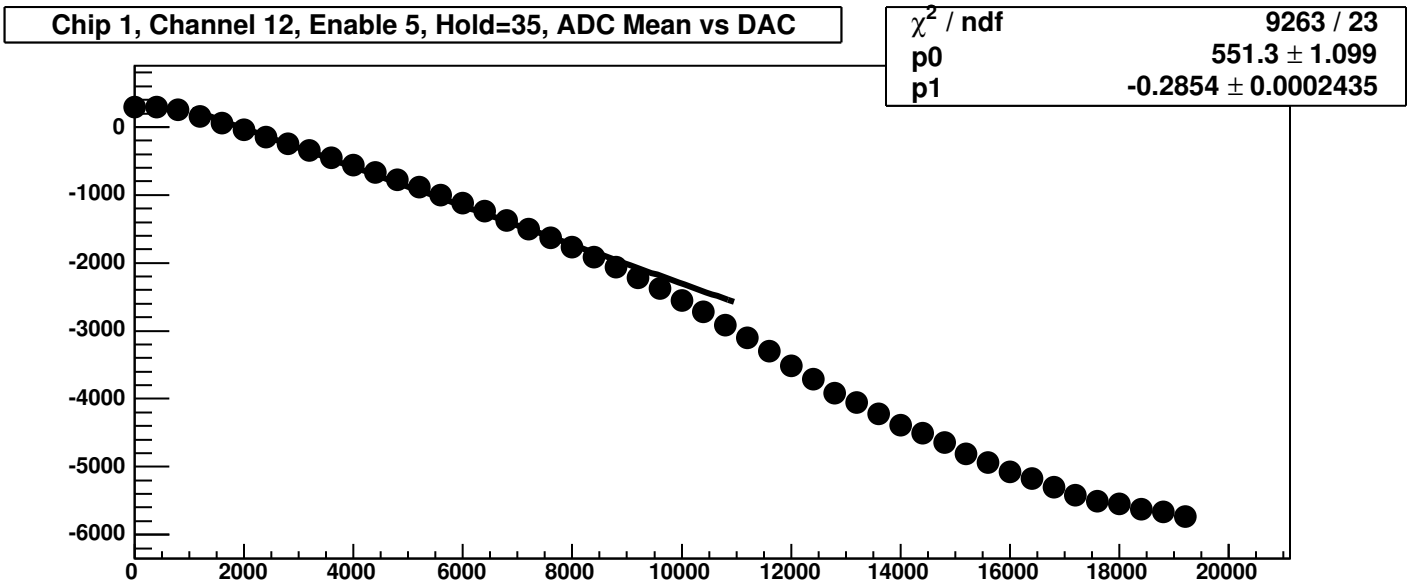
Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

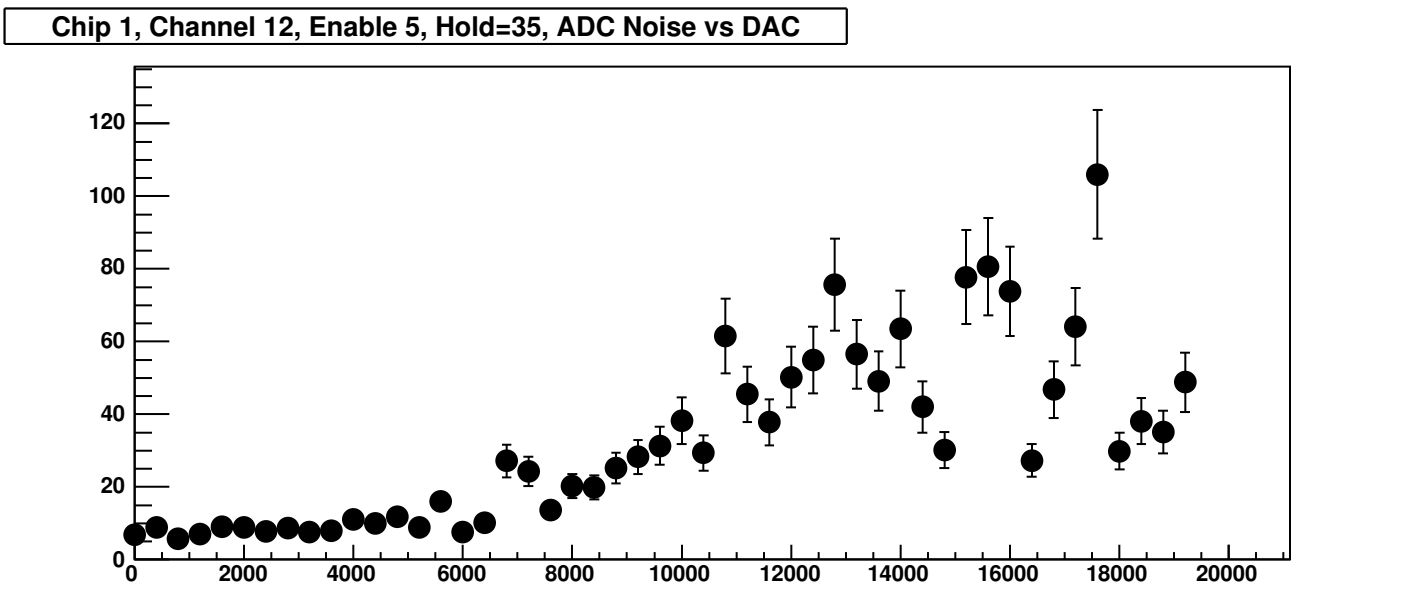


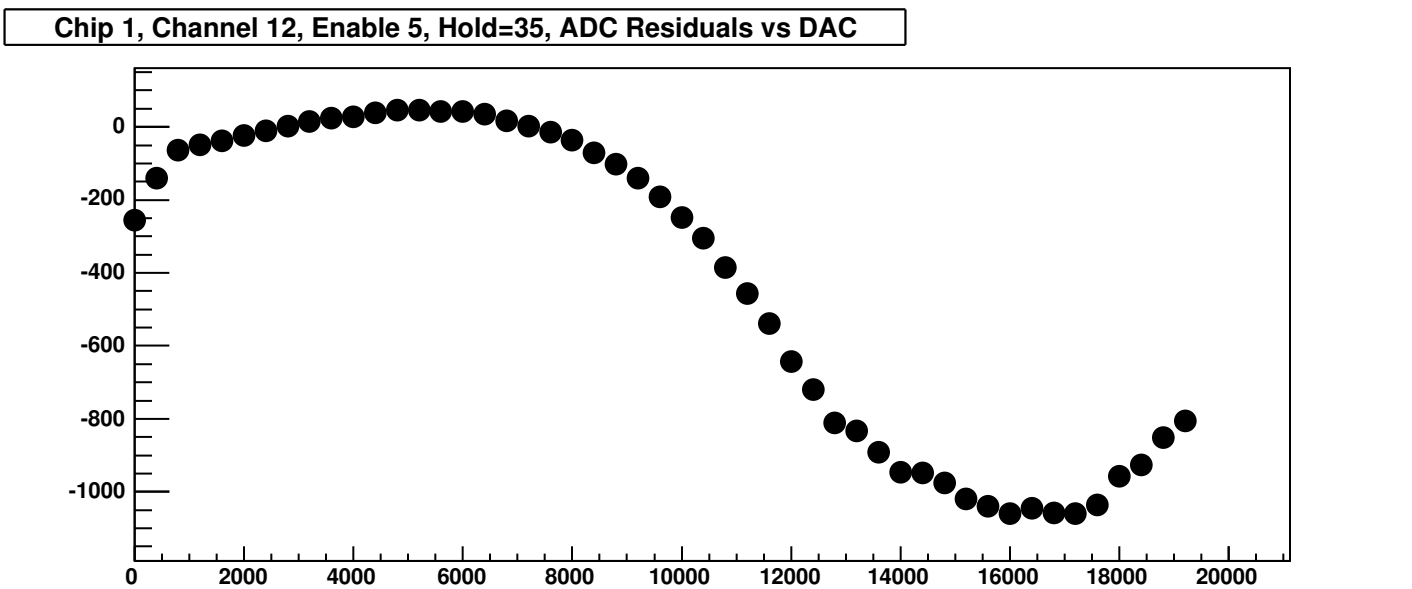

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1345 / 23
p0 545.2 ± 1.291
p1 2.535 ± 0.0003072
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

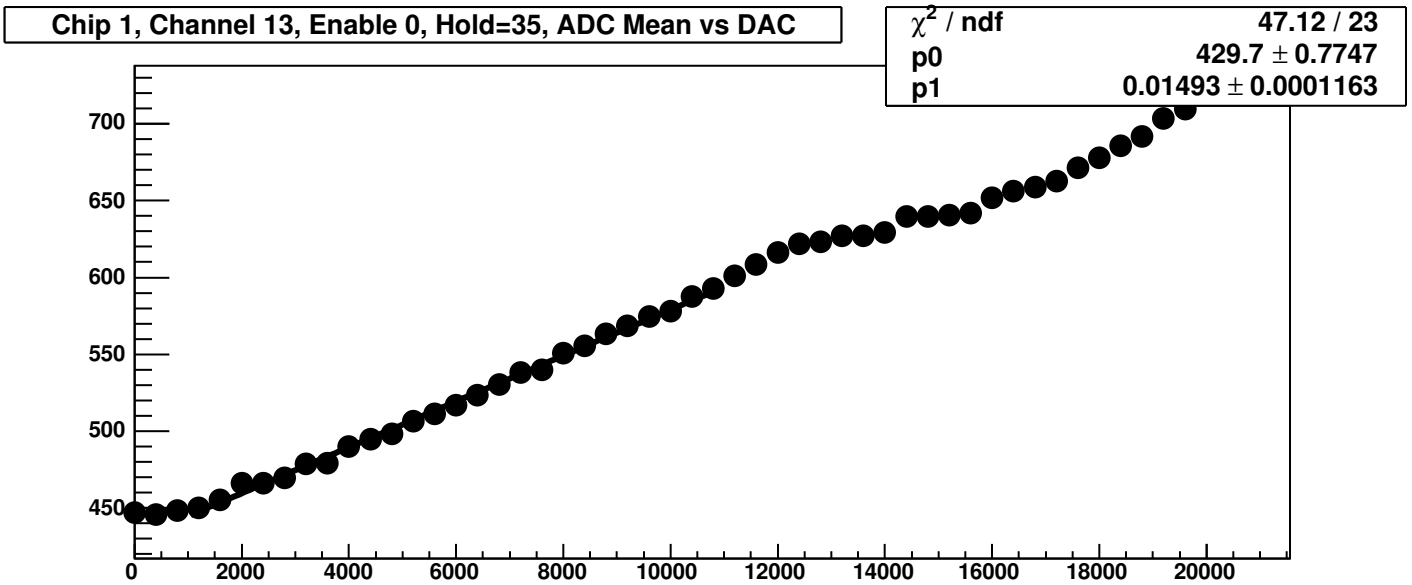

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

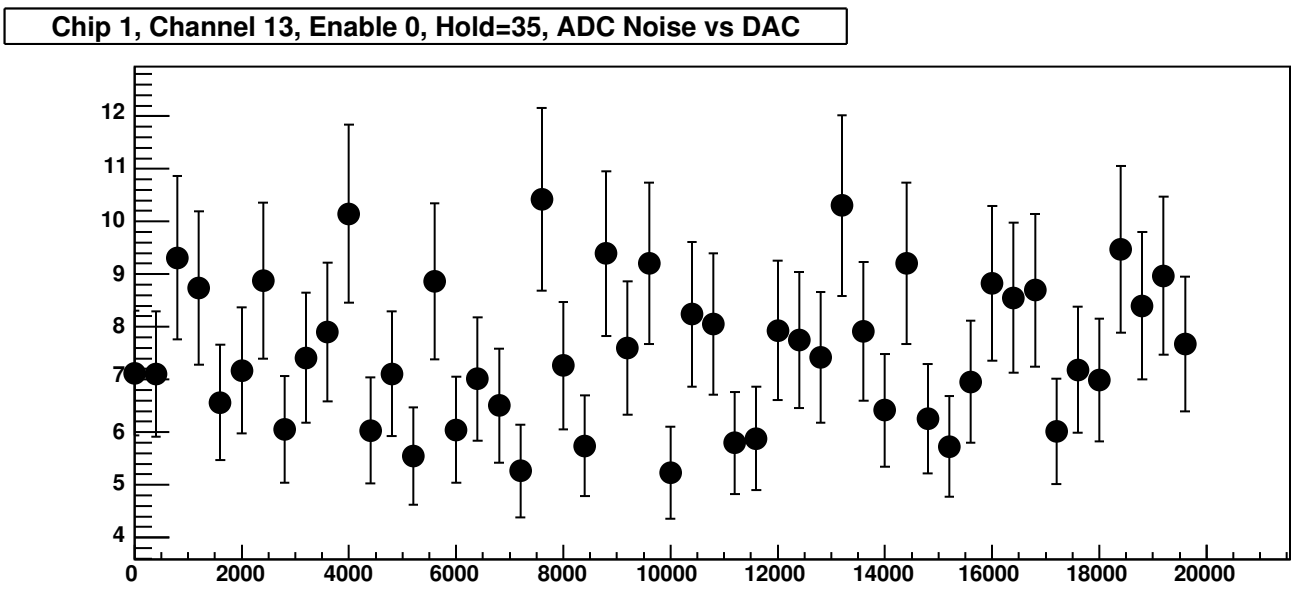

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

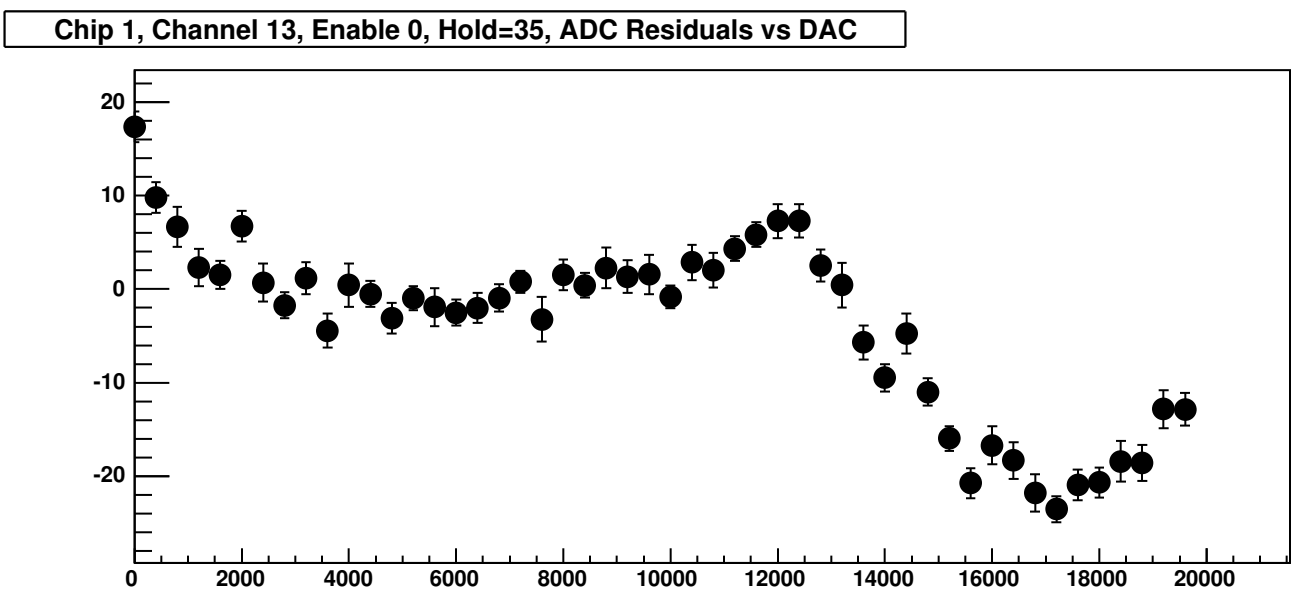

Chip 1, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 9263 / 23
p0 551.3 ± 1.099
p1 -0.2854 ± 0.0002435
0
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

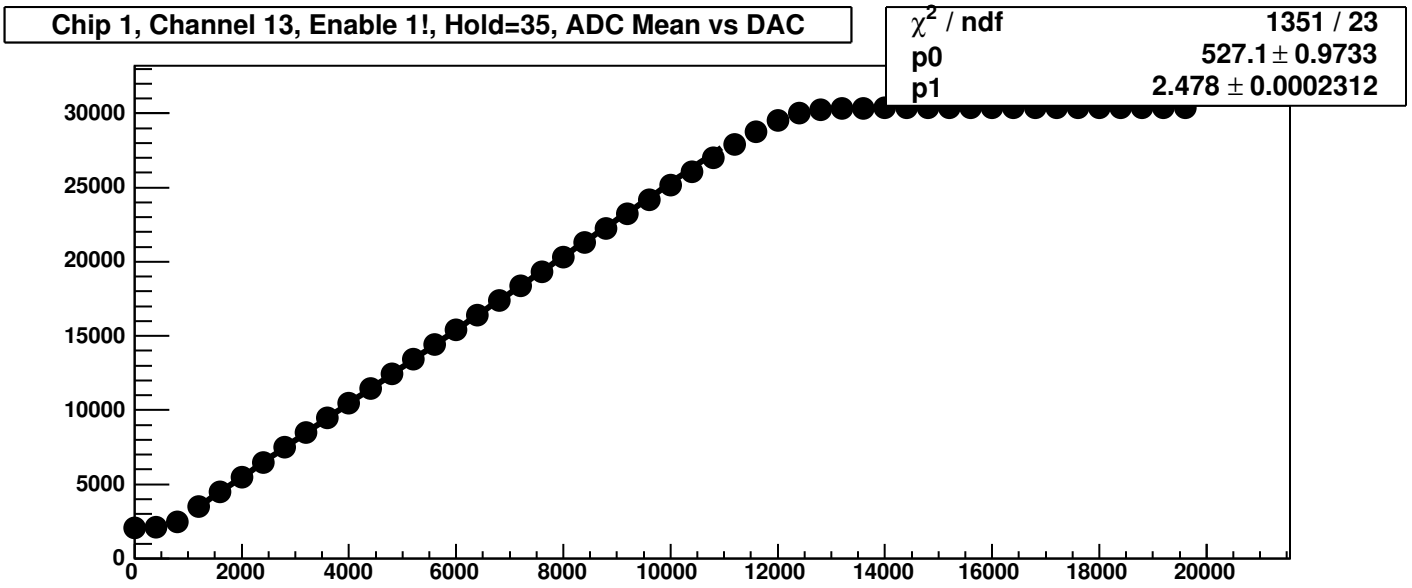

Chip 1, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

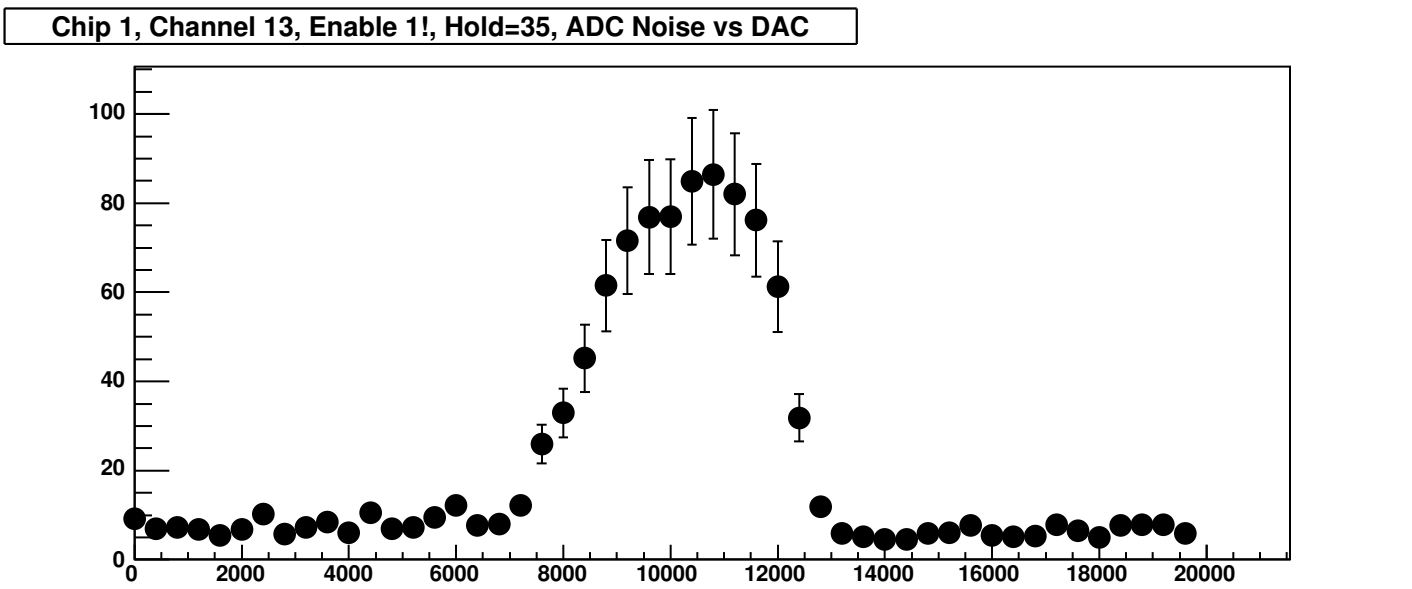

Chip 1, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

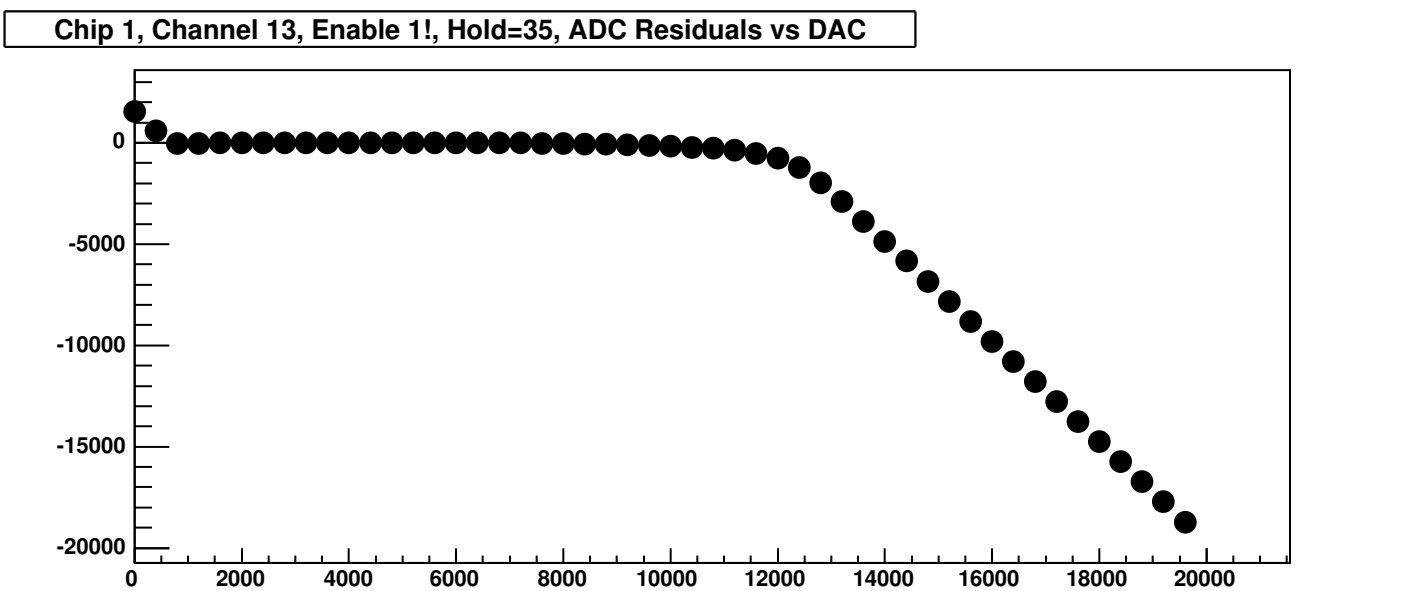

Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 47.12 / 23
p0 429.7 ± 0.7747
p1 0.01493 ± 0.0001163
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

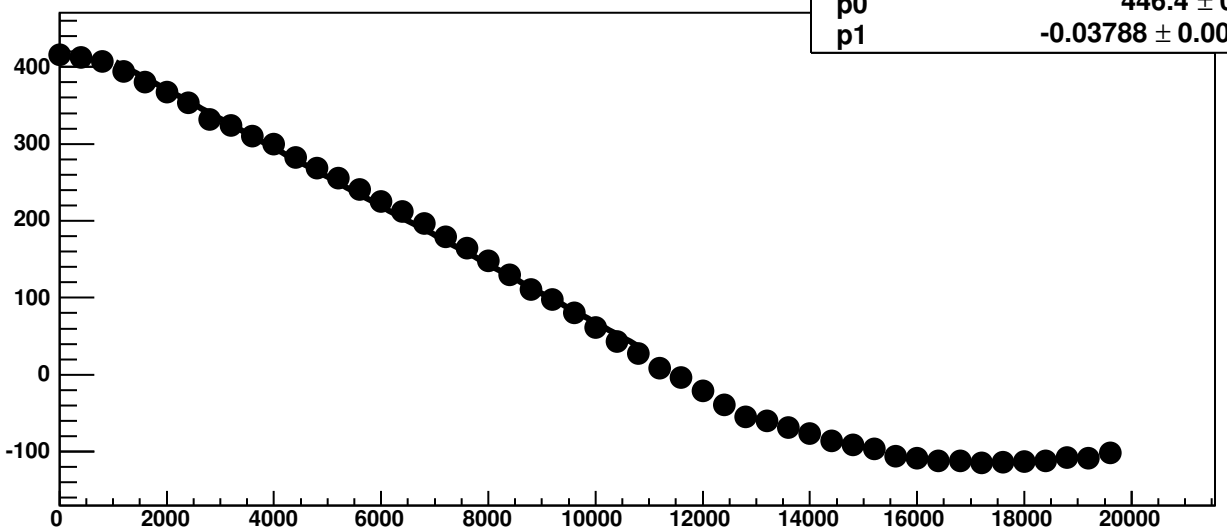

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1351 / 23
p0 527.1 ± 0.9733
p1 2.478 ± 0.0002312
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

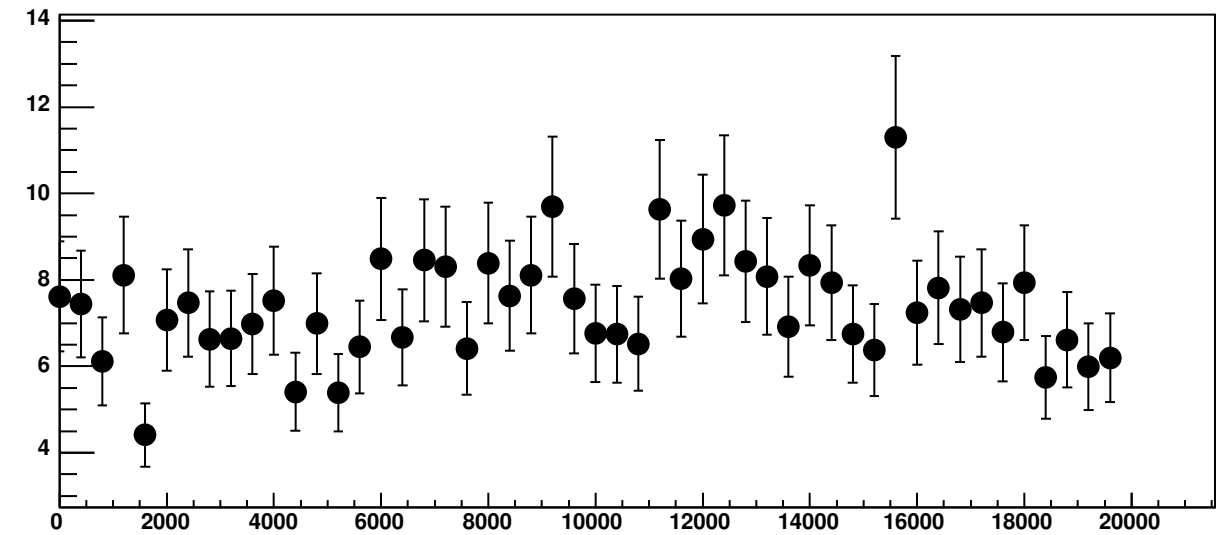

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

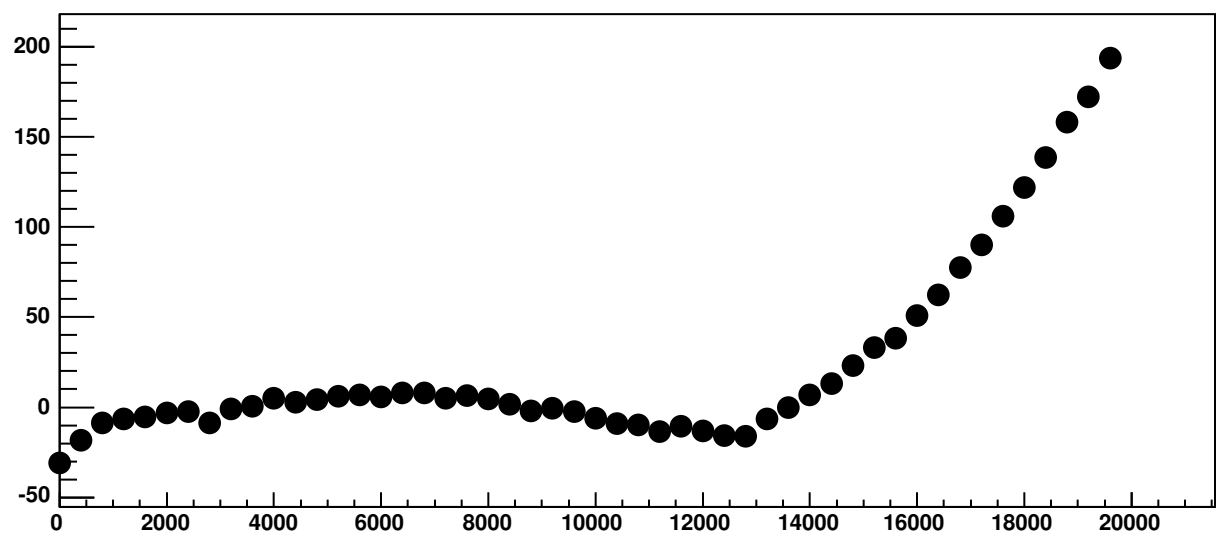
χ^2 / ndf	320.9 / 23
p0	446.4 ± 0.6798
p1	-0.03788 ± 0.0001071

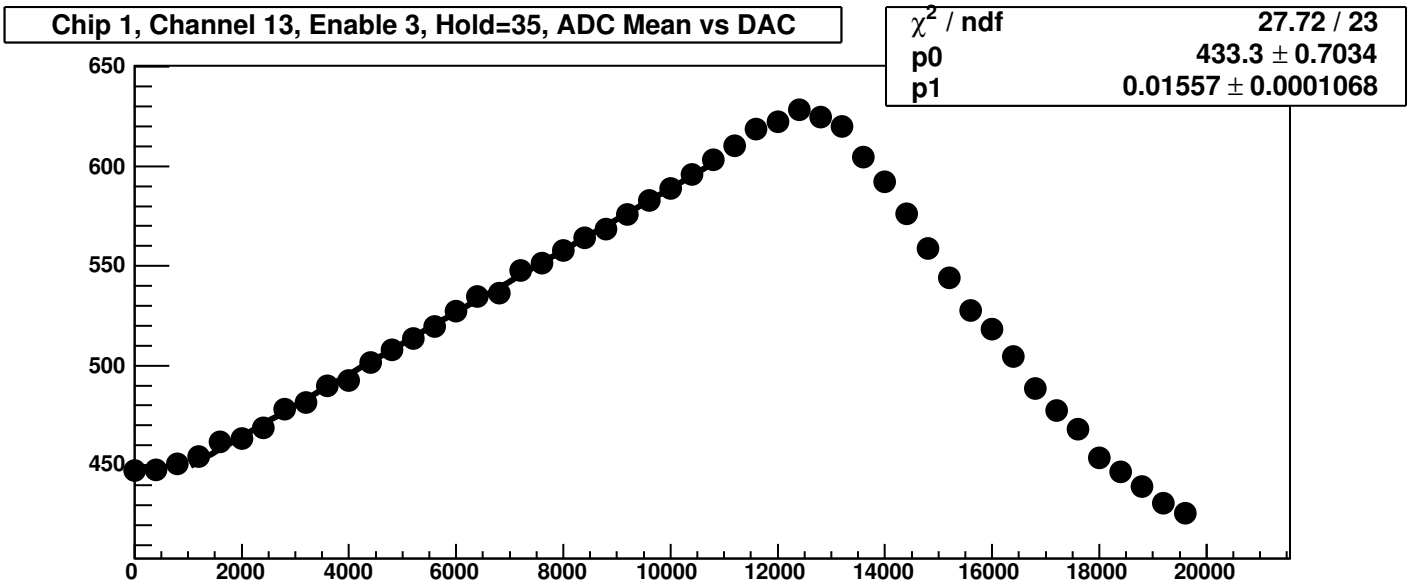


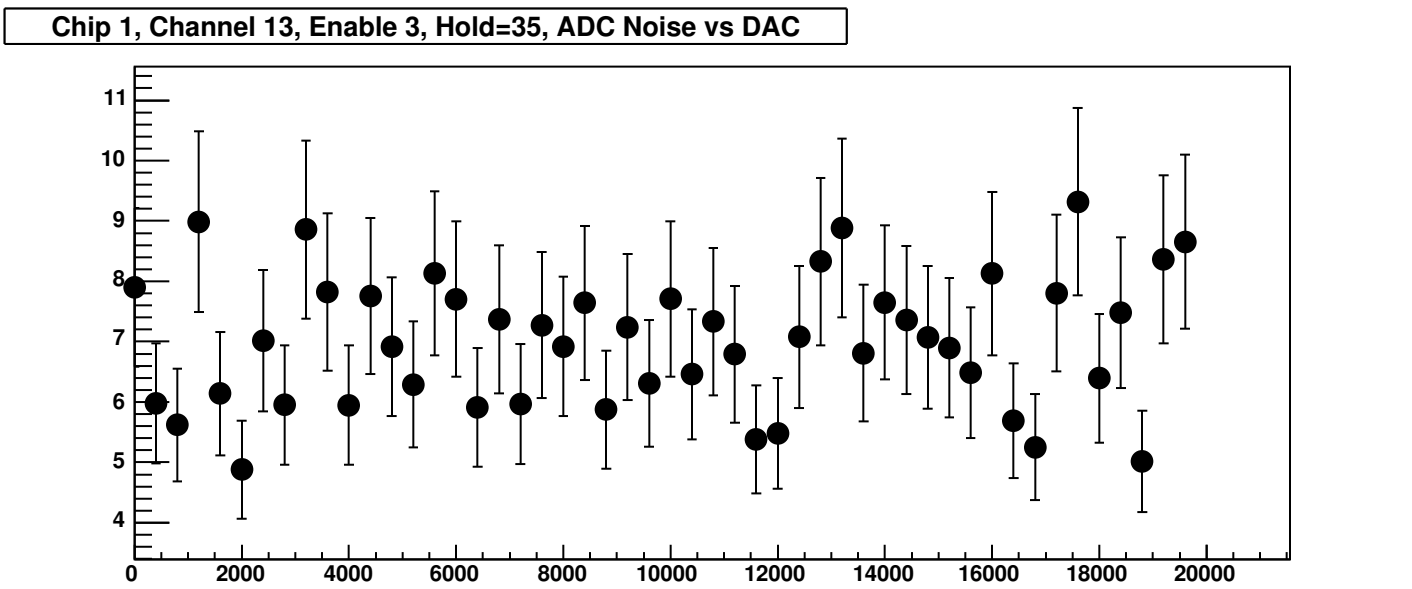
Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC

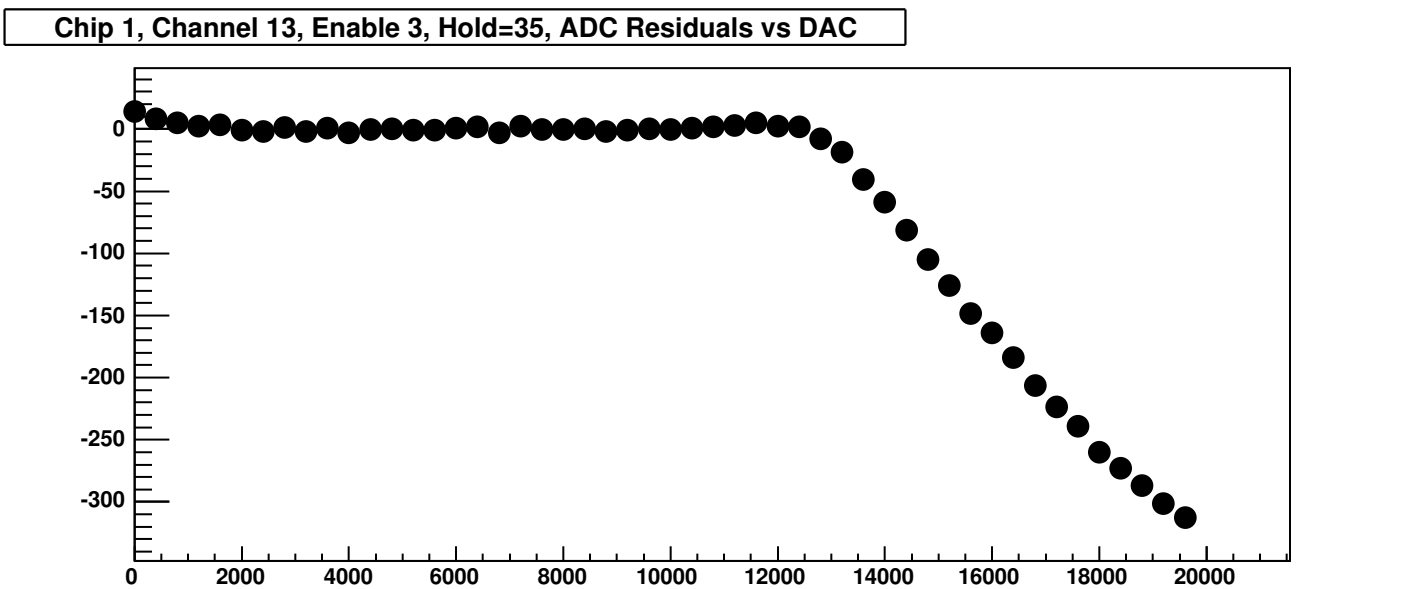


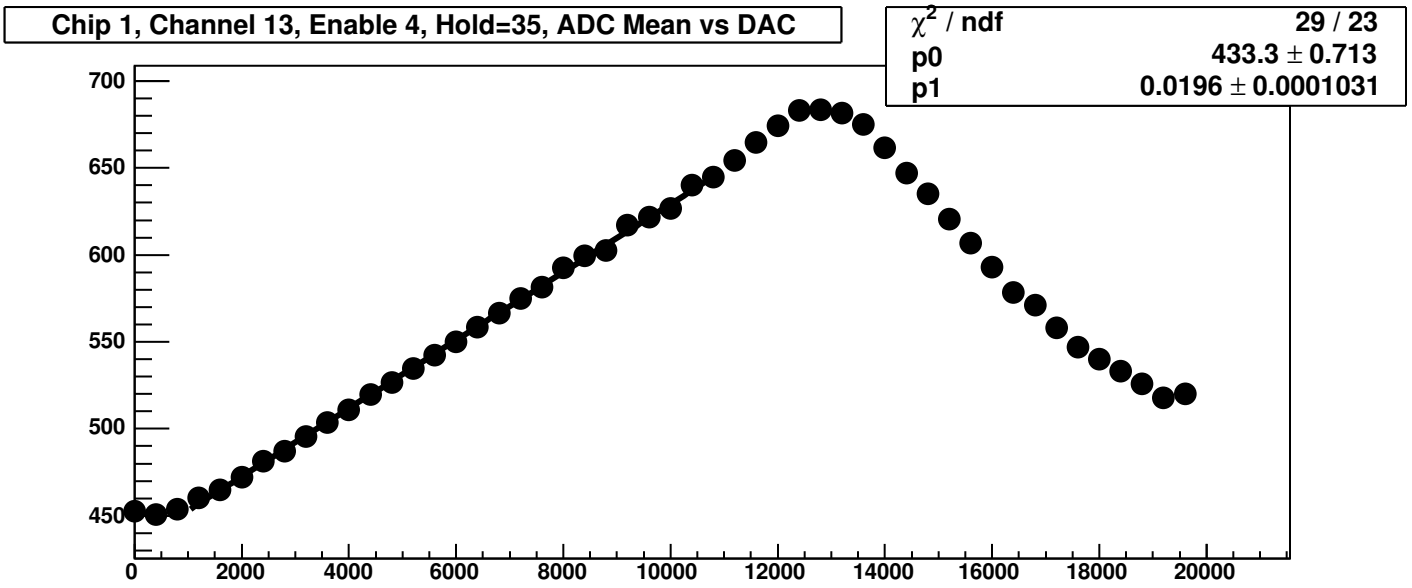
Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

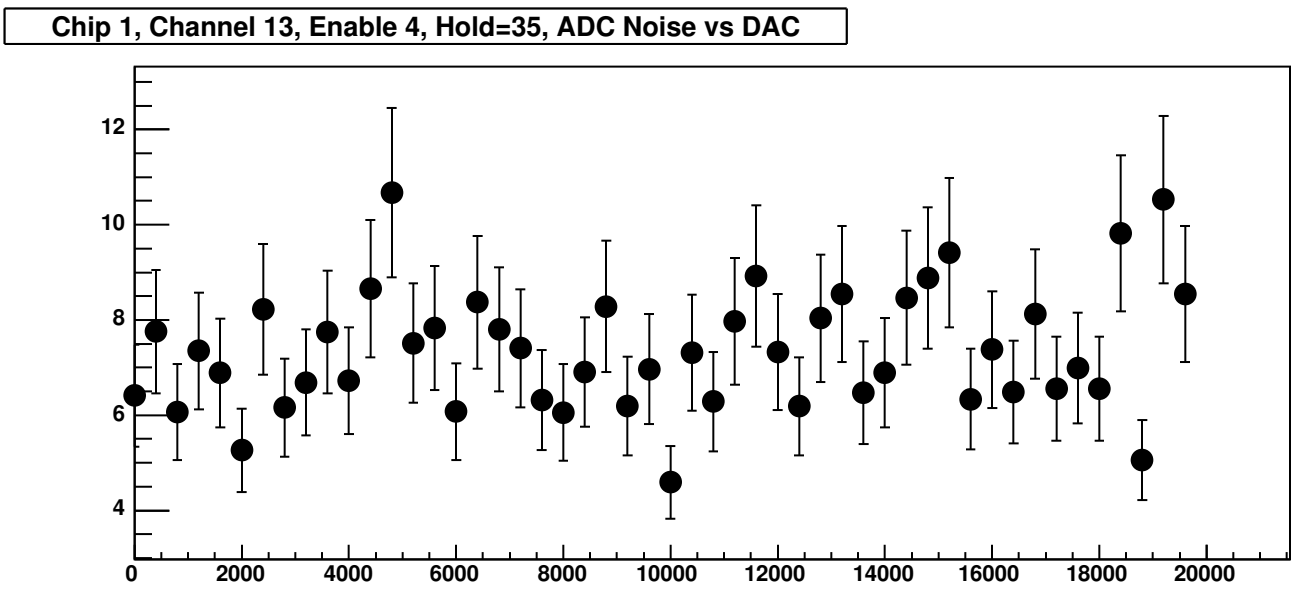


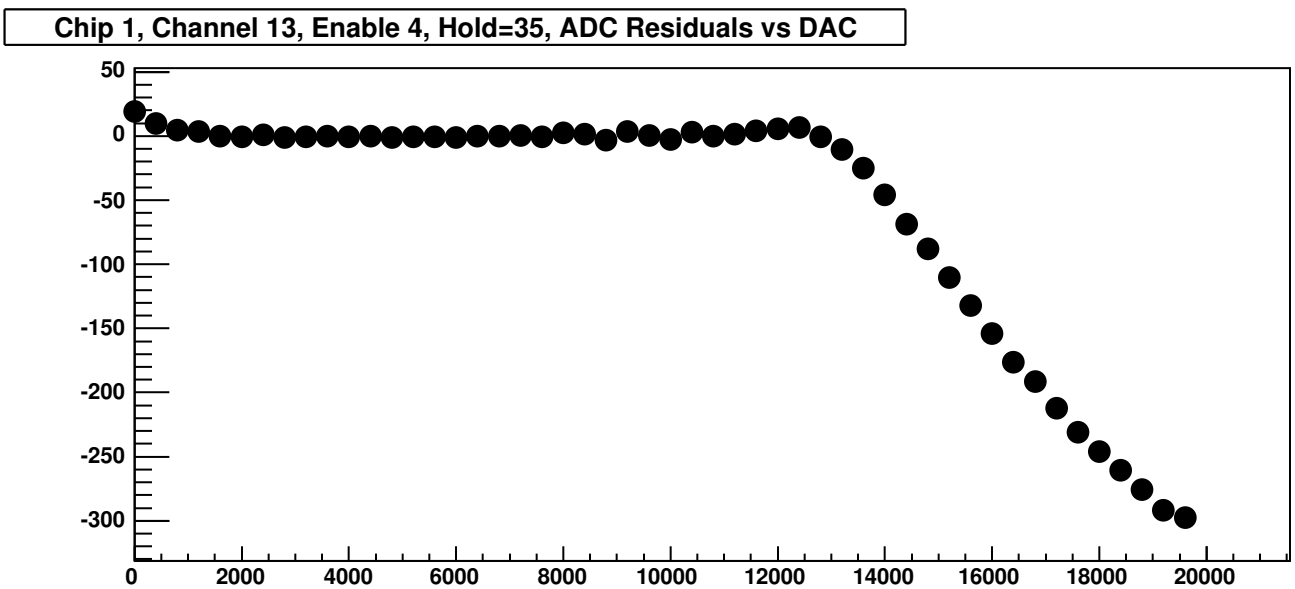

Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.72 / 23
p0 433.3 ± 0.7034
p1 0.01557 ± 0.0001068
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

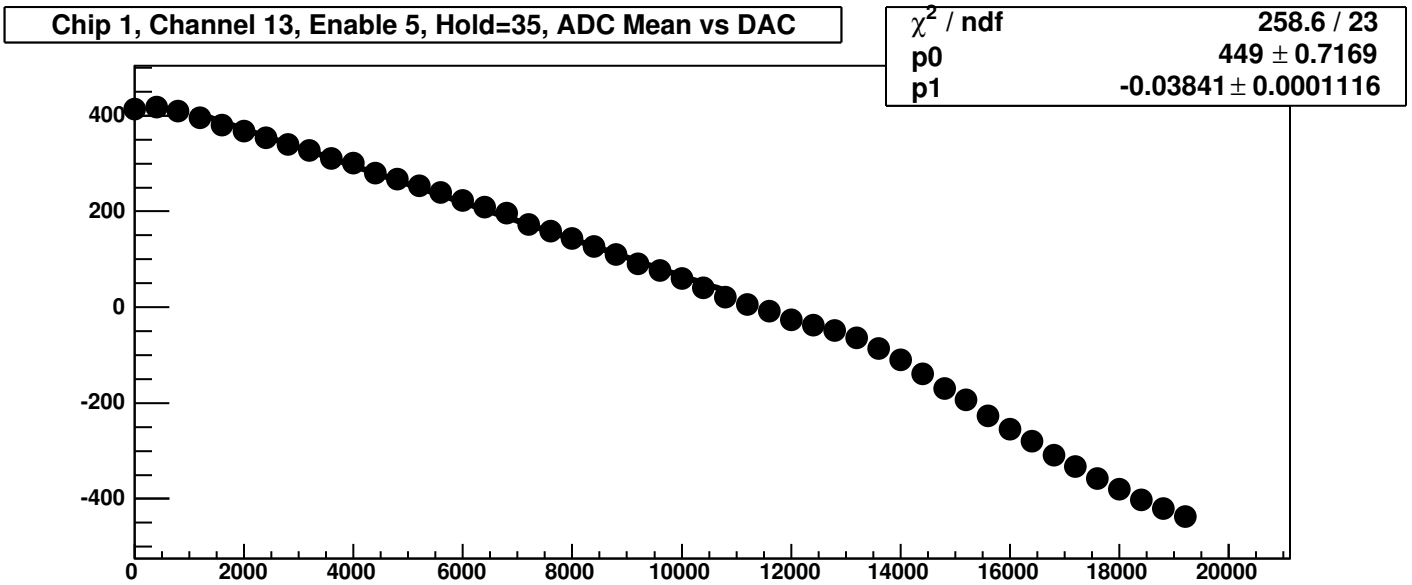

Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

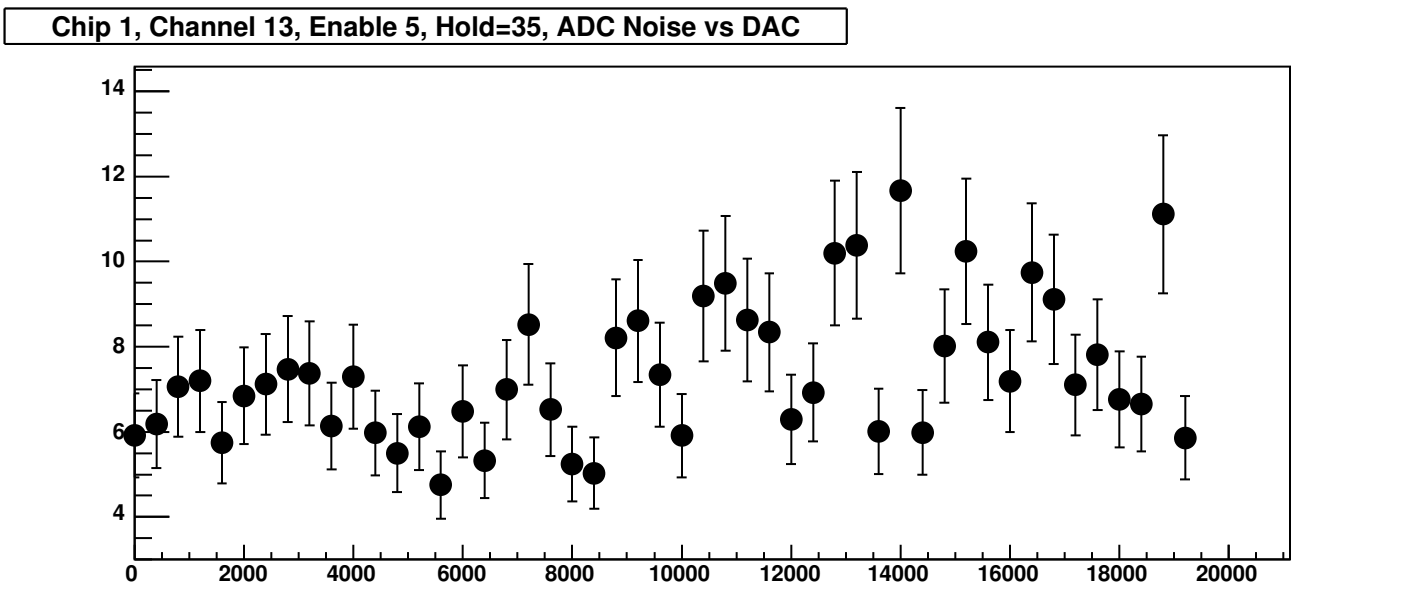

Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

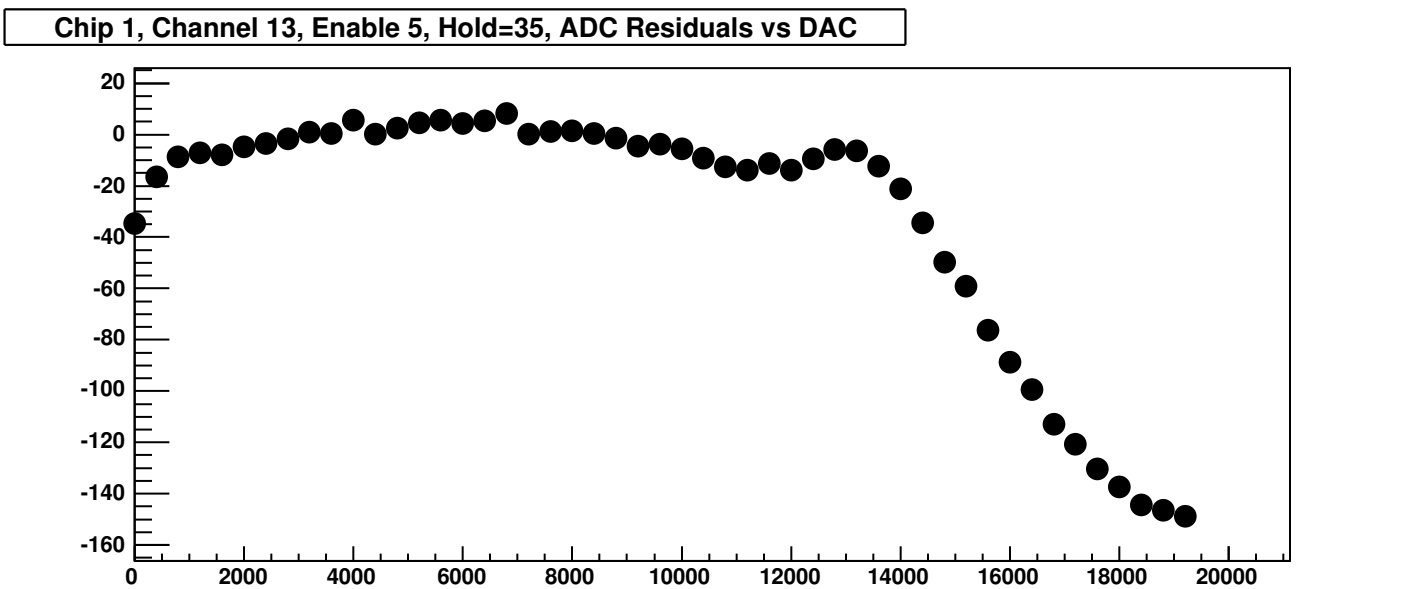

Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29 / 23
p0 433.3 ± 0.713
p1 0.0196 ± 0.0001031
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

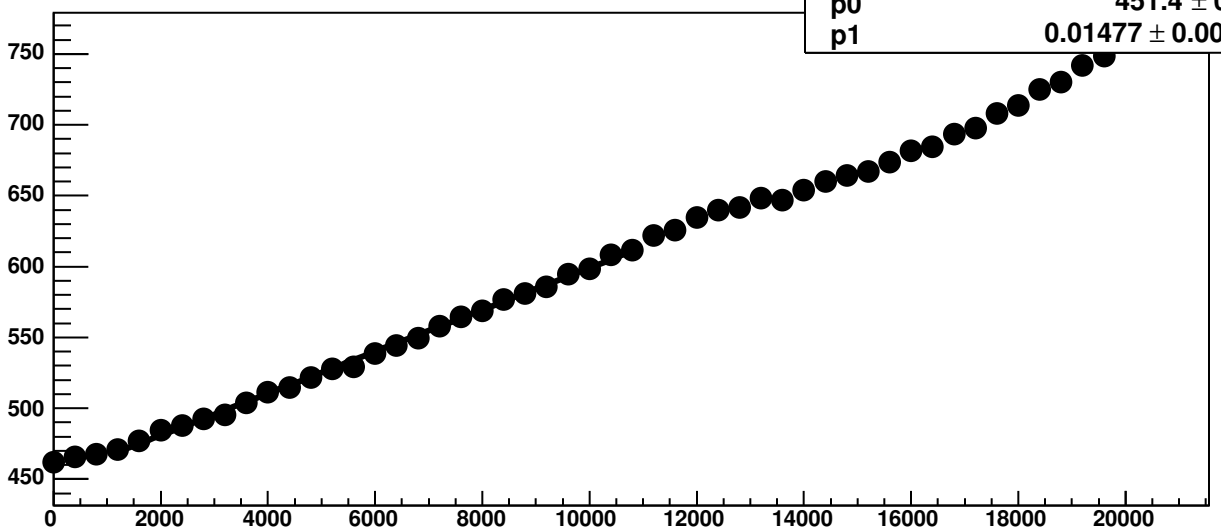

Chip 1, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 258.6 / 23
p0 449 ± 0.7169
p1 -0.03841 ± 0.0001116
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

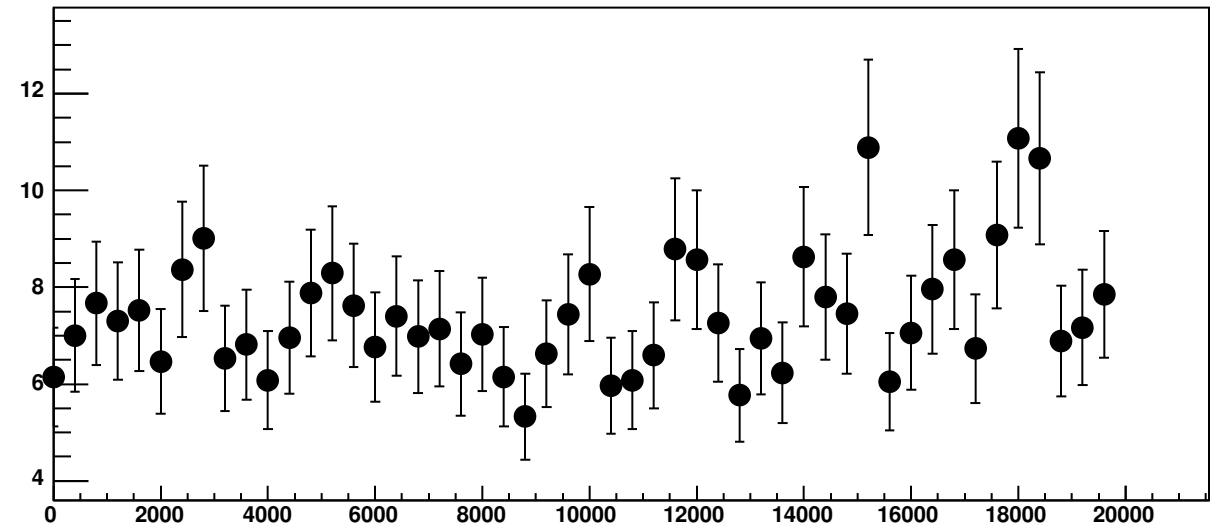

Chip 1, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

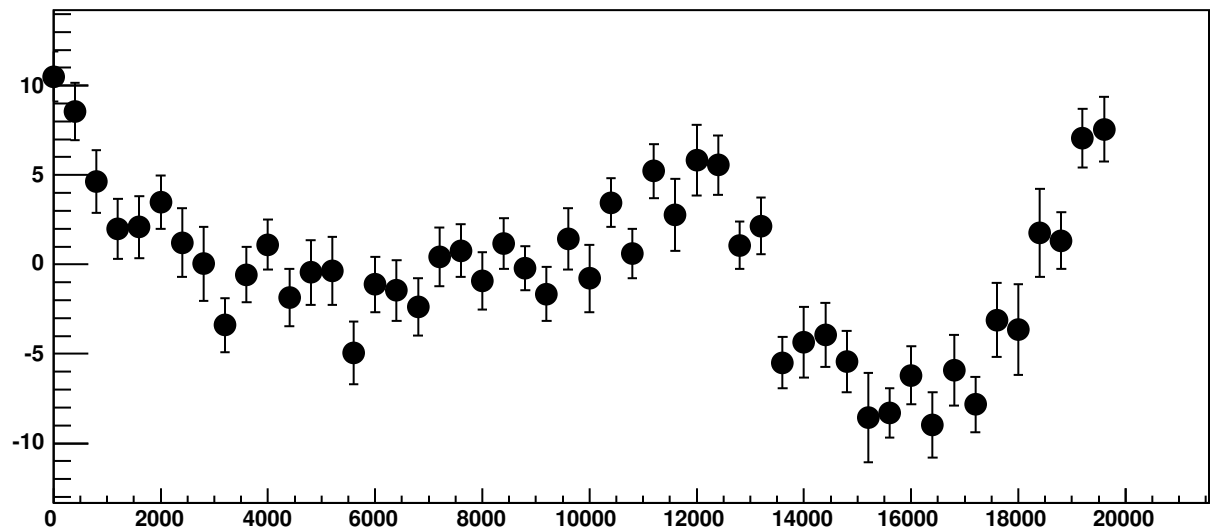
χ^2 / ndf	37.61 / 23
p0	451.4 ± 0.7542
p1	0.01477 ± 0.0001091

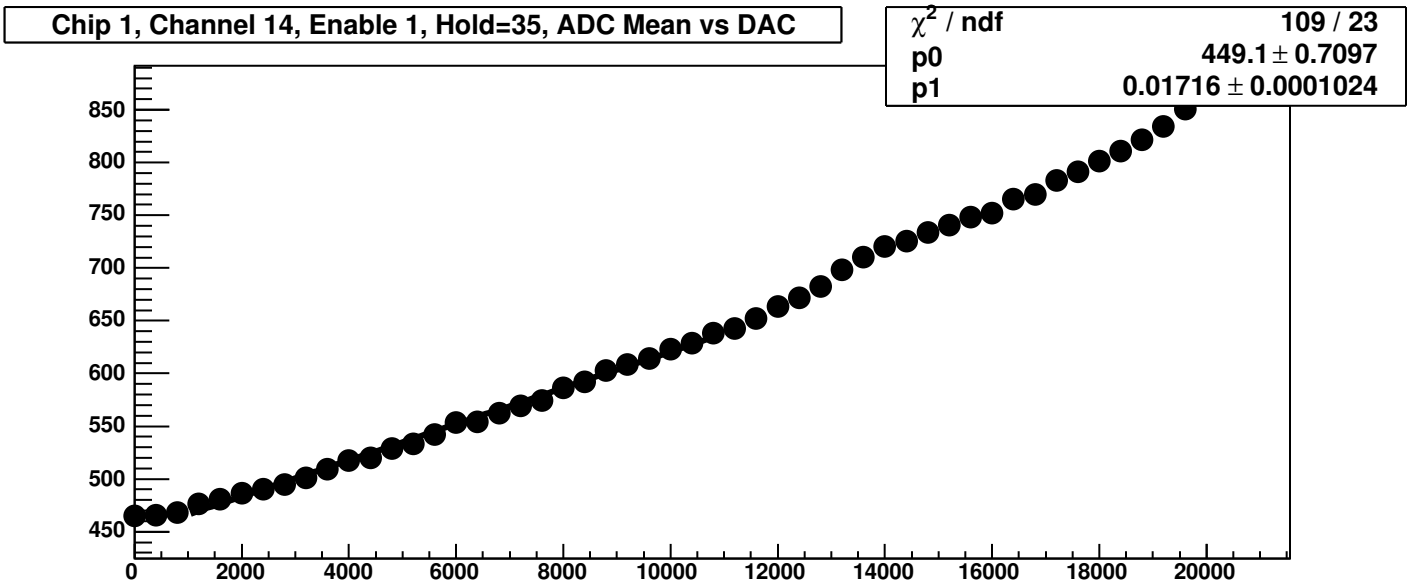


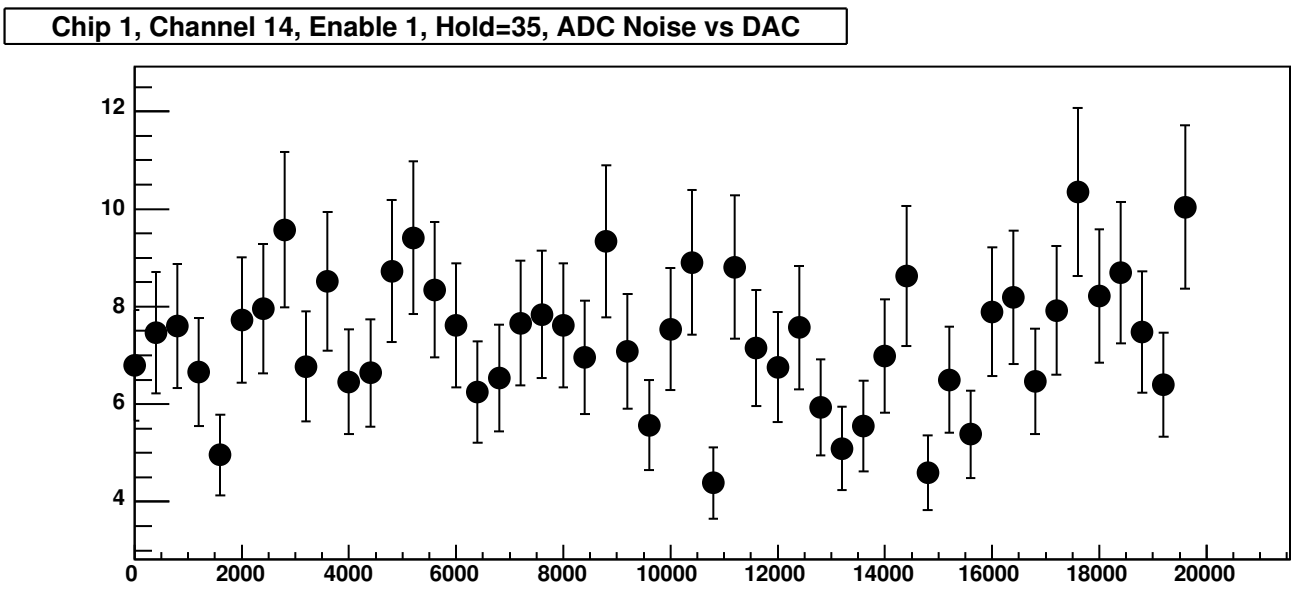
Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

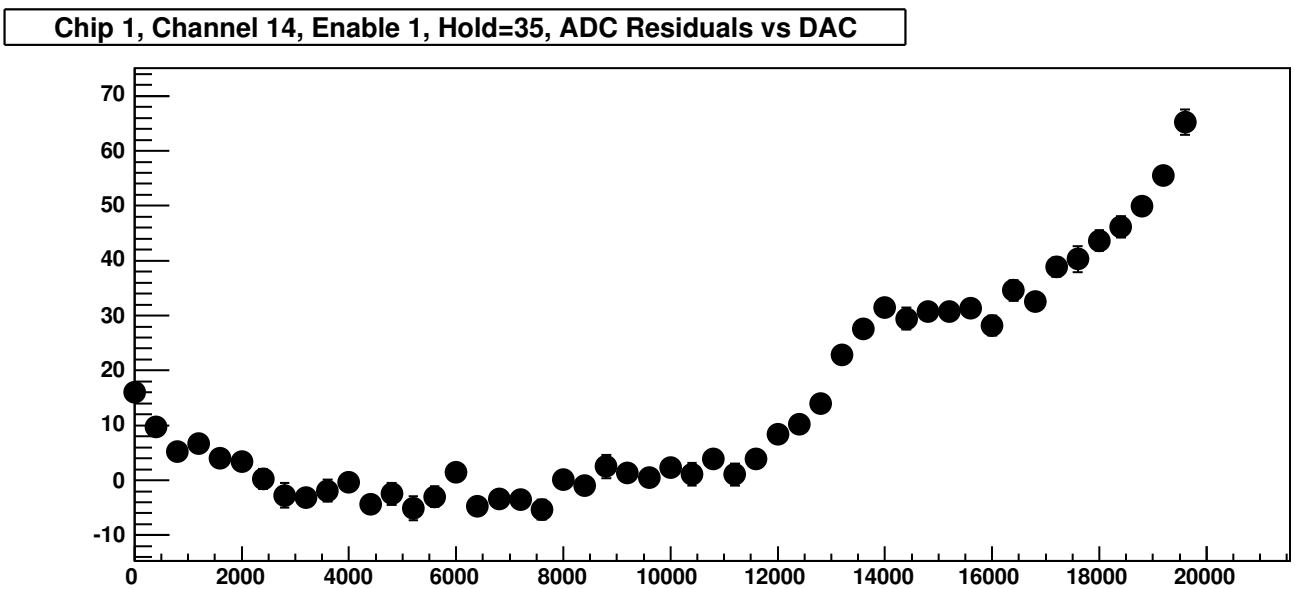


Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



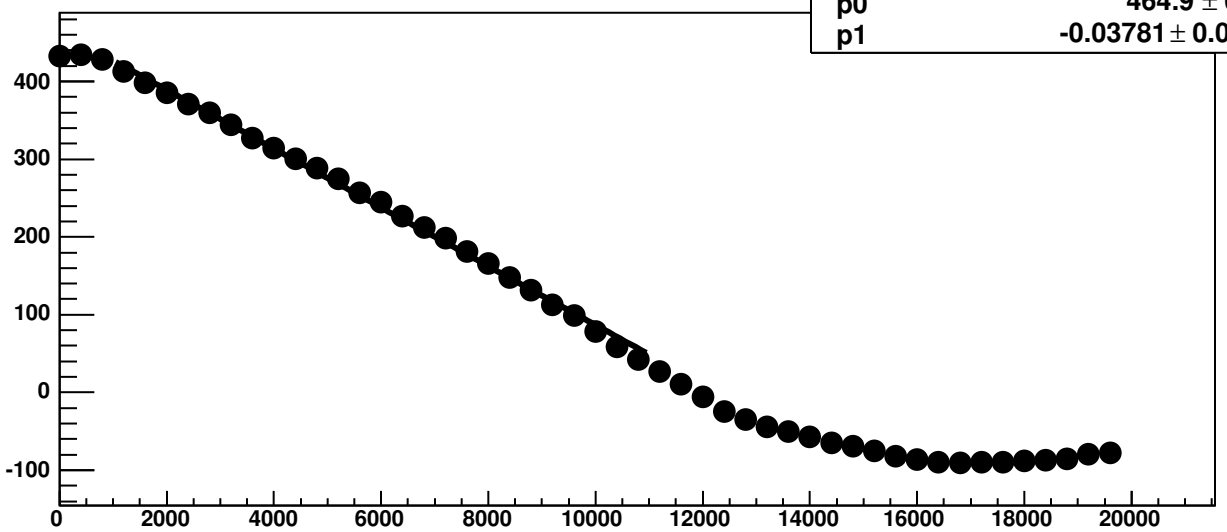

Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 109 / 23
p0 449.1 ± 0.7097
p1 0.01716 ± 0.0001024
850
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

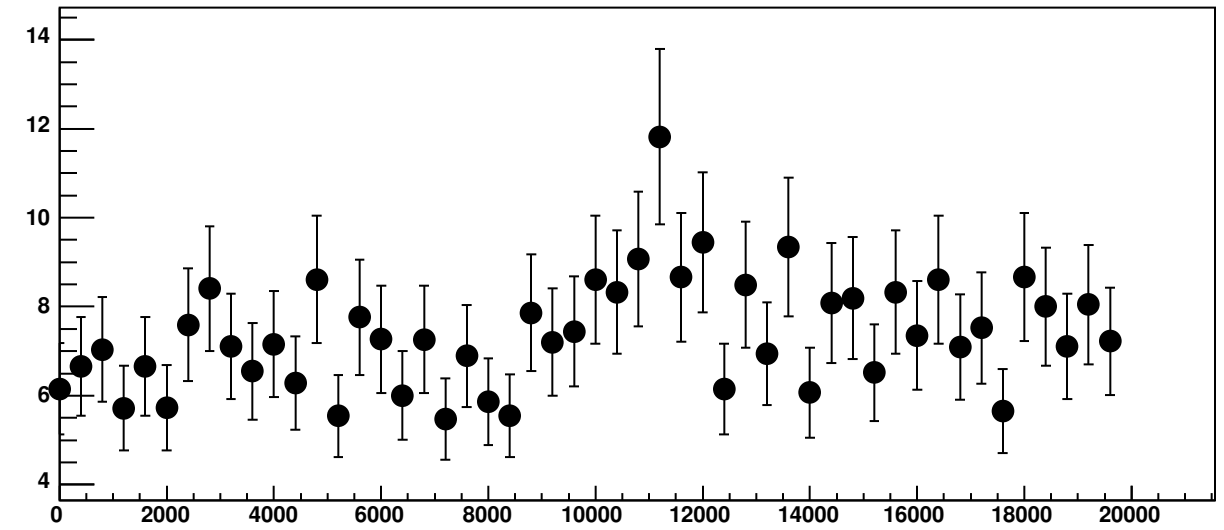

Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
70
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

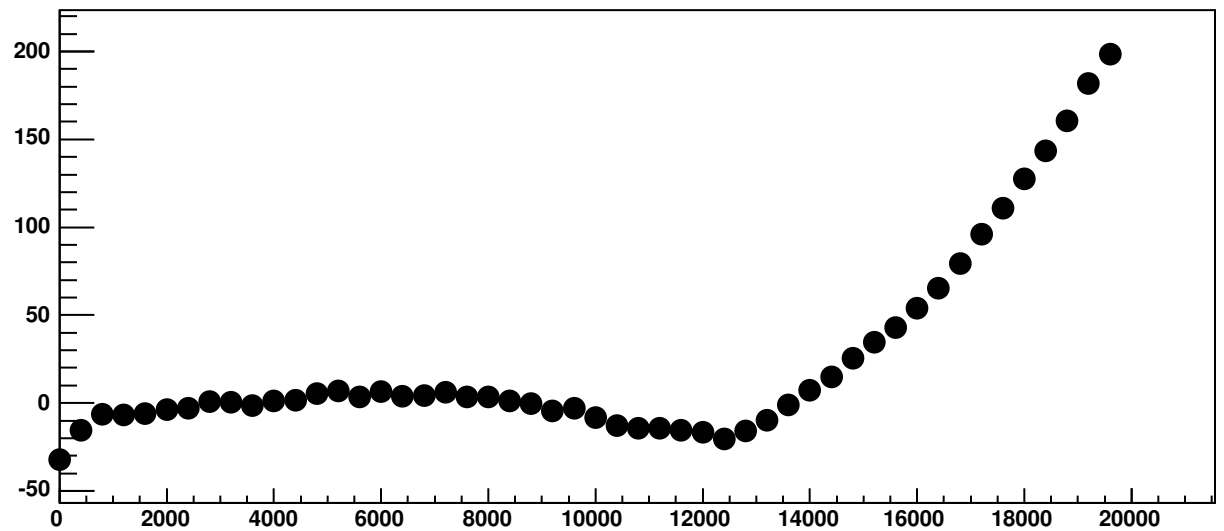
χ^2 / ndf	283 / 23
p0	464.9 ± 0.7161
p1	-0.03781 ± 0.000112

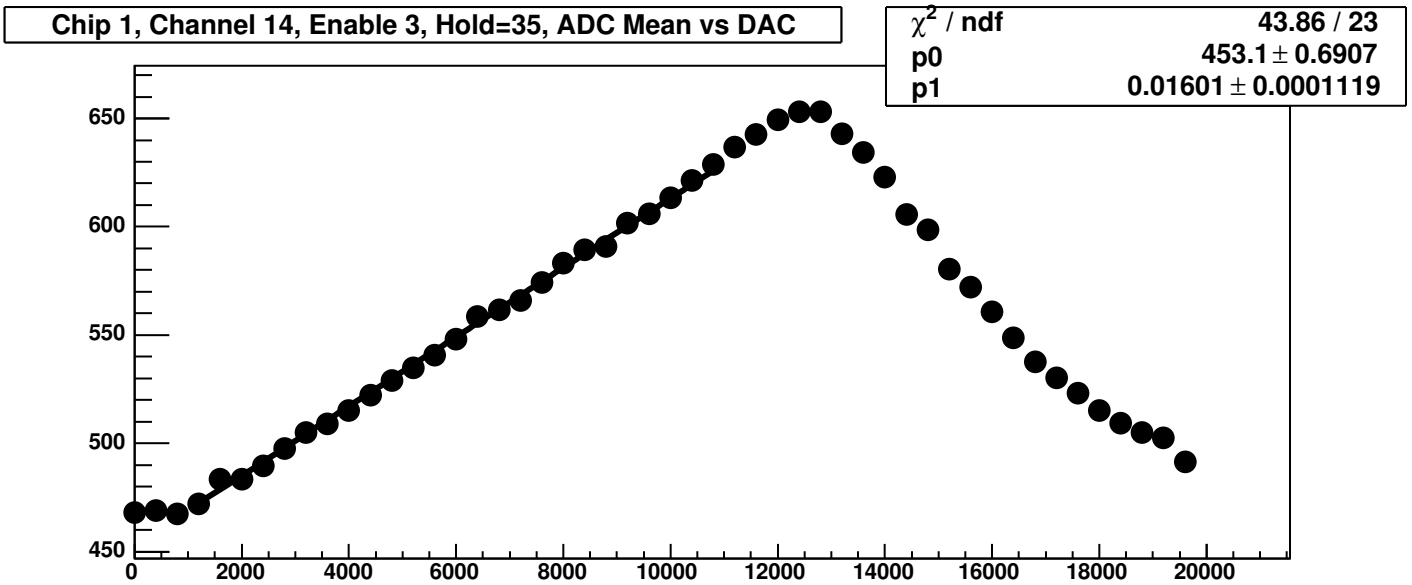


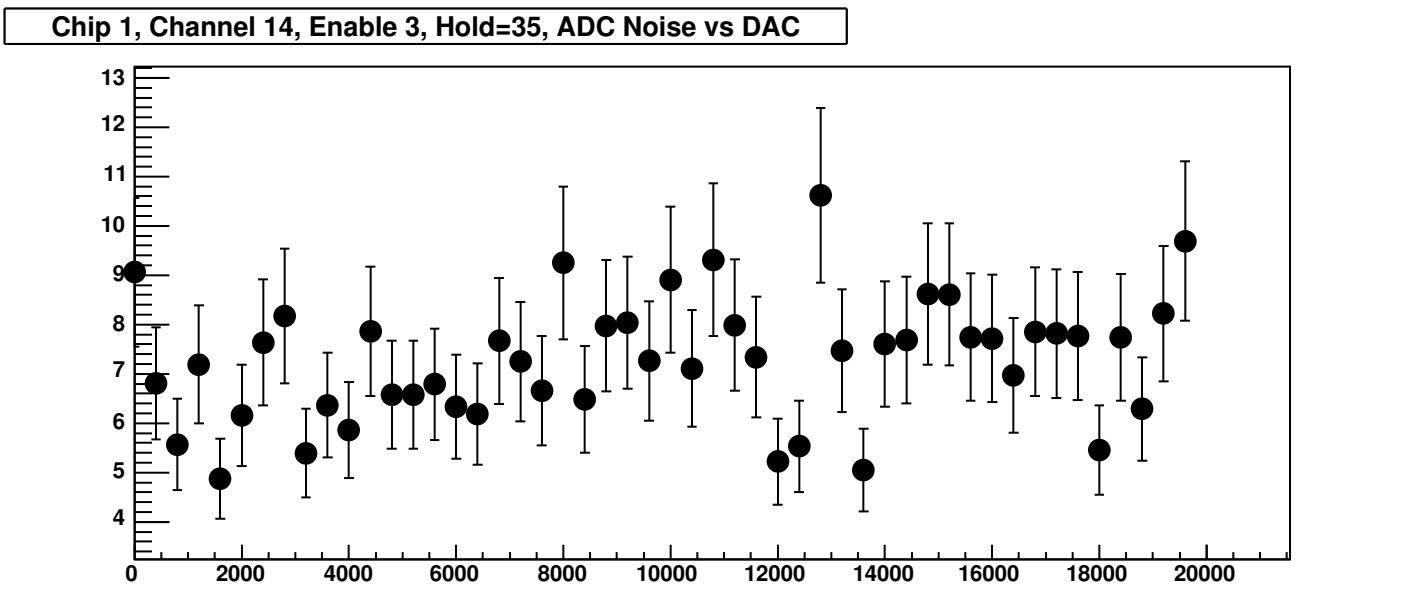
Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

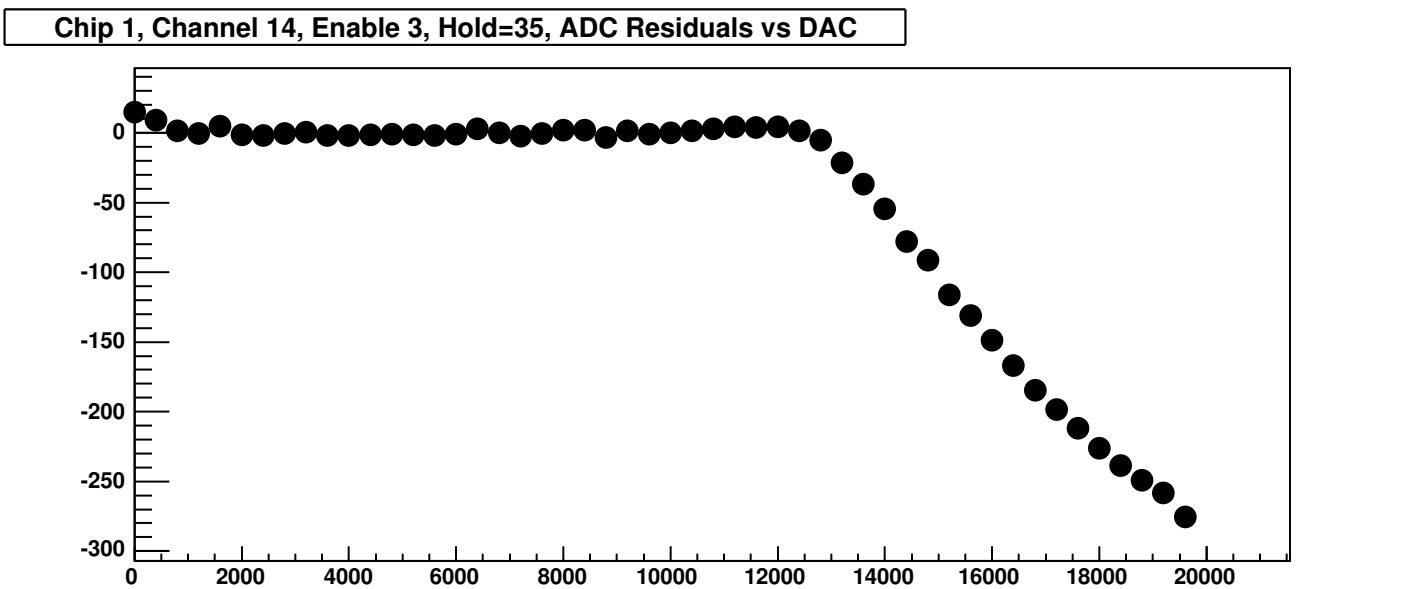


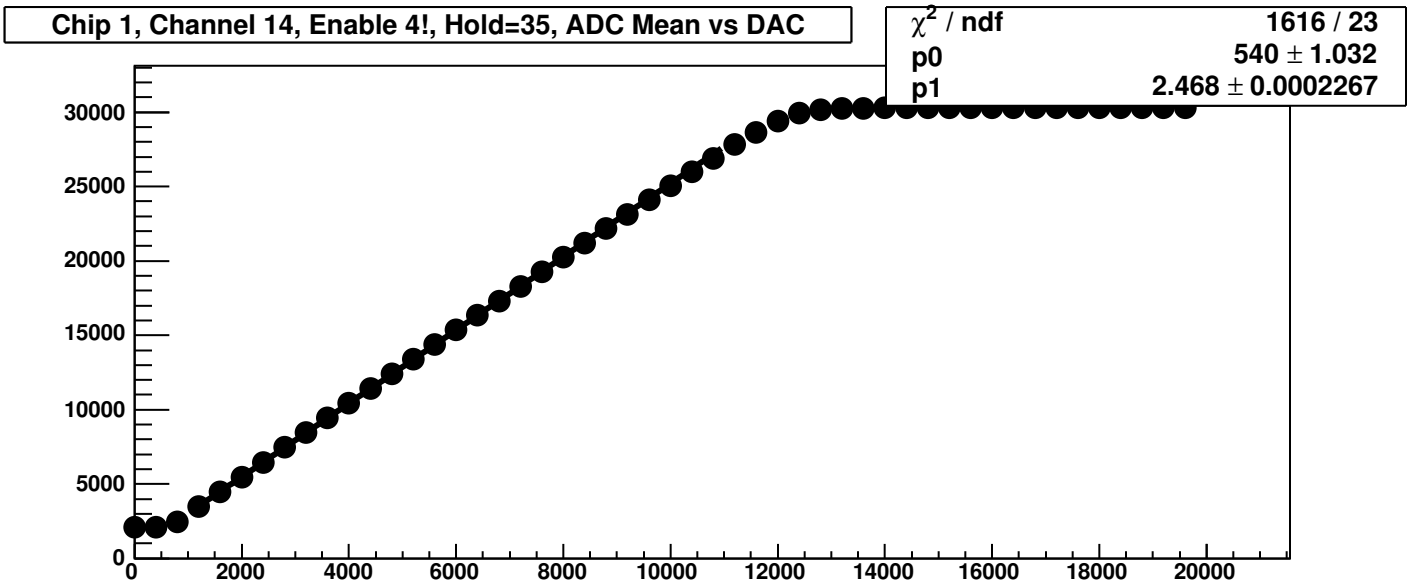
Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

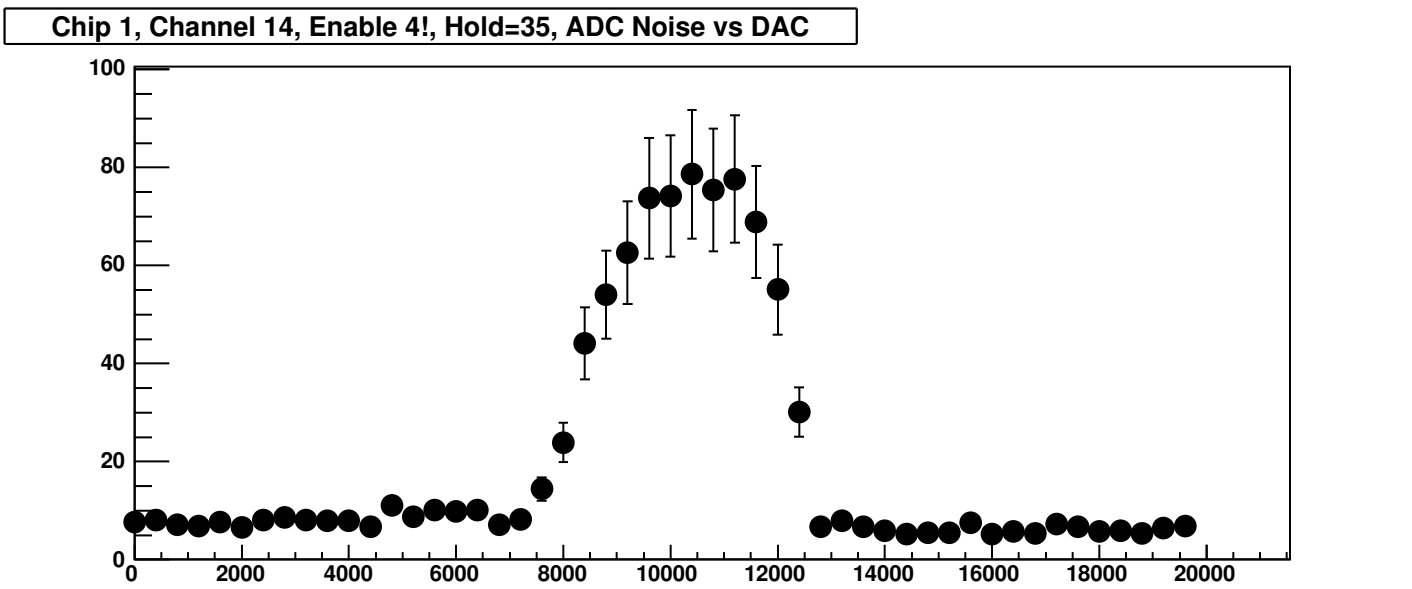


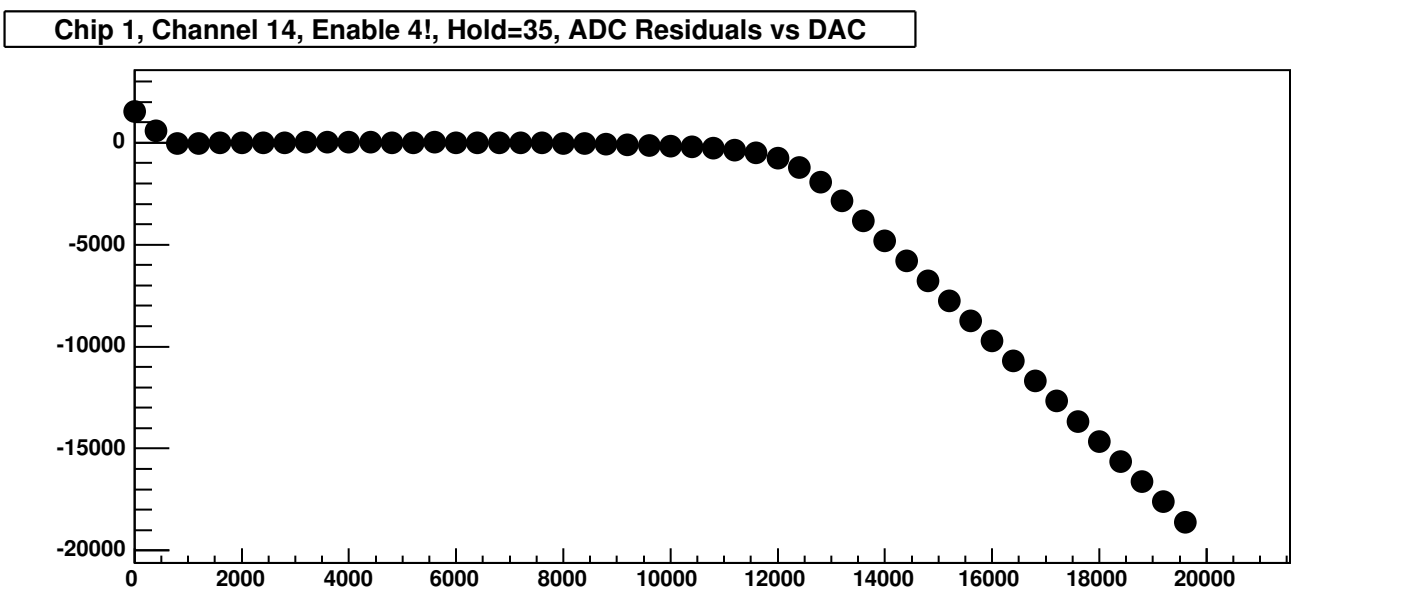

Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 43.86 / 23
p0 453.1 ± 0.6907
p1 0.01601 ± 0.0001119
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

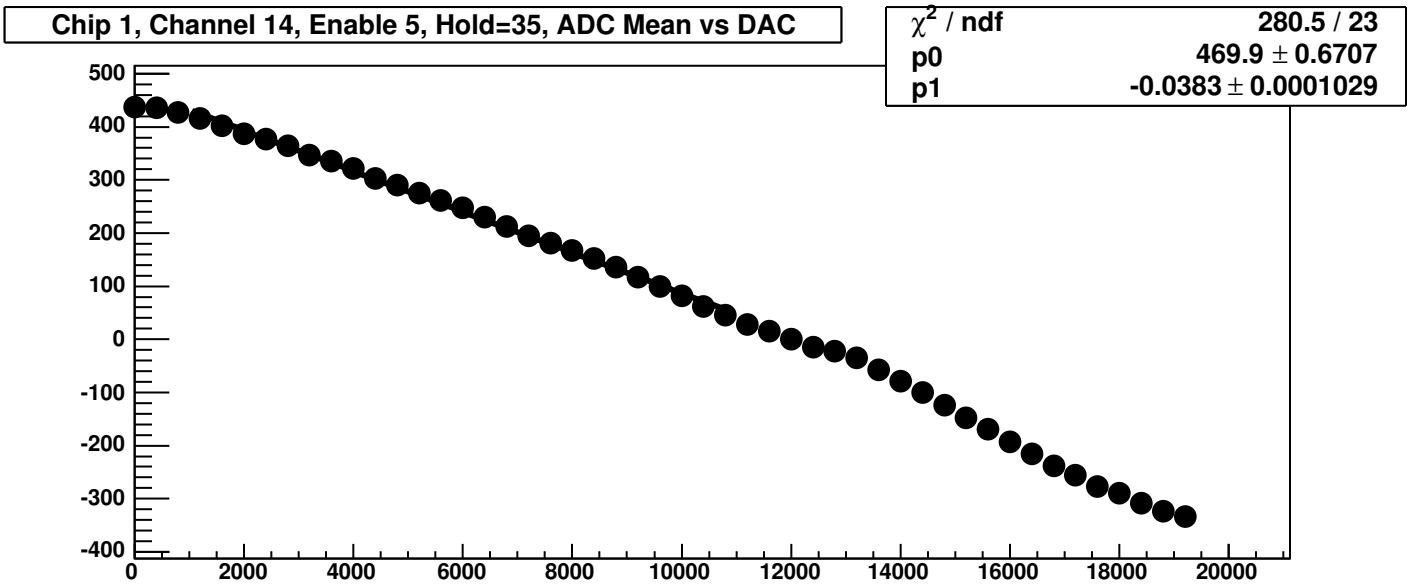

Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

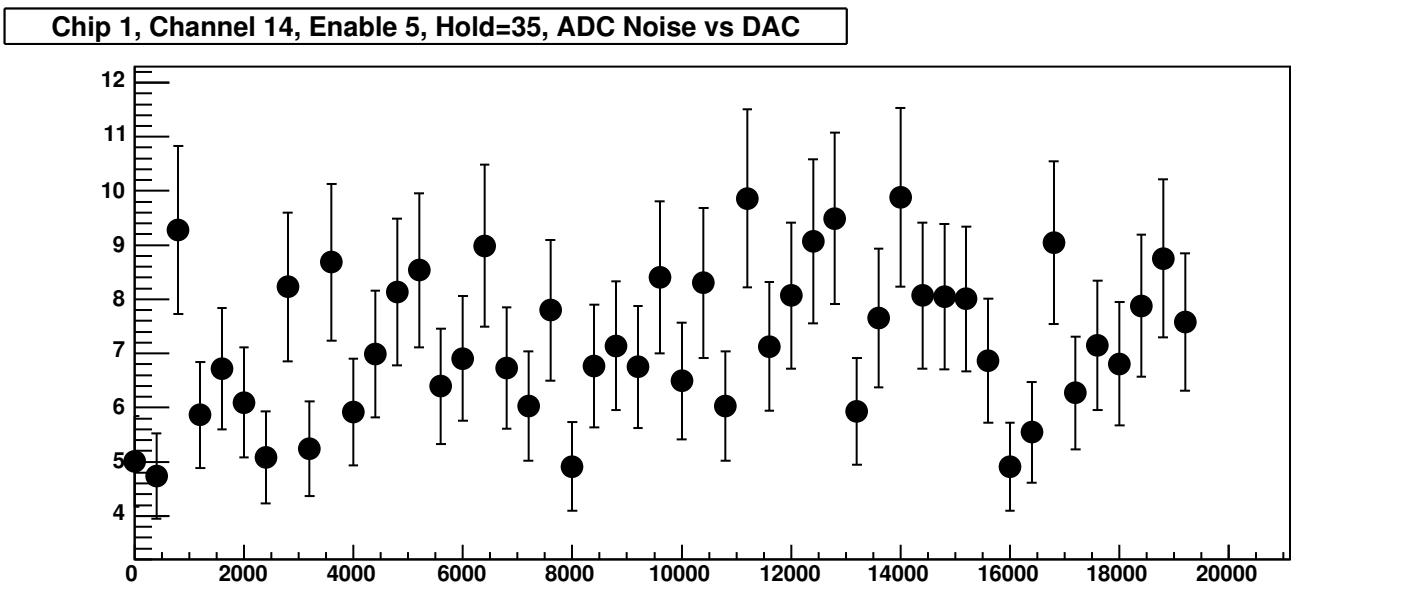

Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

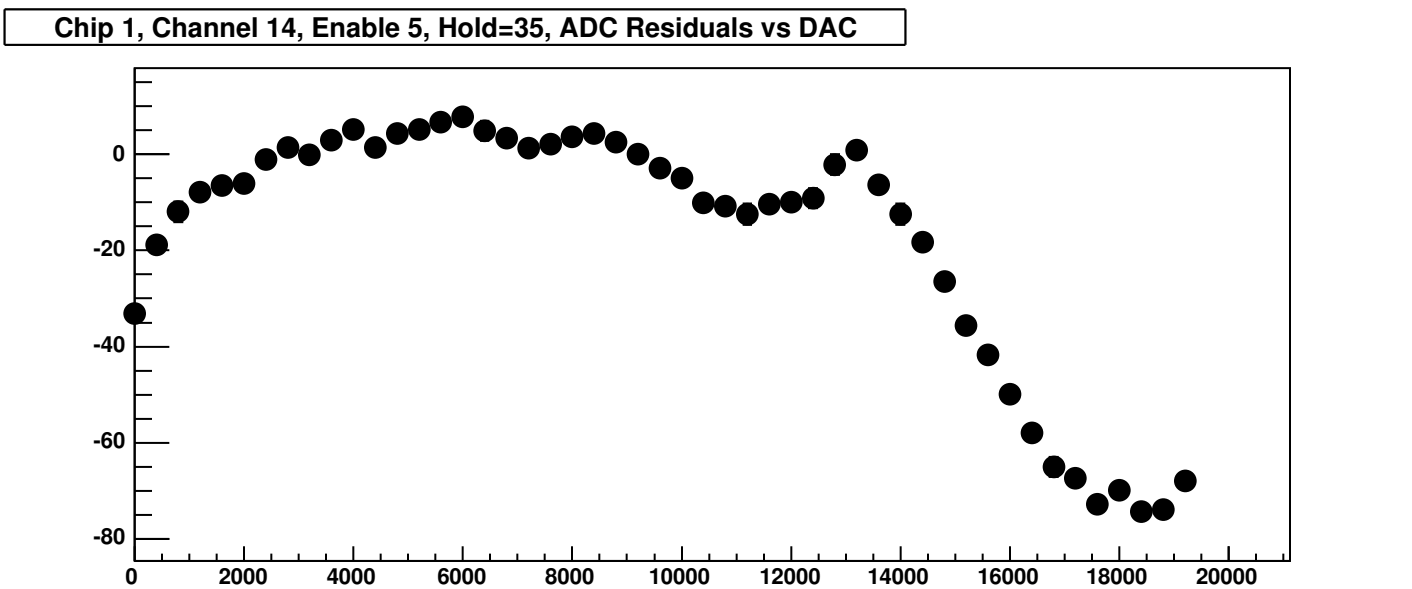

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1616 / 23
p0 540 ± 1.032
p1 2.468 ± 0.0002267
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

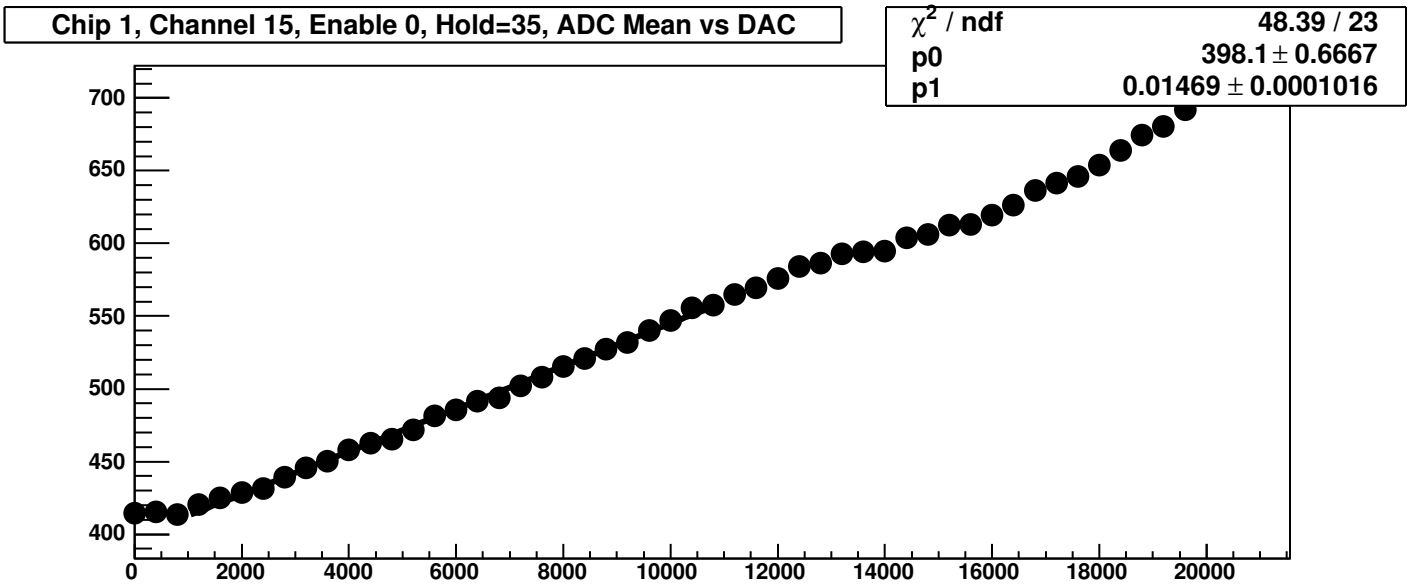

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

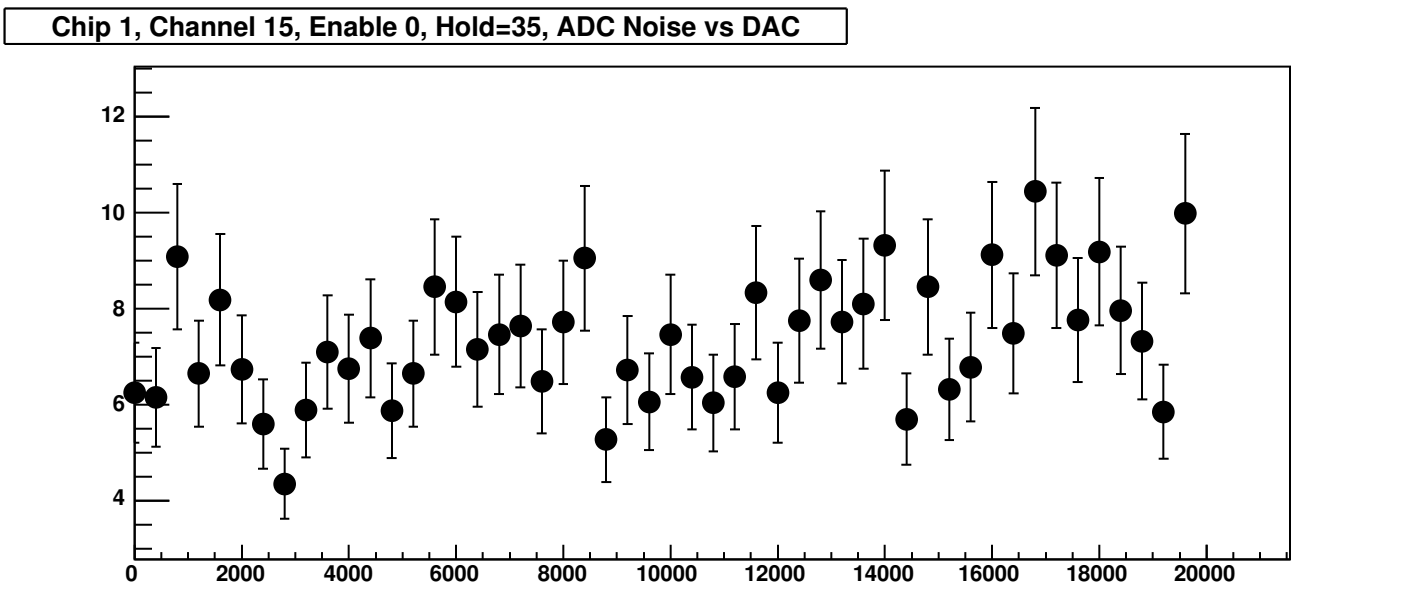

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

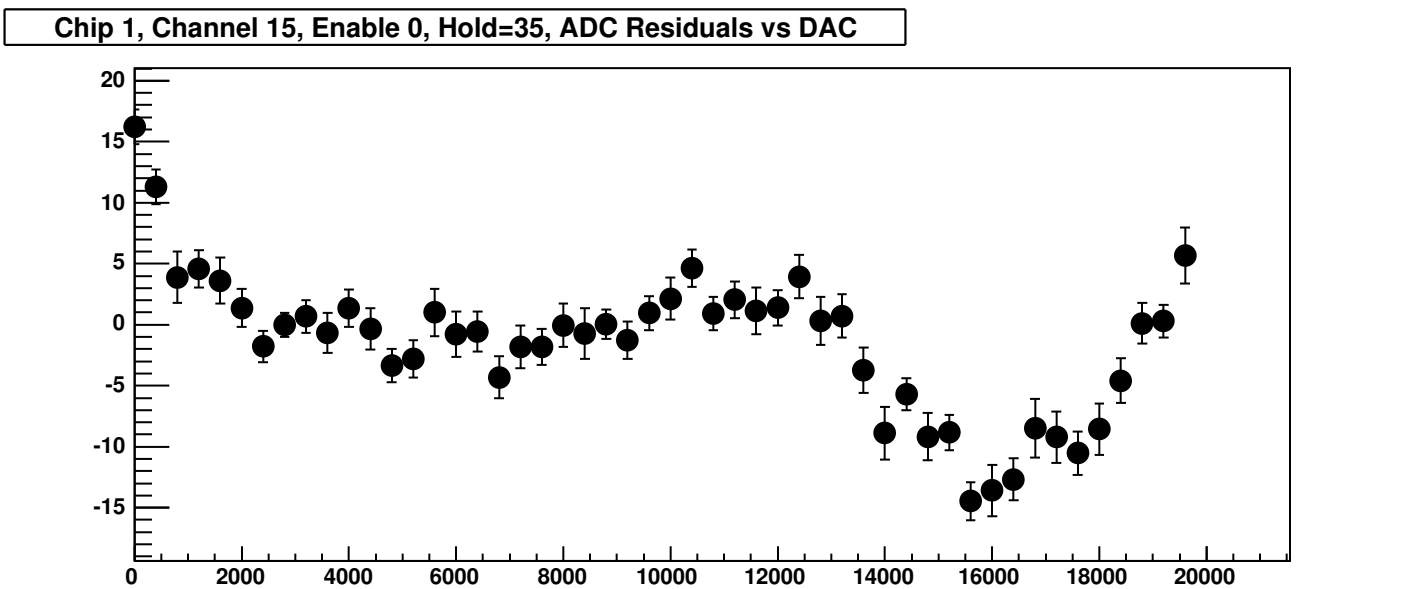

Chip 1, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 280.5 / 23
p0 469.9 ± 0.6707
p1 -0.0383 ± 0.0001029
500
400
300
200
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

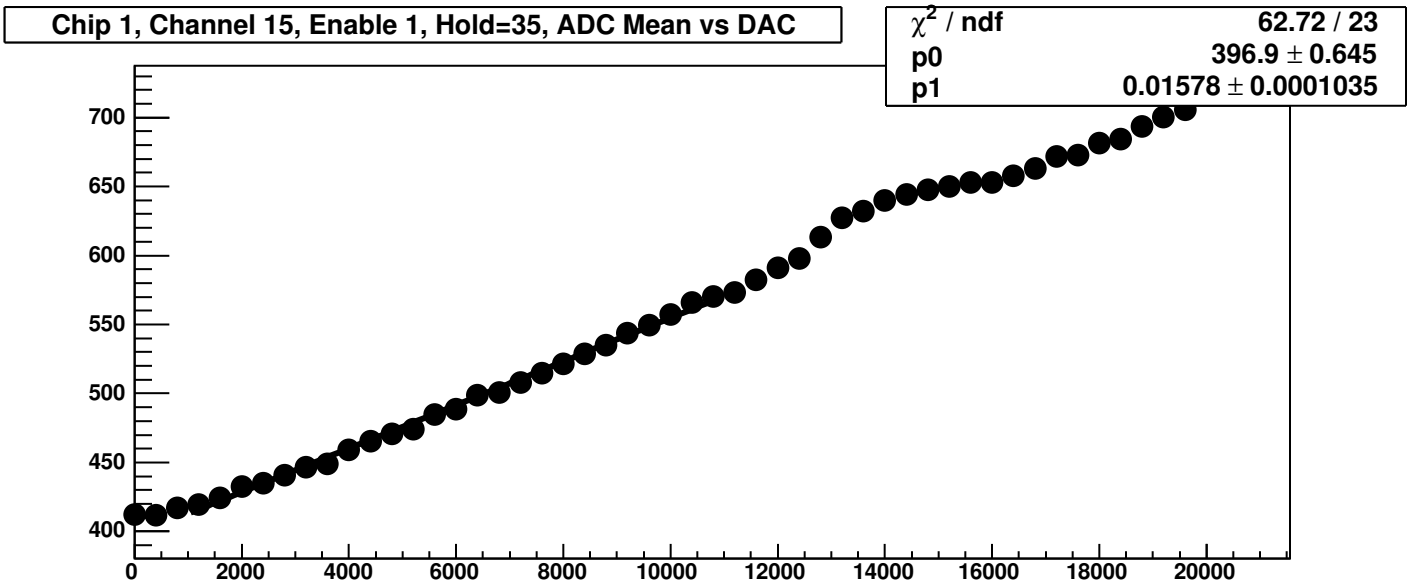

Chip 1, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

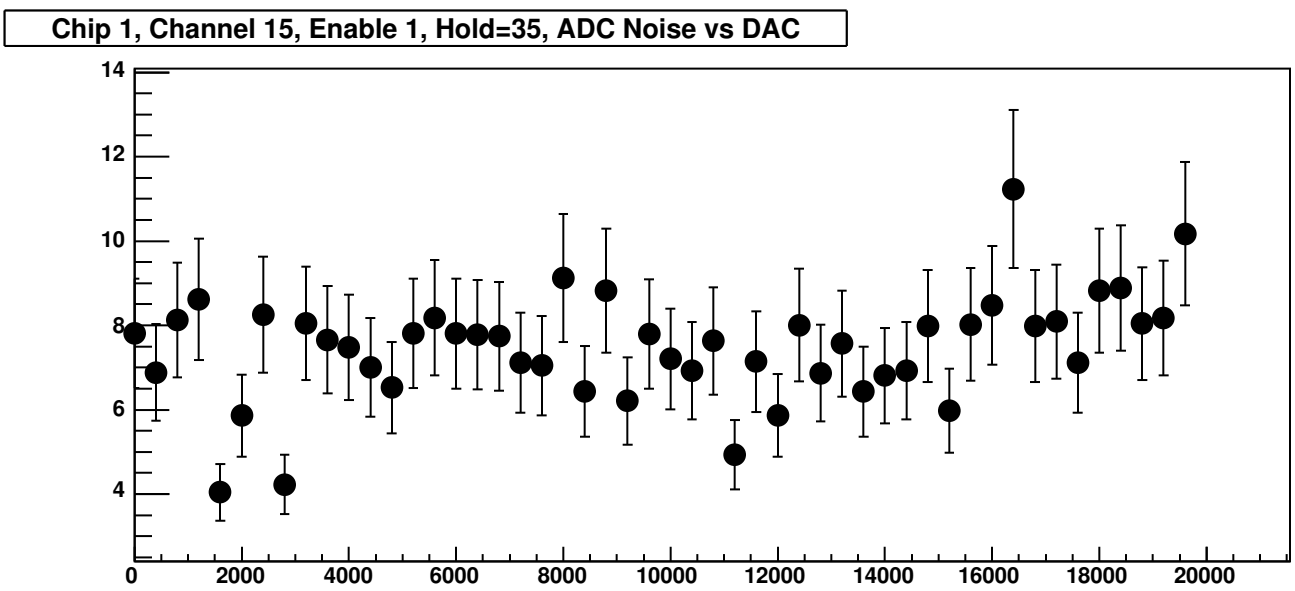

Chip 1, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

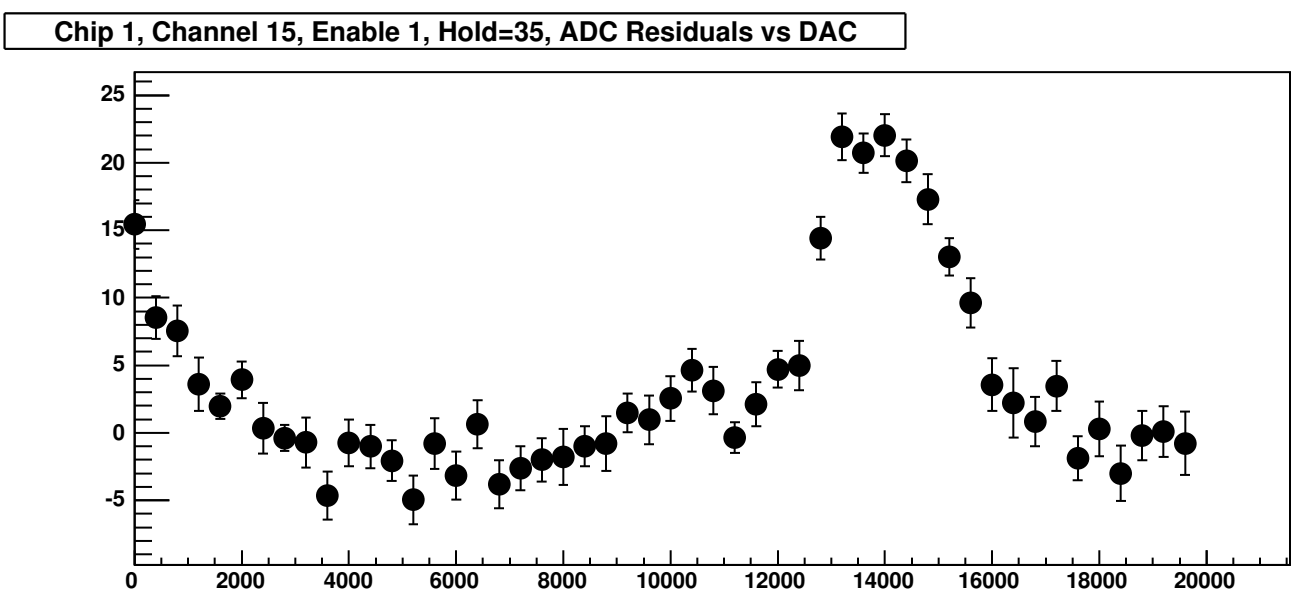

Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 48.39 / 23
p0 398.1 ± 0.6667
p1 0.01469 ± 0.0001016
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

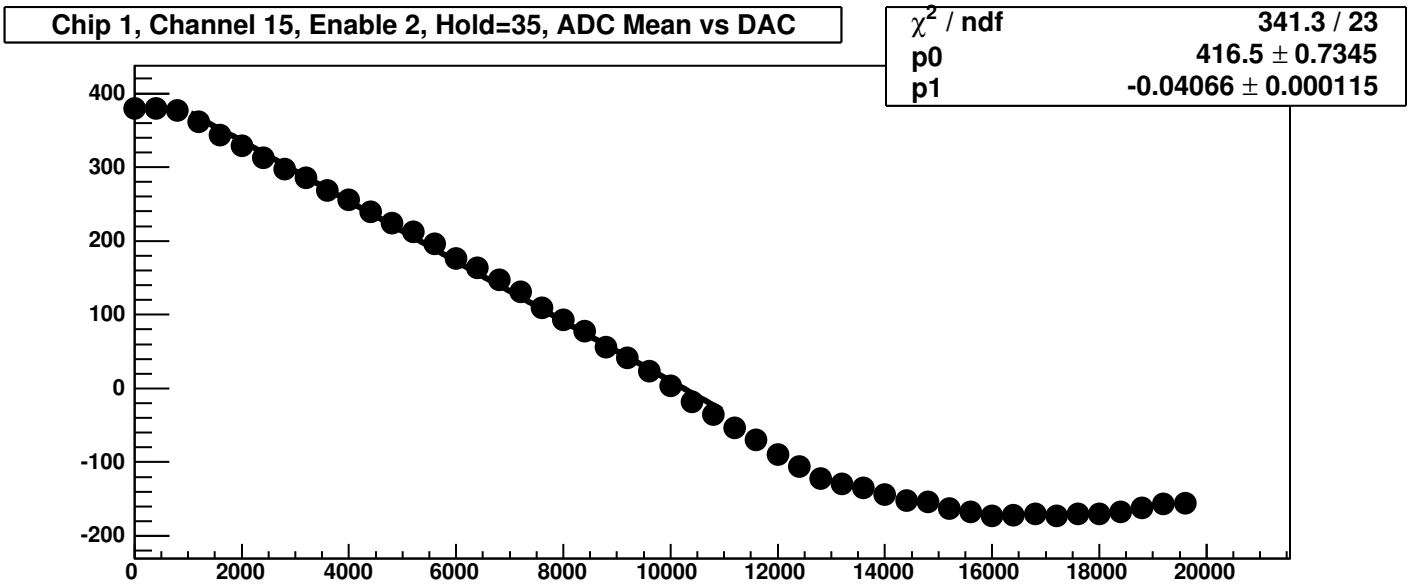

Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

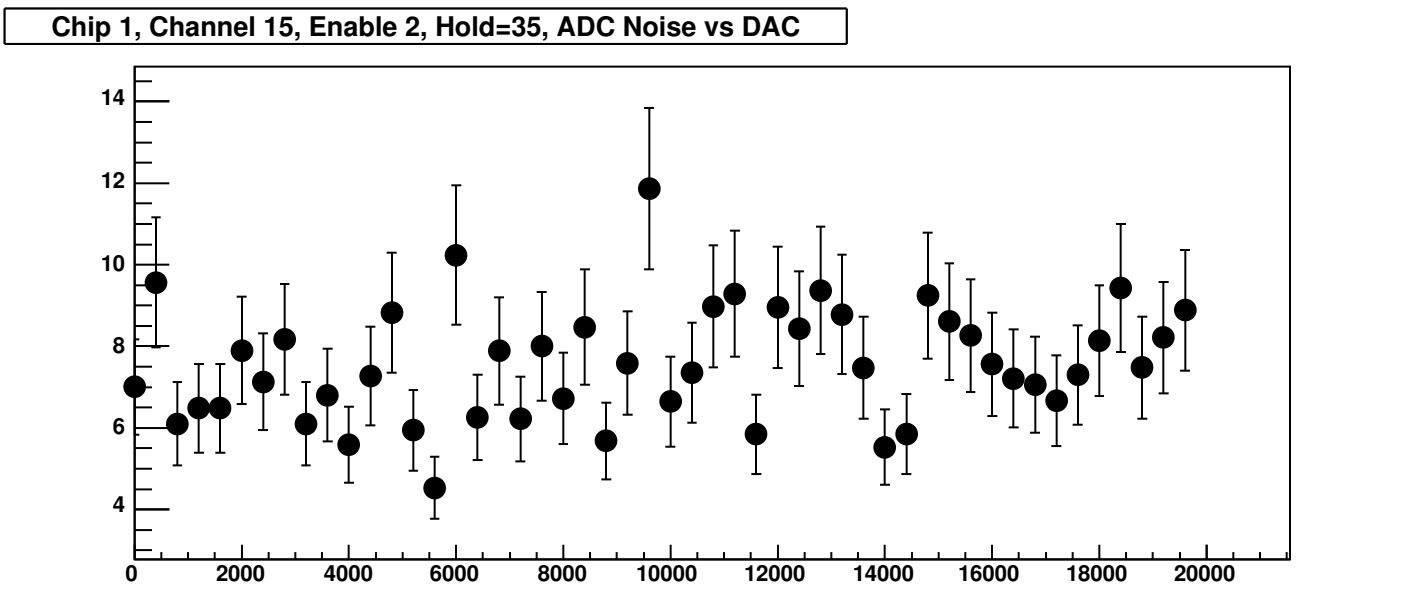

Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

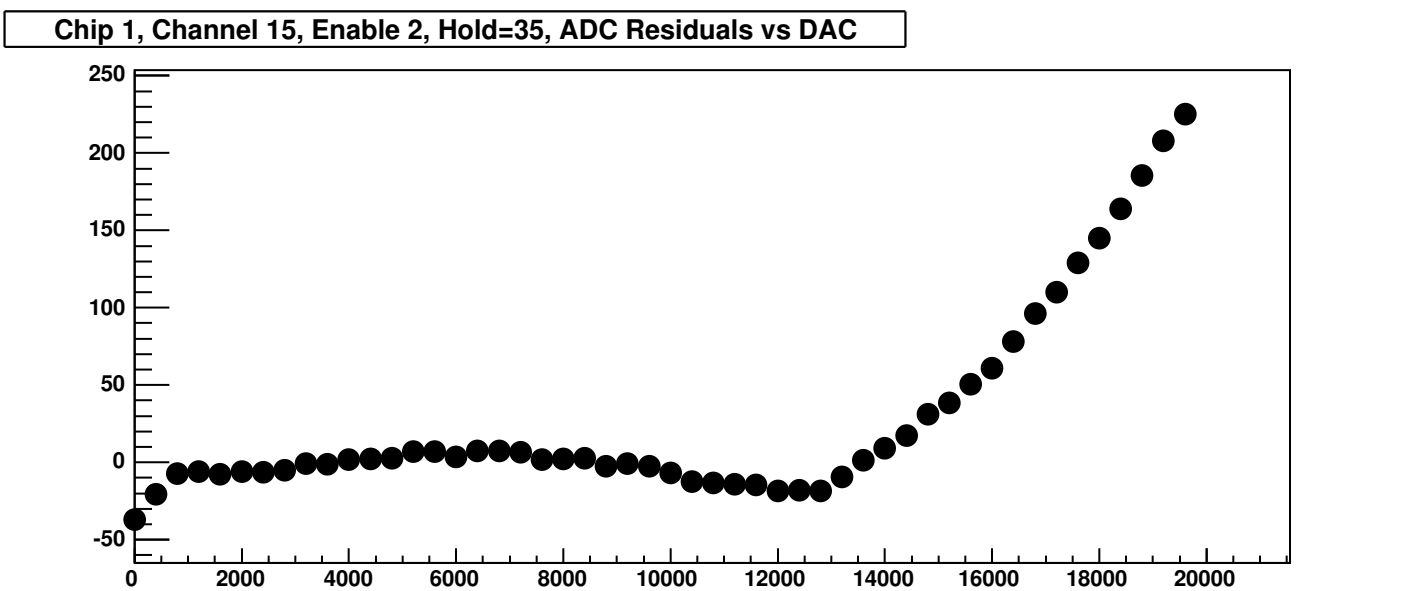

Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 62.72 / 23
p0 396.9 ± 0.645
p1 0.01578 ± 0.0001035
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

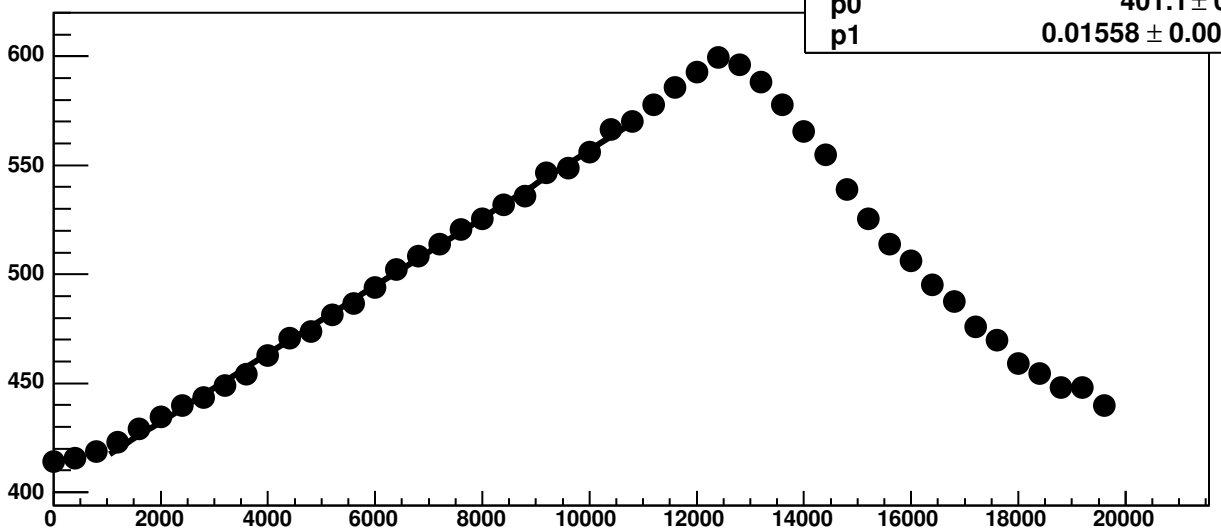

Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 341.3 / 23
p0 416.5 ± 0.7345
p1 -0.04066 ± 0.000115
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

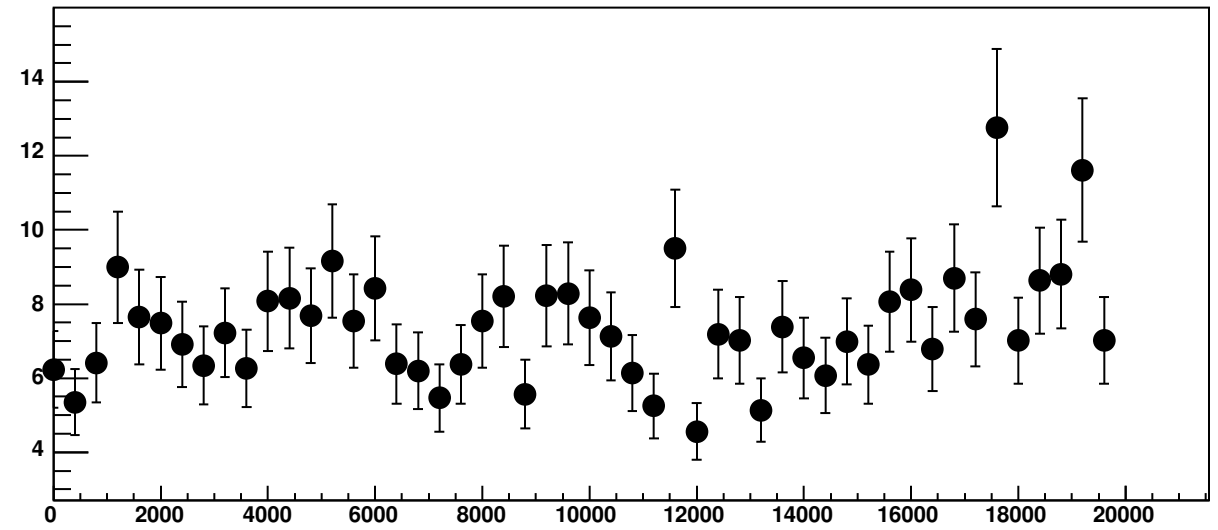

Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

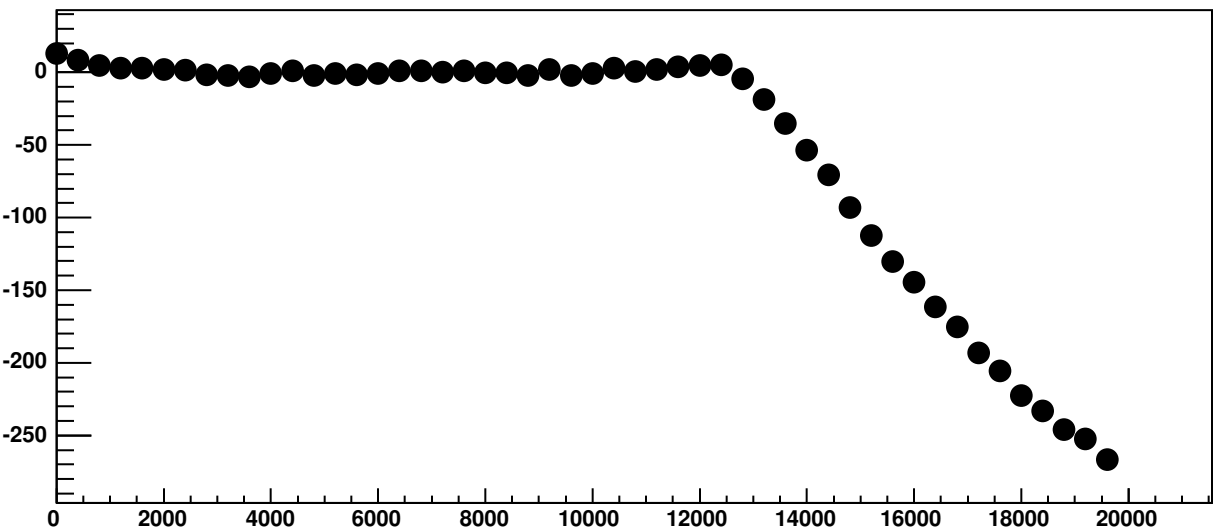
χ^2 / ndf	29.57 / 23
p0	401.1 ± 0.7804
p1	0.01558 ± 0.0001148



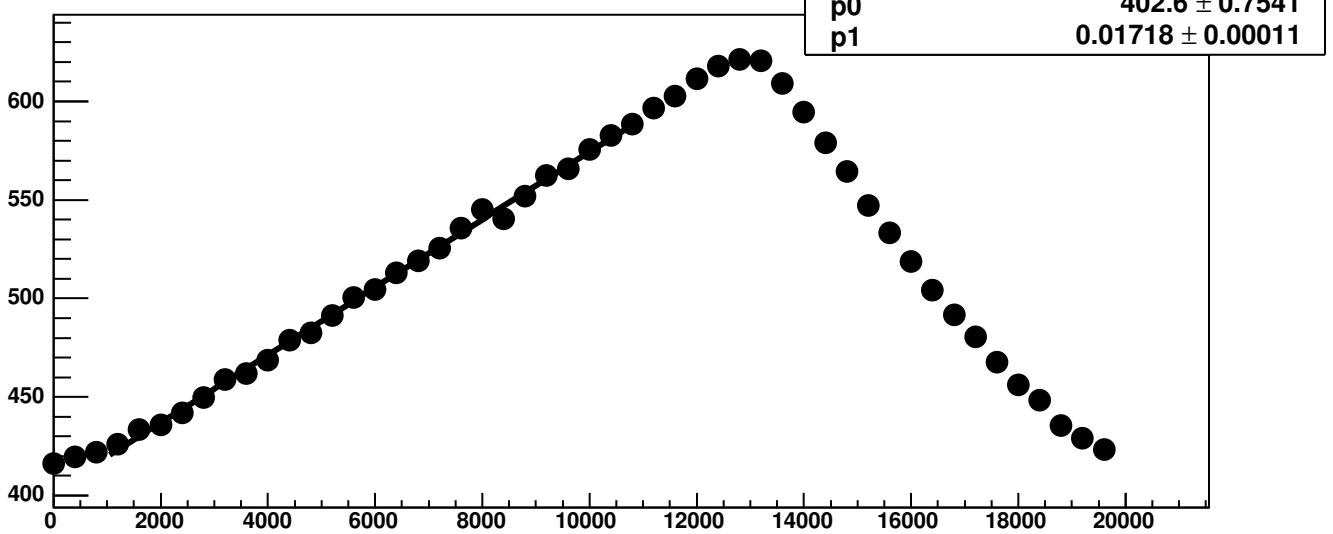
Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



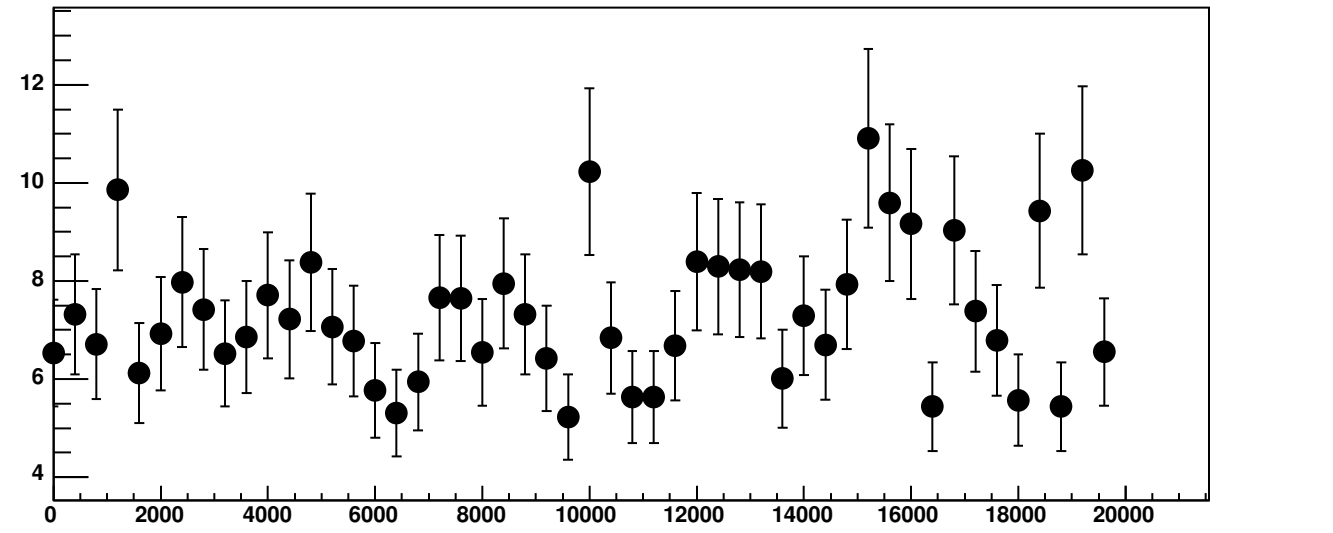
Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



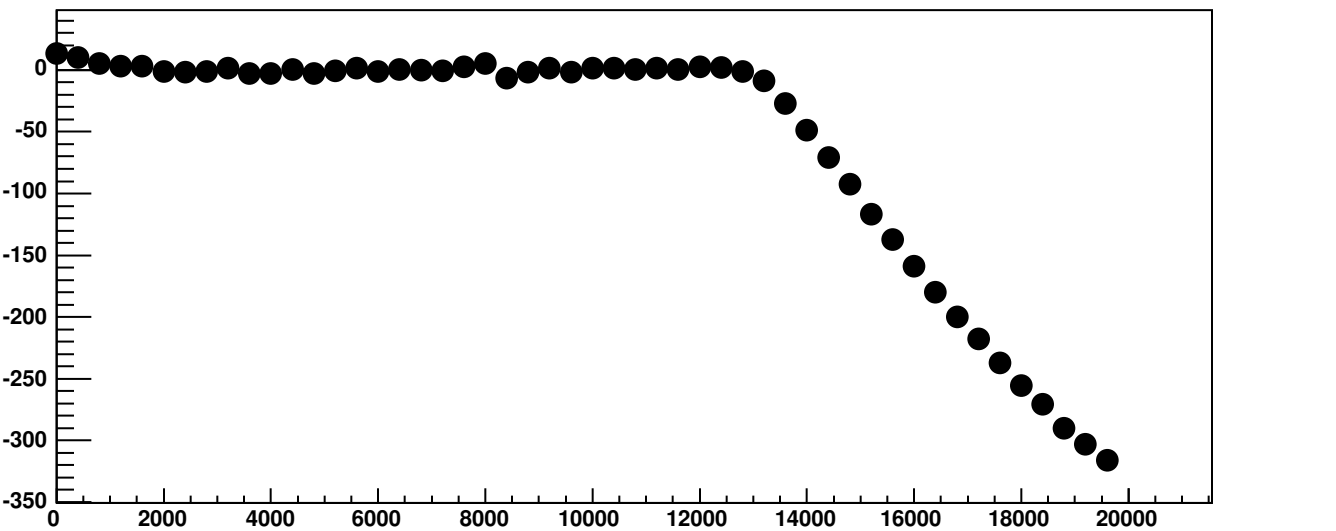
Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

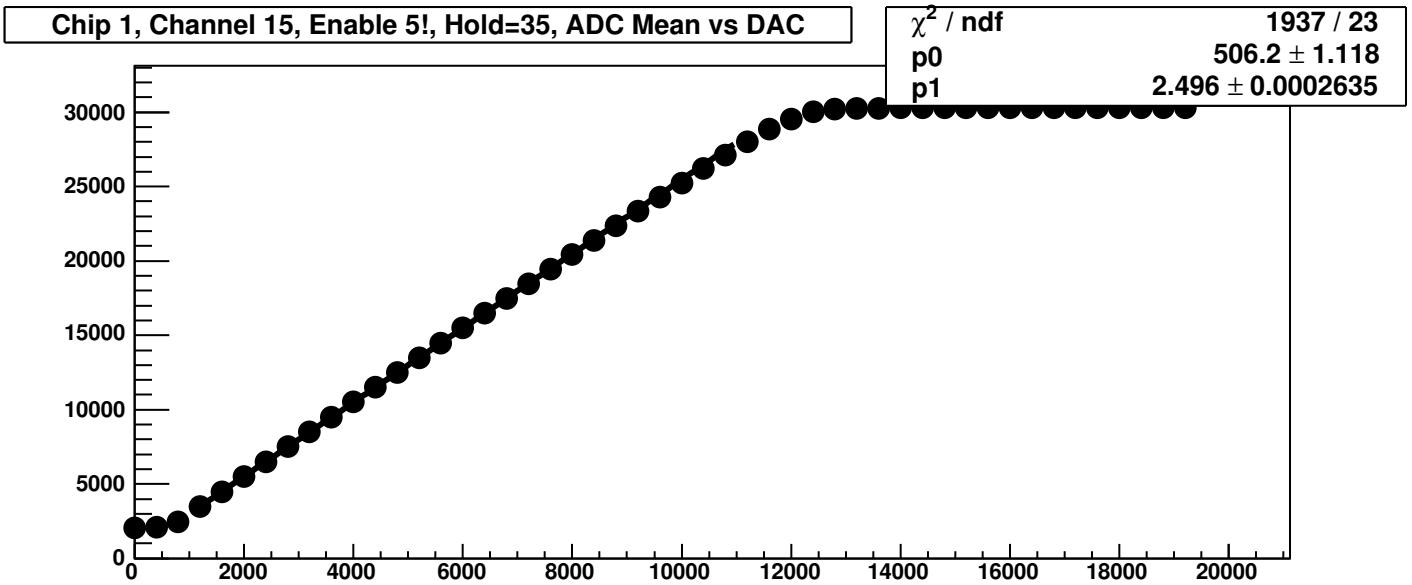


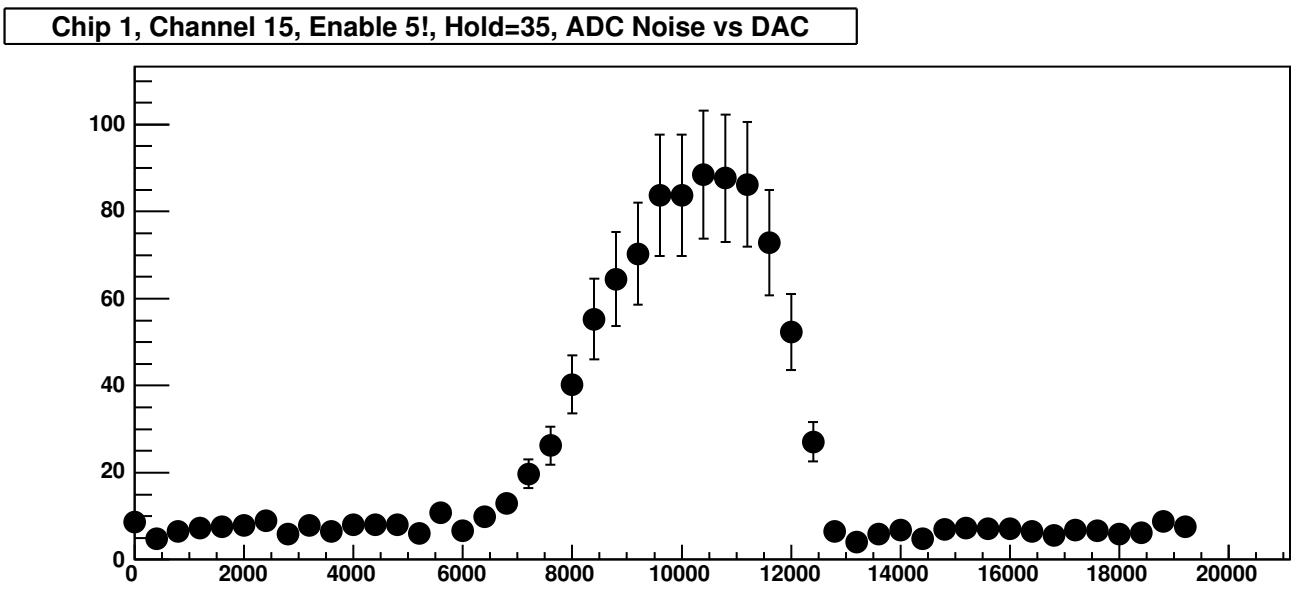
Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

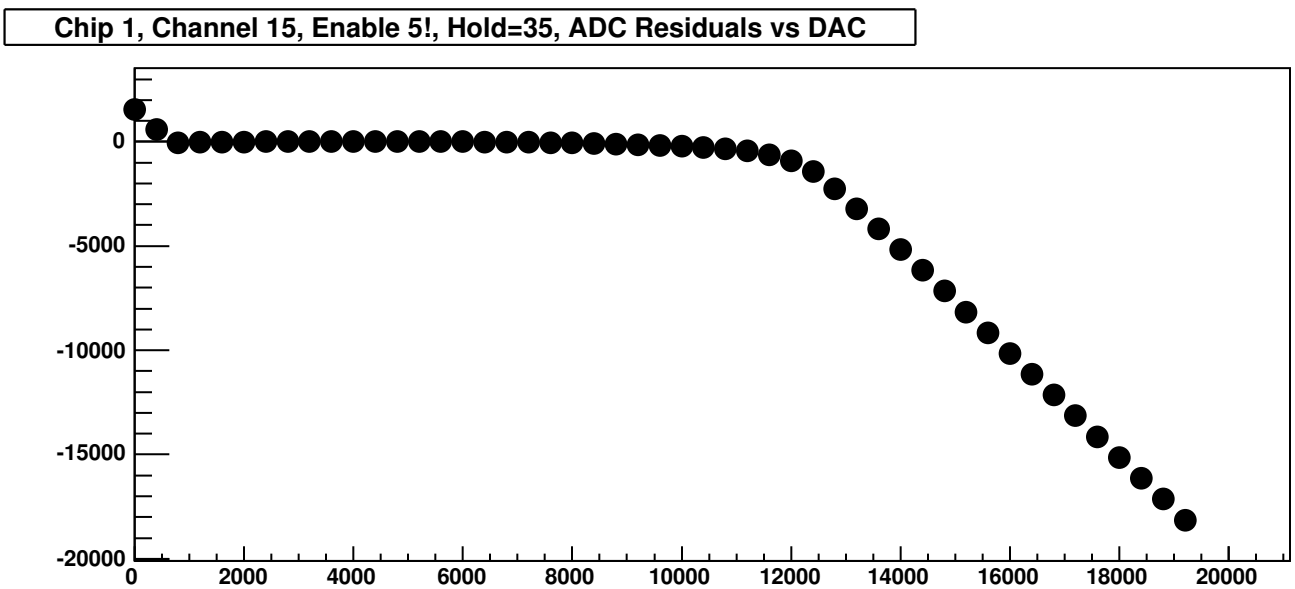


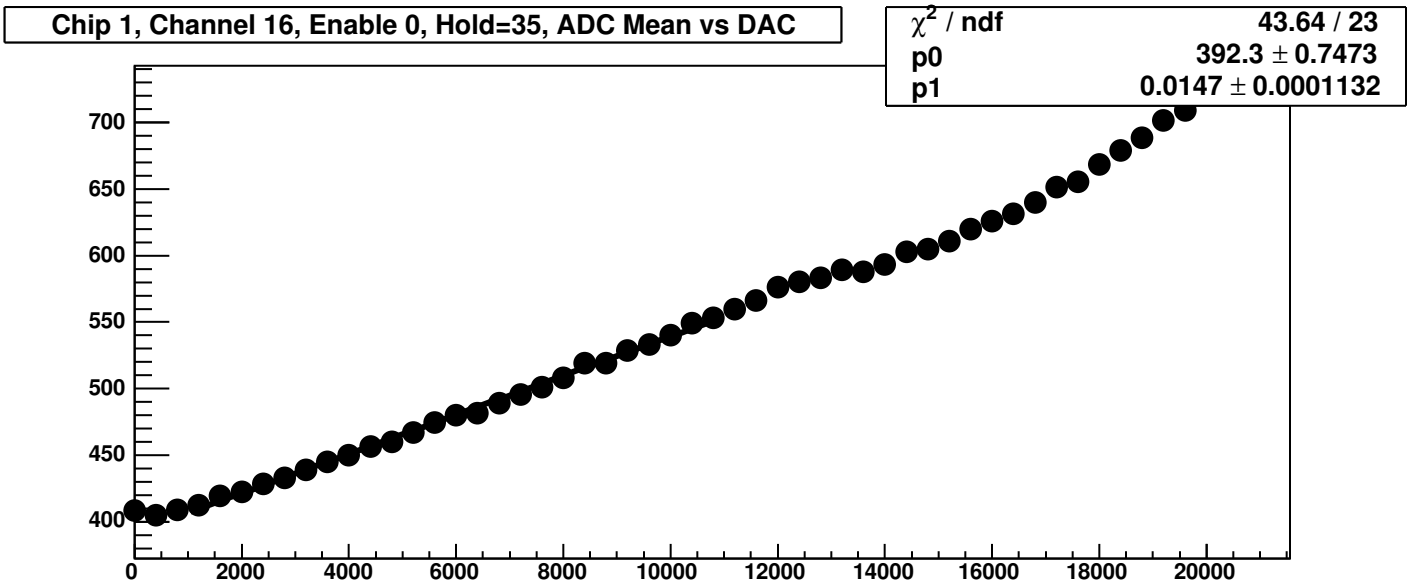
Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

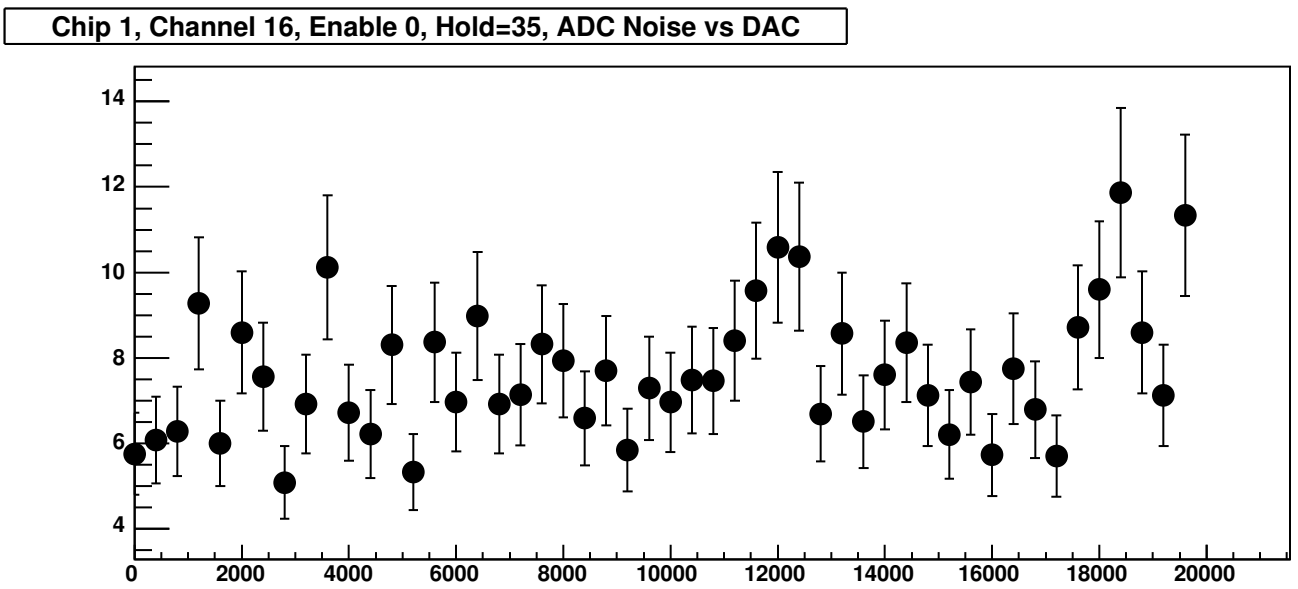


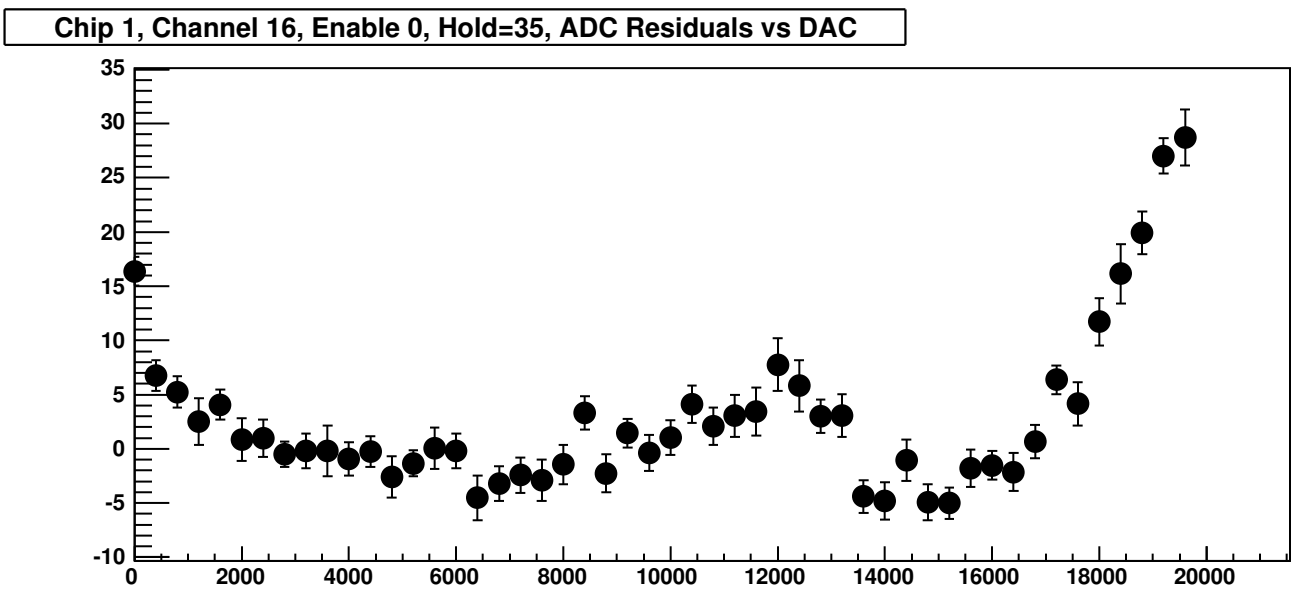

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1937 / 23
p0 506.2 ± 1.118
p1 2.496 ± 0.0002635
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

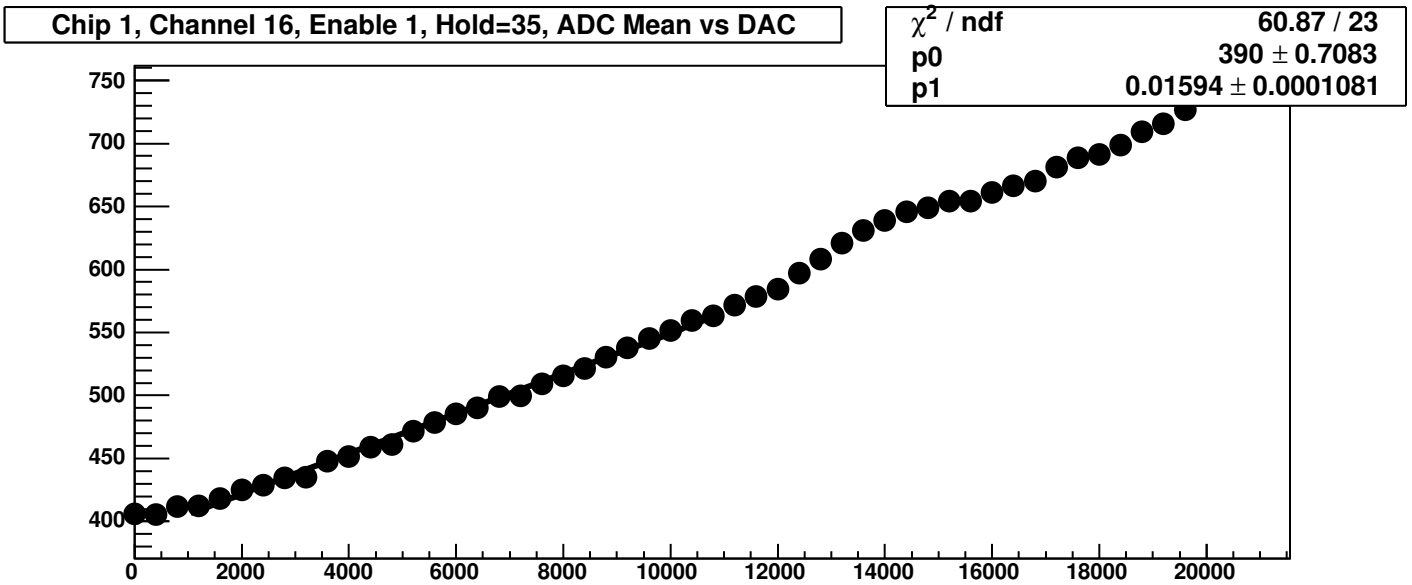

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

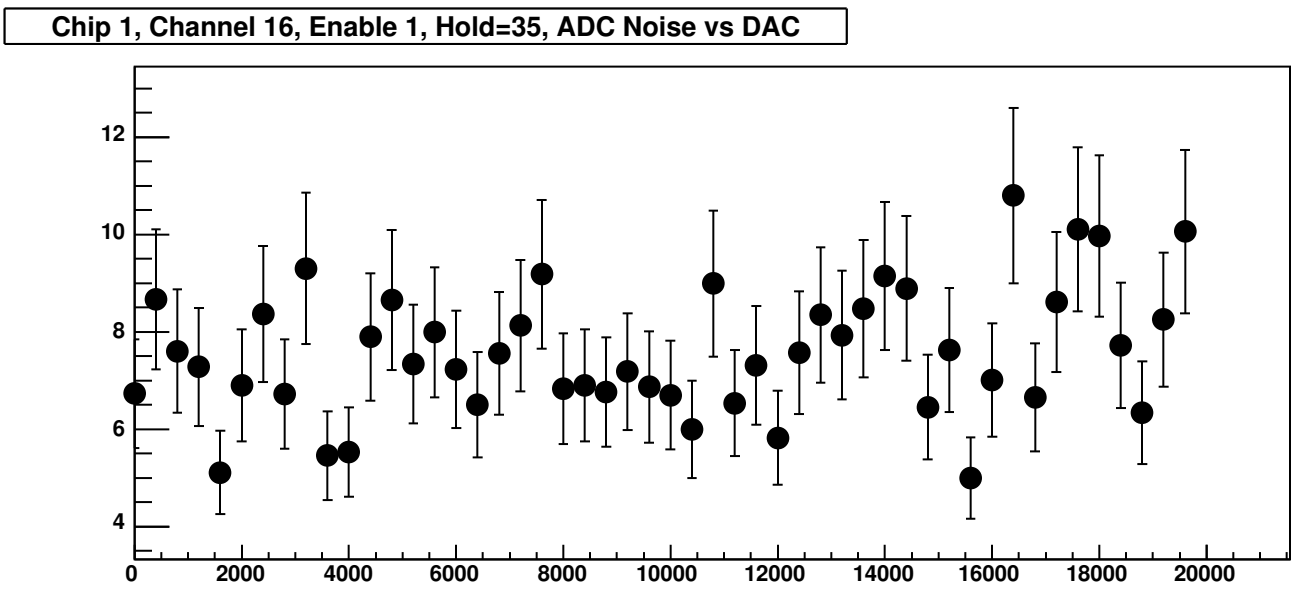

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

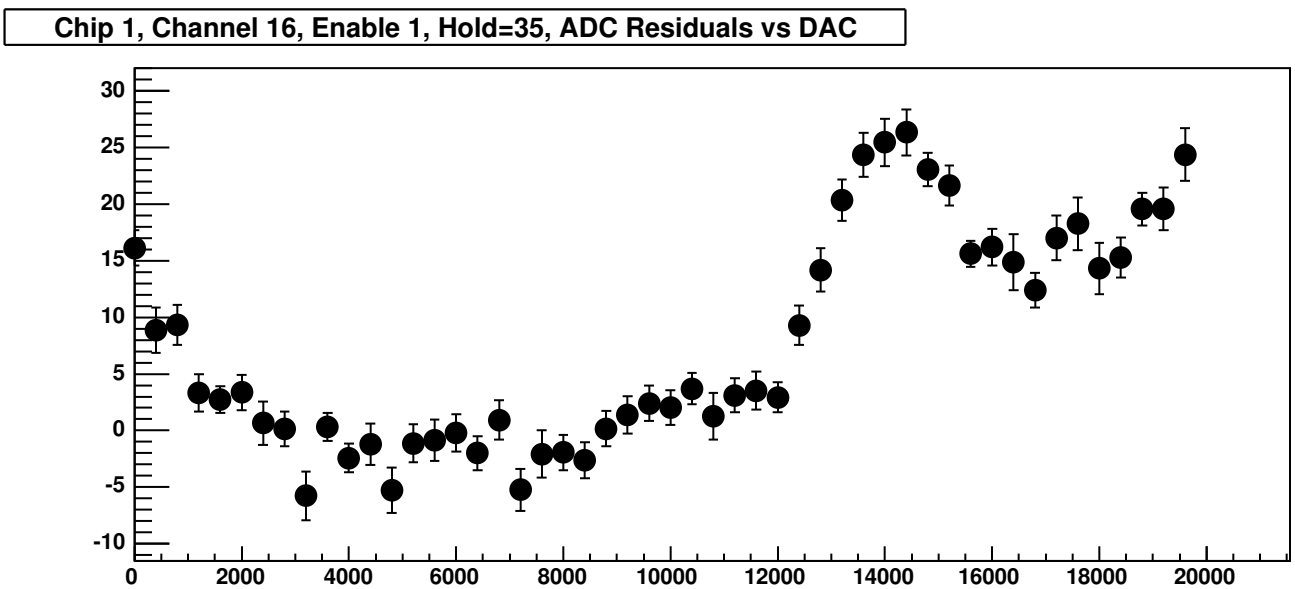

Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 43.64 / 23
p0 392.3 ± 0.7473
p1 0.0147 ± 0.0001132
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

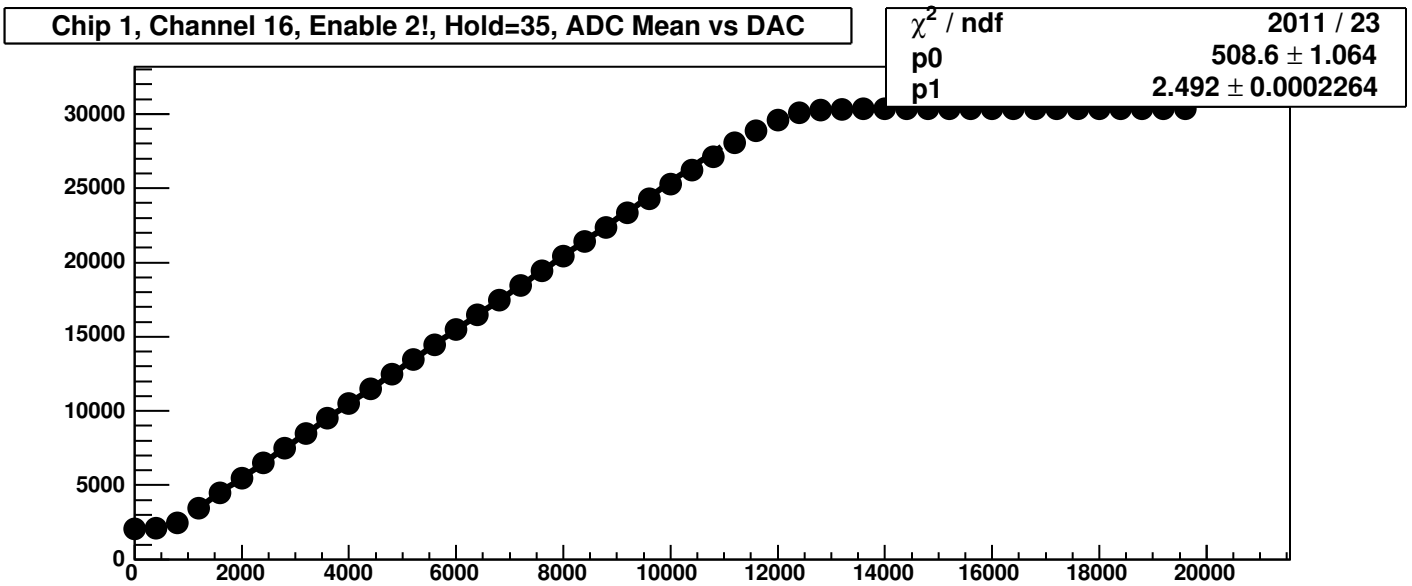

Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

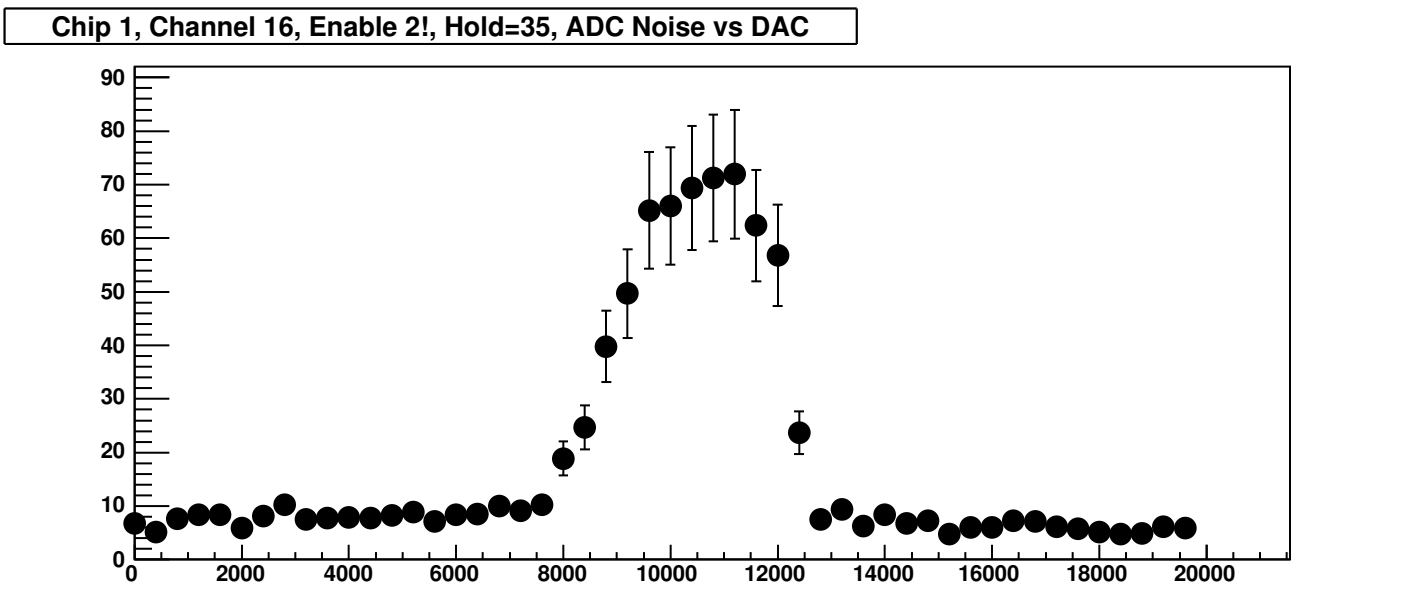

Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

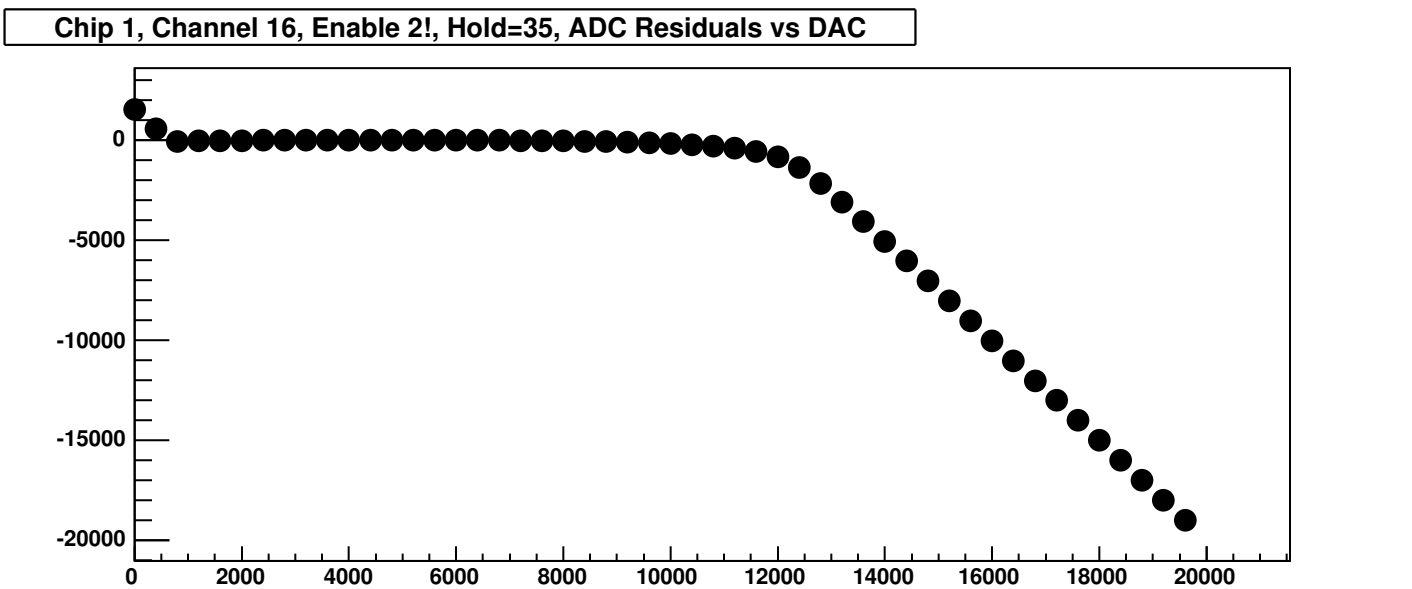

Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60.87 / 23
p0 390 ± 0.7083
p1 0.01594 ± 0.0001081
750
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

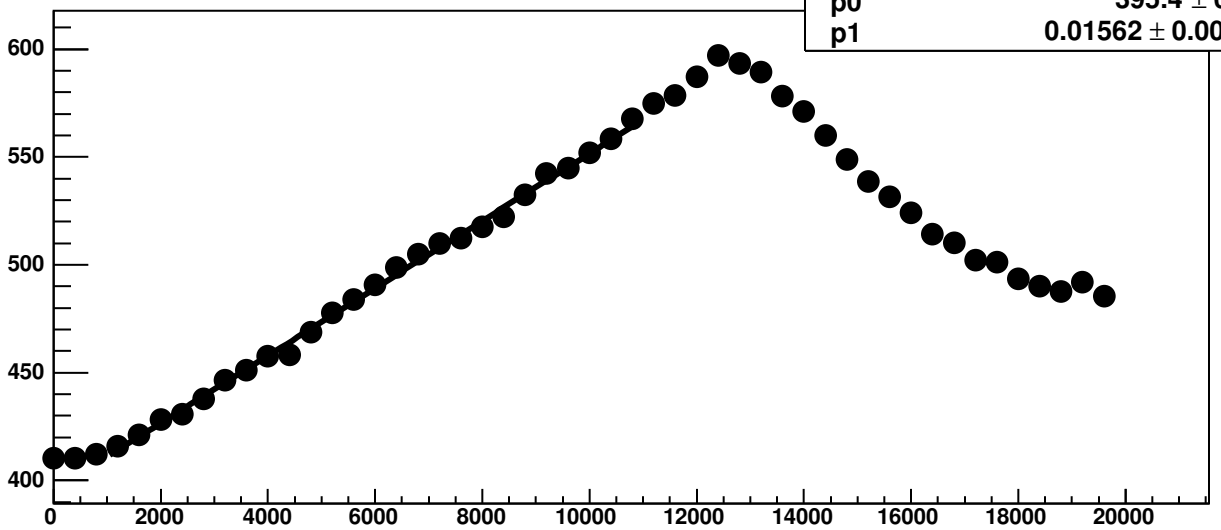

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 2011 / 23
p0 508.6 ± 1.064
p1 2.492 ± 0.0002264
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

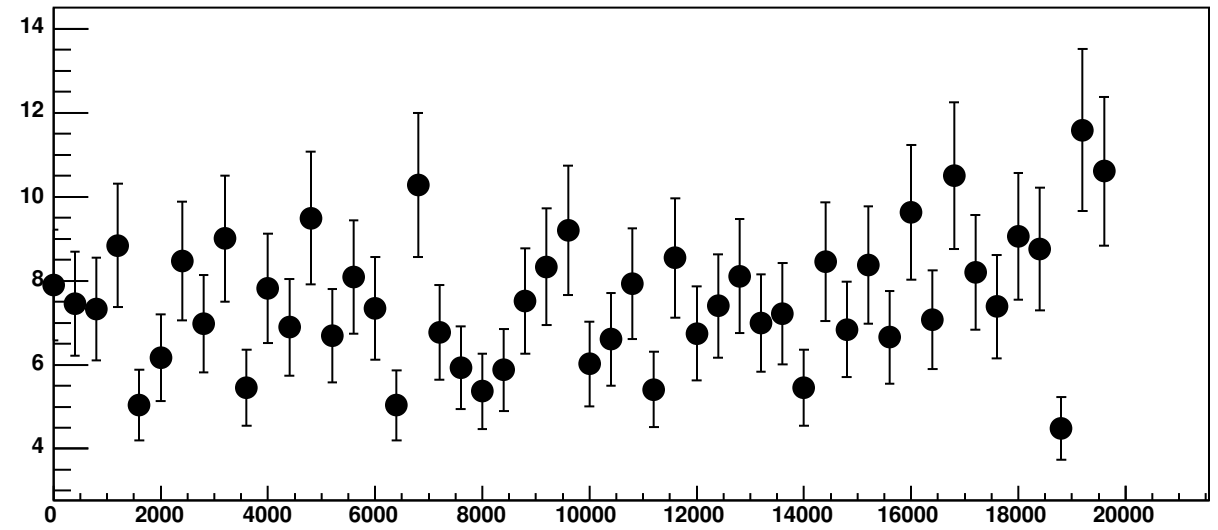

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

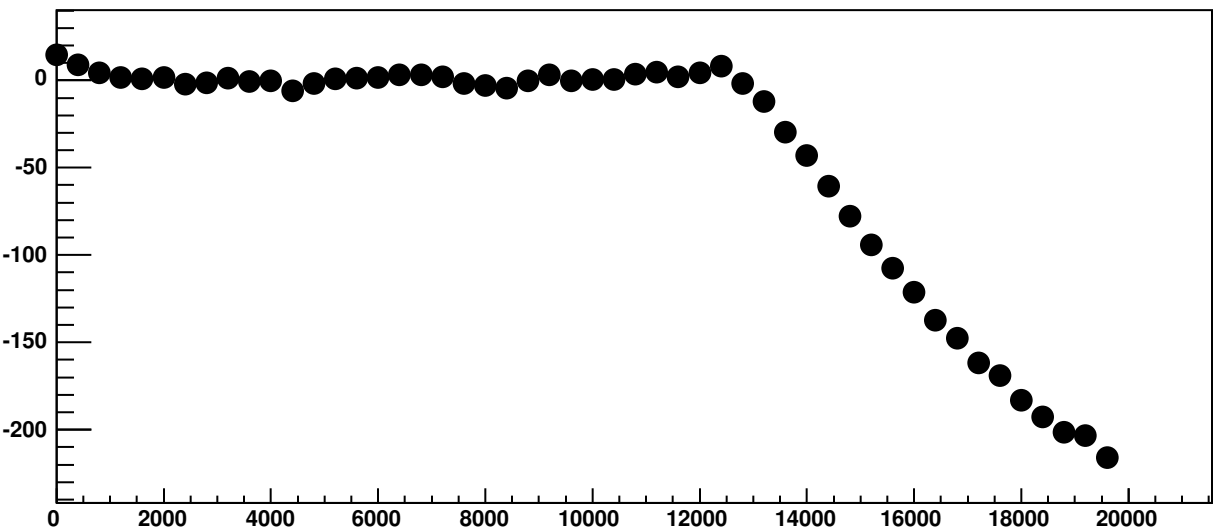
χ^2 / ndf	58.81 / 23
p0	395.4 ± 0.7226
p1	0.01562 ± 0.0001091

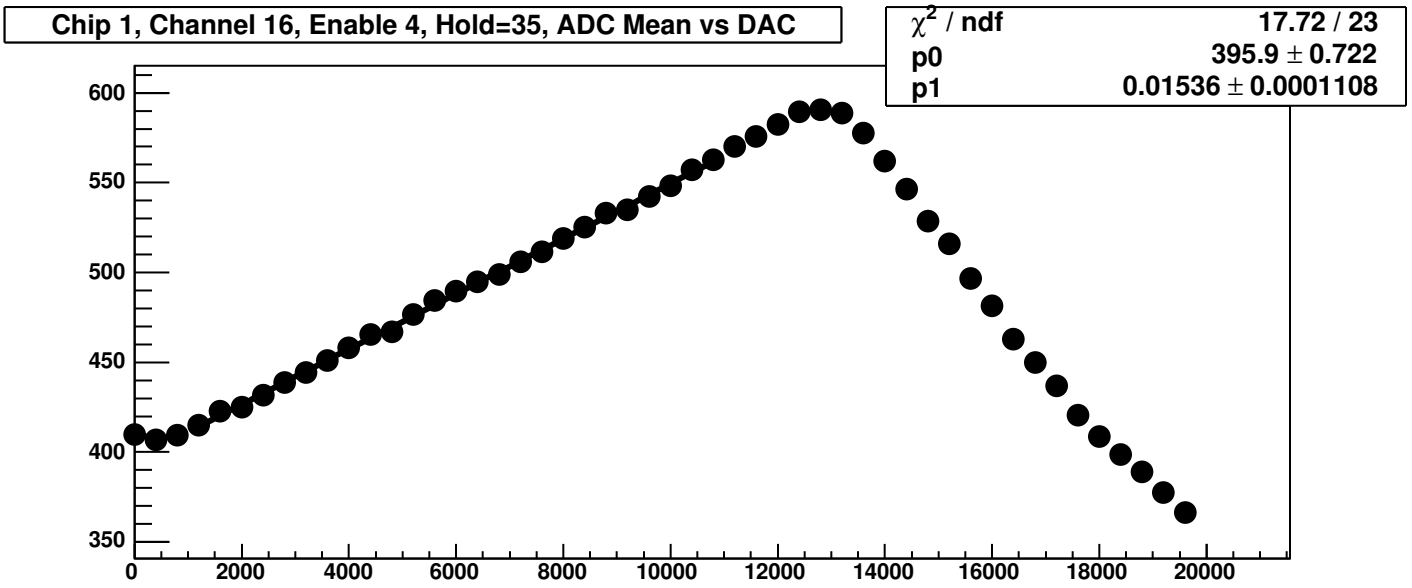


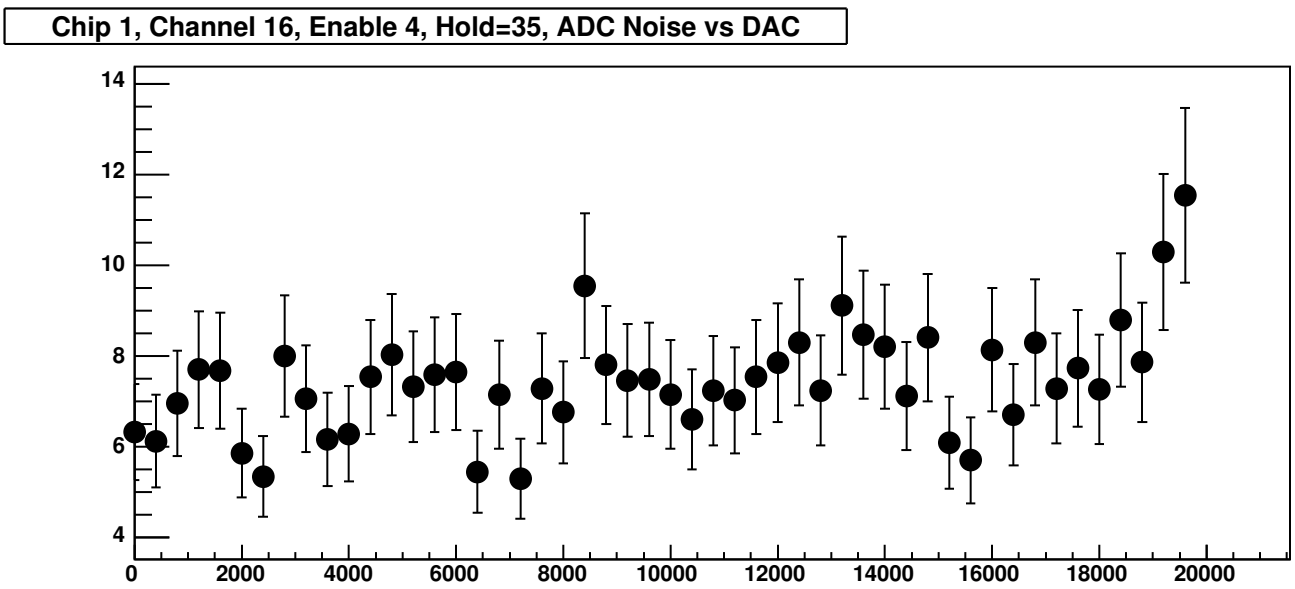
Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC

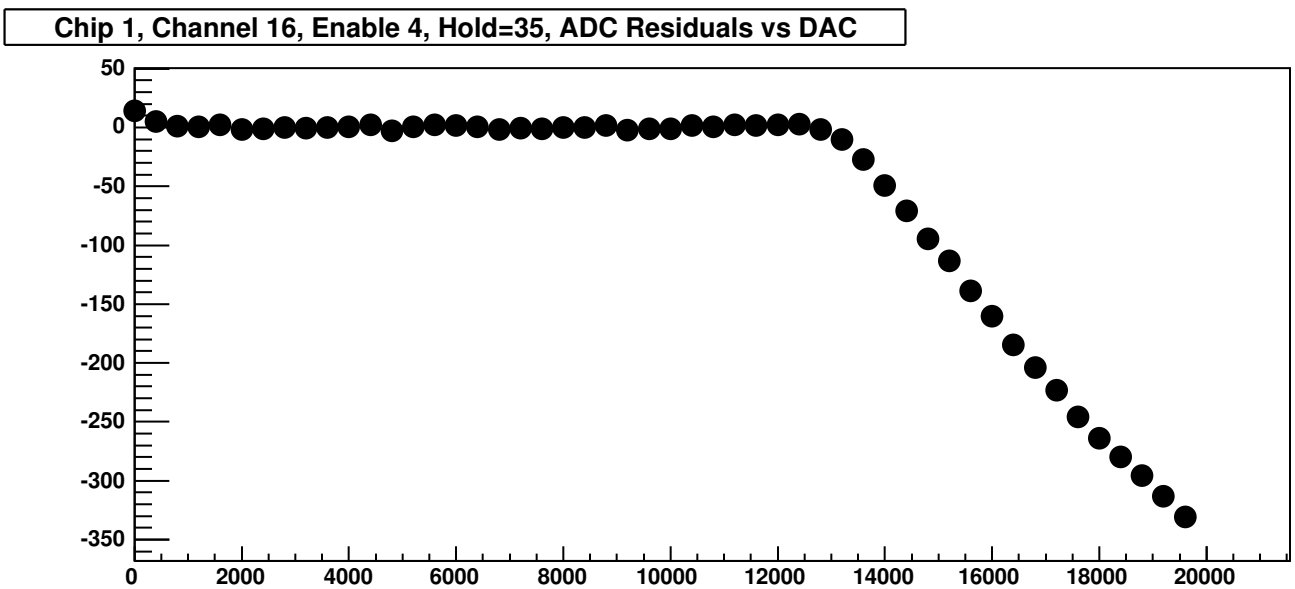


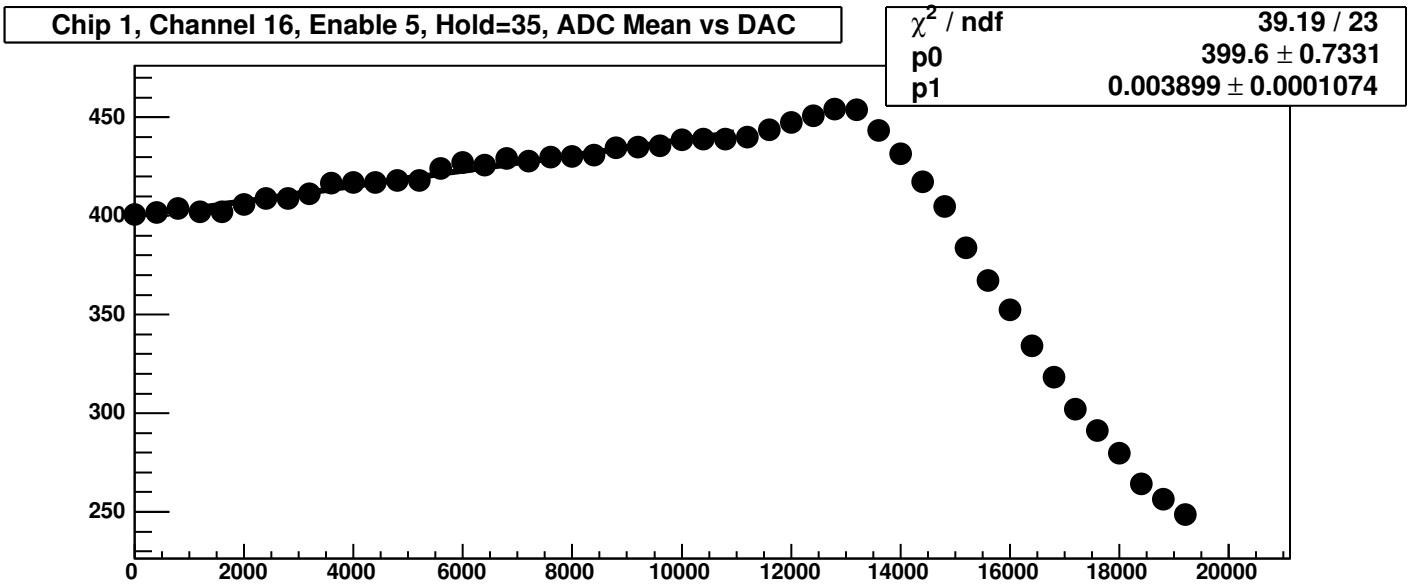
Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

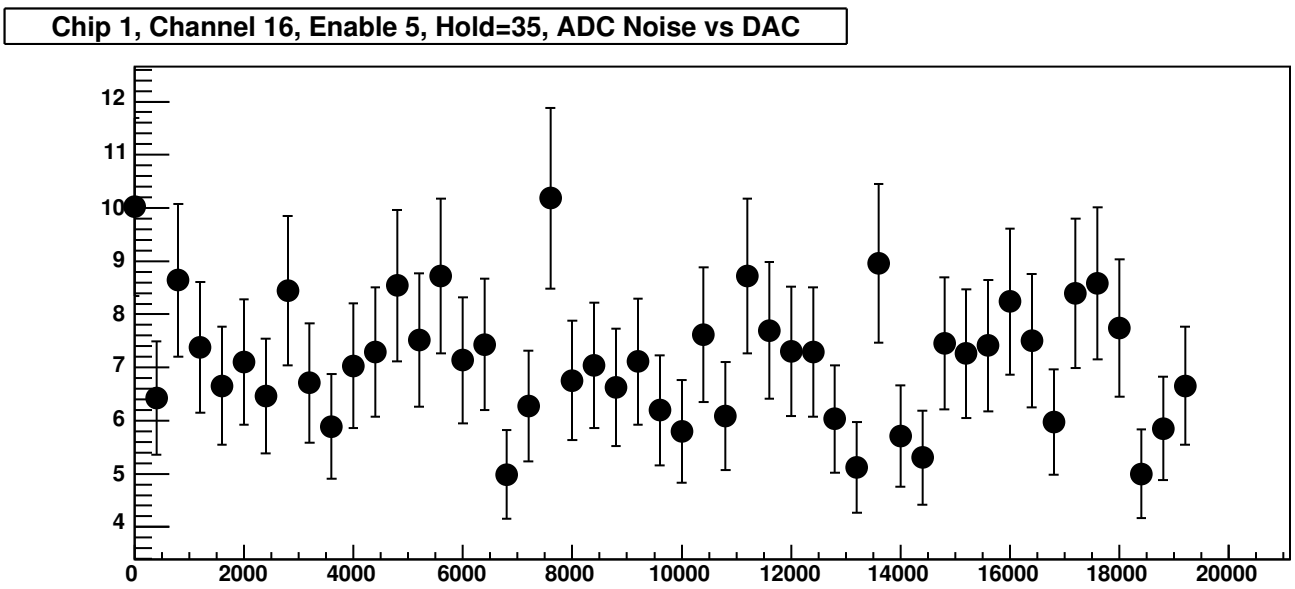


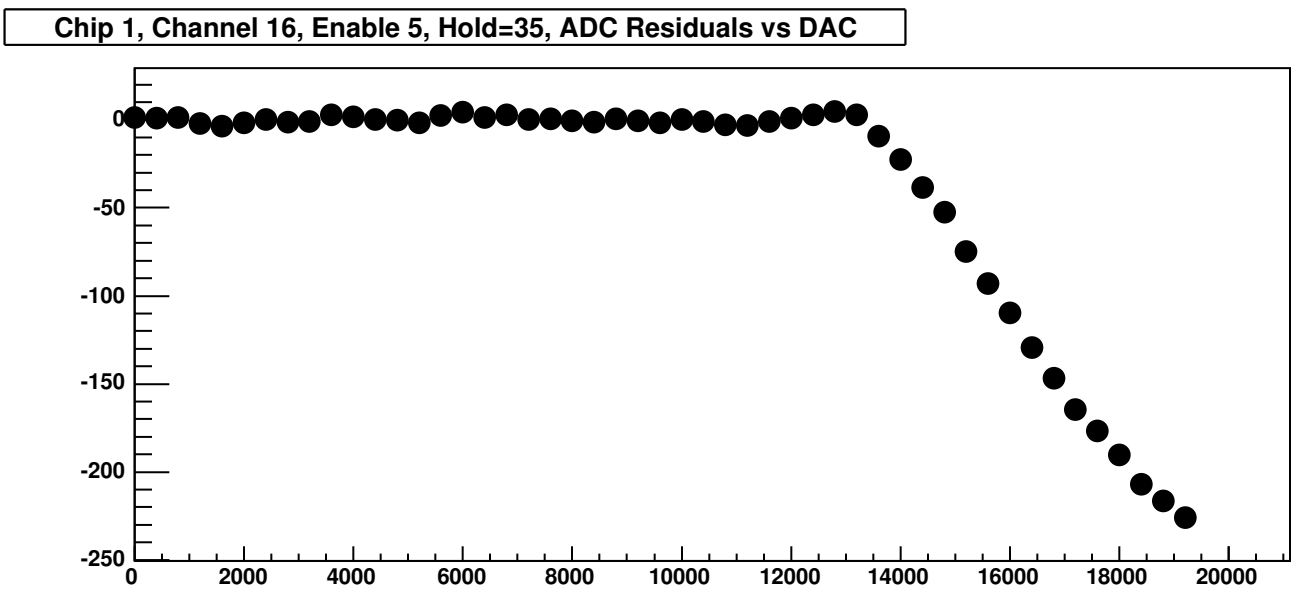

Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.72 / 23
p0 395.9 ± 0.722
p1 0.01536 ± 0.0001108
600
550
500
450
400
350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

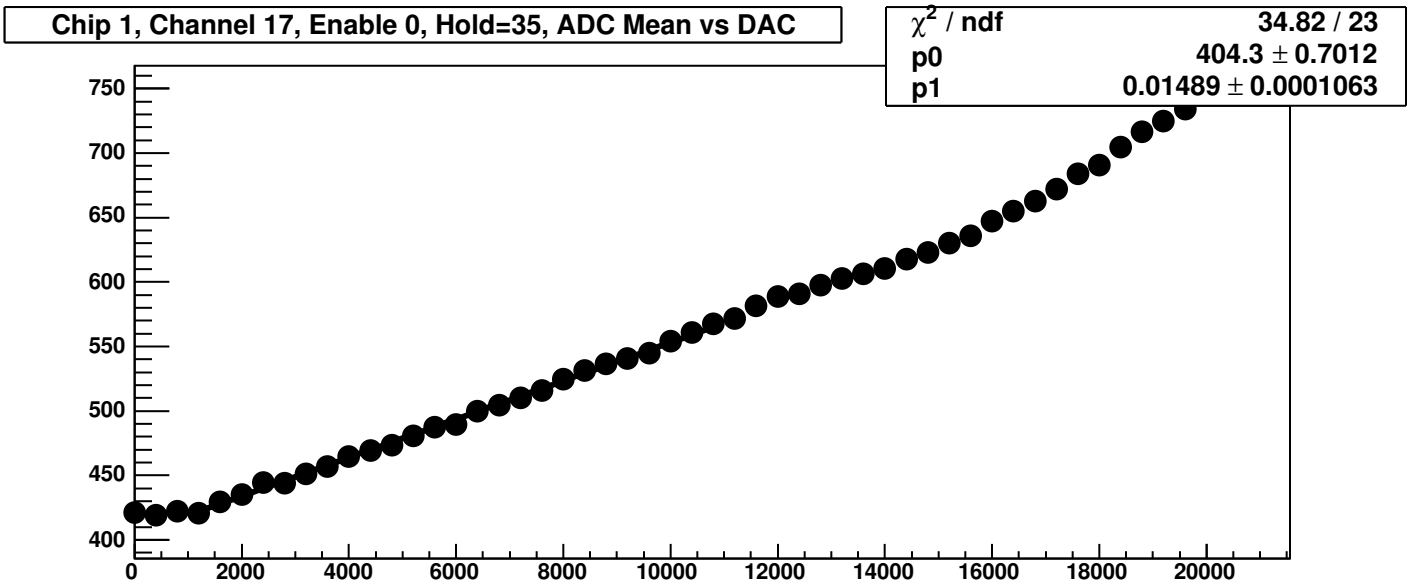

Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

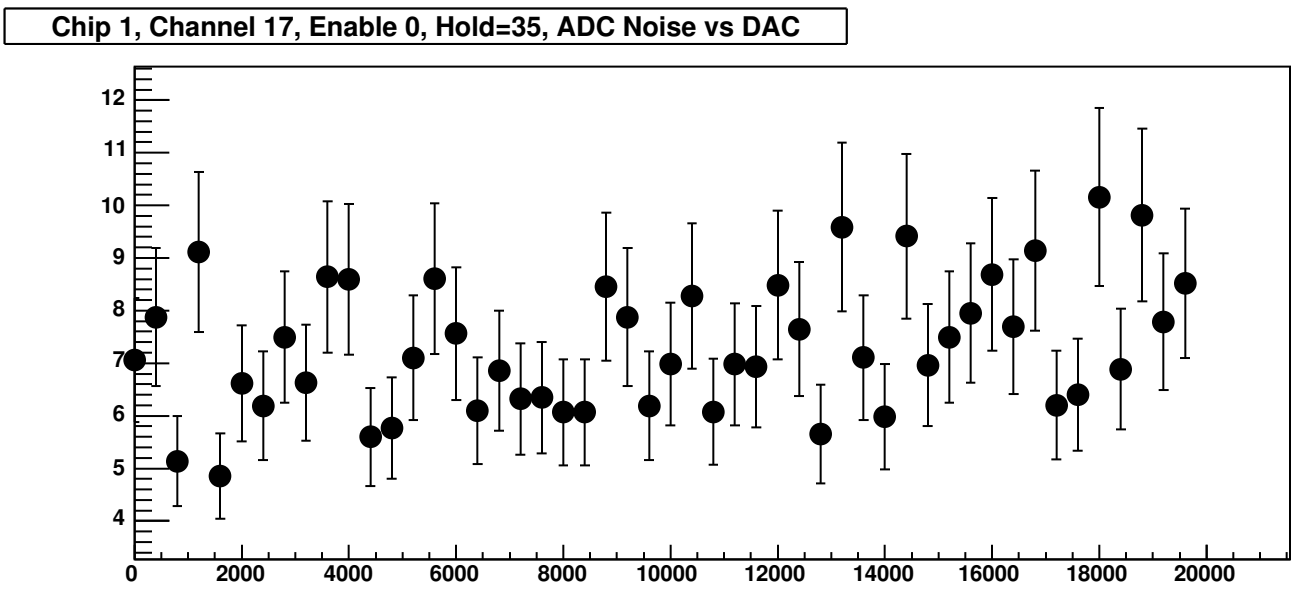

Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

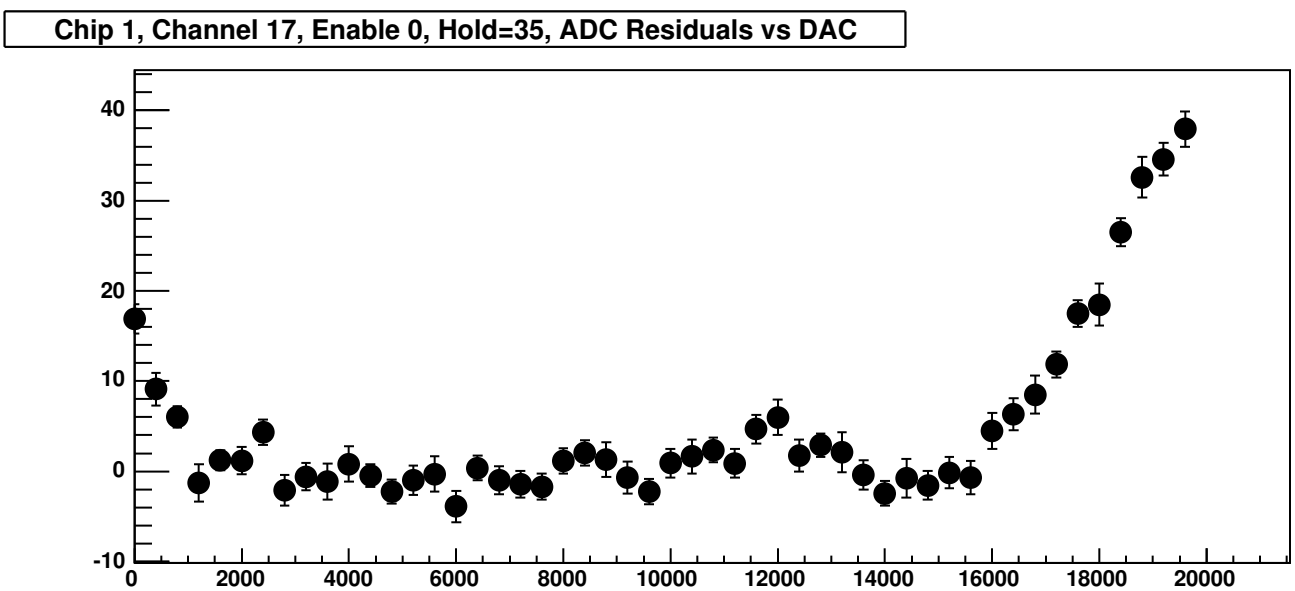

Chip 1, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 39.19 / 23
p0 399.6 ± 0.7331
p1 0.003899 ± 0.0001074
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

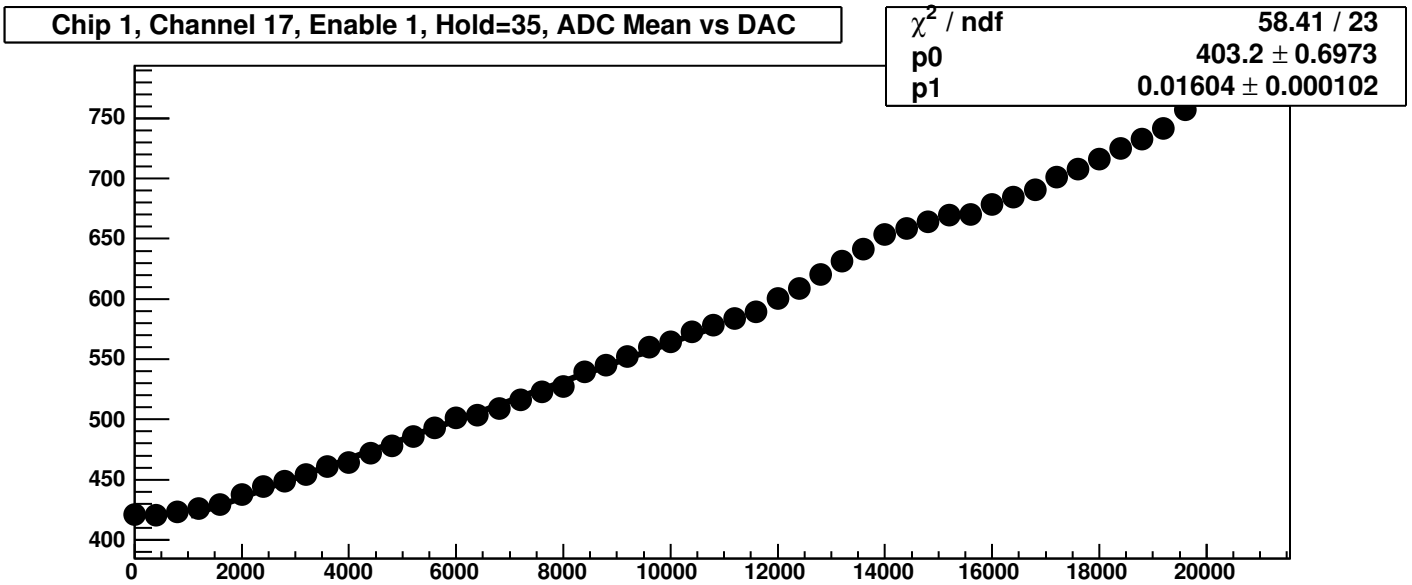

Chip 1, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

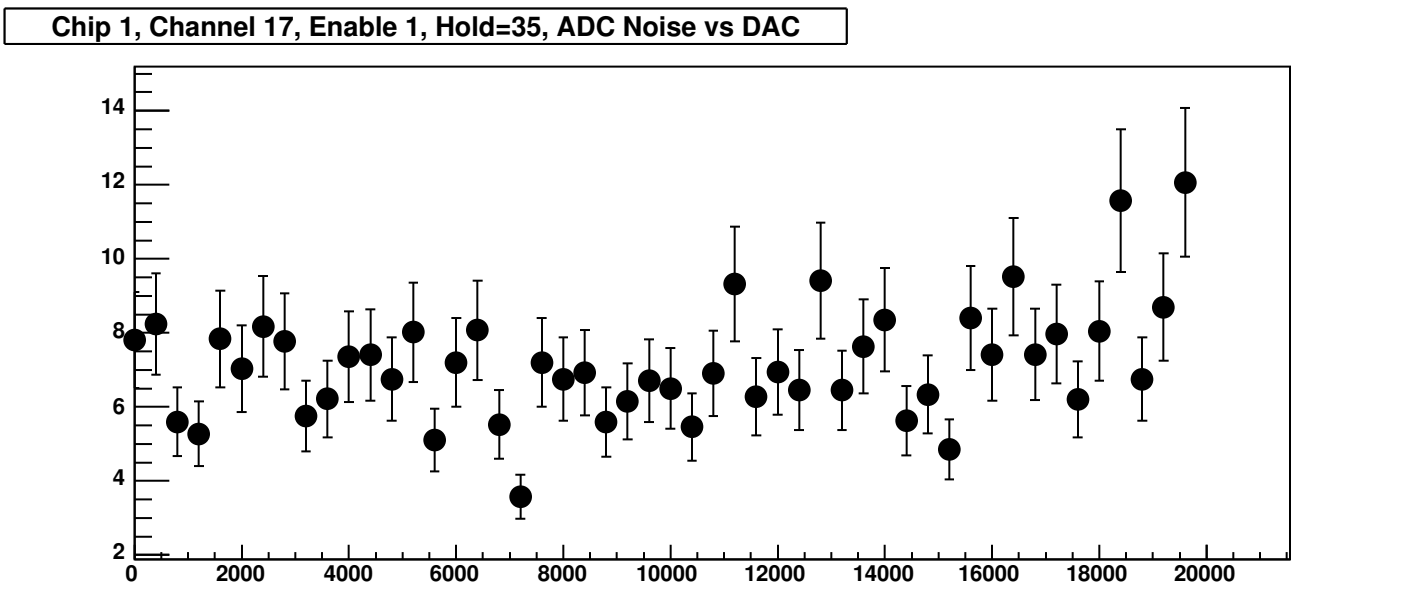

Chip 1, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

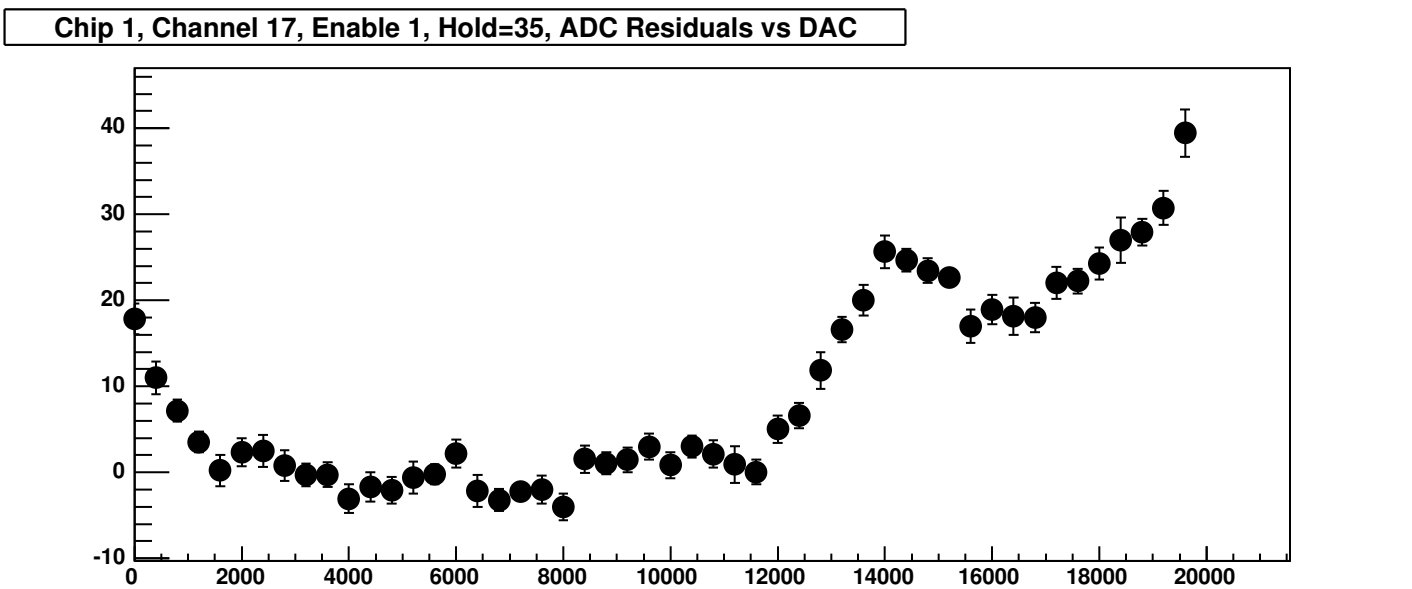

Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 34.82 / 23
p0 404.3 ± 0.7012
p1 0.01489 ± 0.0001063
750
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

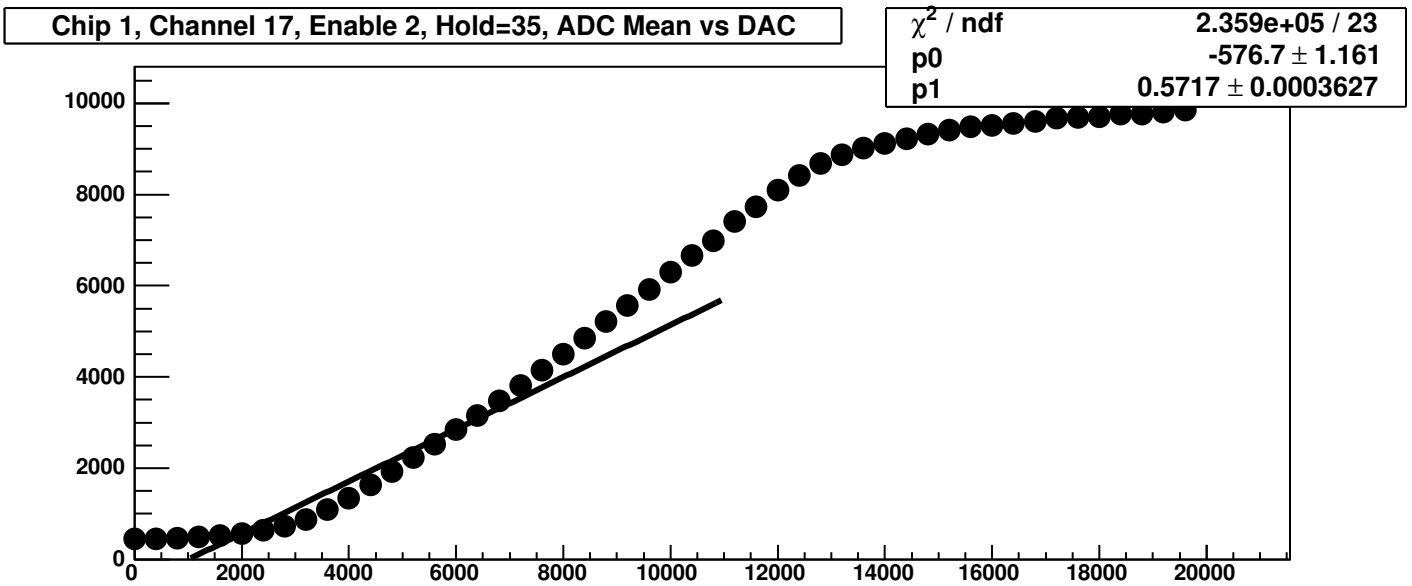

Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

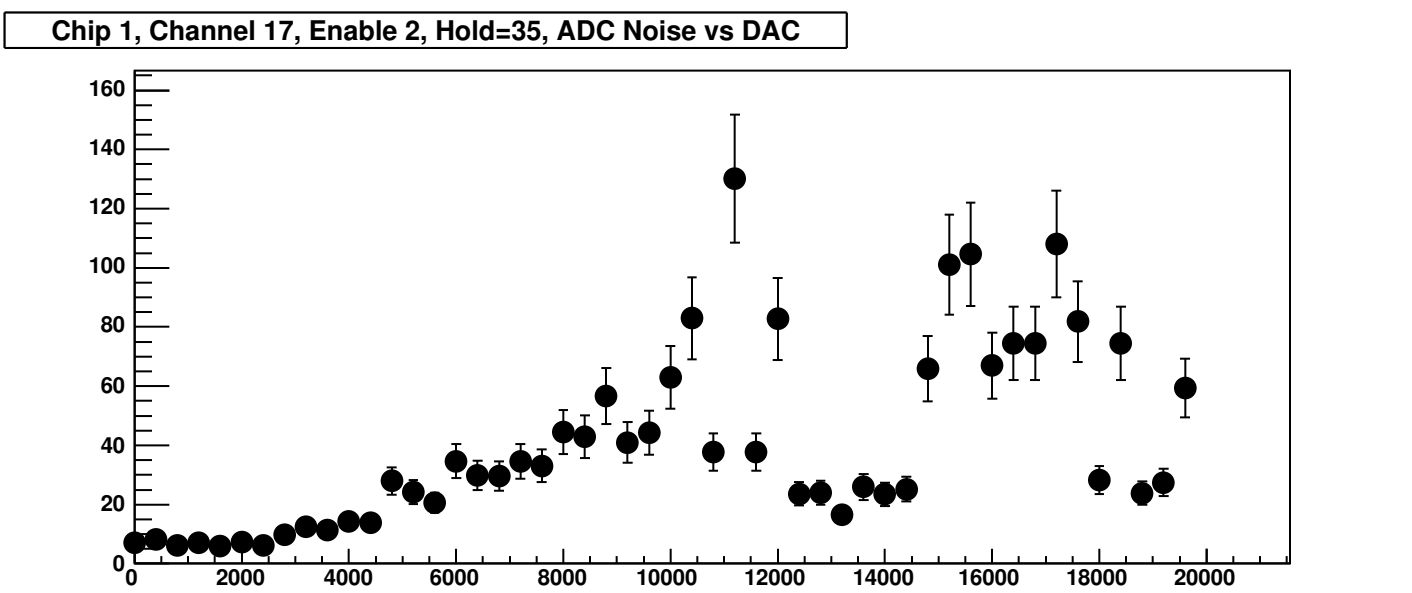

Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

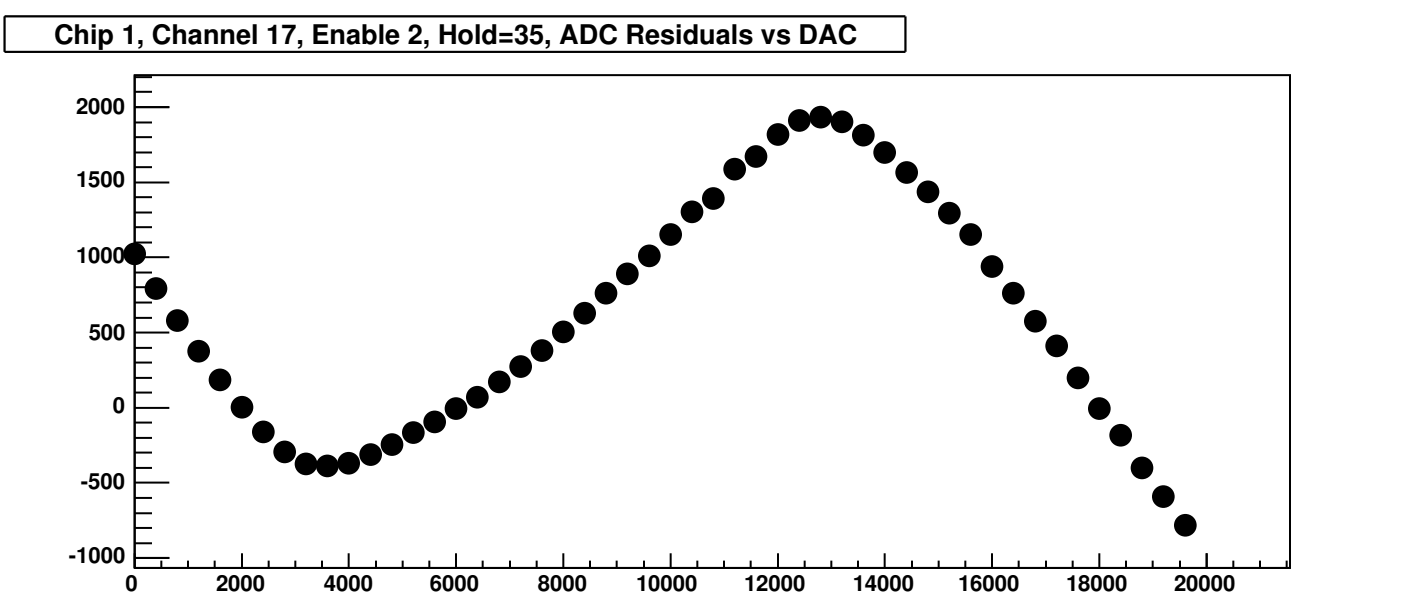

Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 58.41 / 23
p0 403.2 ± 0.6973
p1 0.01604 ± 0.000102
750
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

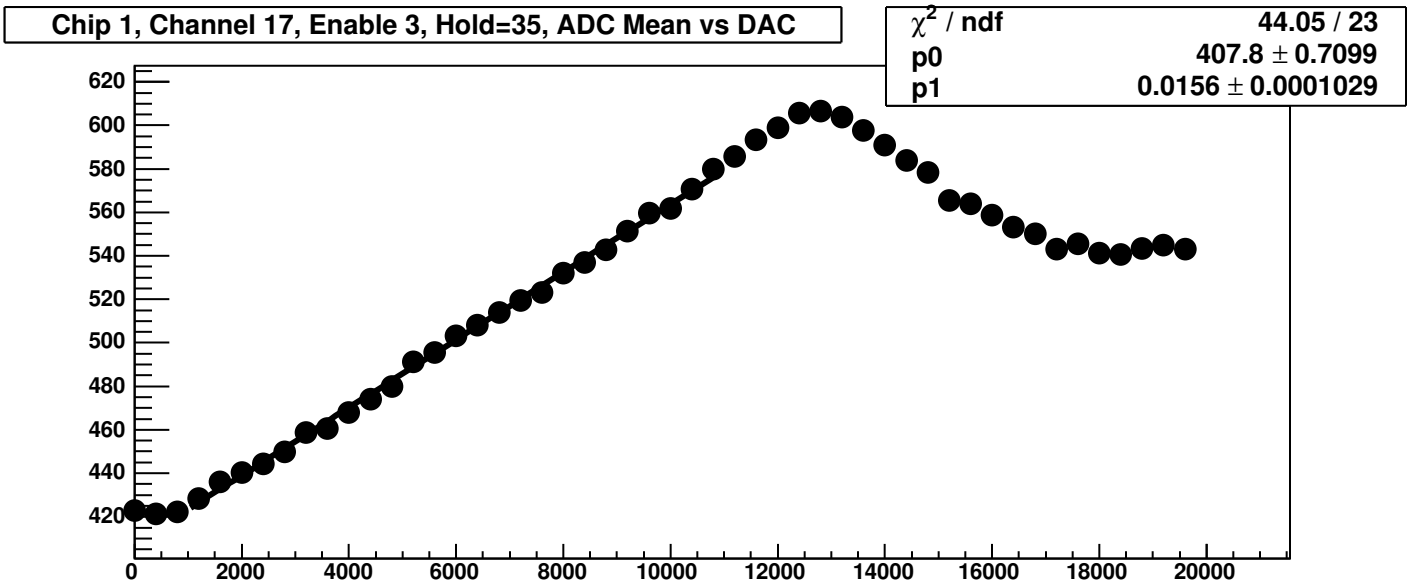

Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

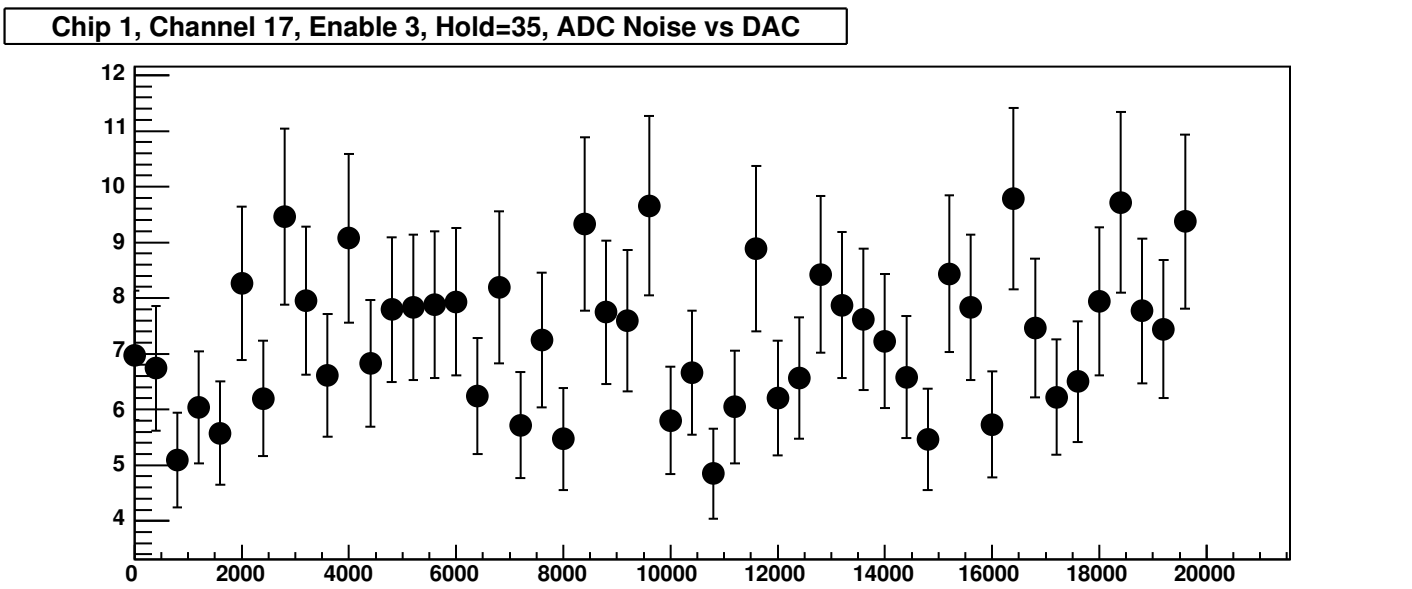

Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

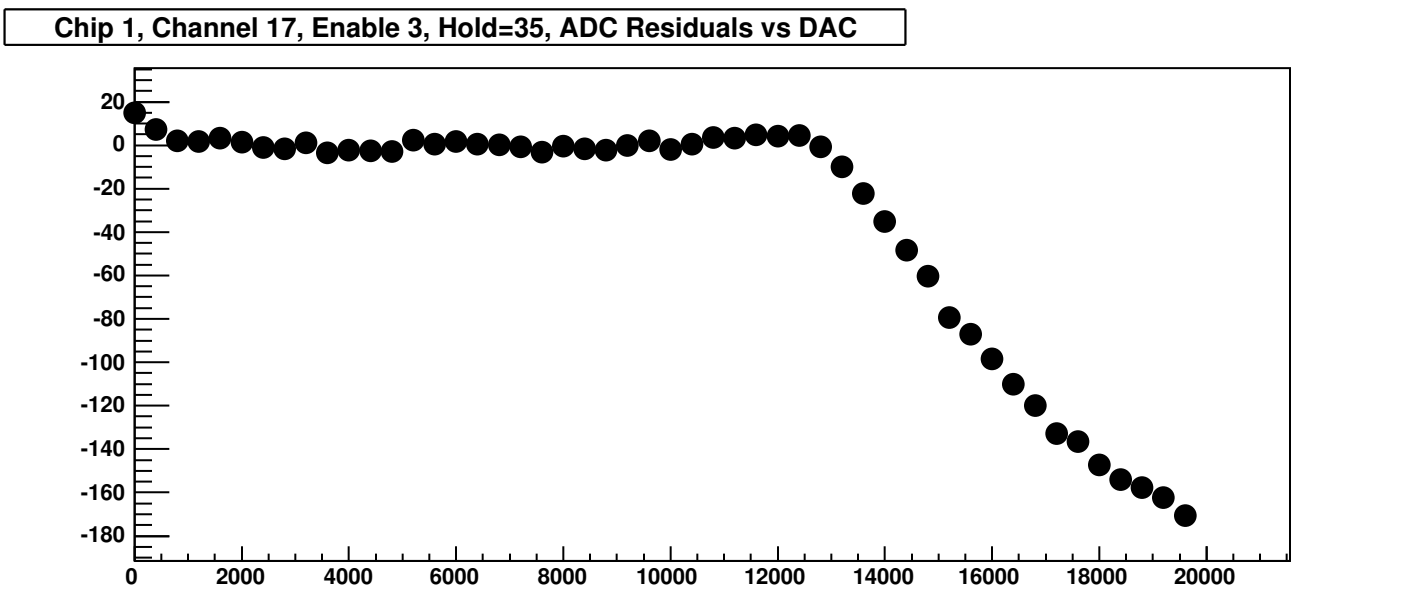

Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 2.359e+05 / 23
p0 -576.7 ± 1.161
p1 0.5717 ± 0.0003627
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000
0 2000 4000 6000 8000 10000

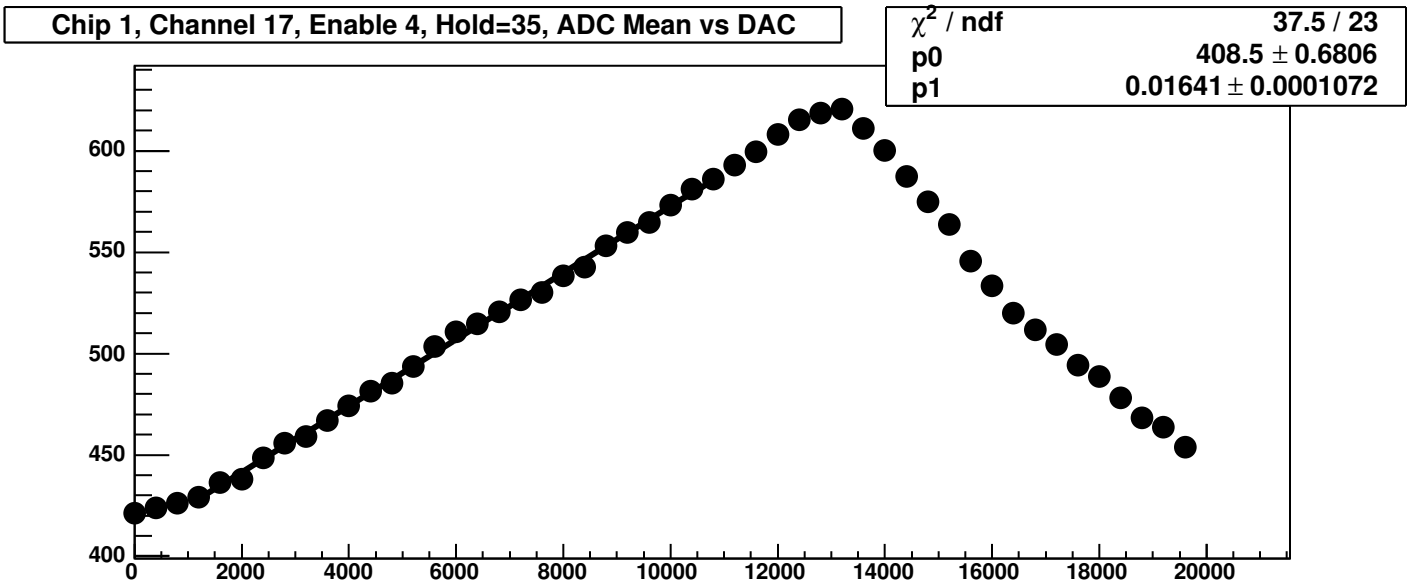

Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000
0 20 40 60 80 100 120 140 160

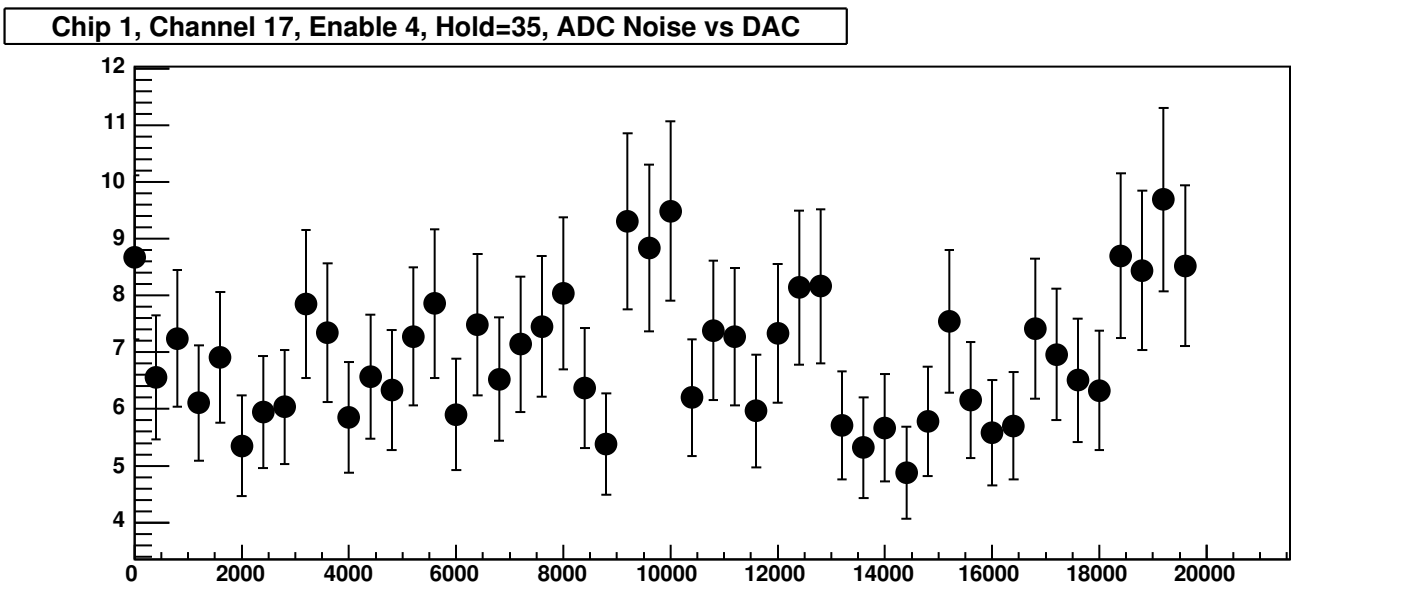

Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000
-1000 -500 0 500 1000 1500 2000

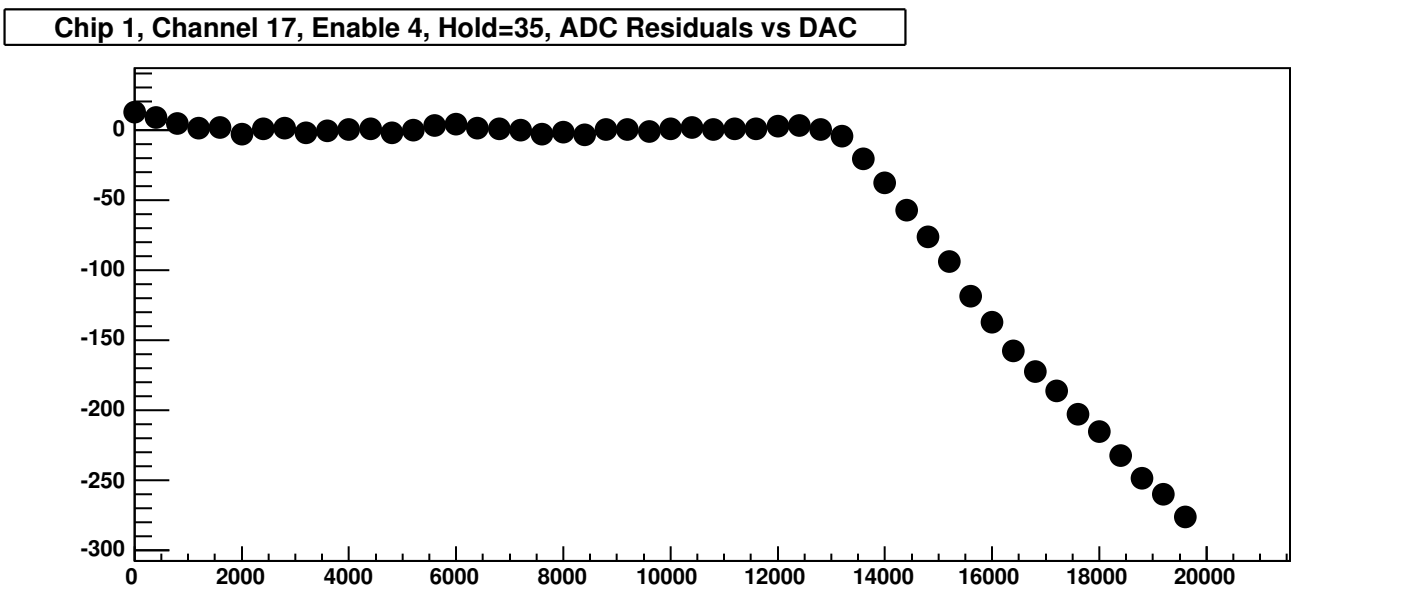

Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.05 / 23
p0 407.8 ± 0.7099
p1 0.0156 ± 0.0001029
620
600
580
560
540
520
500
480
460
440
420
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

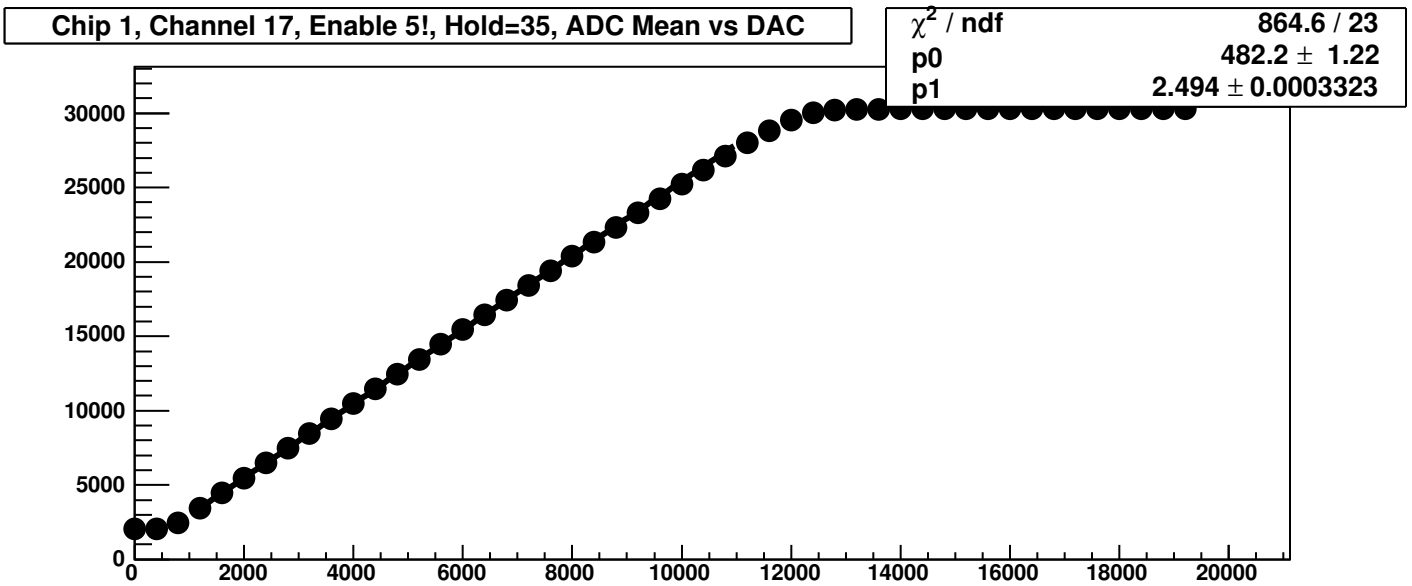

Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

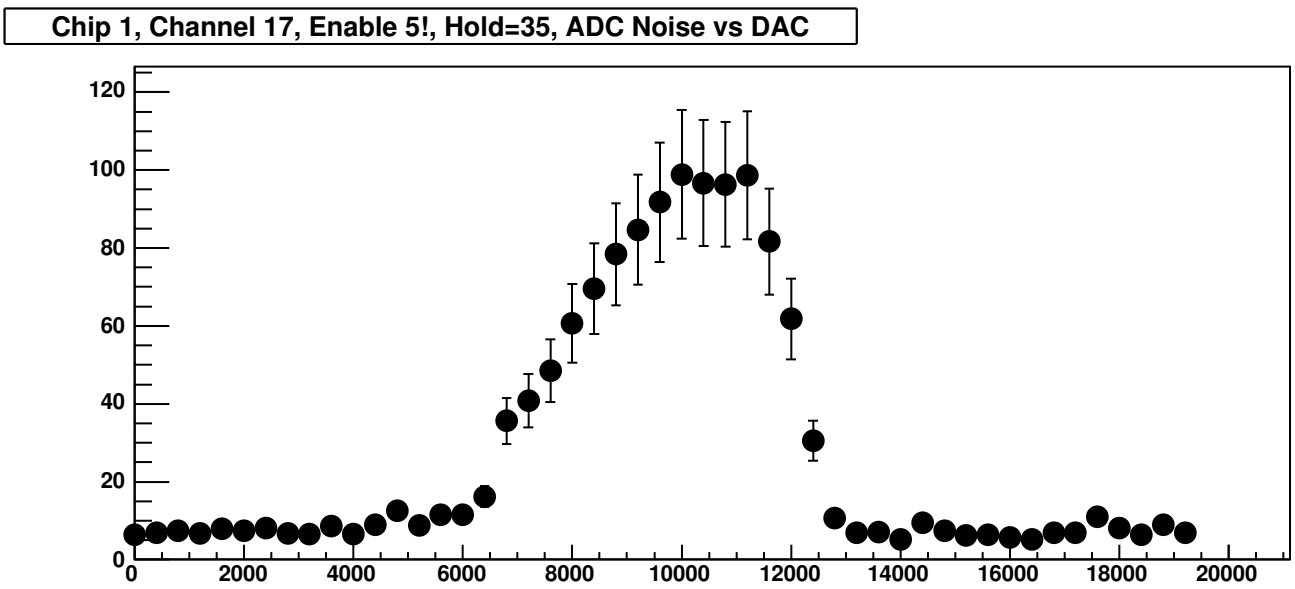

Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

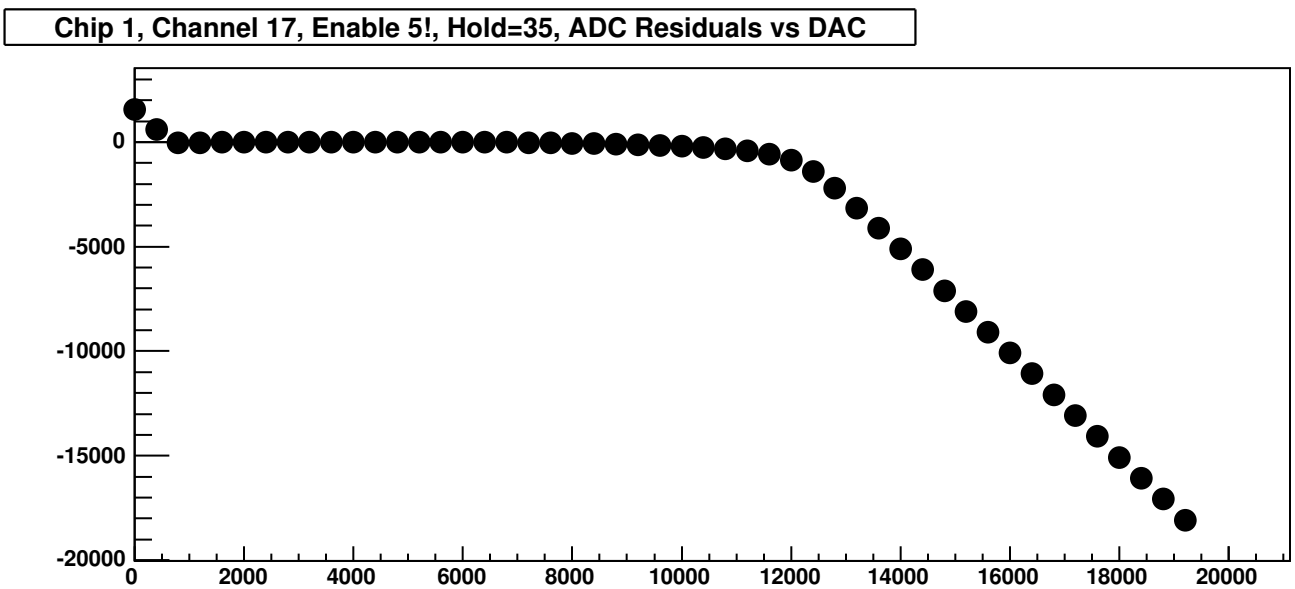

Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.5 / 23
p0 408.5 ± 0.6806
p1 0.01641 ± 0.0001072
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

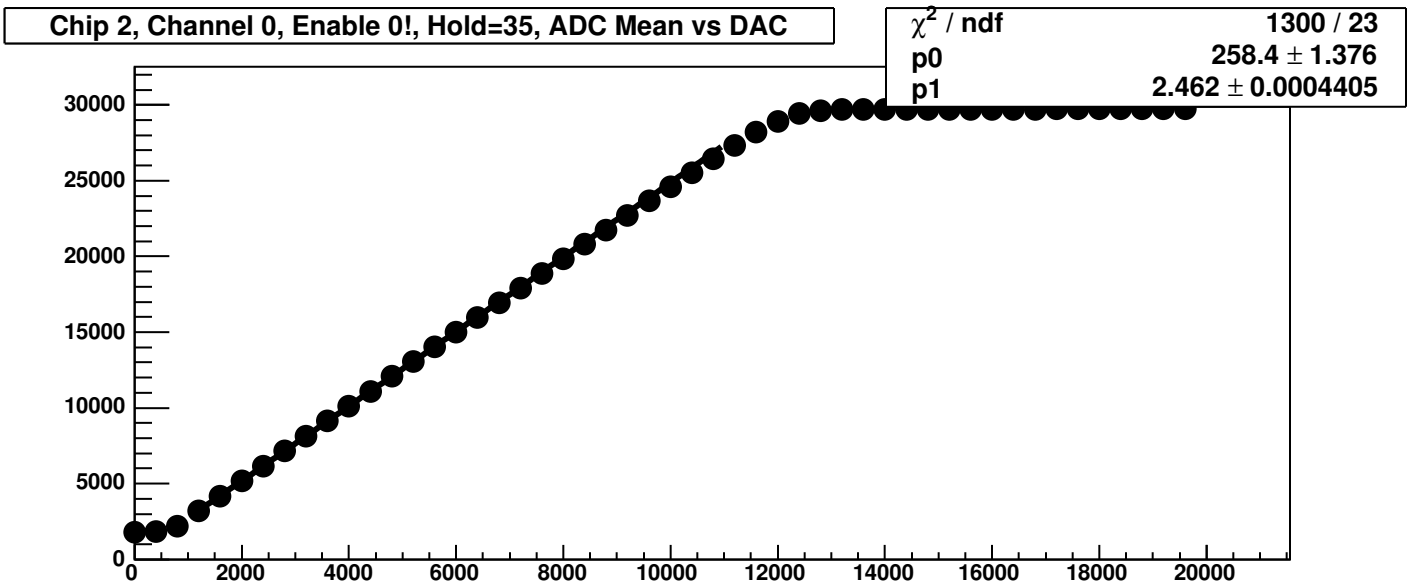

Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

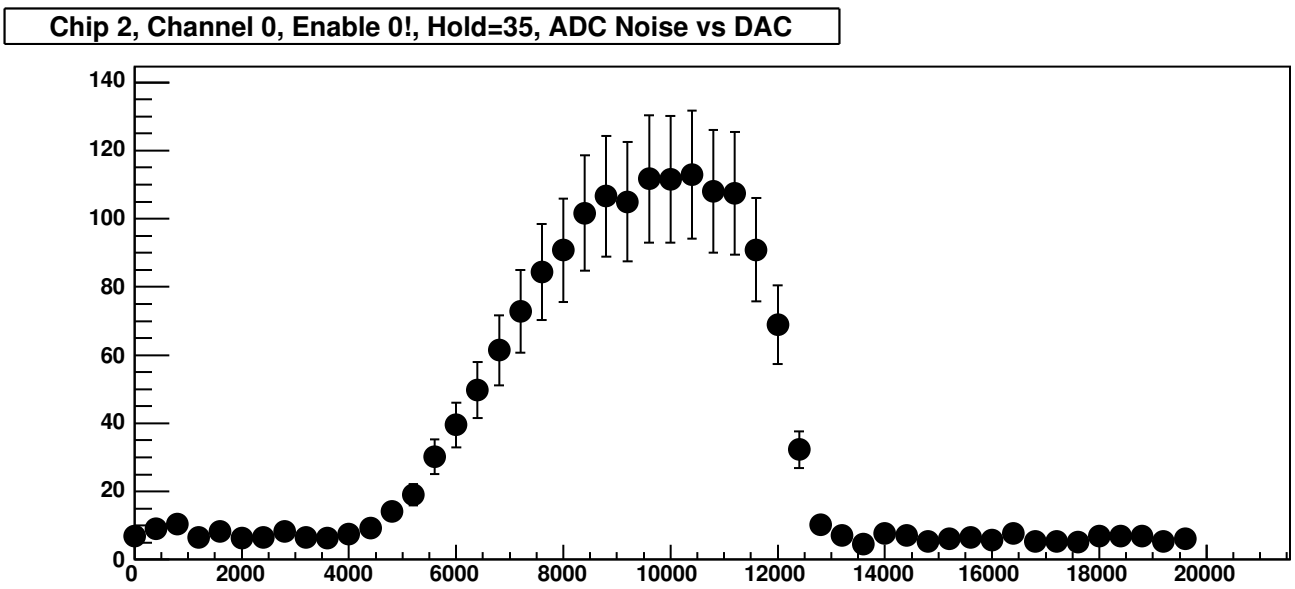

Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

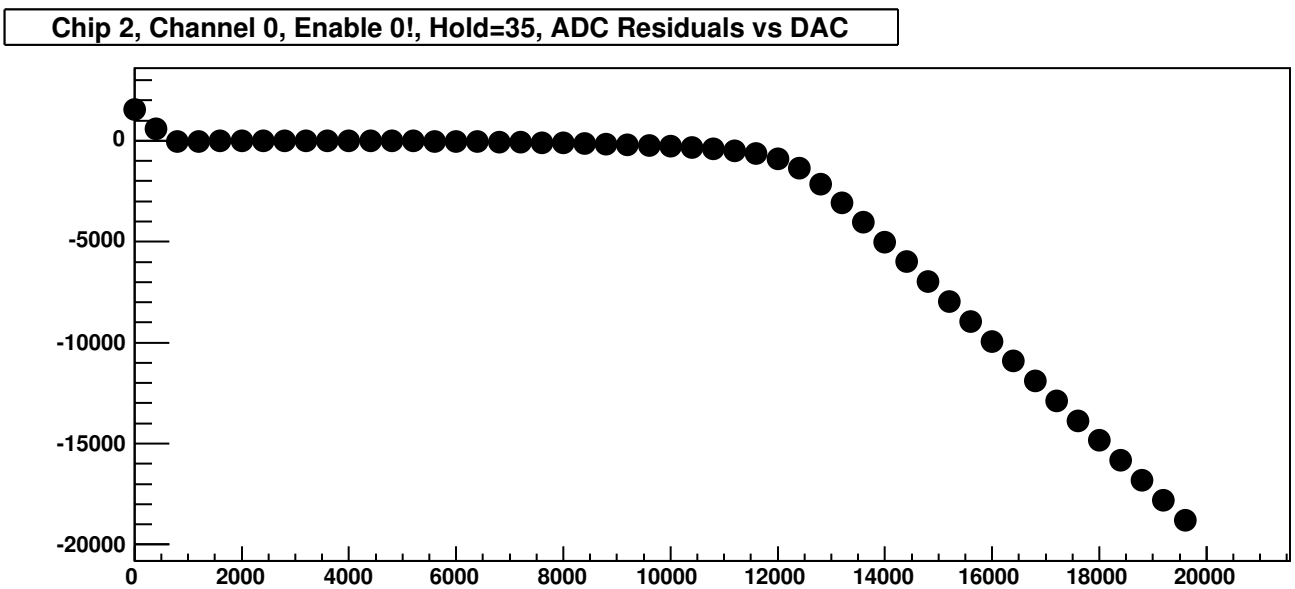

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 864.6 / 23
p0 482.2 ± 1.22
p1 2.494 ± 0.0003323
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

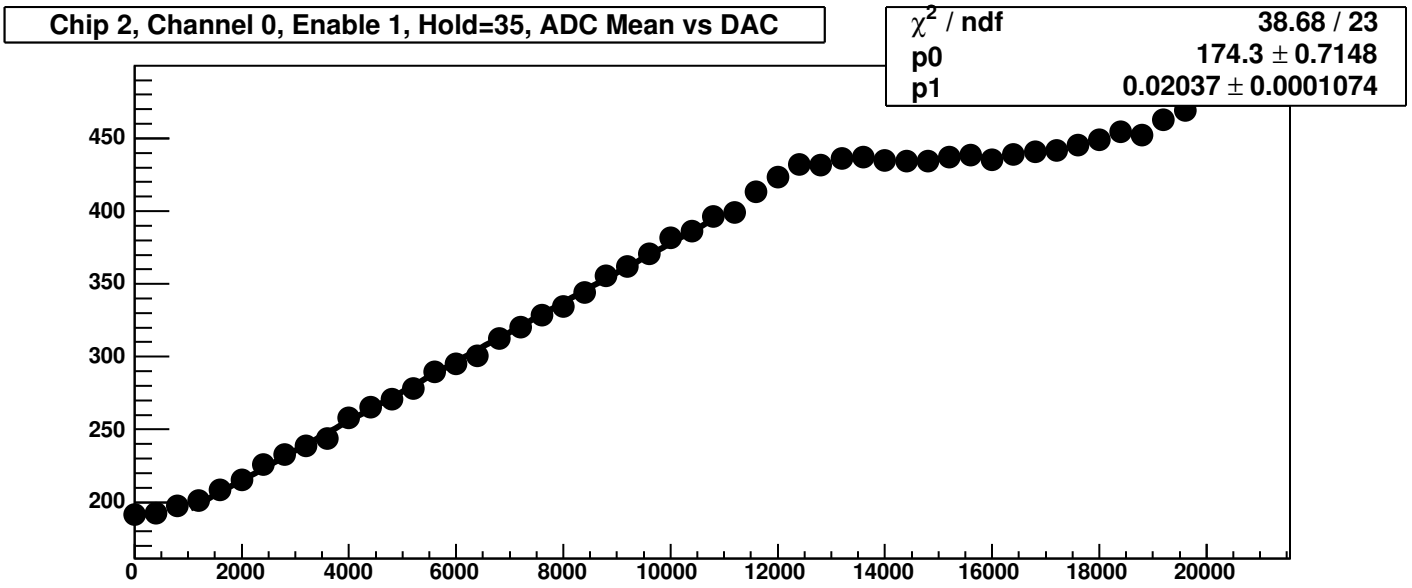

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

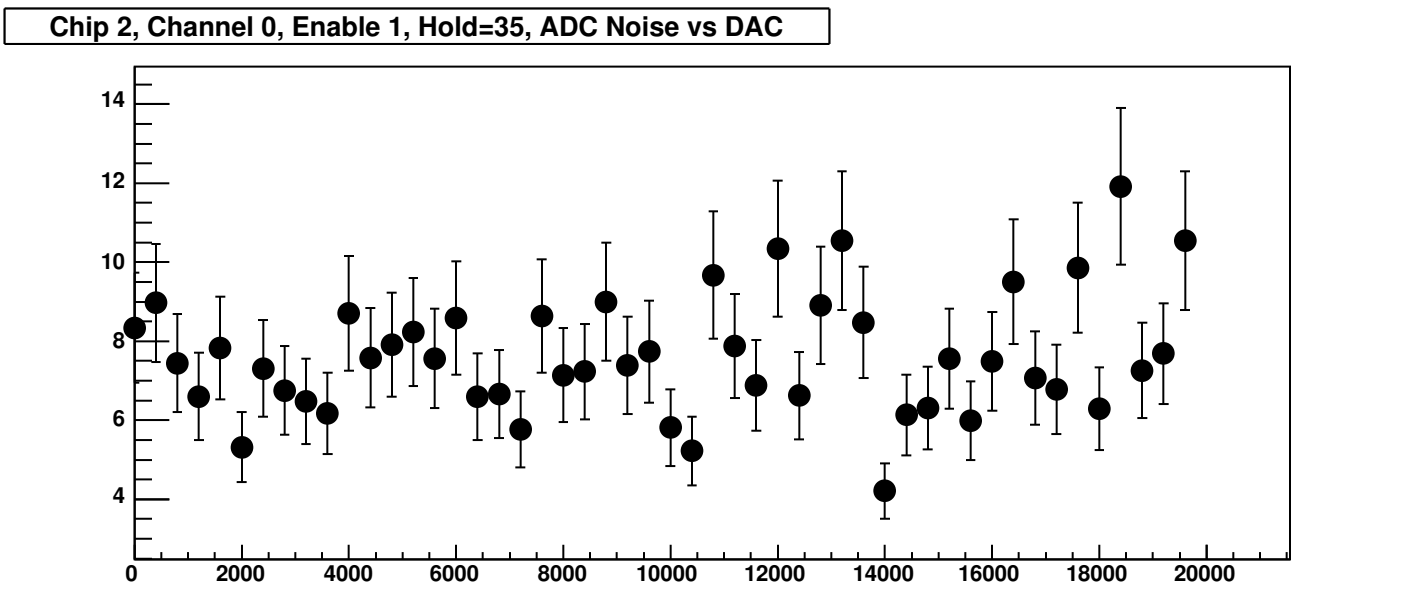

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

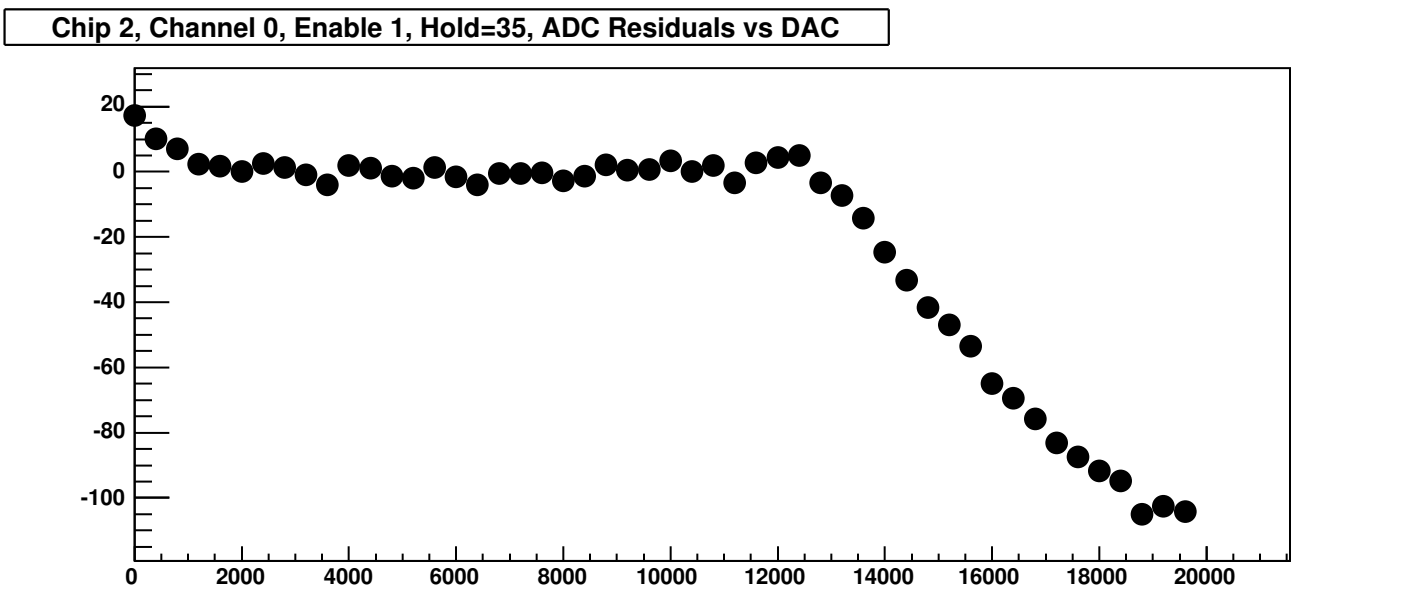

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1300 / 23
p0 258.4 ± 1.376
p1 2.462 ± 0.0004405
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

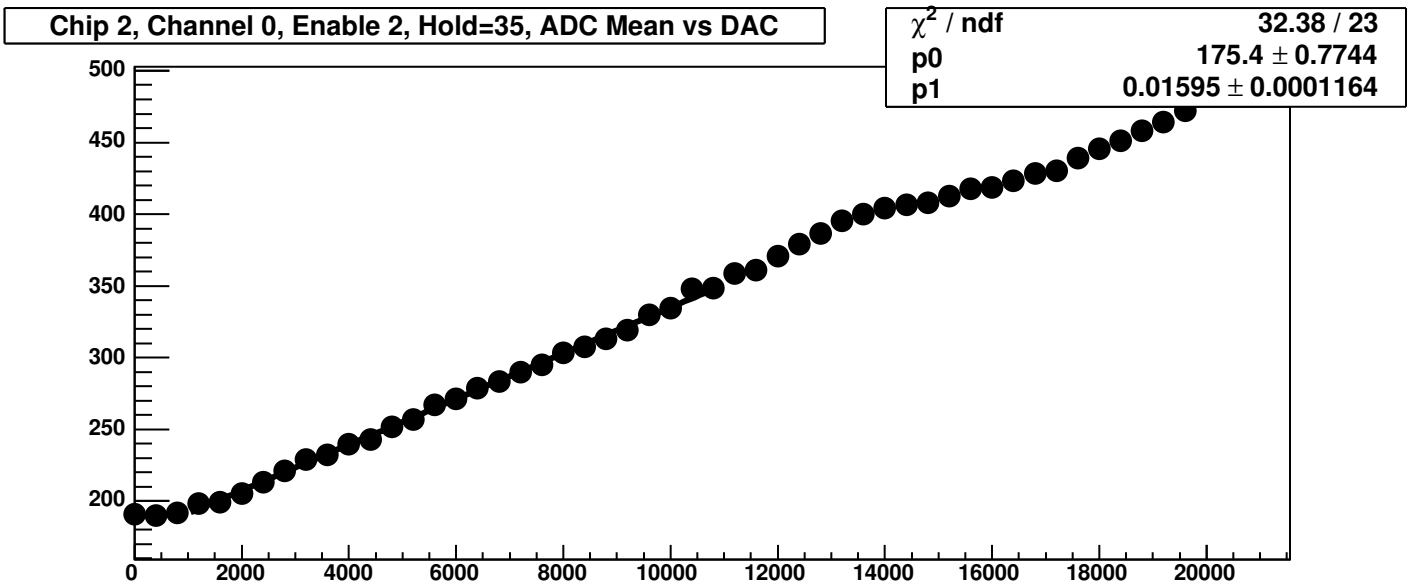

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

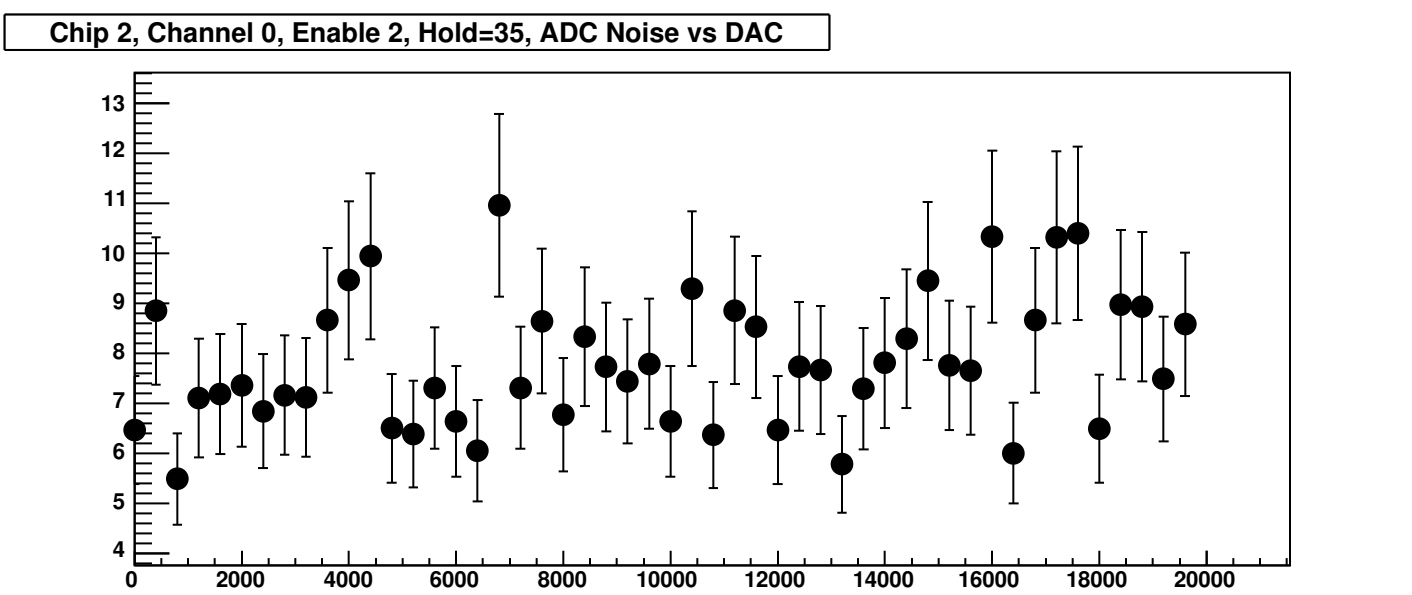

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

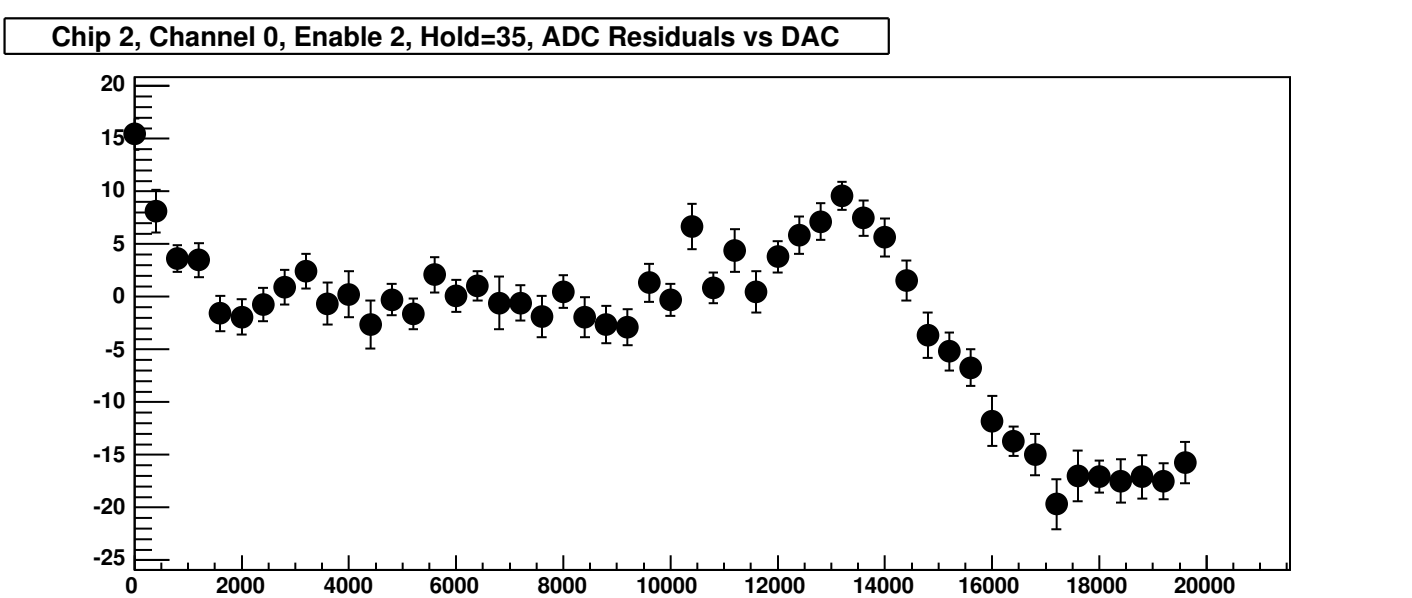

Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.68 / 23
p0 174.3 ± 0.7148
p1 0.02037 ± 0.0001074
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

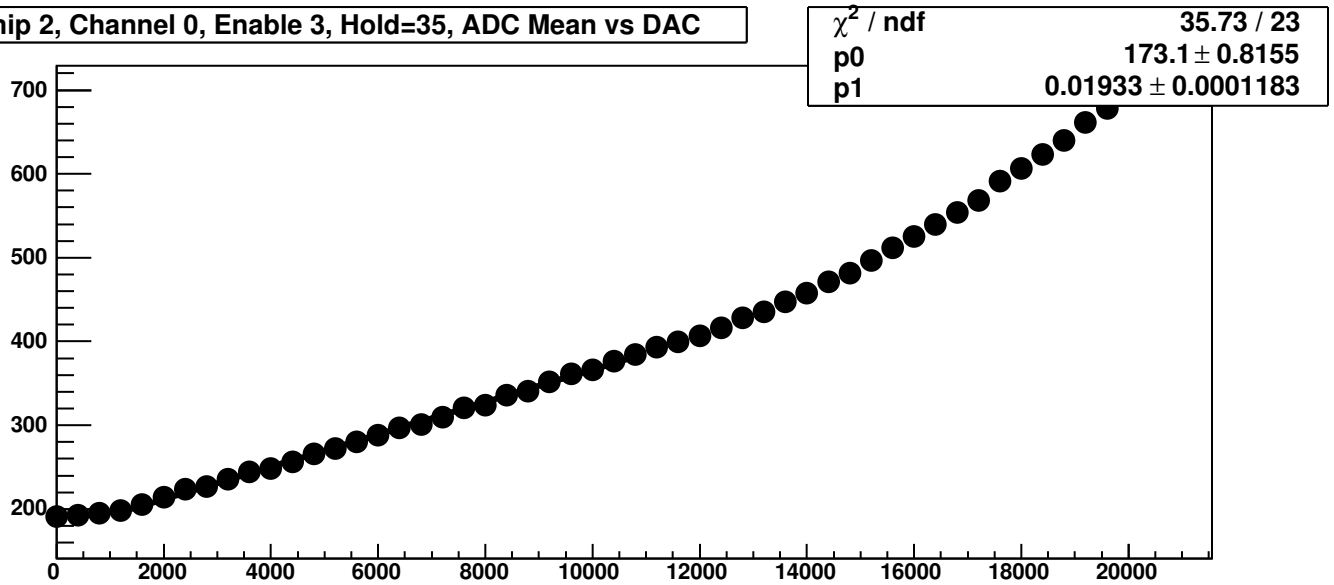

Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.38 / 23
p0 175.4 ± 0.7744
p1 0.01595 ± 0.0001164
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

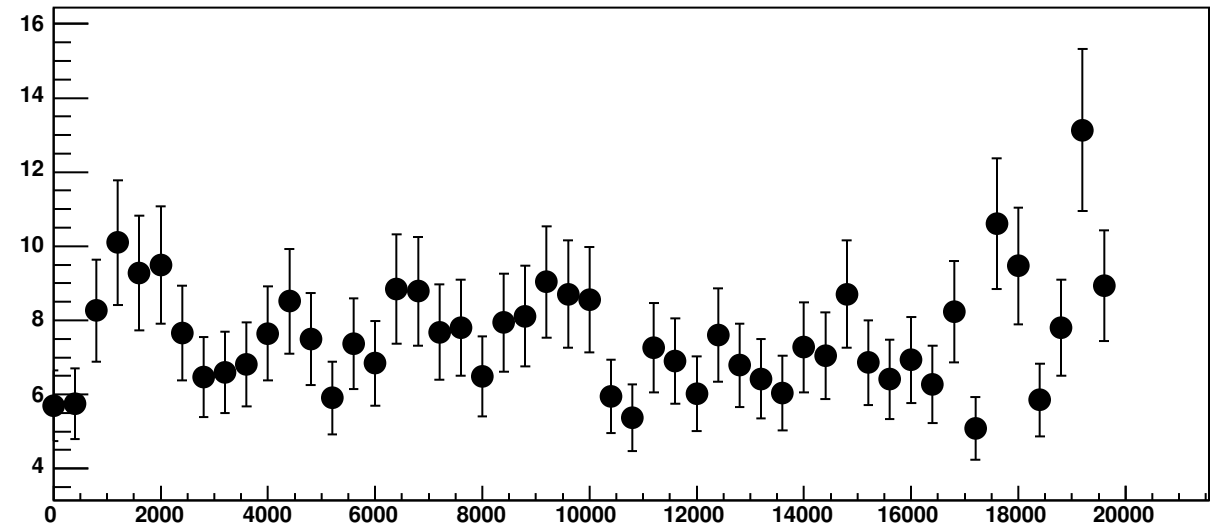

Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

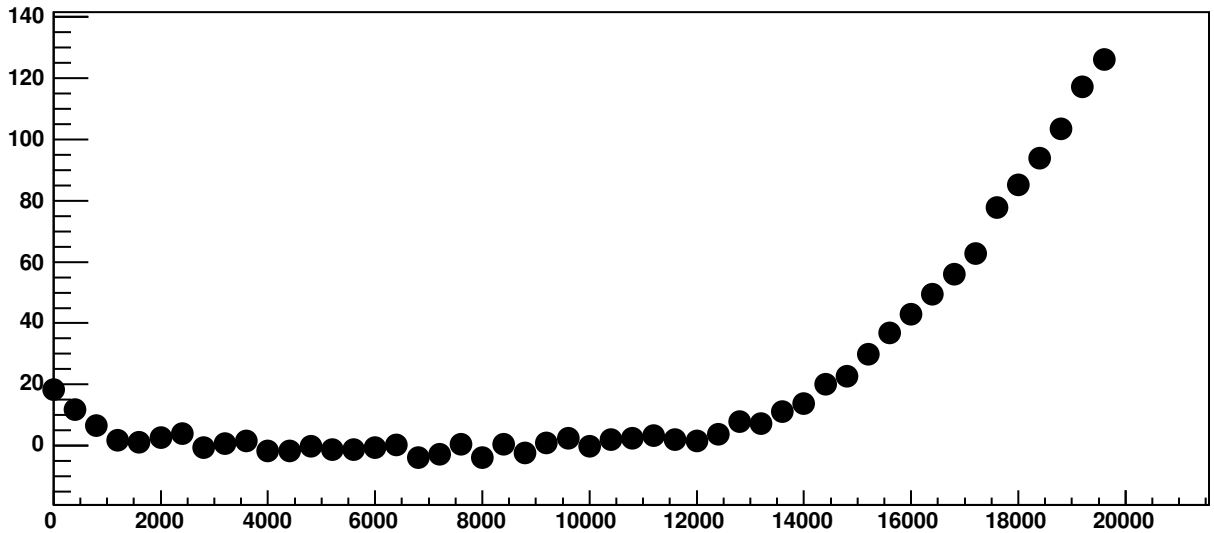
Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC



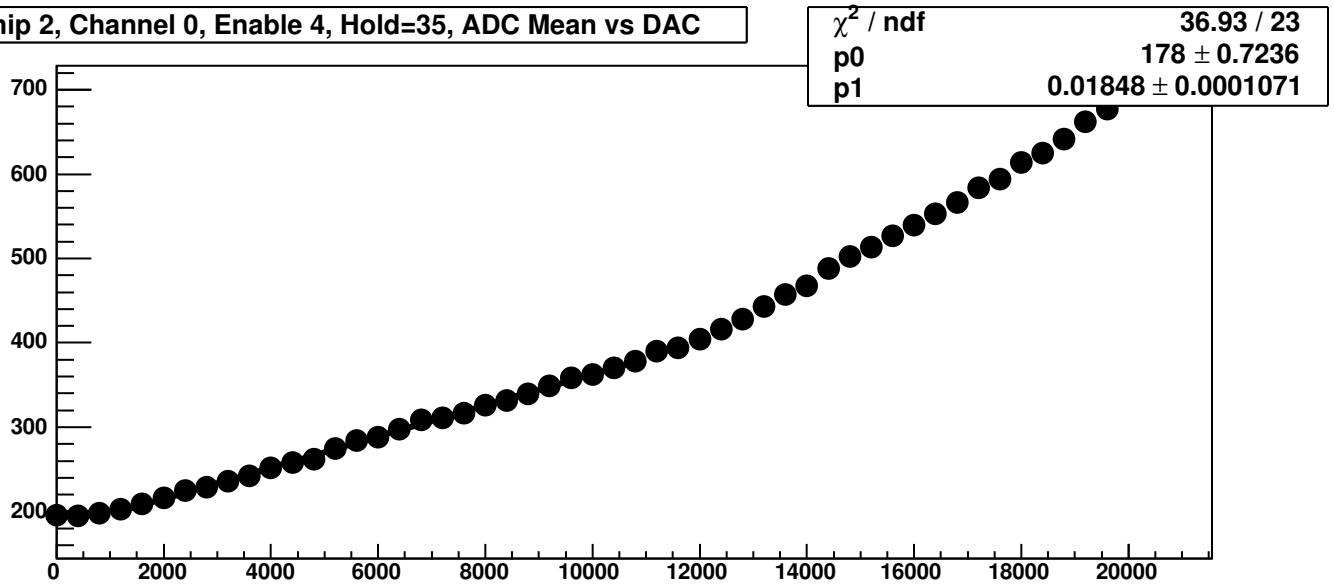
Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



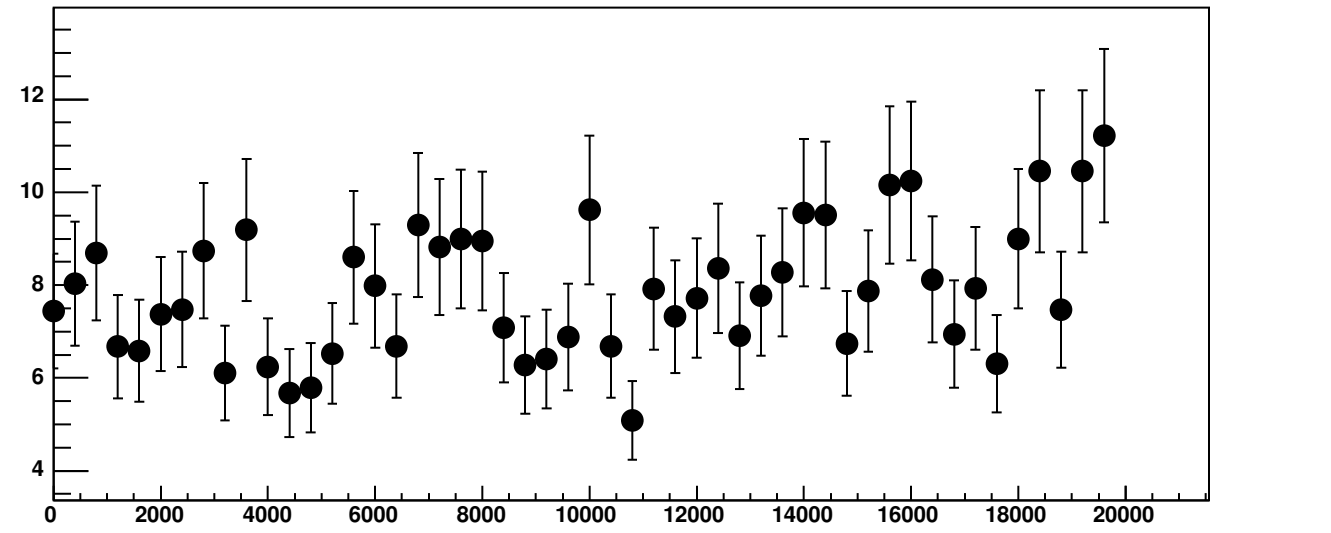
Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



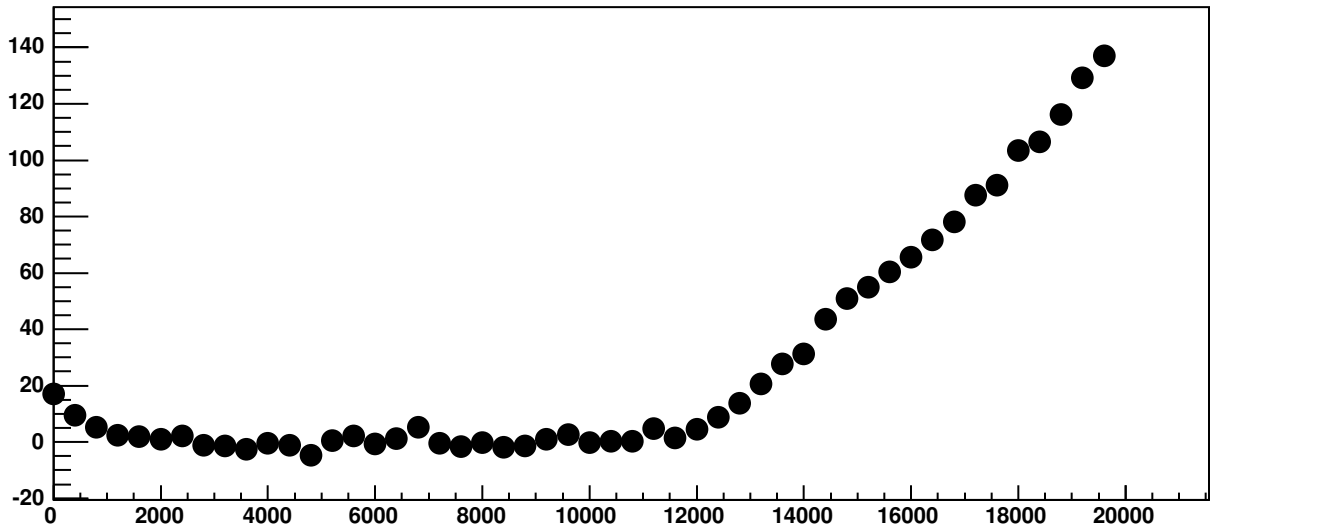
Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



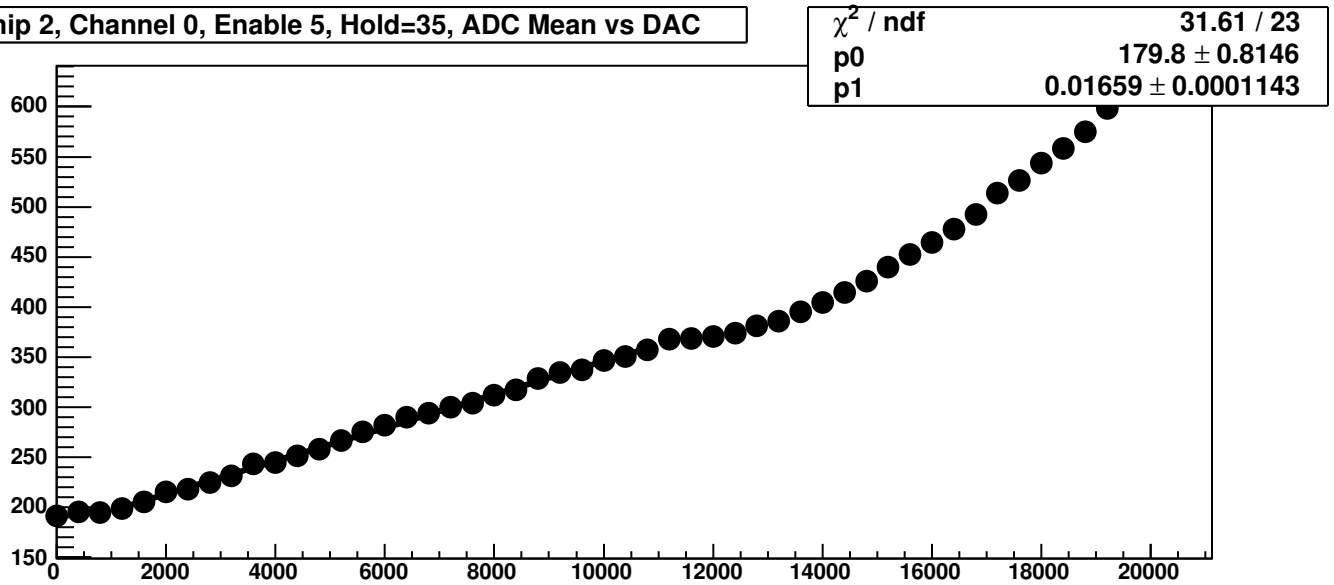
Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



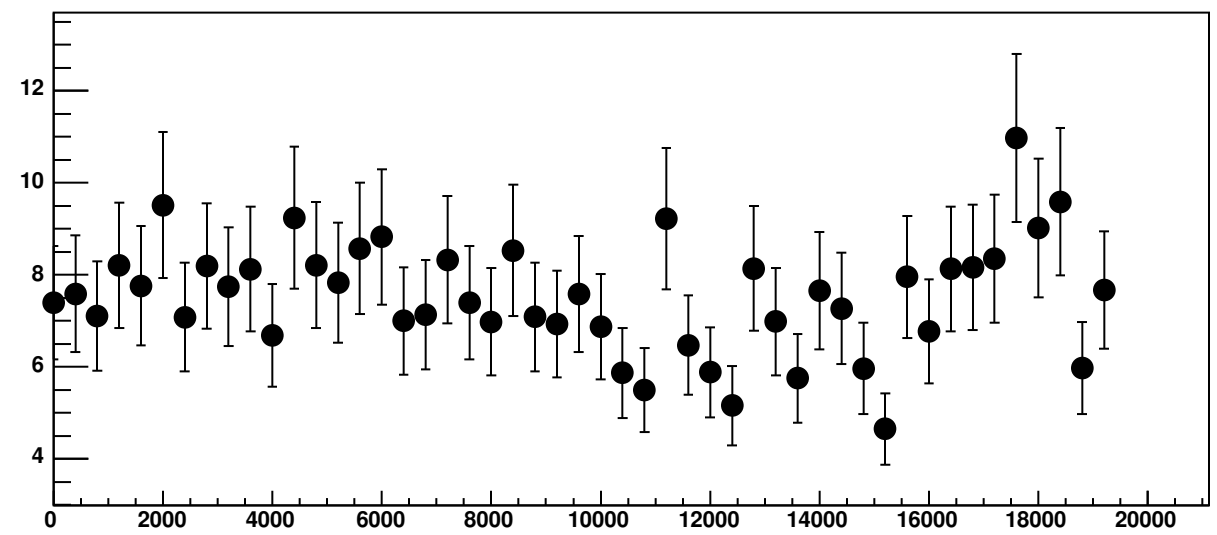
Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



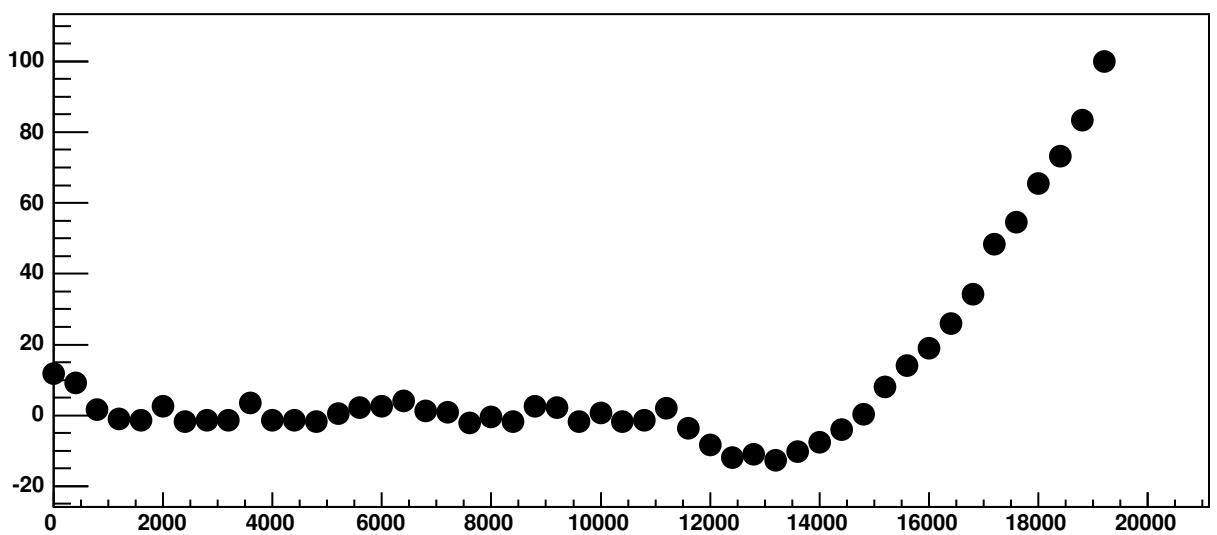
Chip 2, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



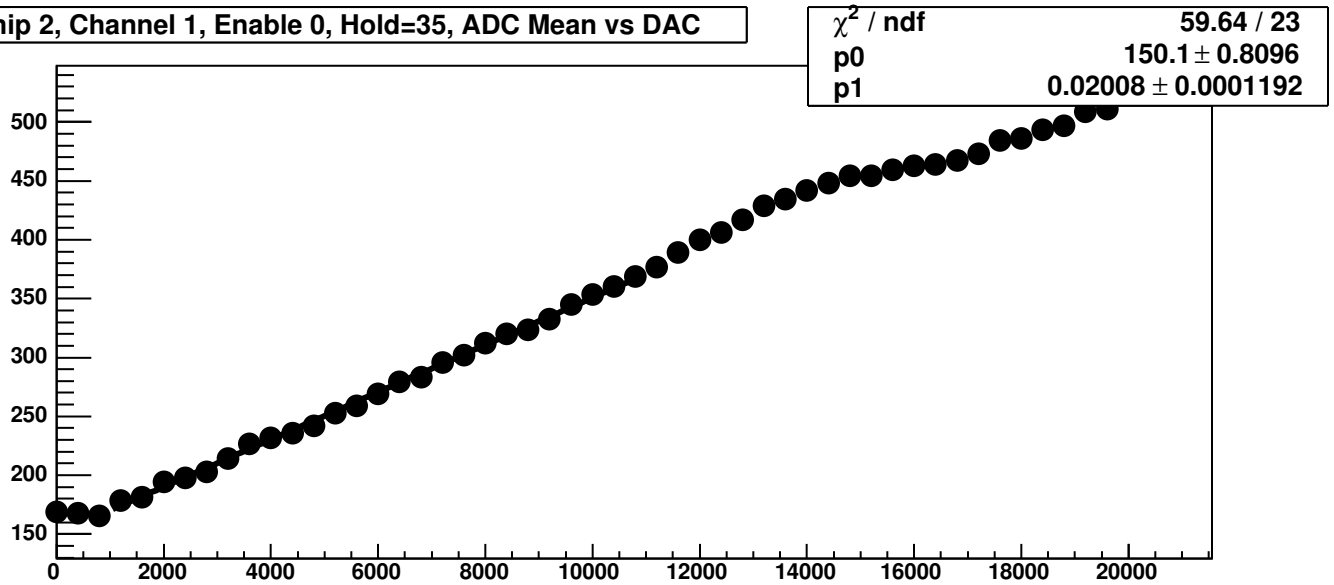
Chip 2, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



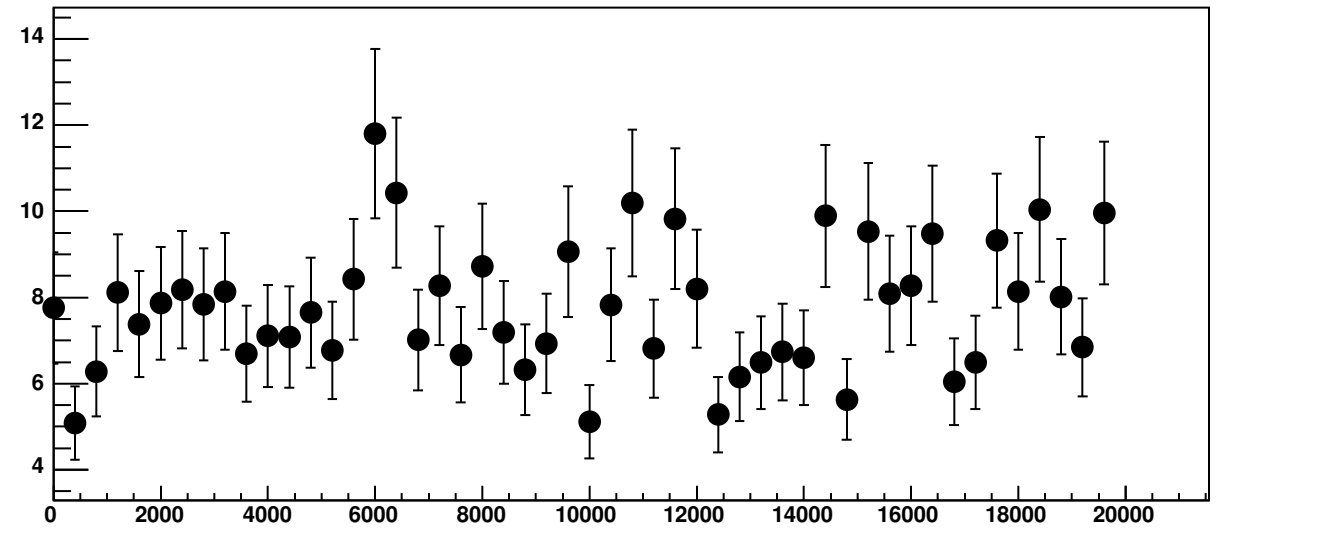
Chip 2, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



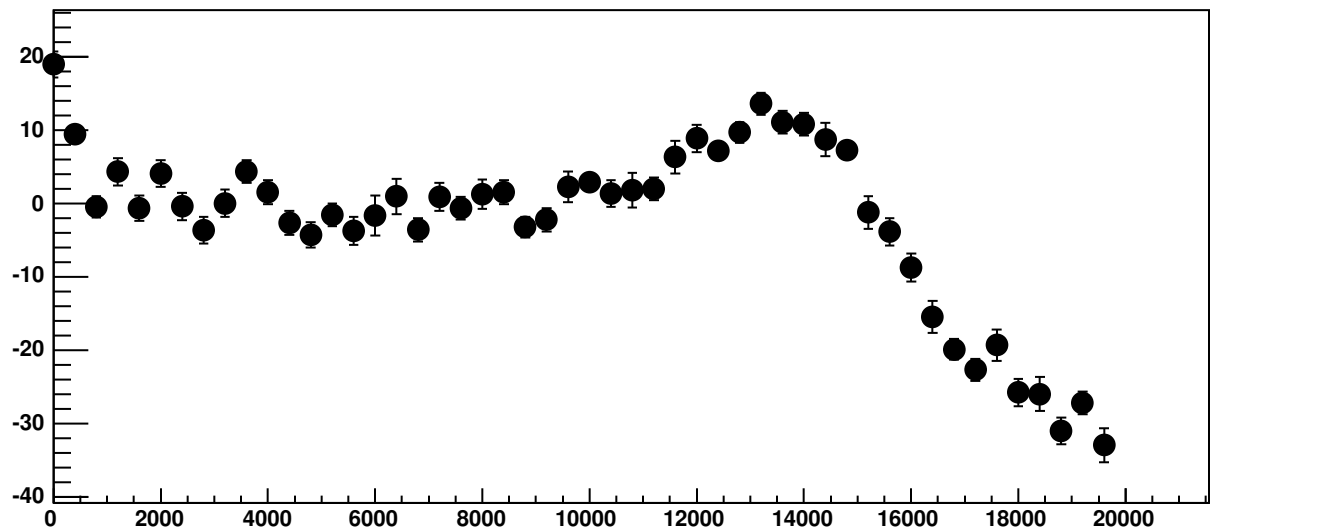
Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



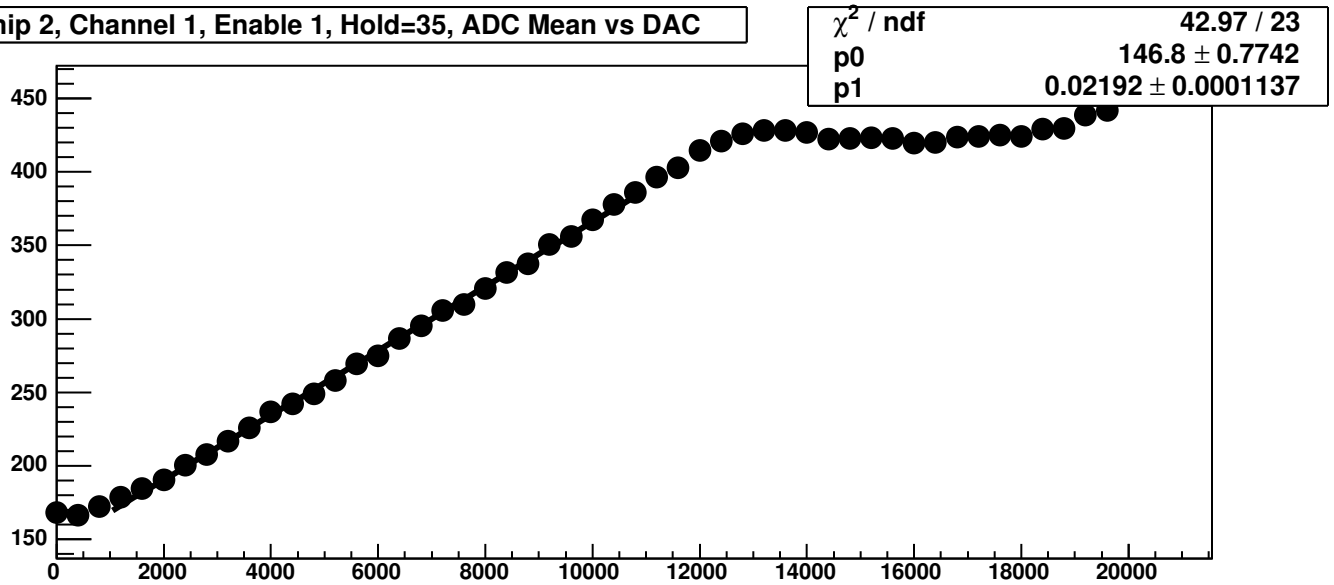
Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



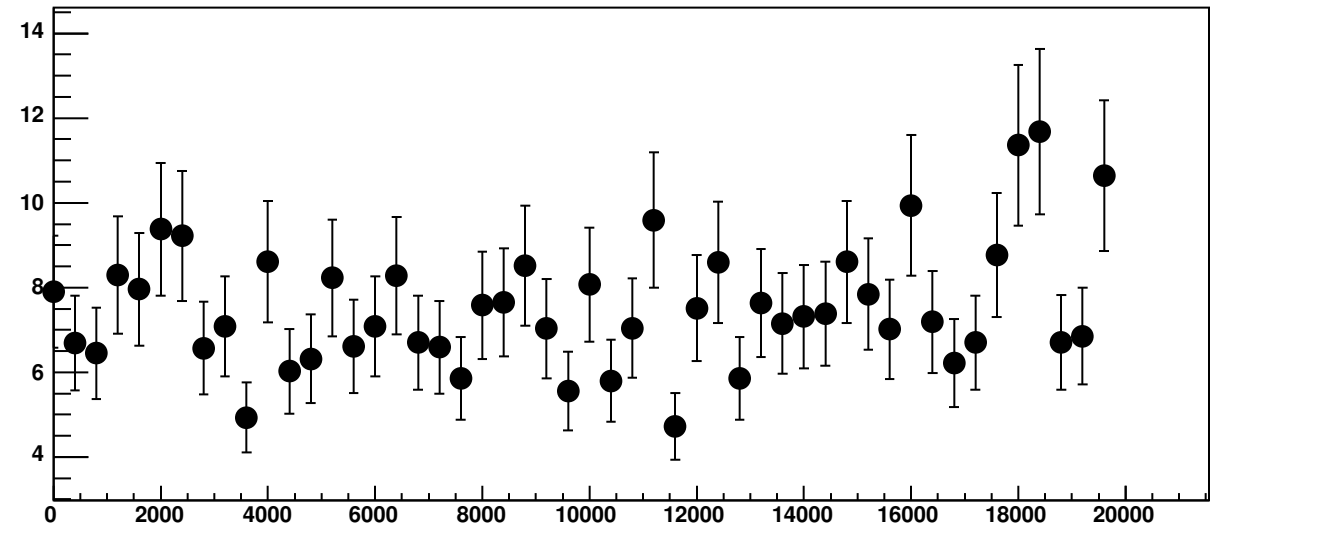
Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC



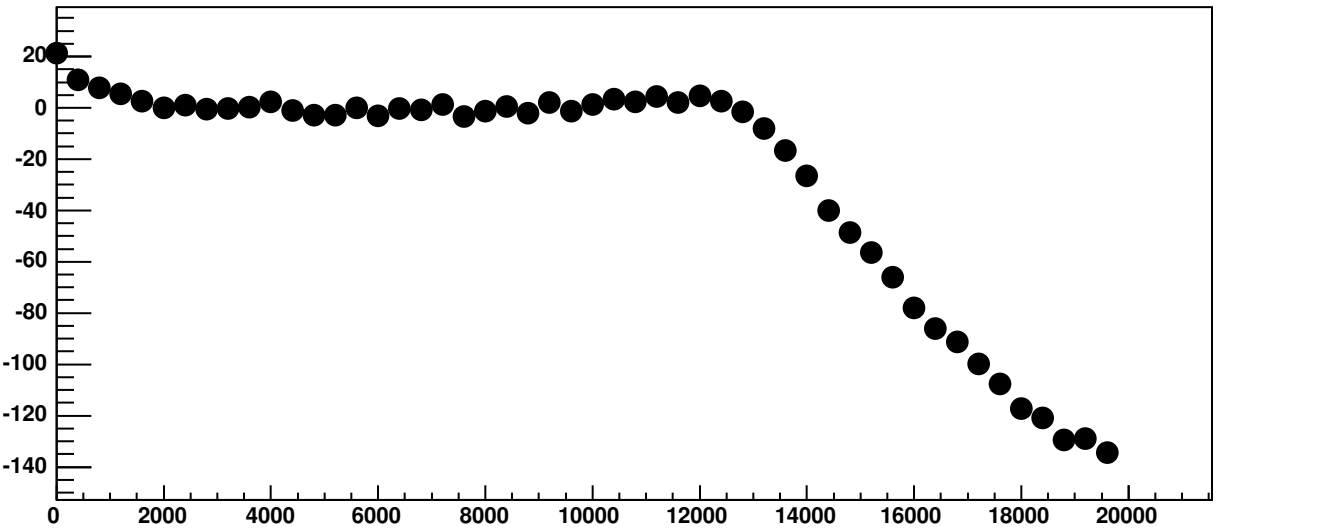
Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



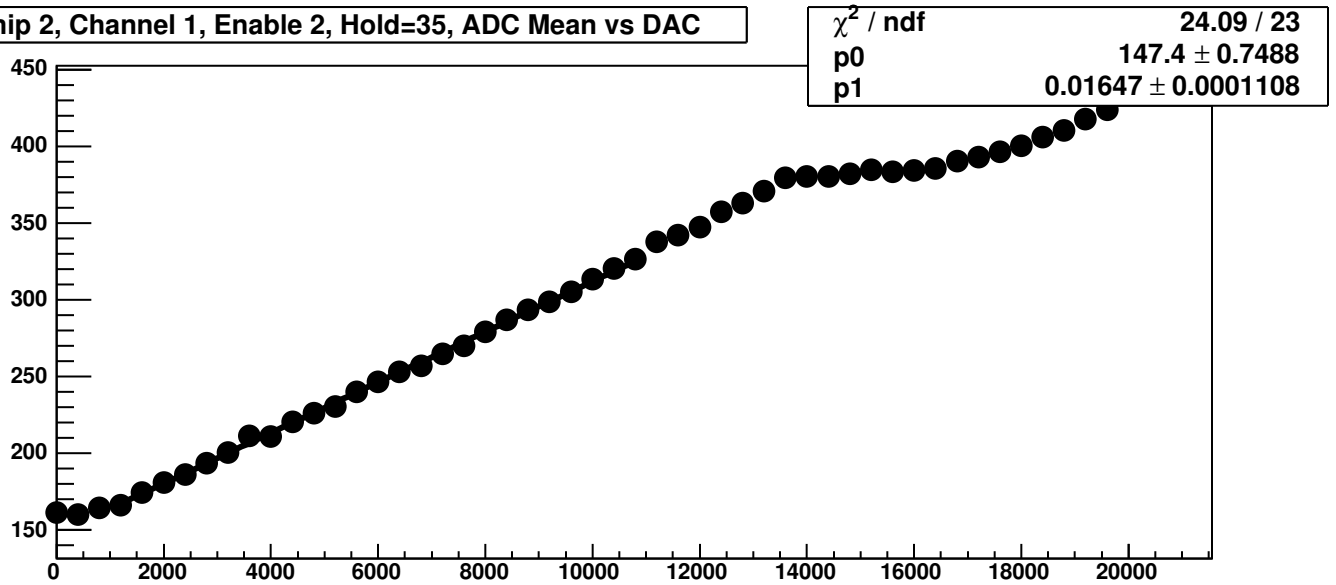
Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



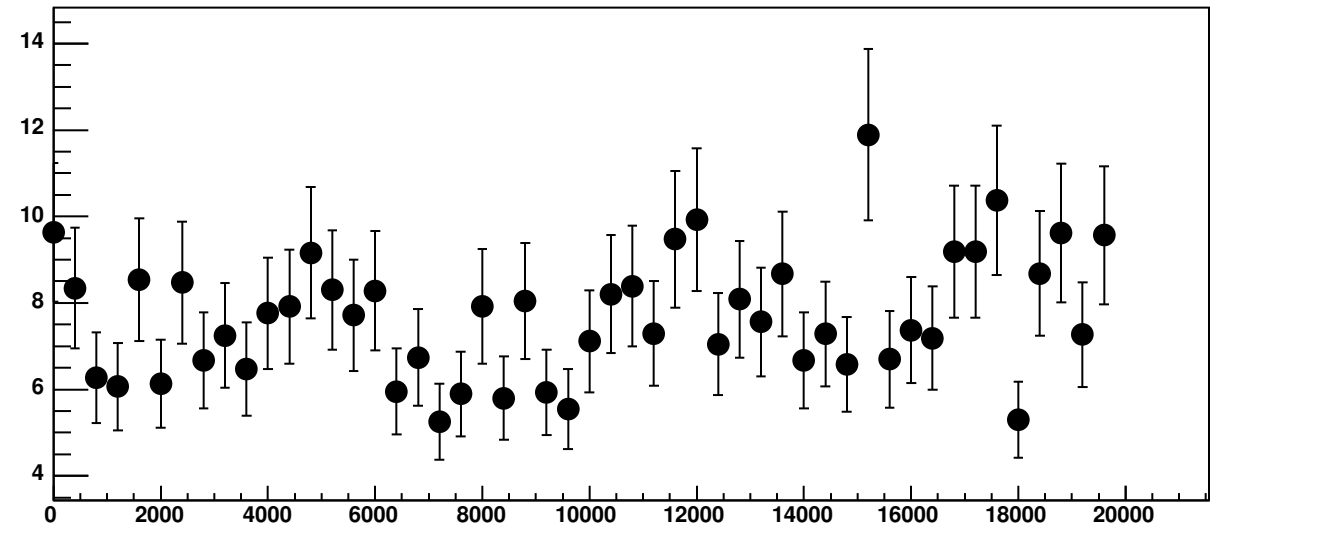
Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



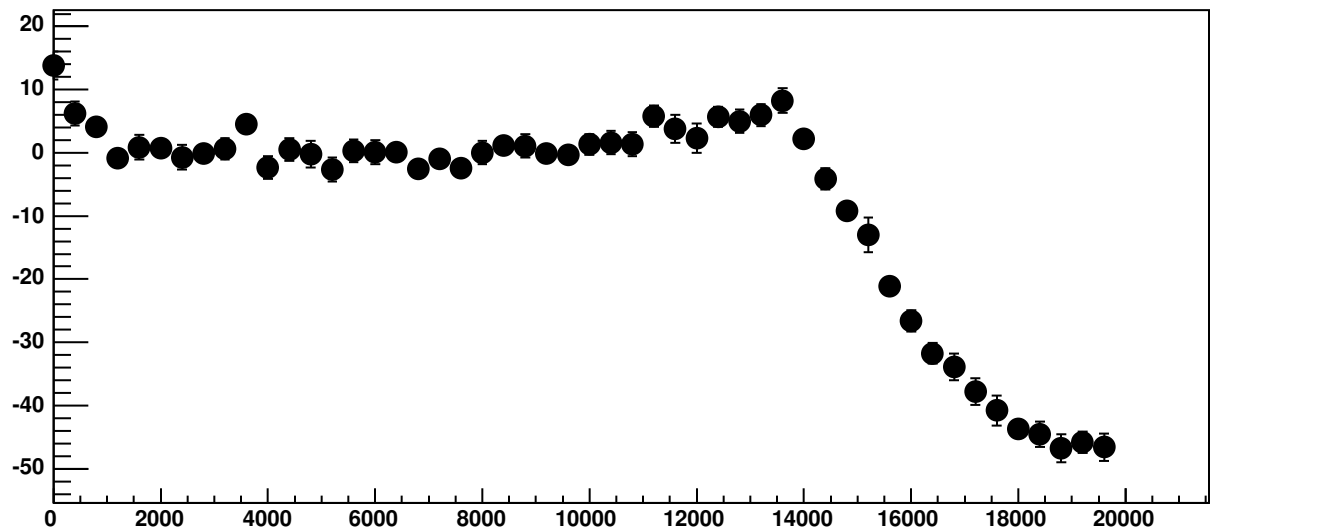
Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

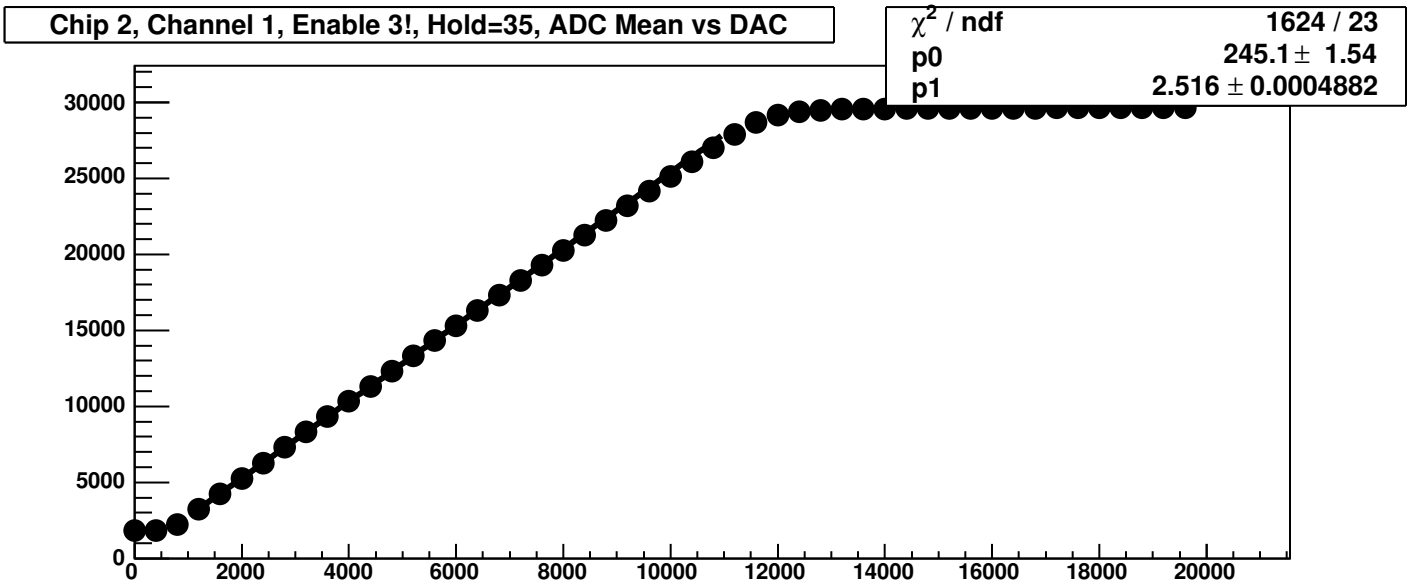


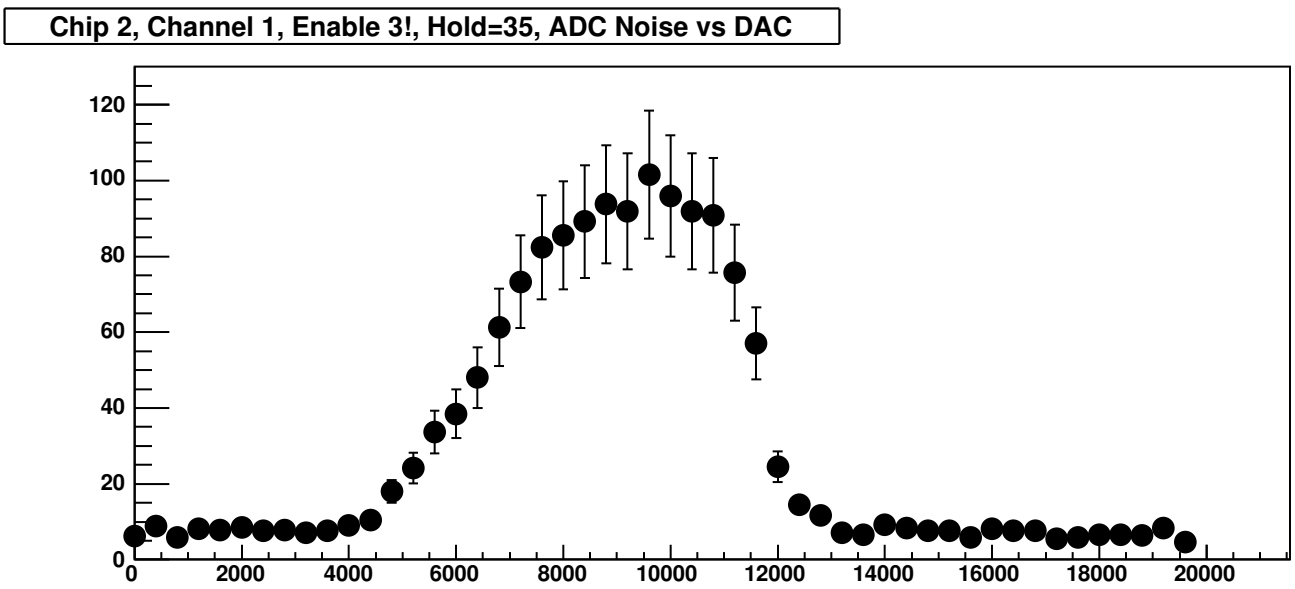
Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

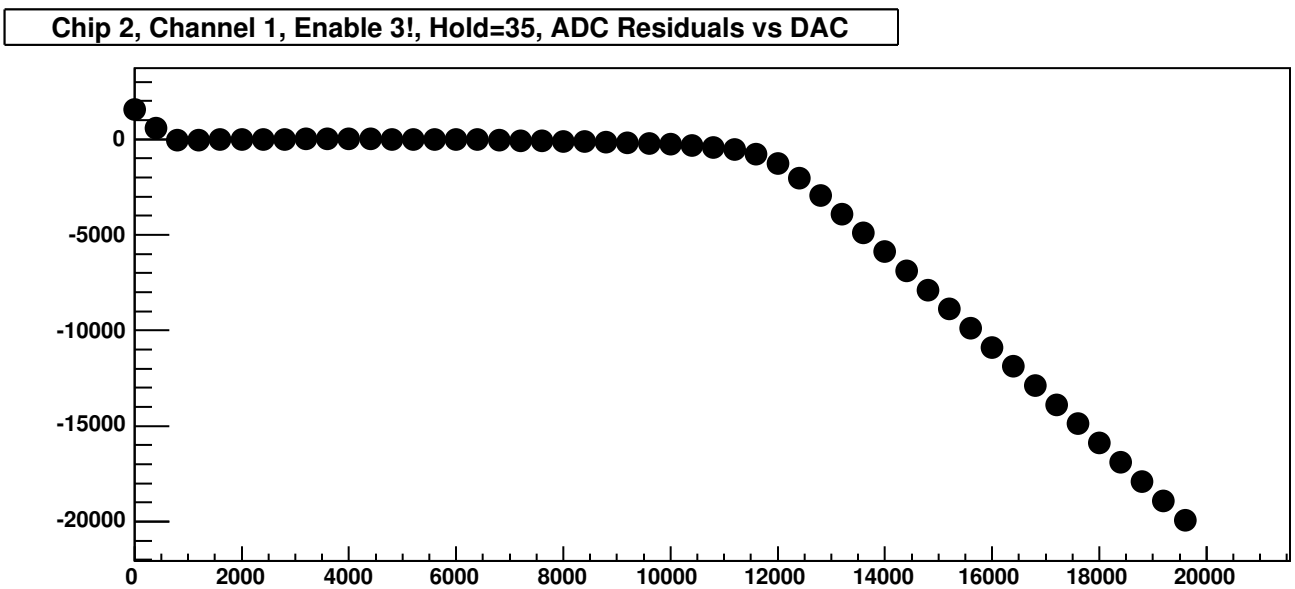


Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

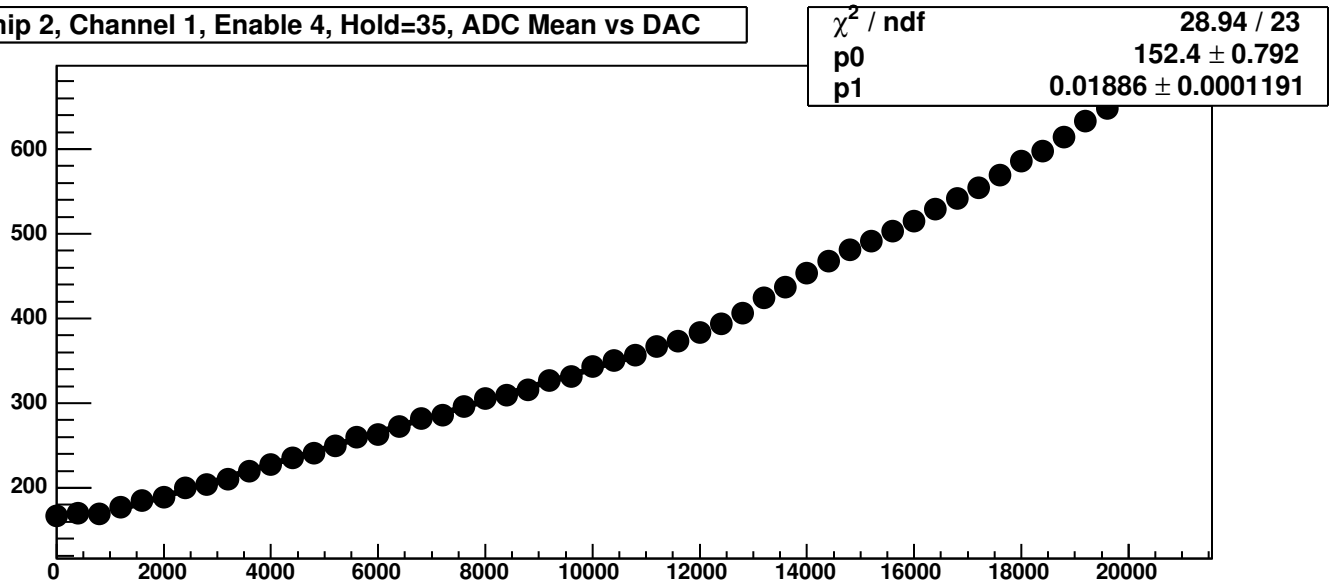



Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1624 / 23
p0 245.1 ± 1.54
p1 2.516 ± 0.0004882
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

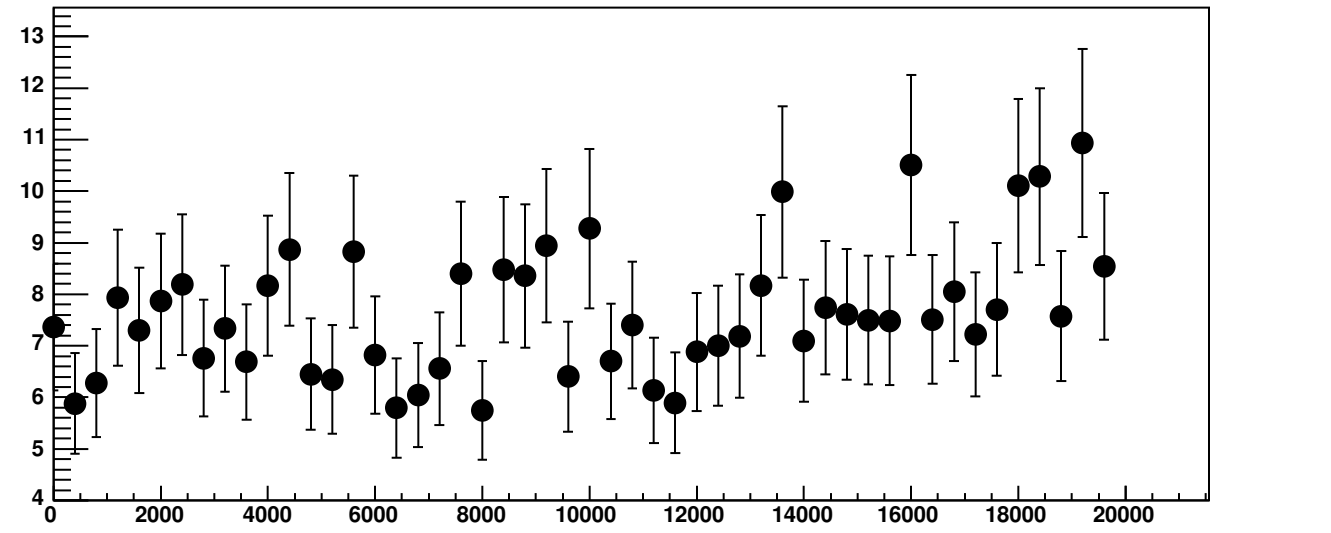

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

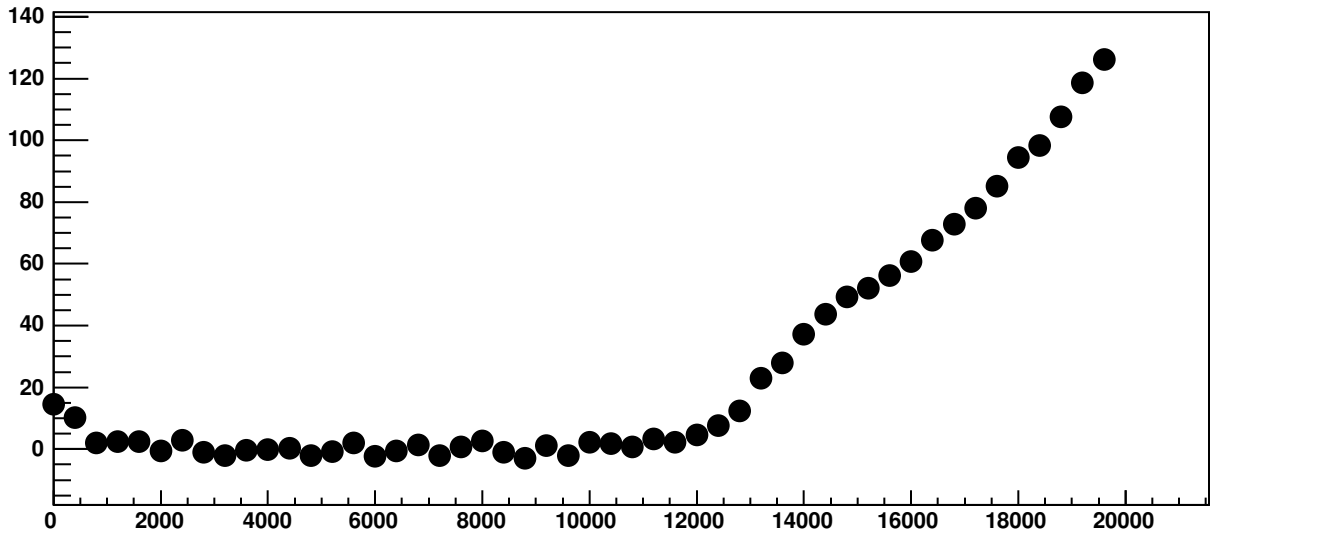
Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

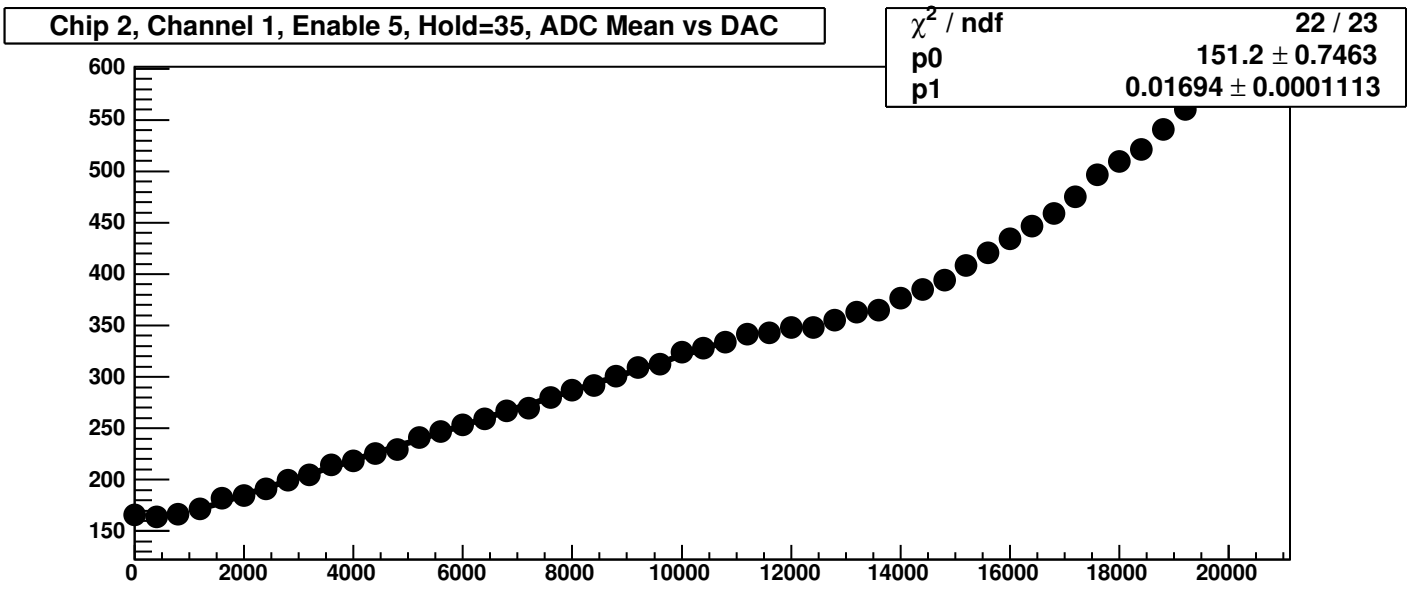


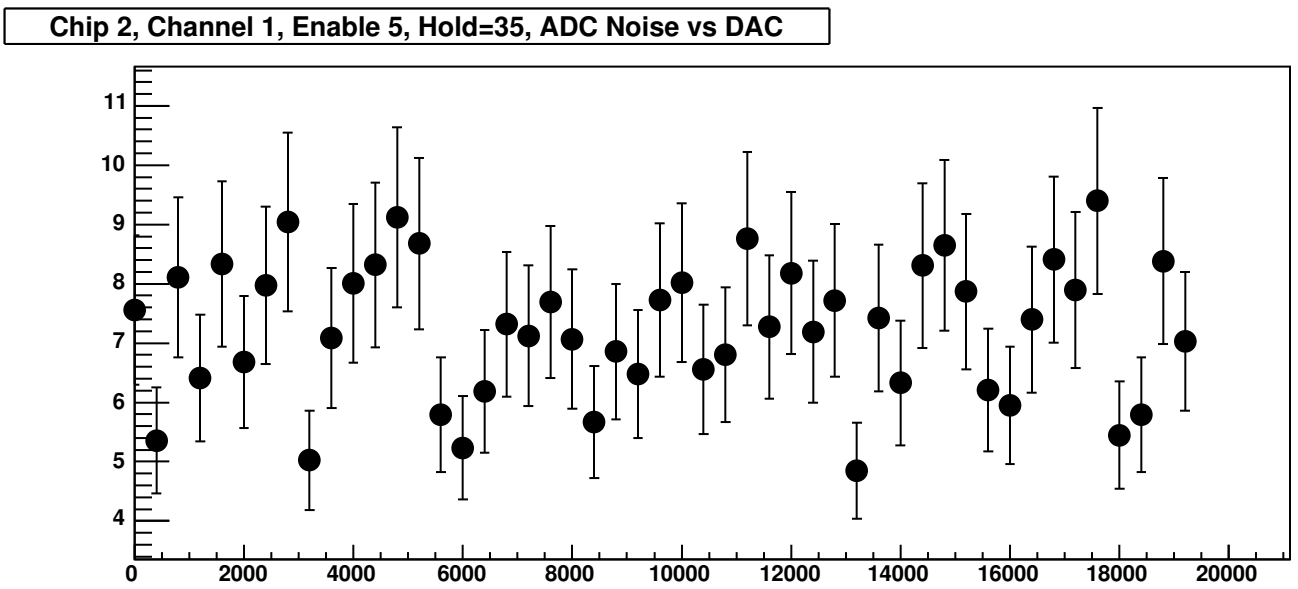
Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

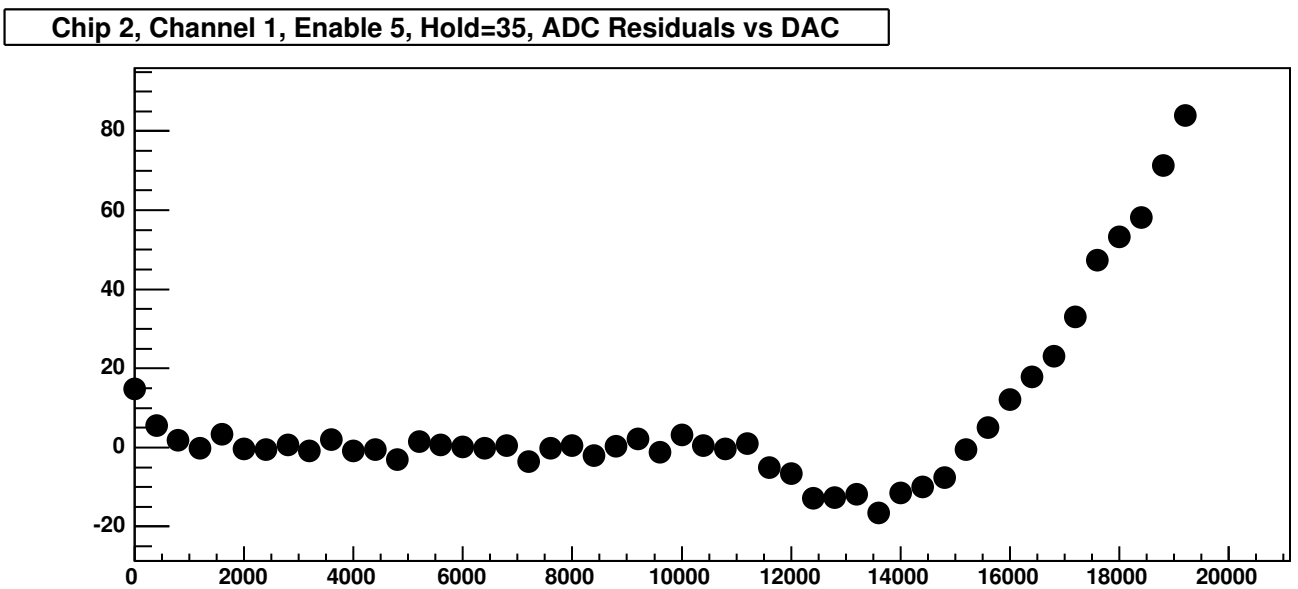


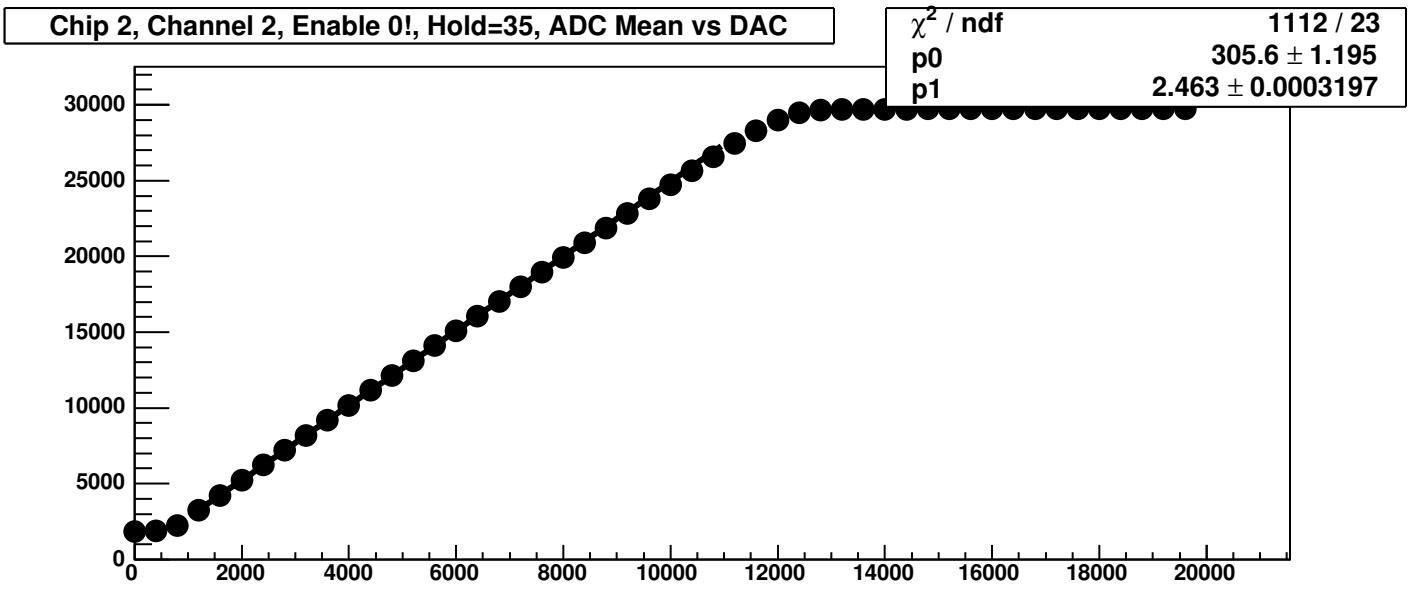
Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

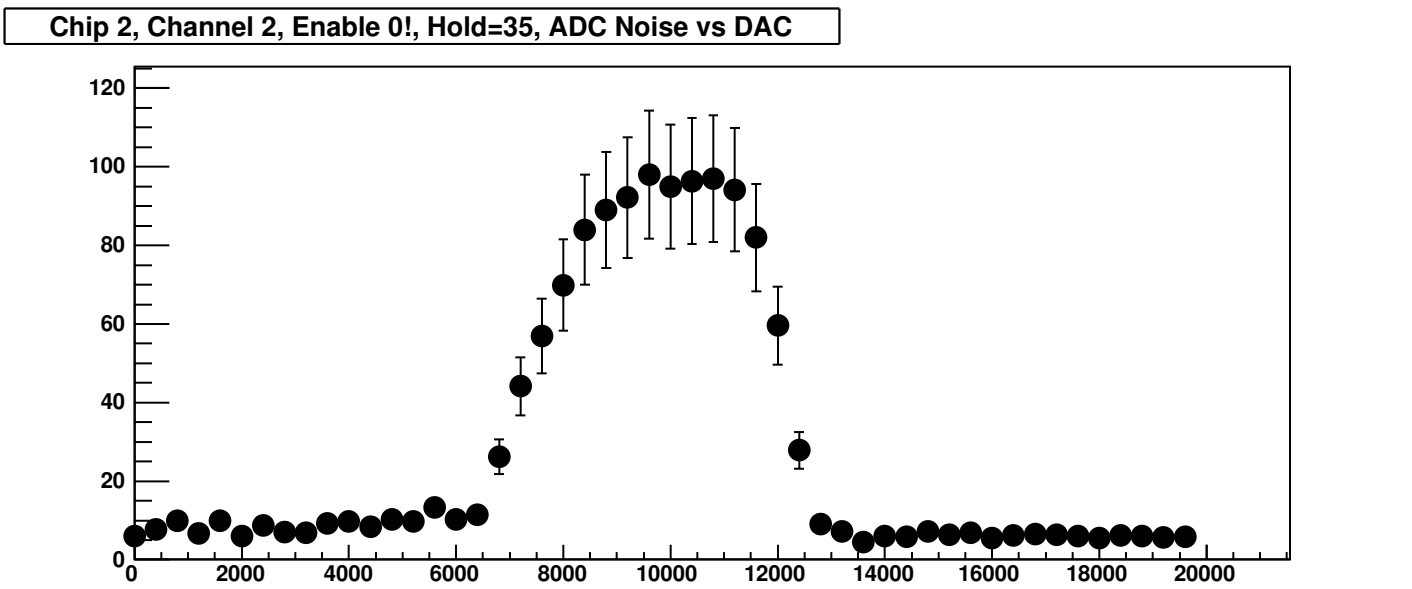


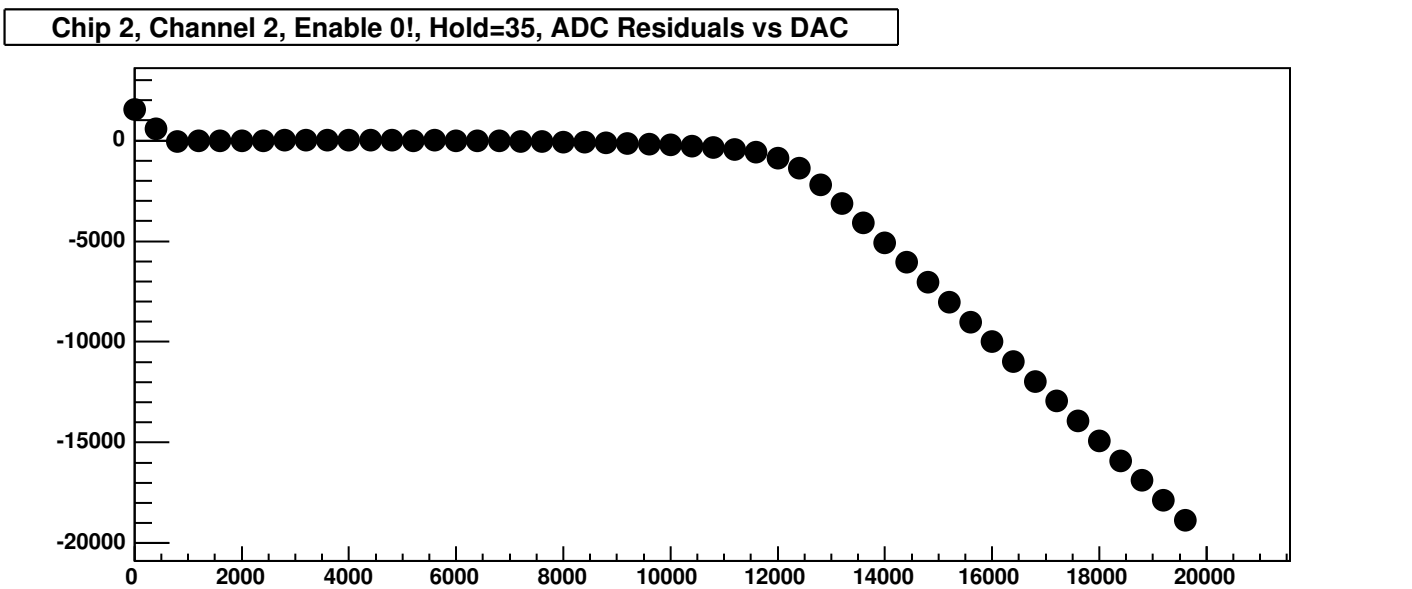

Chip 2, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 22 / 23
p0 151.2 ± 0.7463
p1 0.01694 ± 0.0001113
600
550
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

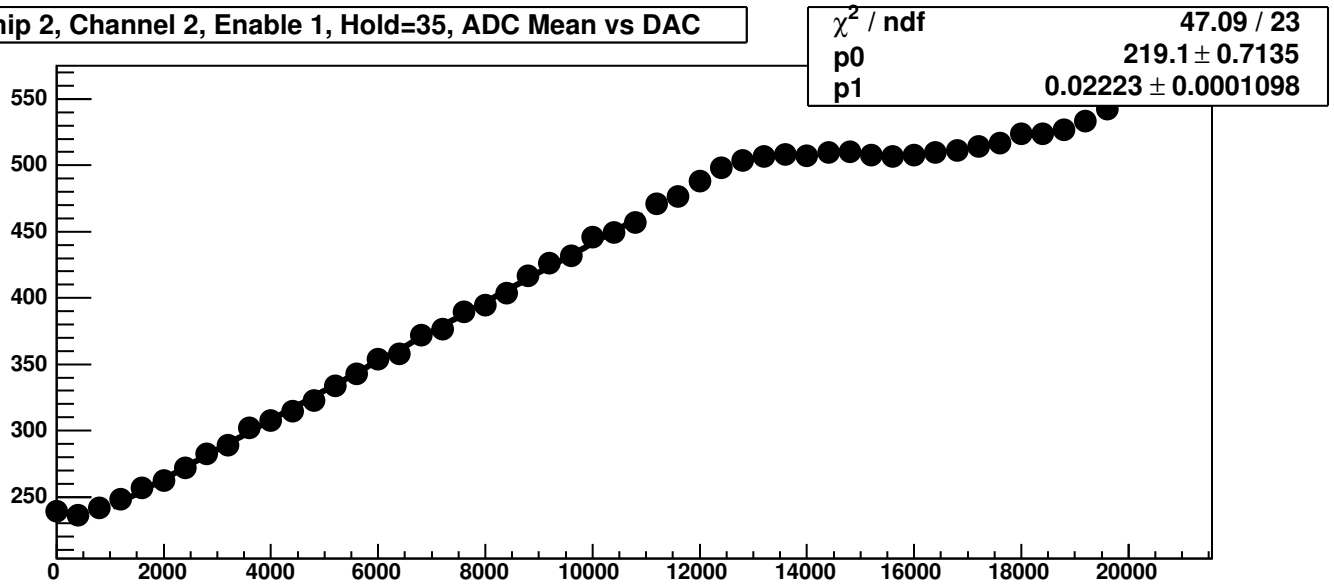

Chip 2, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1112 / 23
p0 305.6 ± 1.195
p1 2.463 ± 0.0003197
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

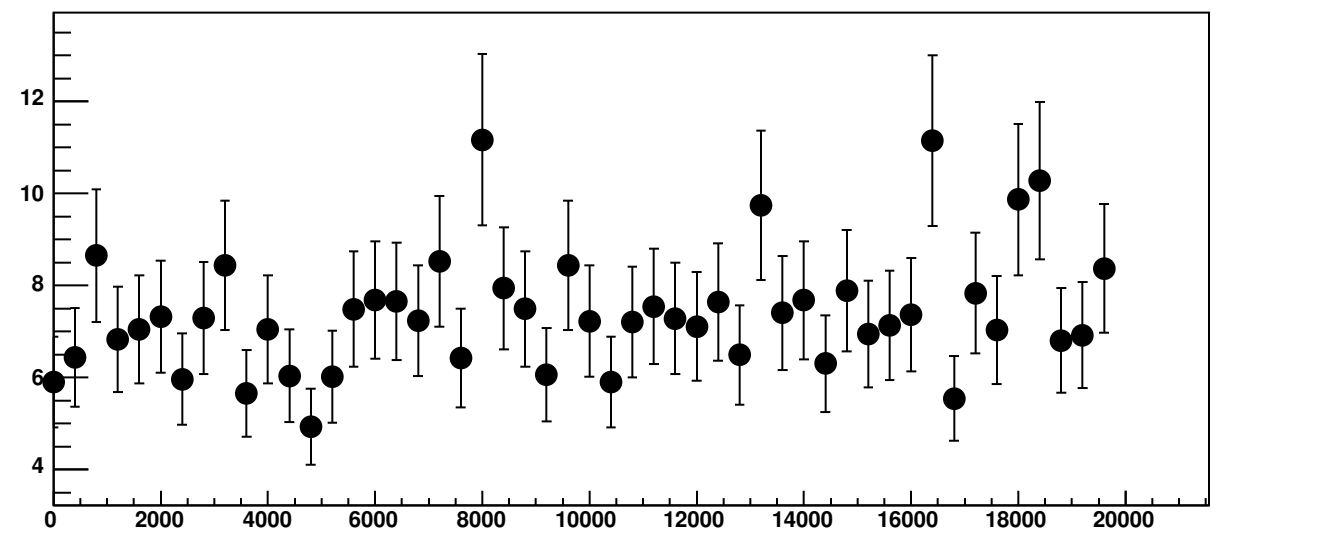

Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

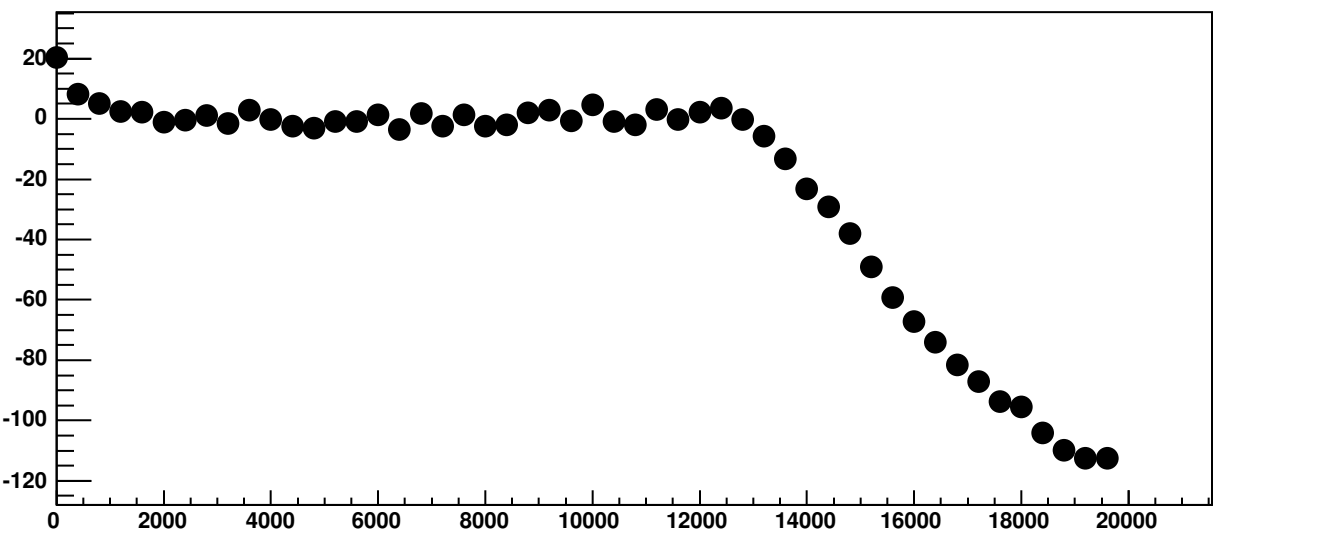
Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



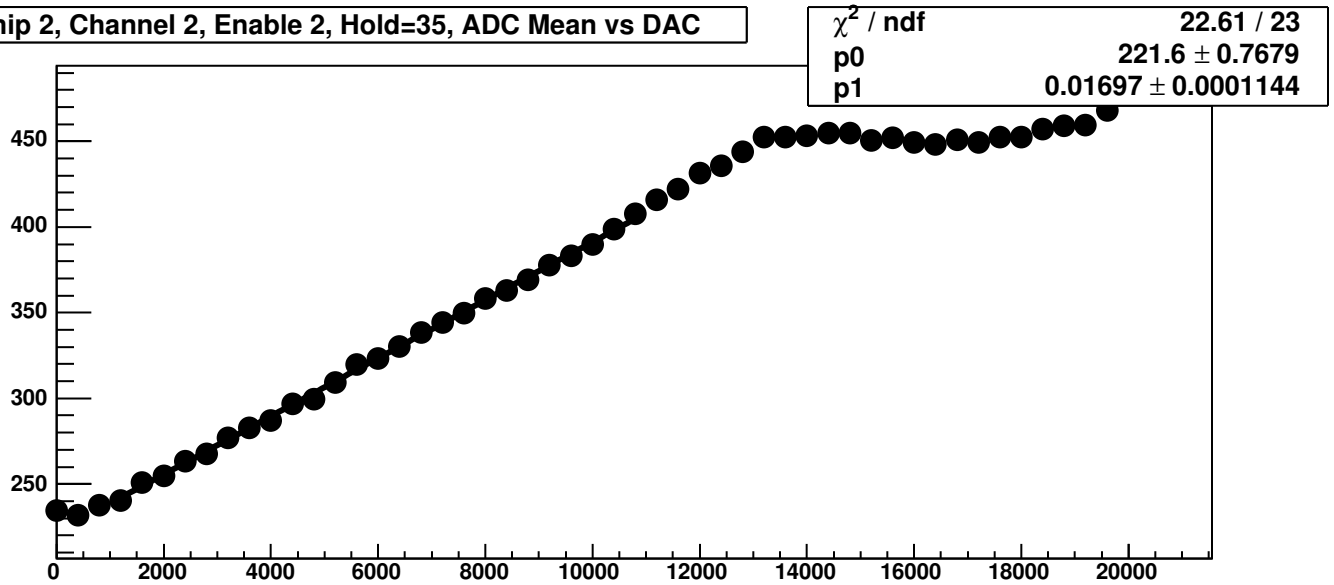
Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



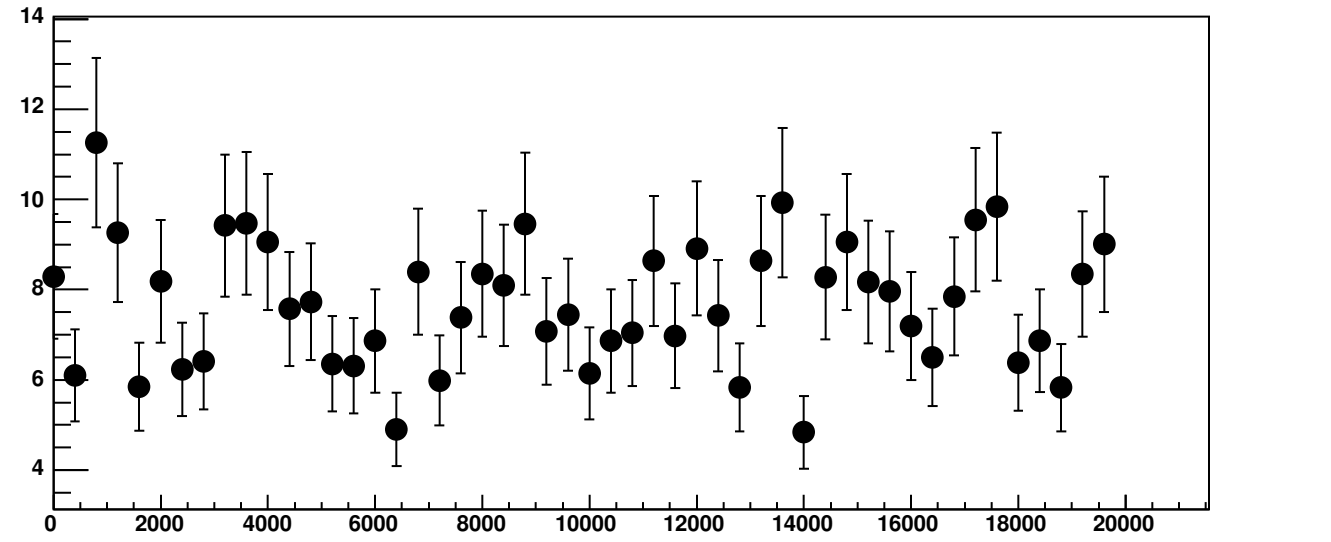
Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



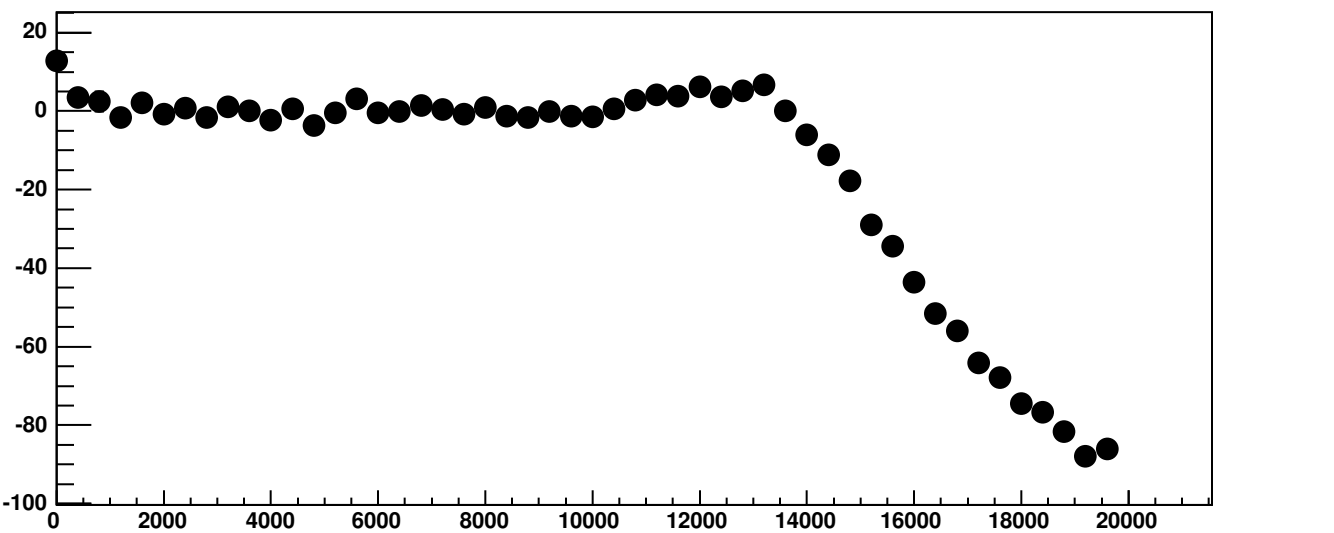
Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



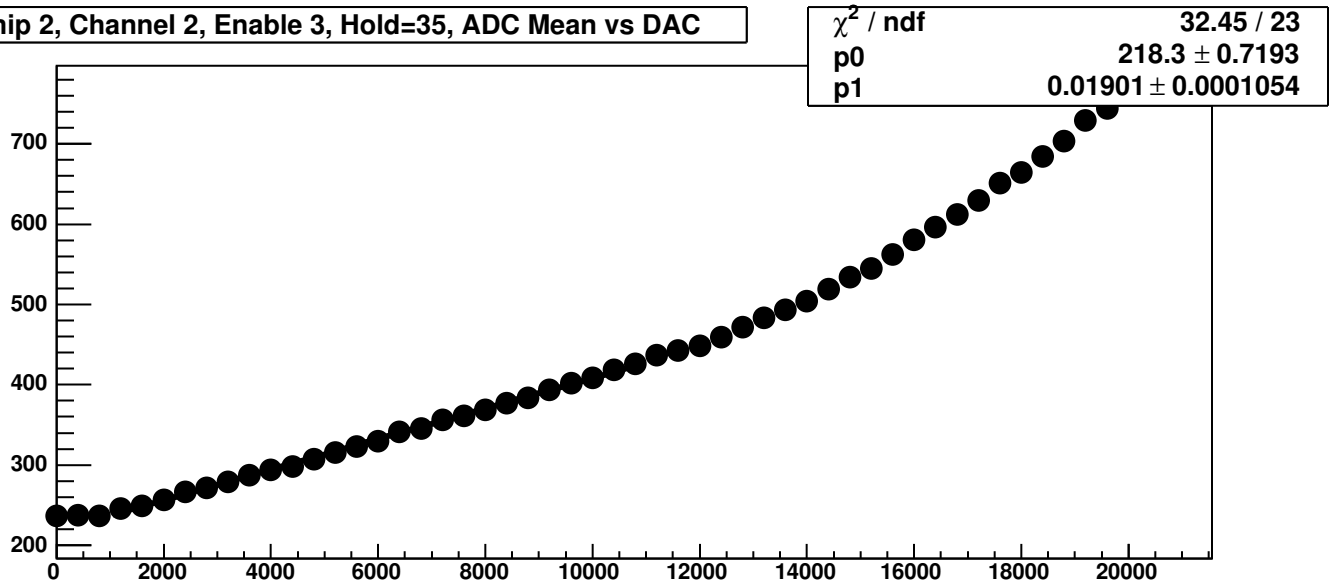
Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



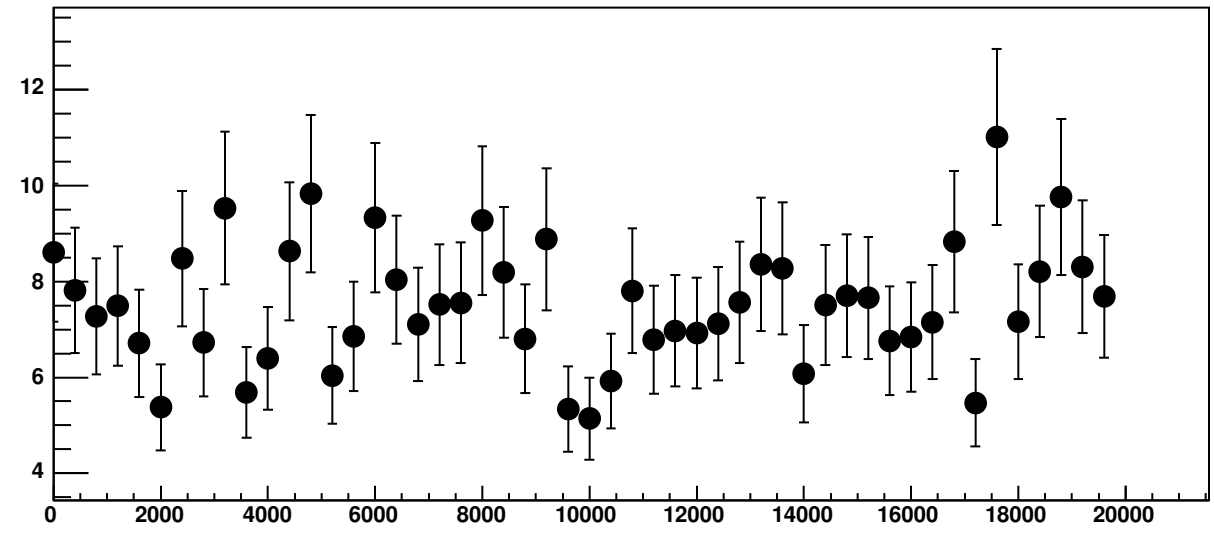
Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



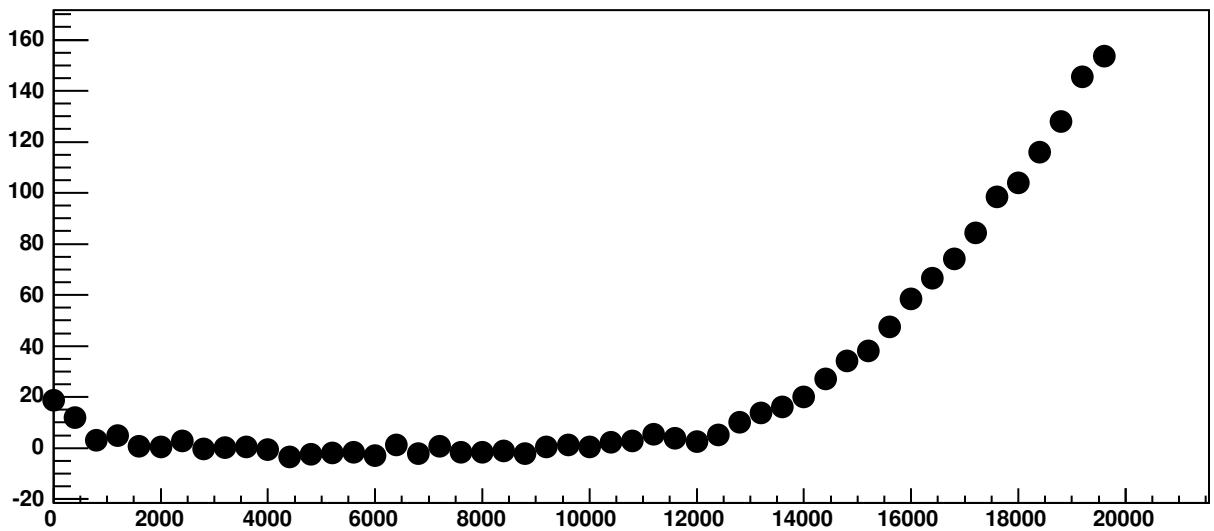
Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

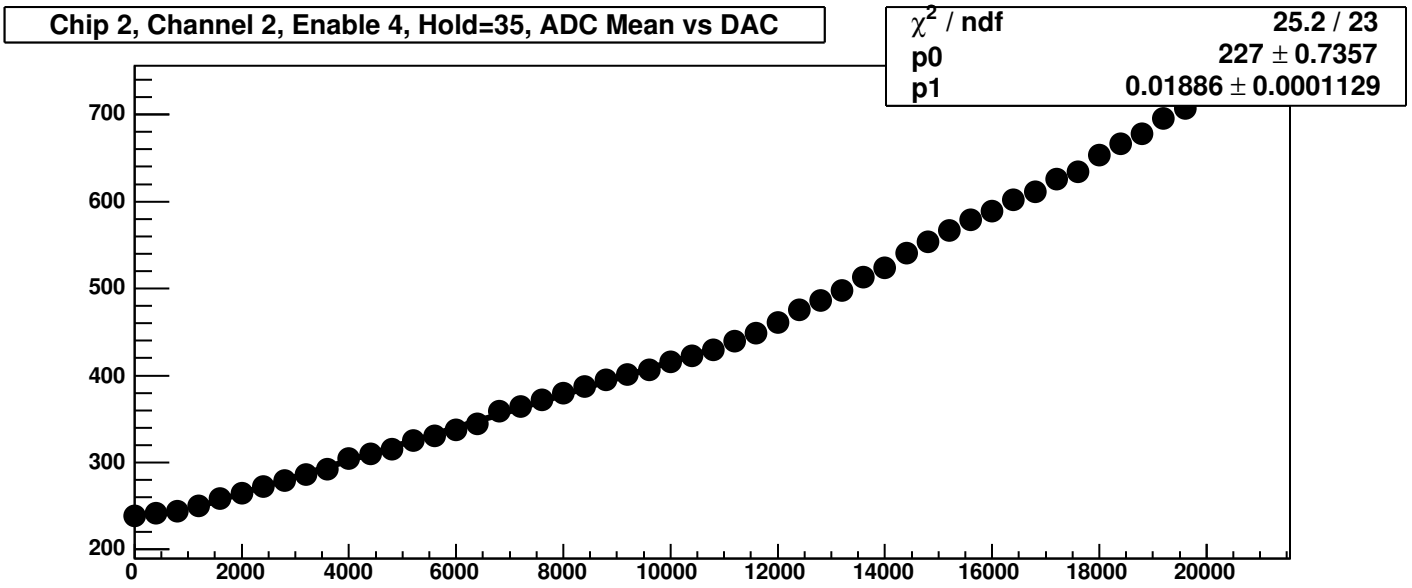


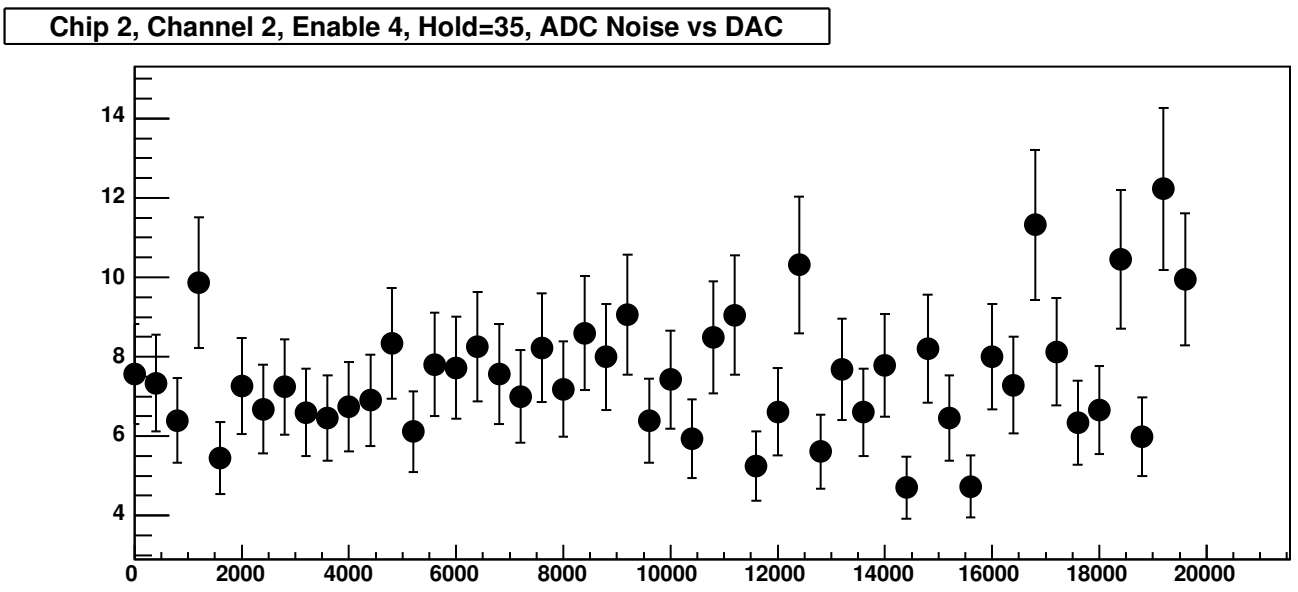
Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC

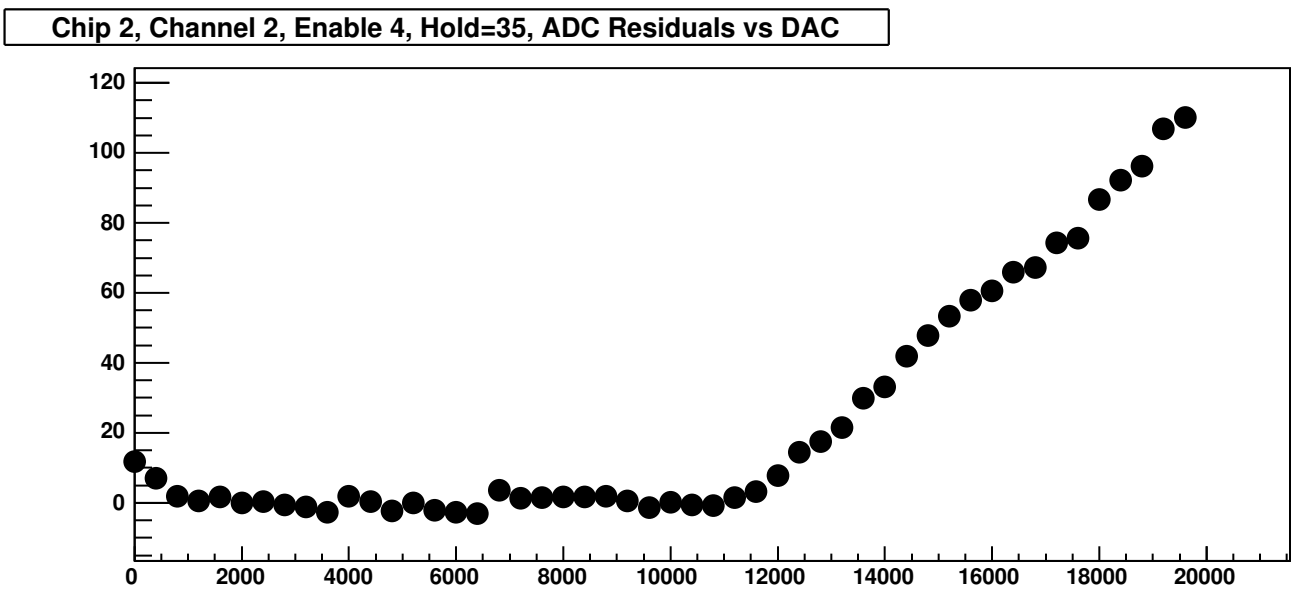


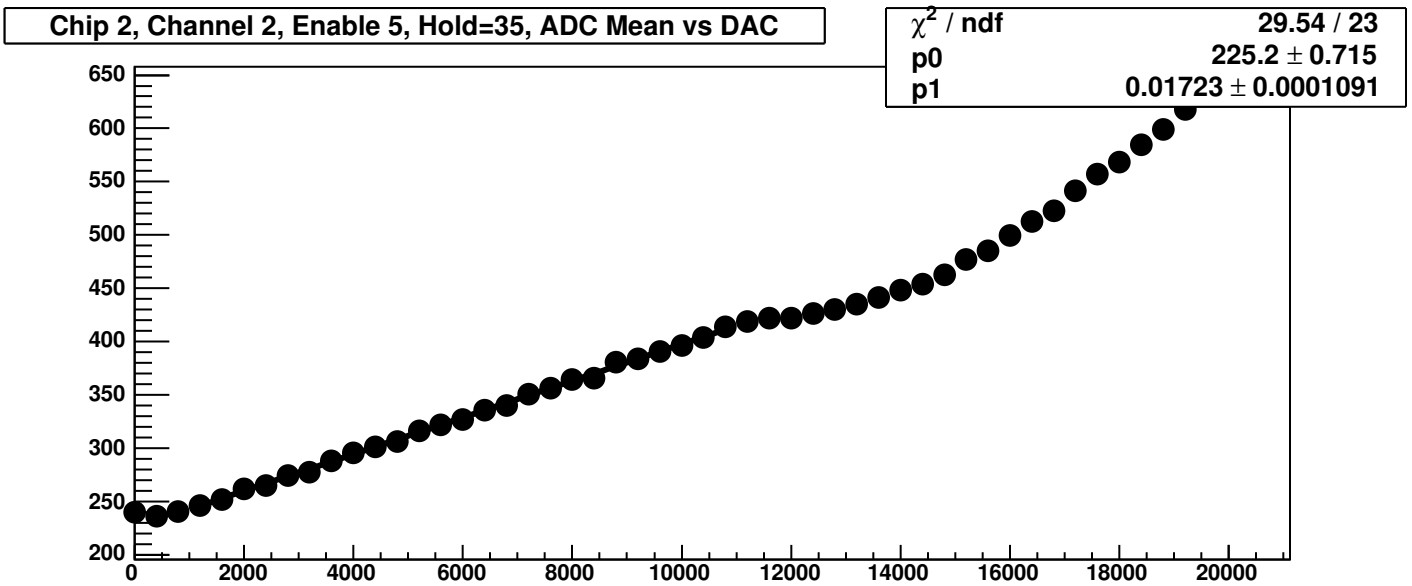
Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

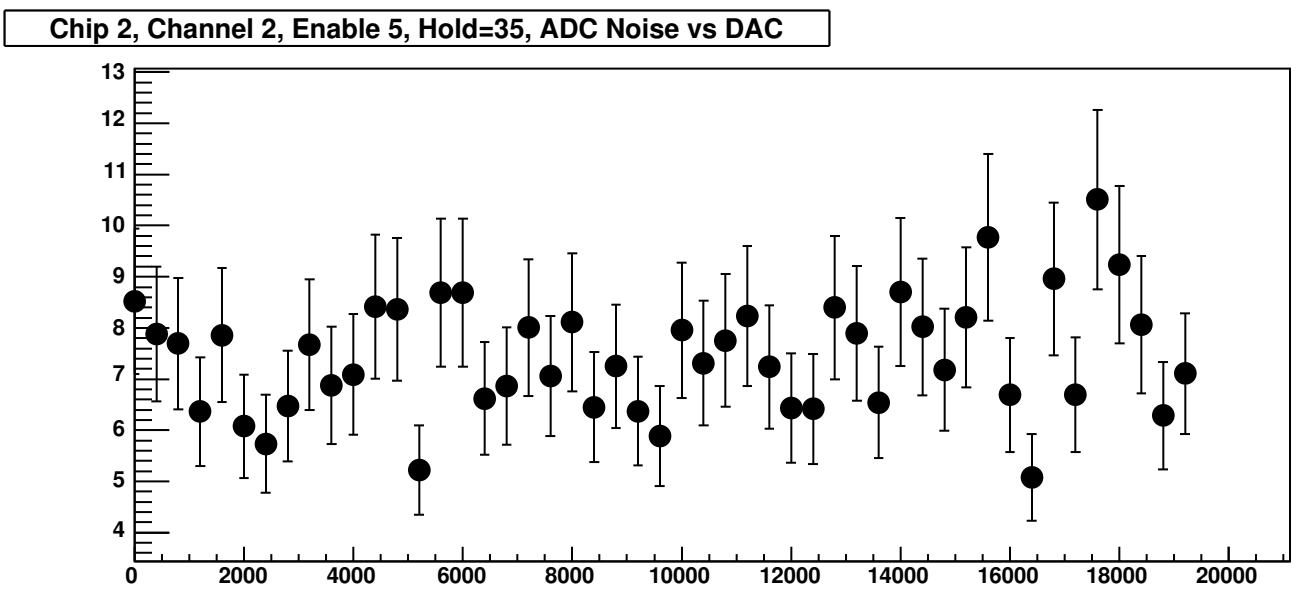


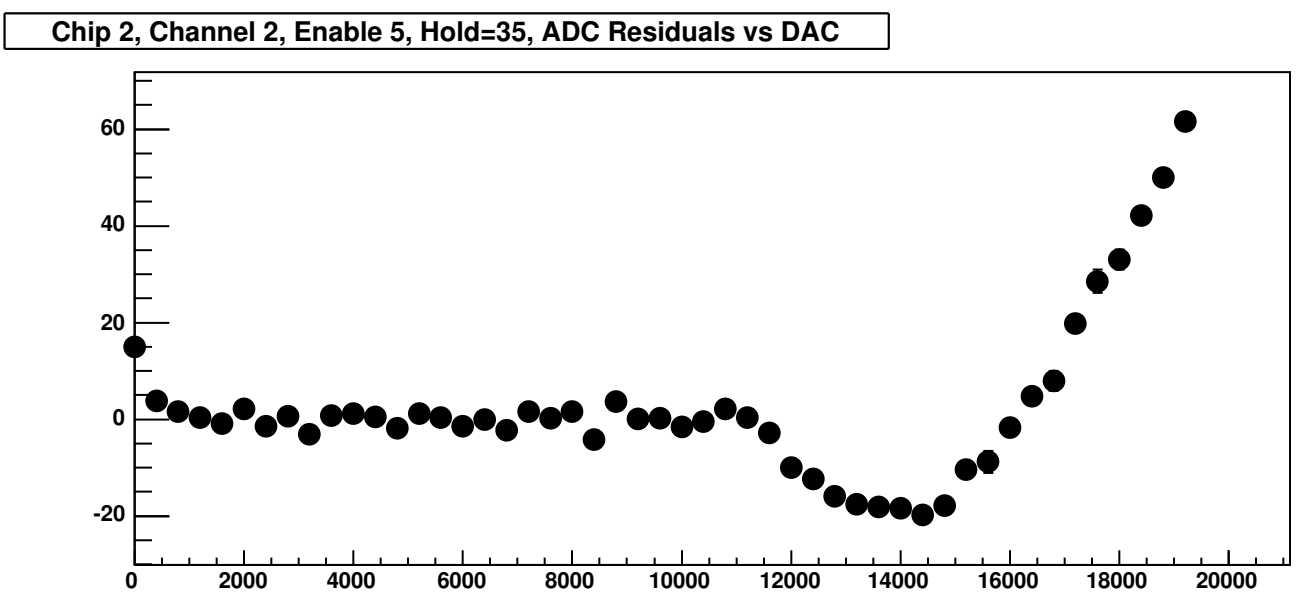

Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.2 / 23
p0 227 ± 0.7357
p1 0.01886 ± 0.0001129
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

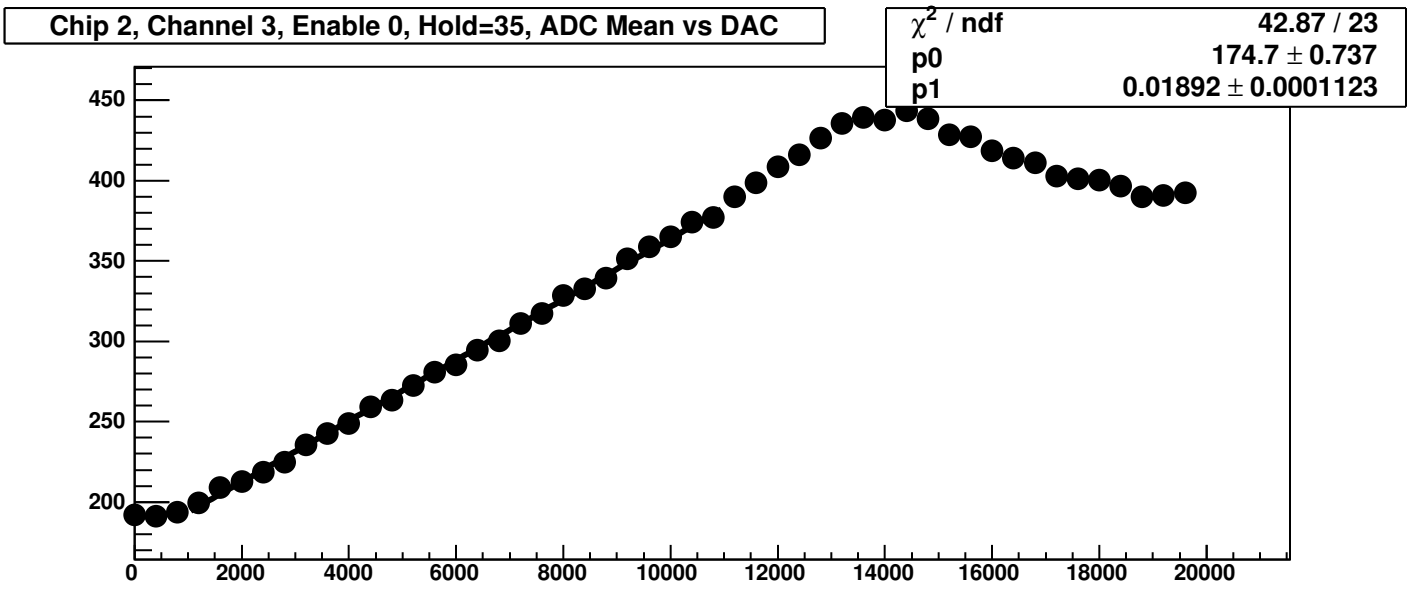

Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

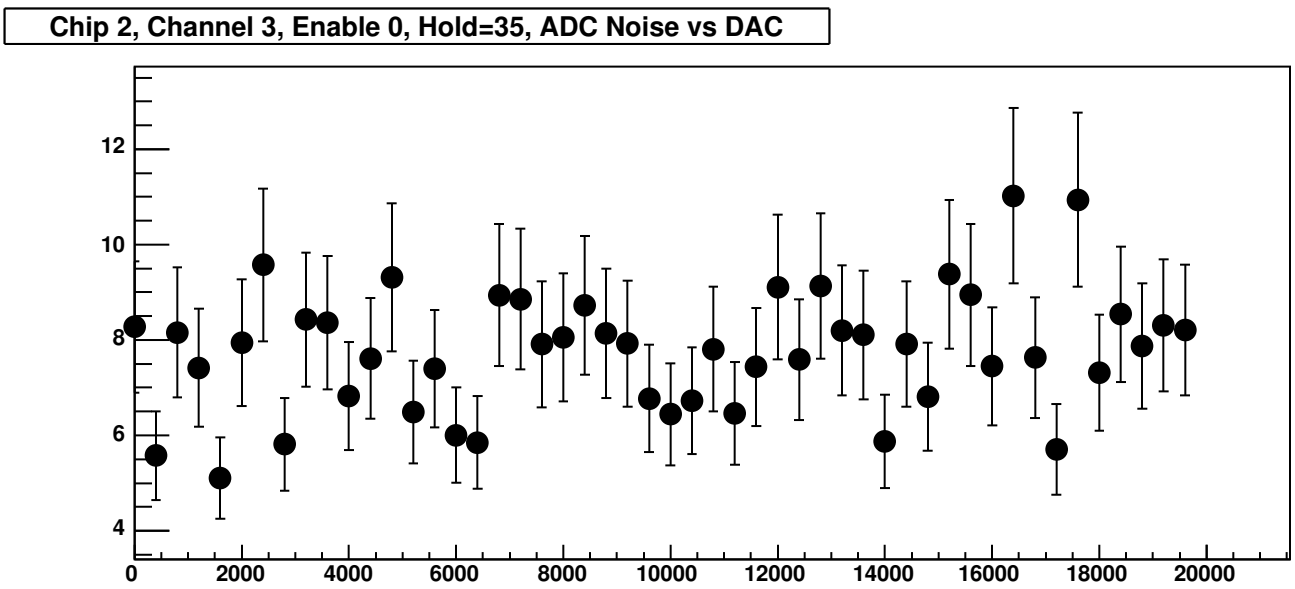

Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

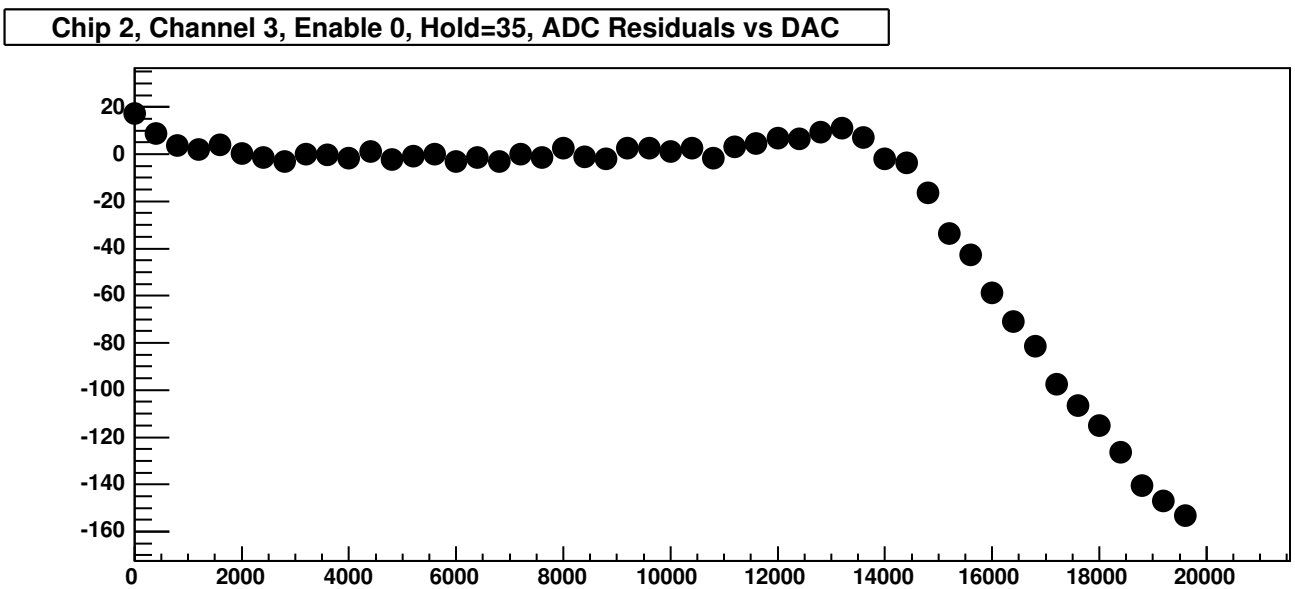

Chip 2, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 29.54 / 23
p0 225.2 ± 0.715
p1 0.01723 ± 0.0001091
650
600
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

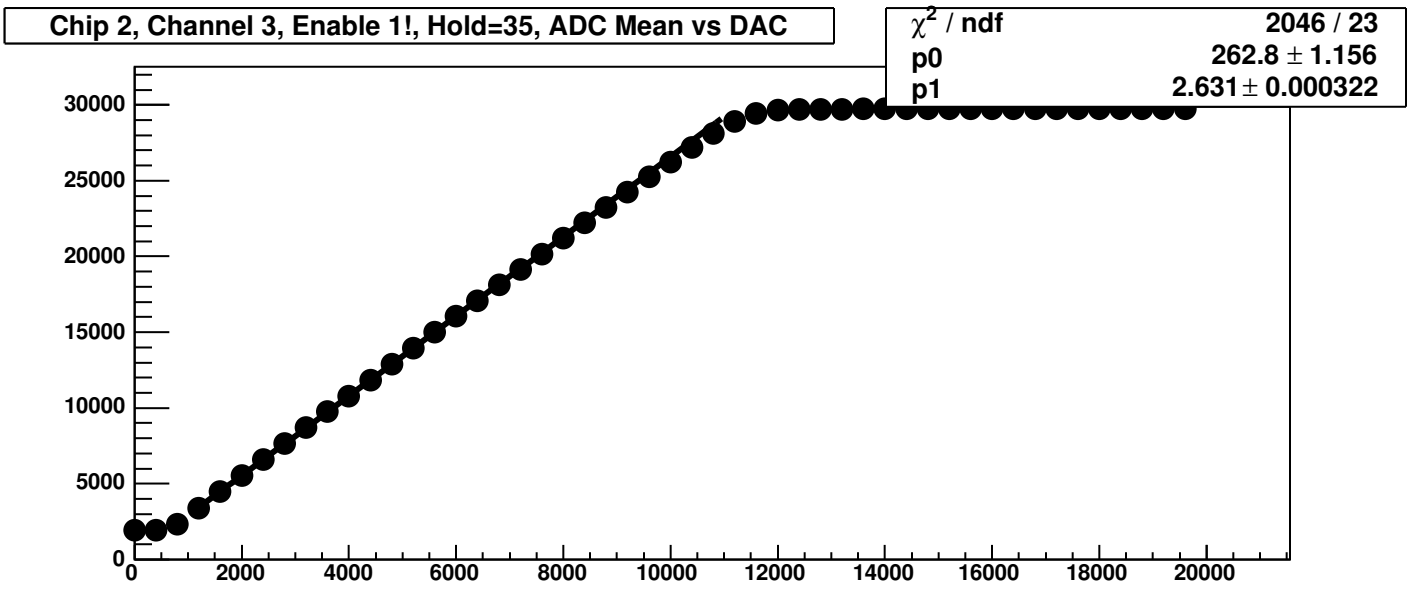

Chip 2, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

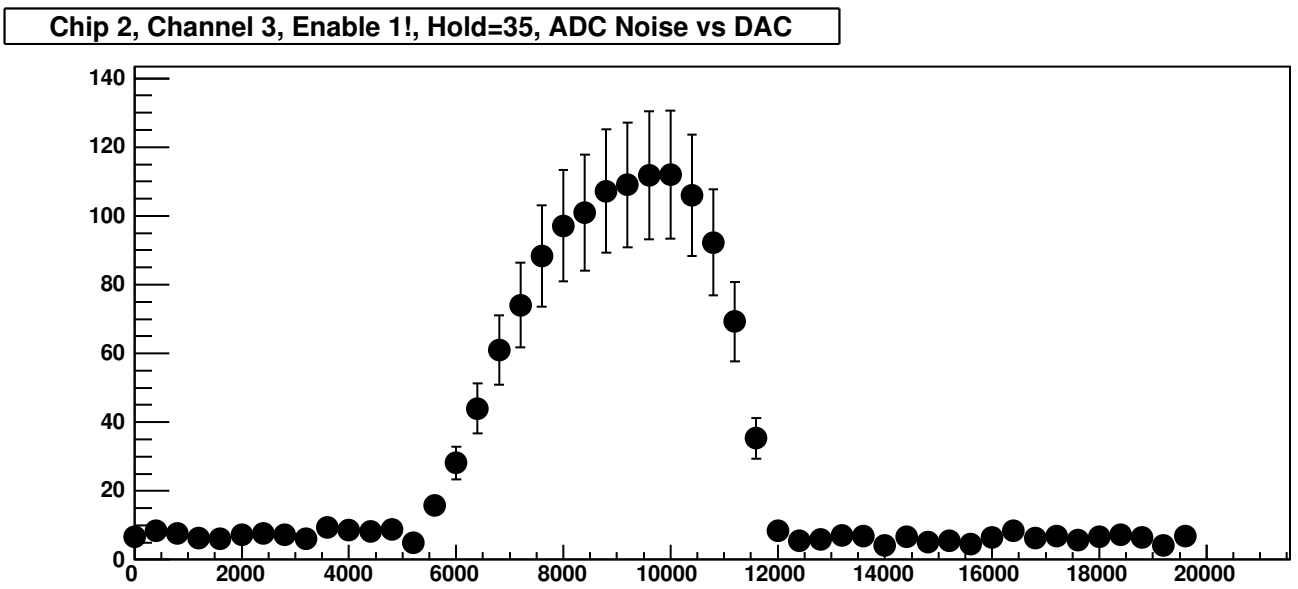

Chip 2, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

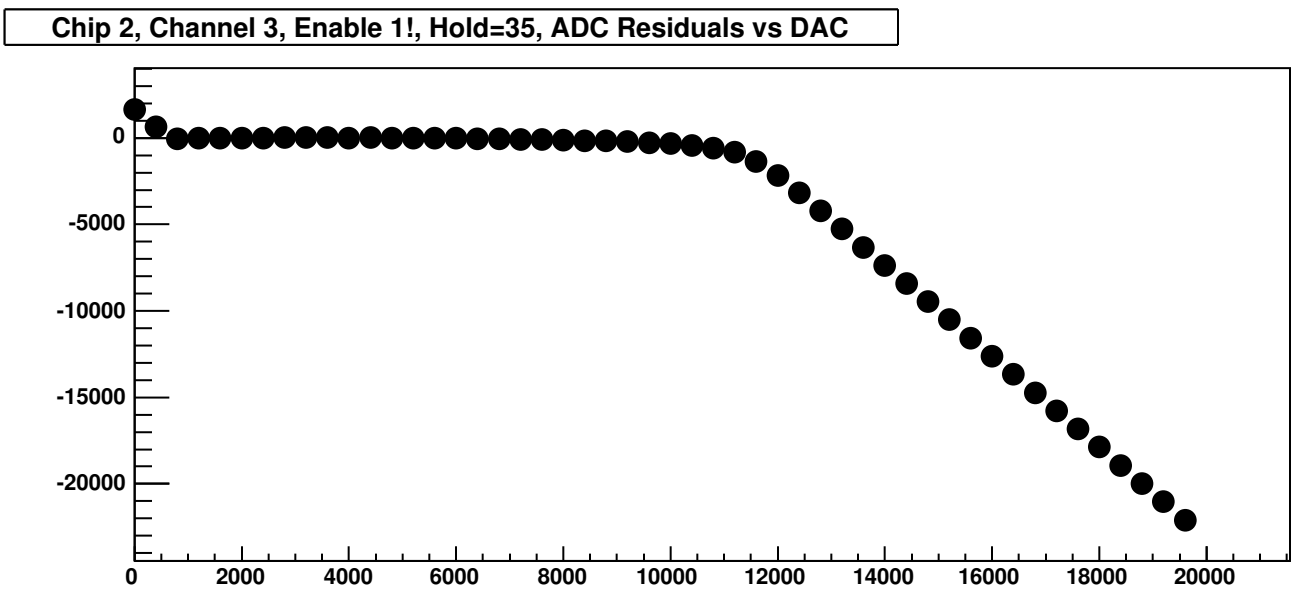

Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 42.87 / 23
p0 174.7 ± 0.737
p1 0.01892 ± 0.0001123
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

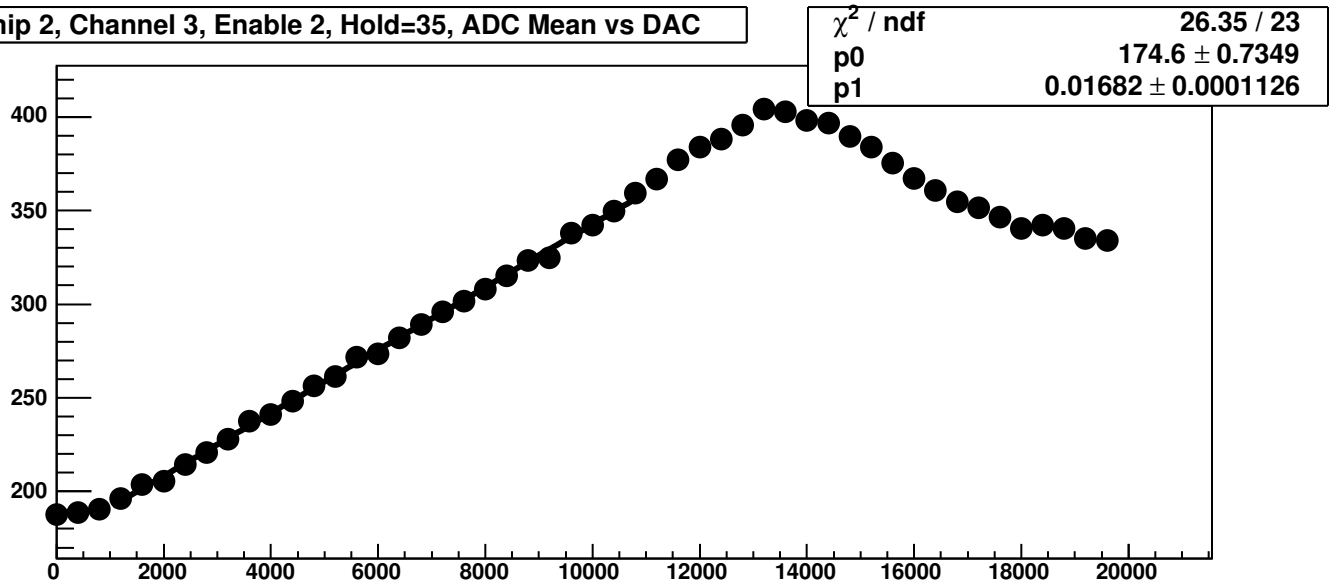

Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 2046 / 23
p0 262.8 ± 1.156
p1 2.631 ± 0.000322
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

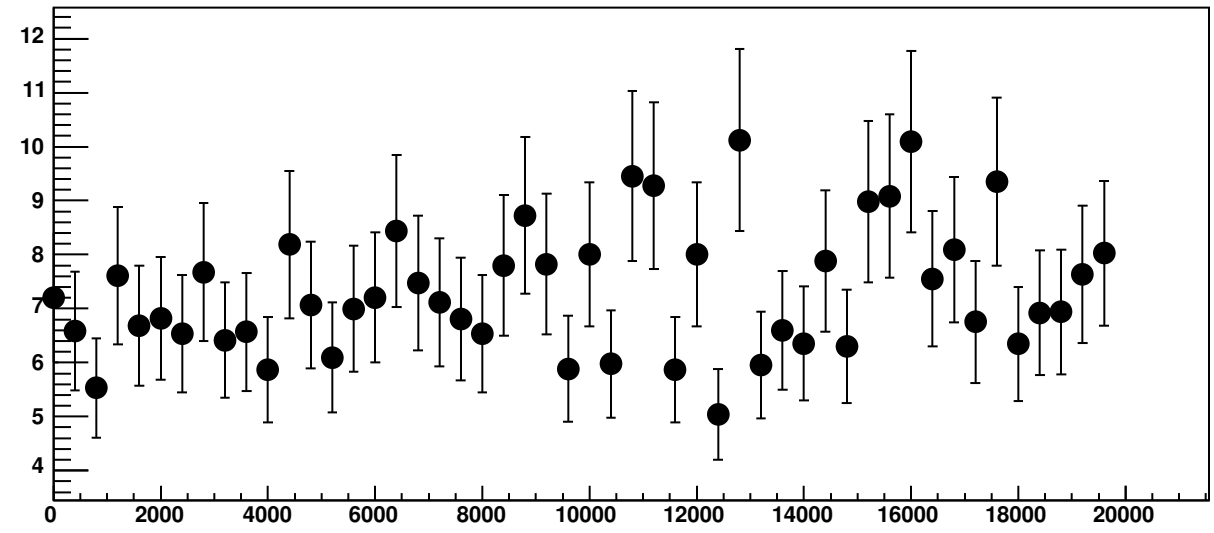

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

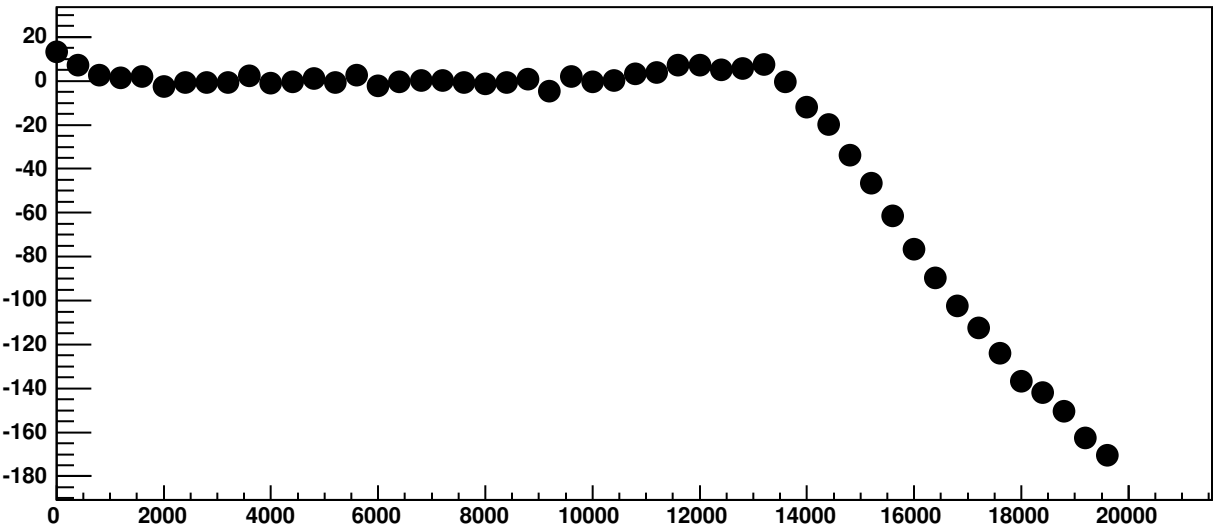
Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



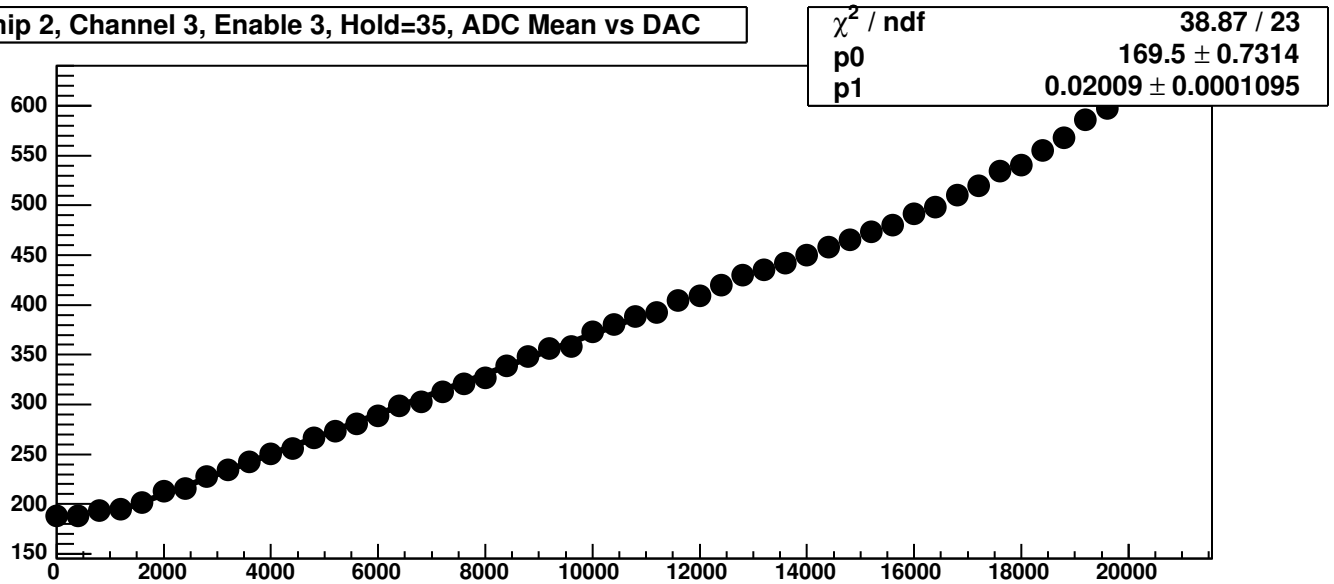
Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



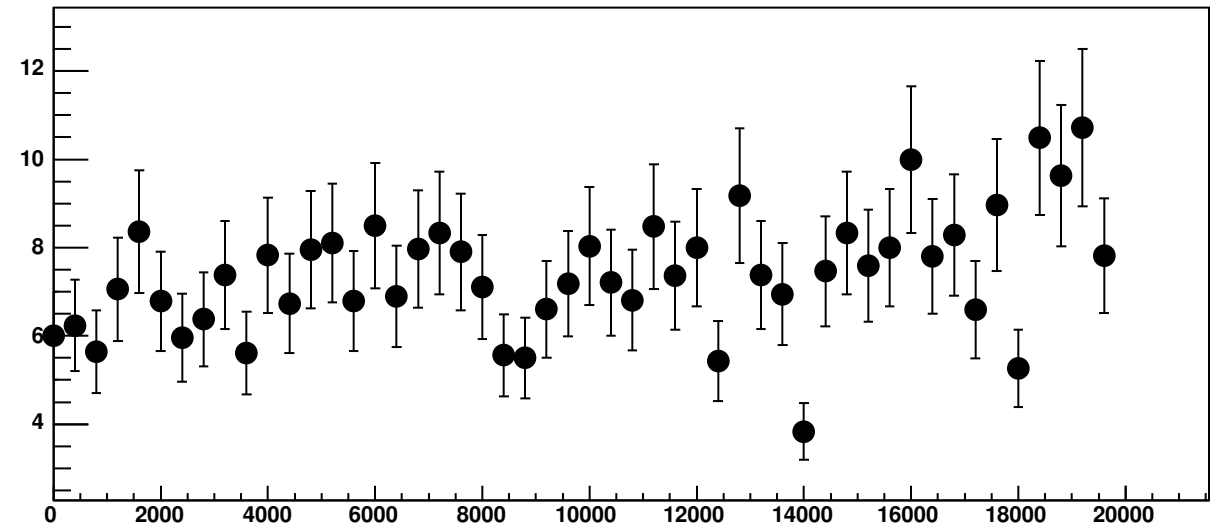
Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



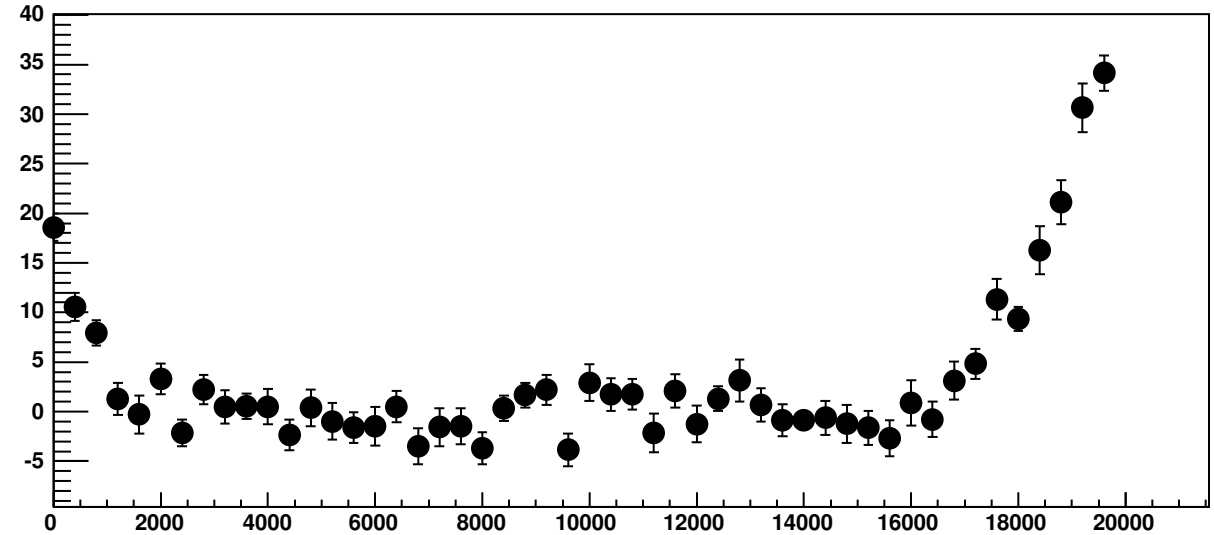
Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



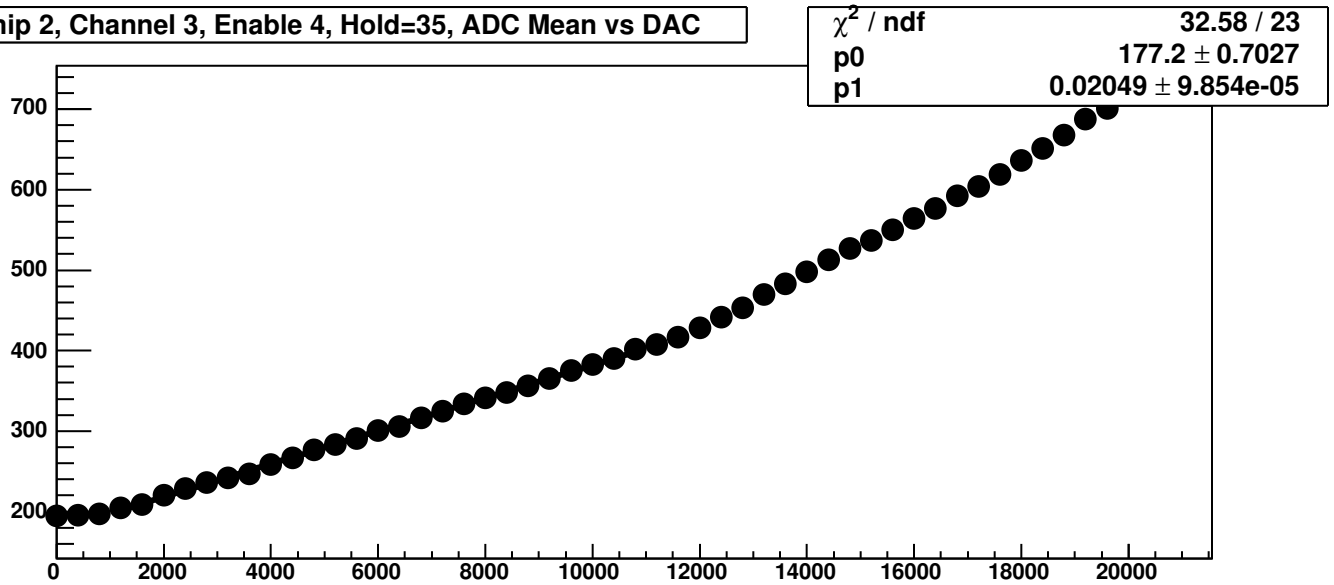
Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



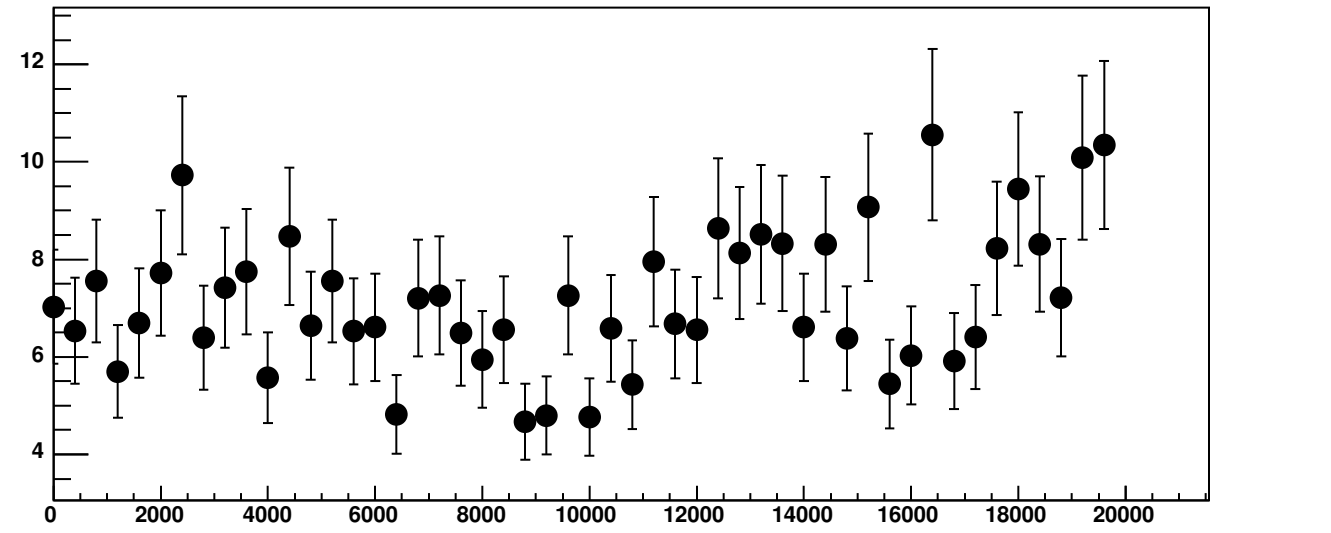
Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



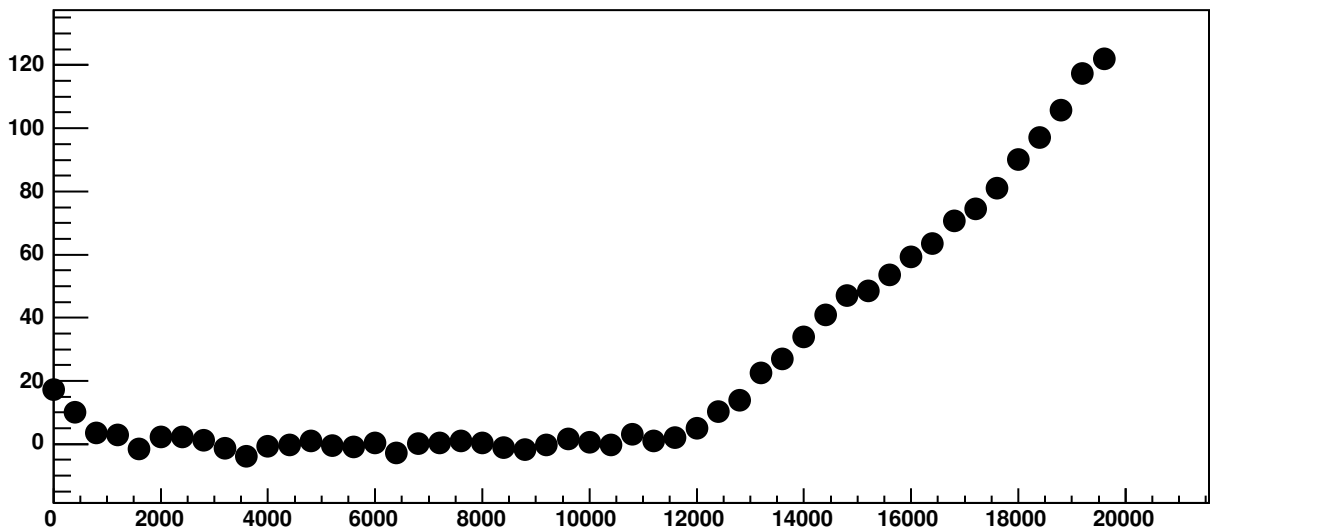
Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC



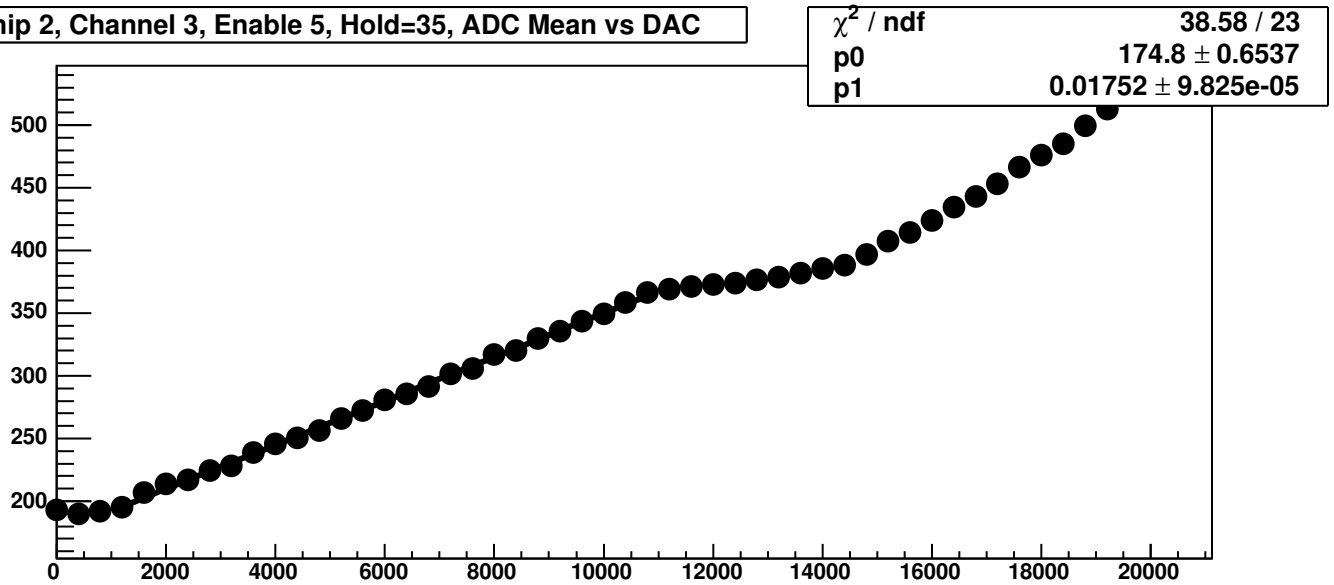
Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



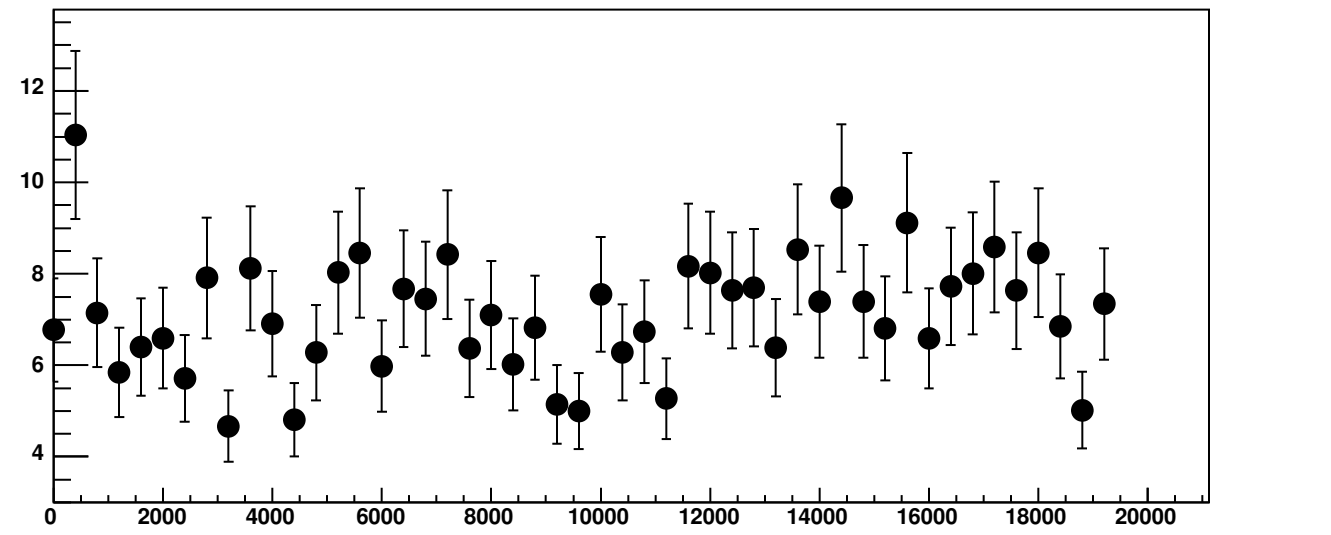
Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



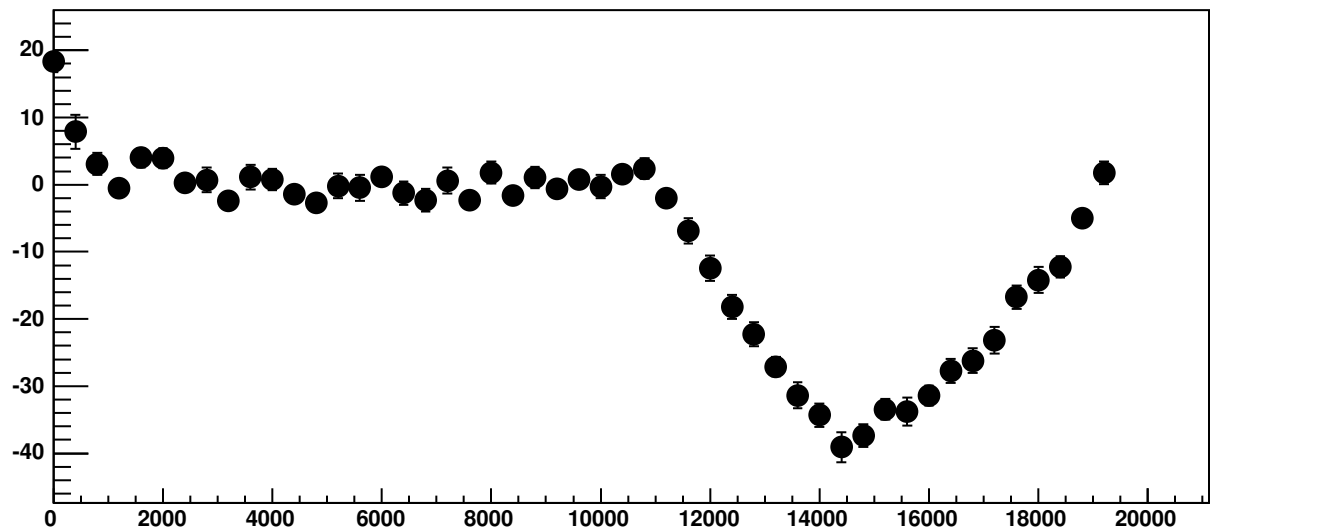
Chip 2, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



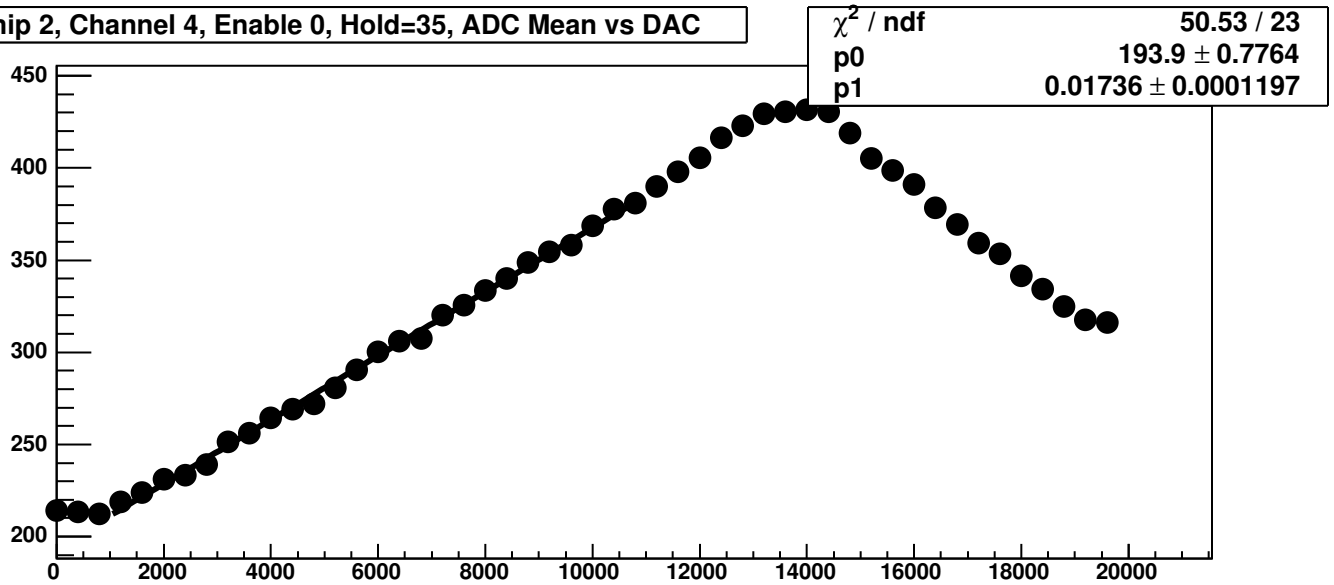
Chip 2, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



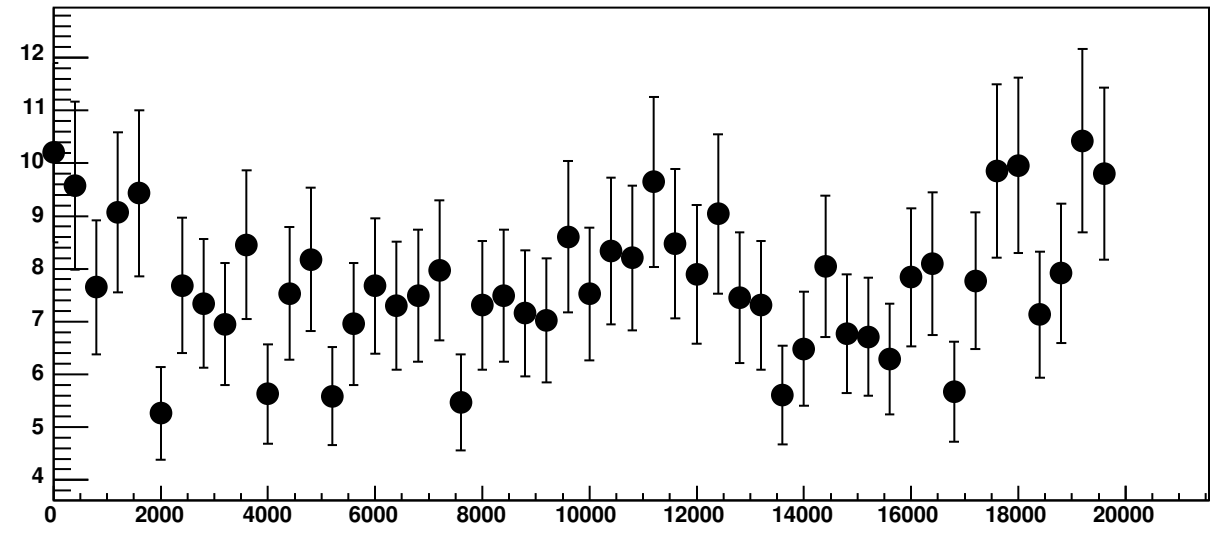
Chip 2, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



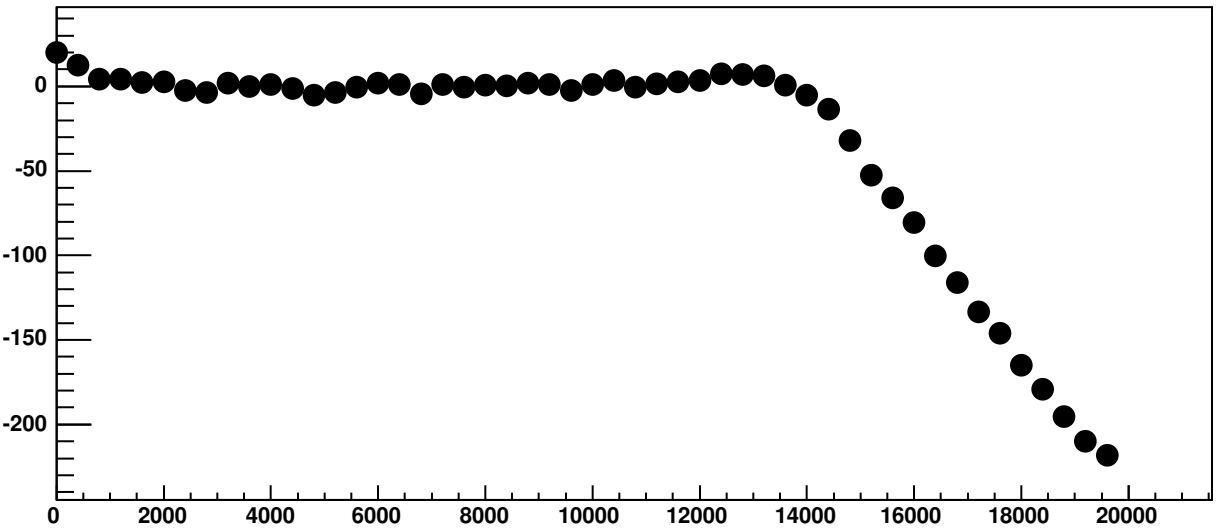
Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



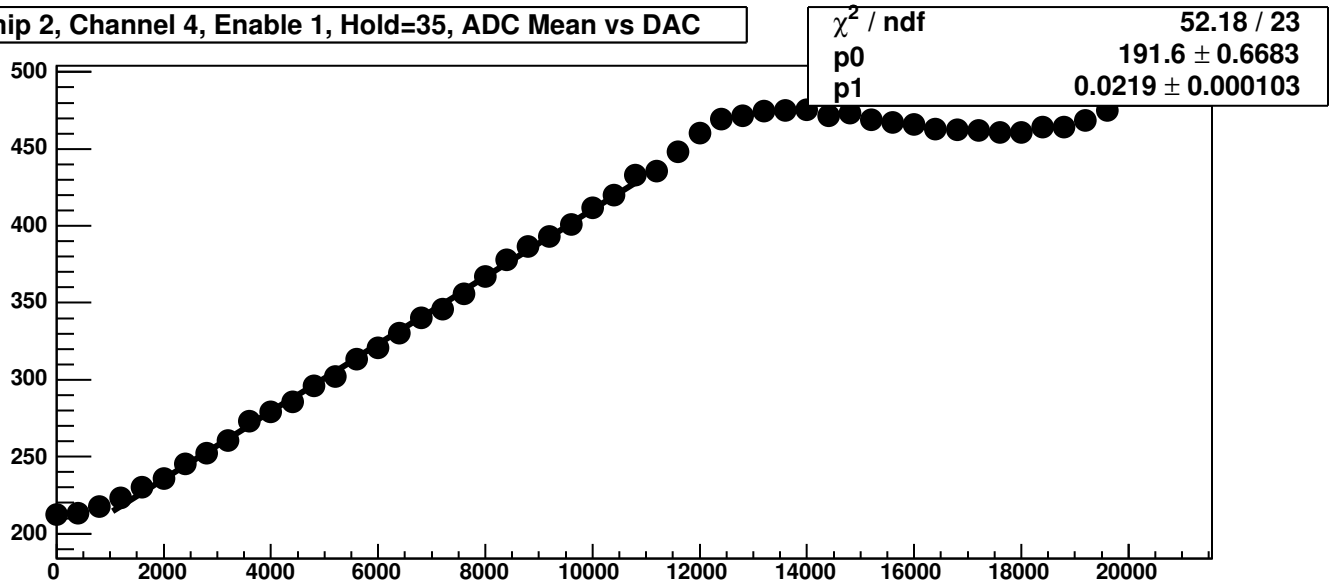
Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



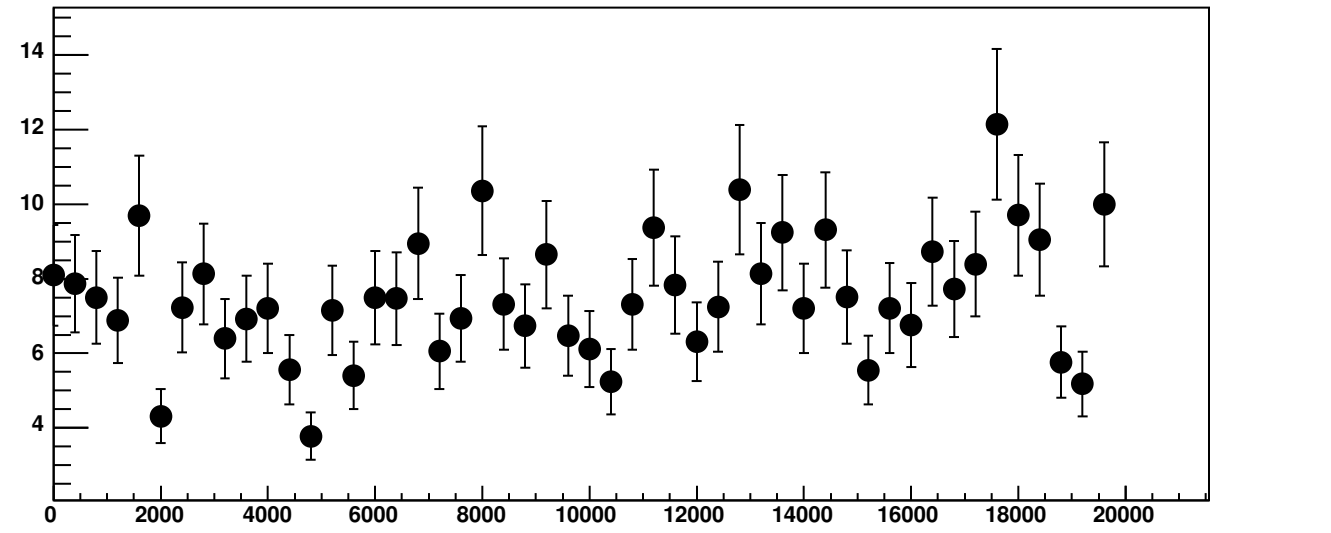
Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



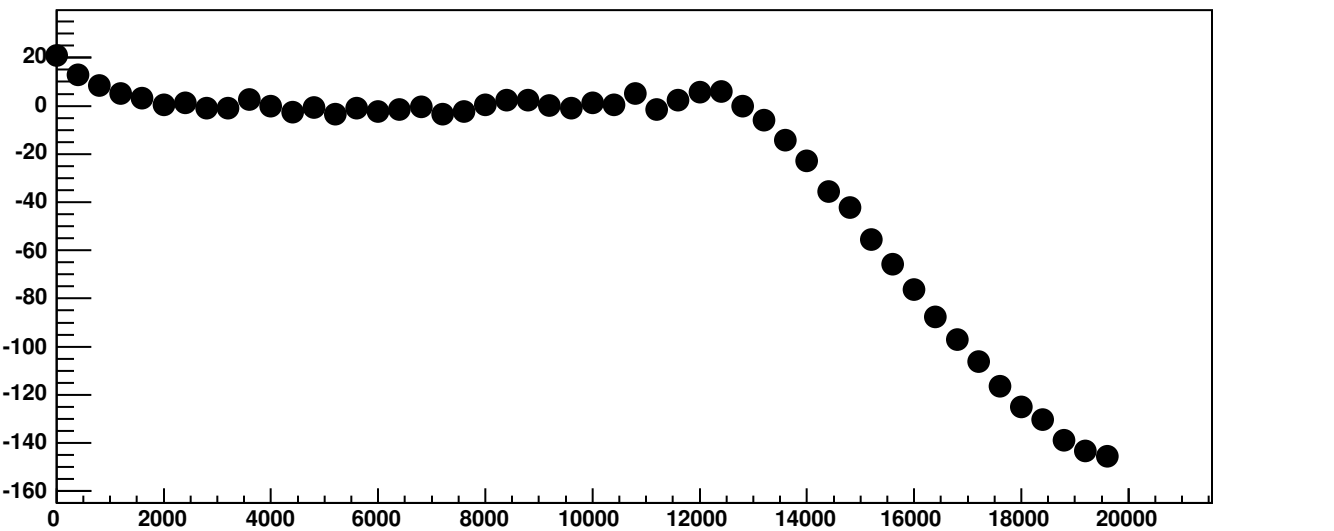
Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



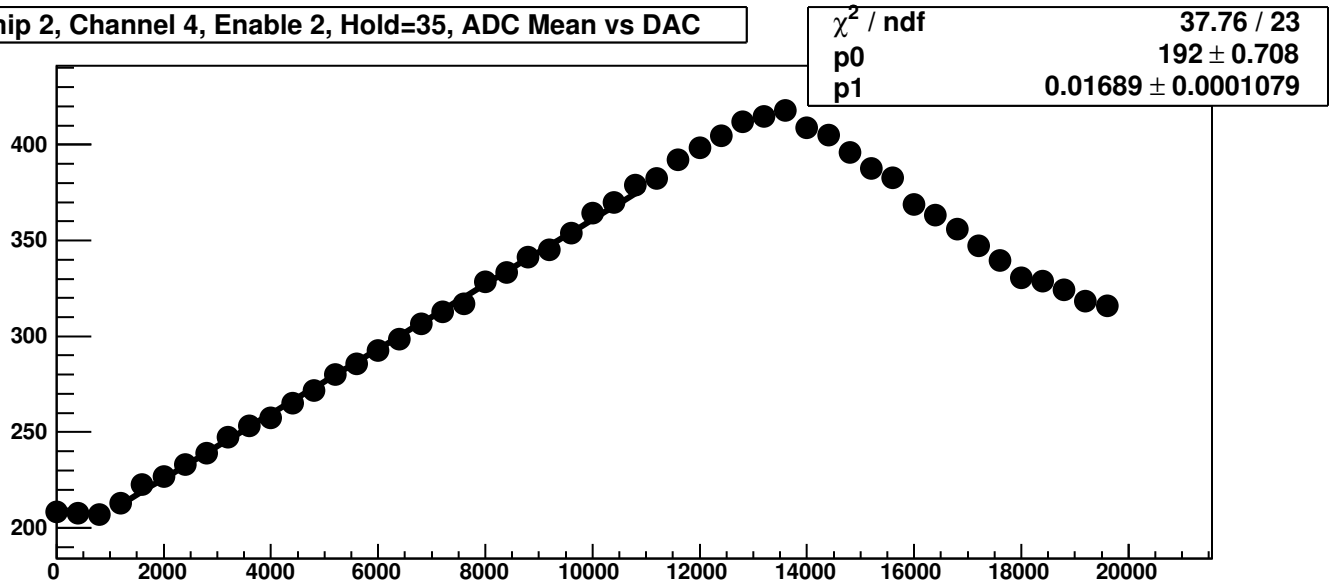
Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



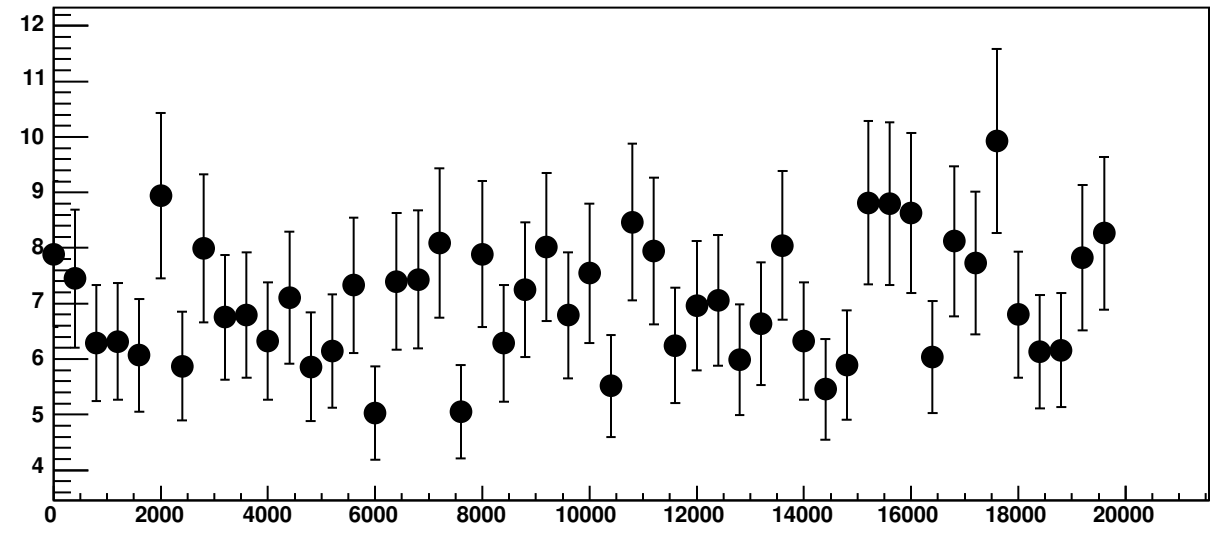
Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



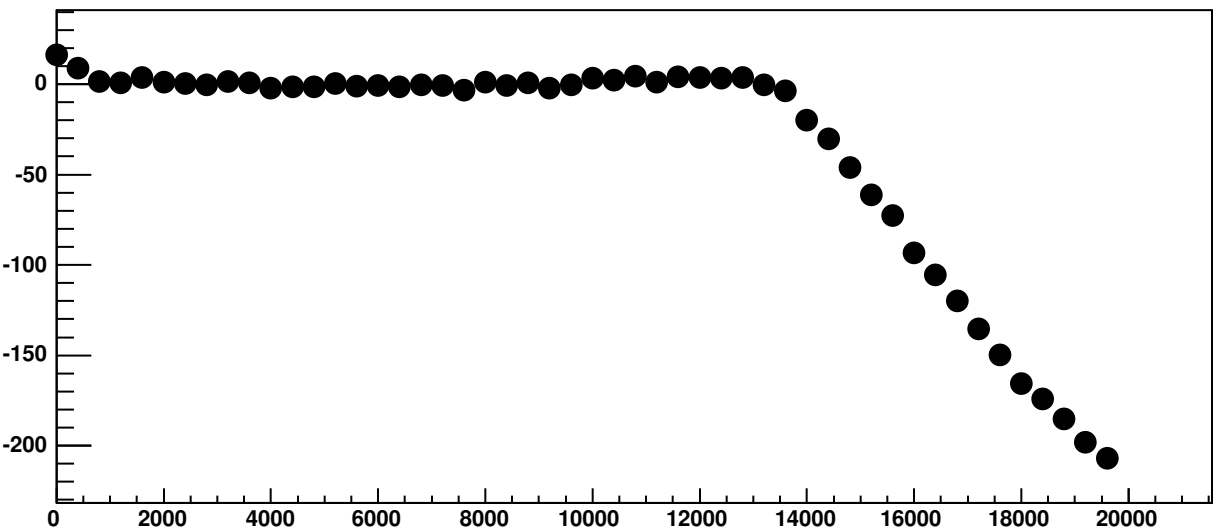
Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



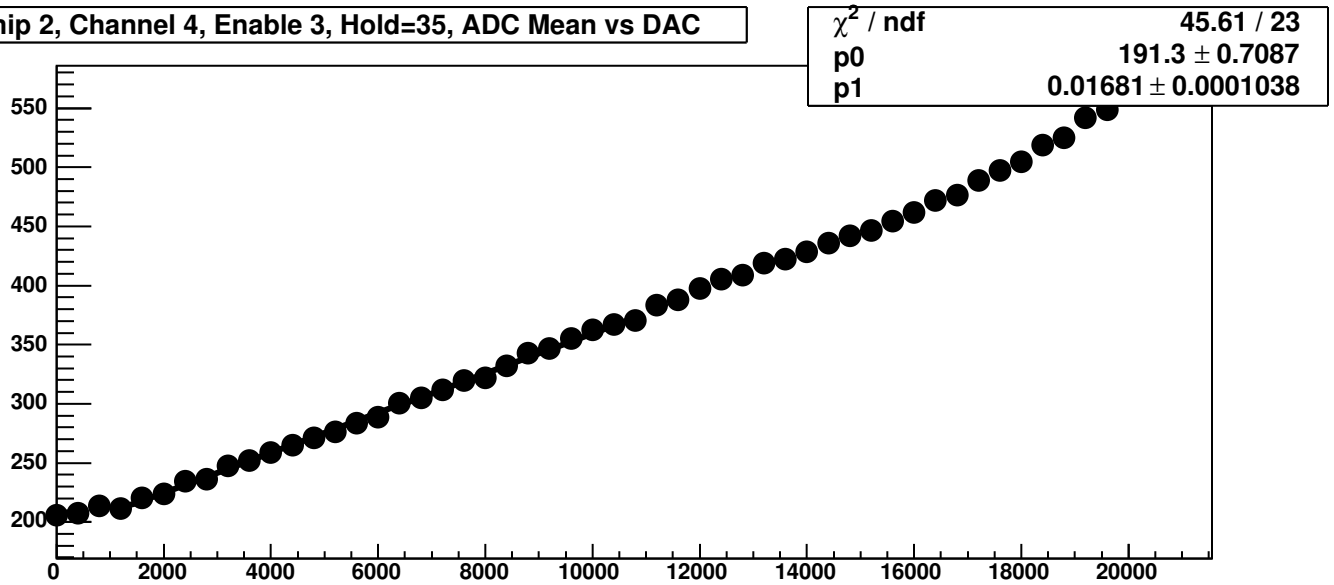
Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



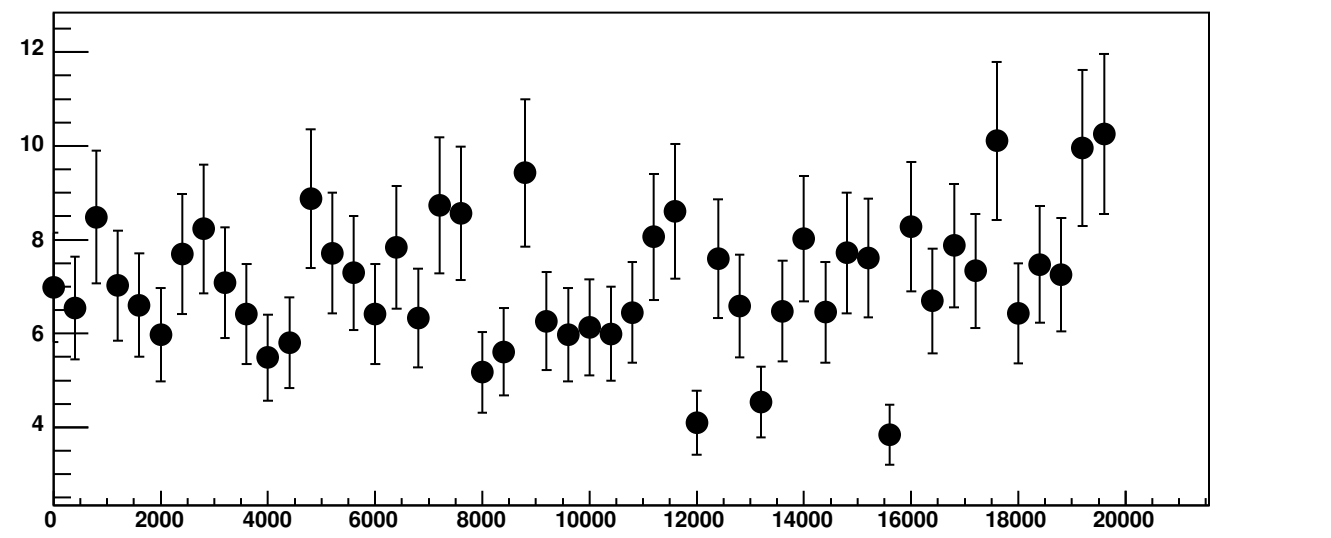
Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



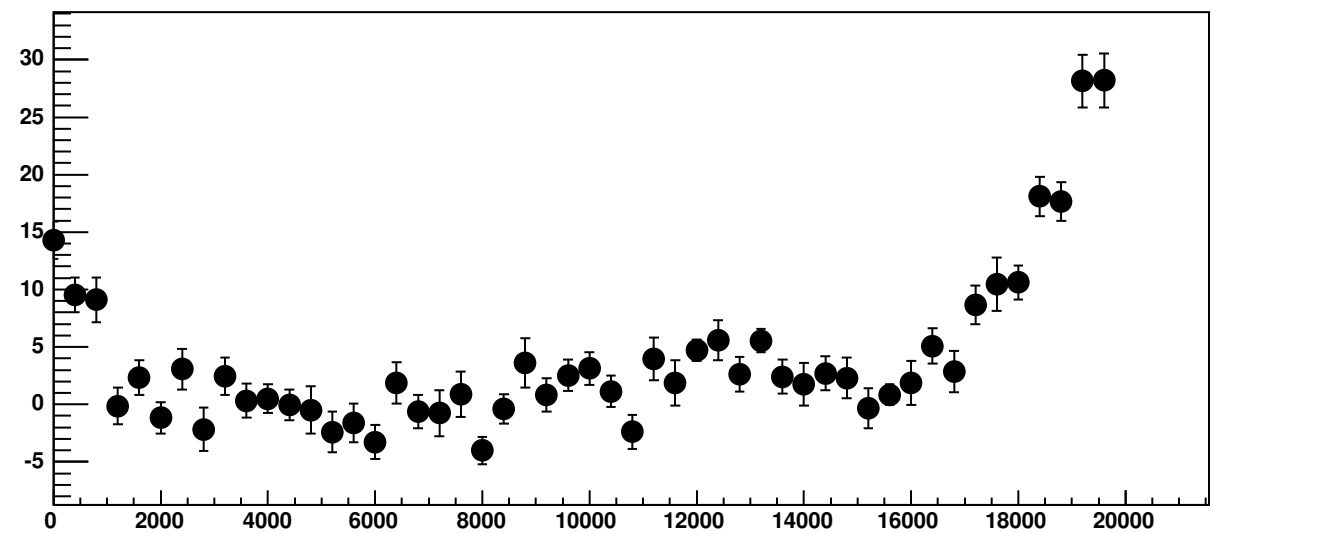
Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

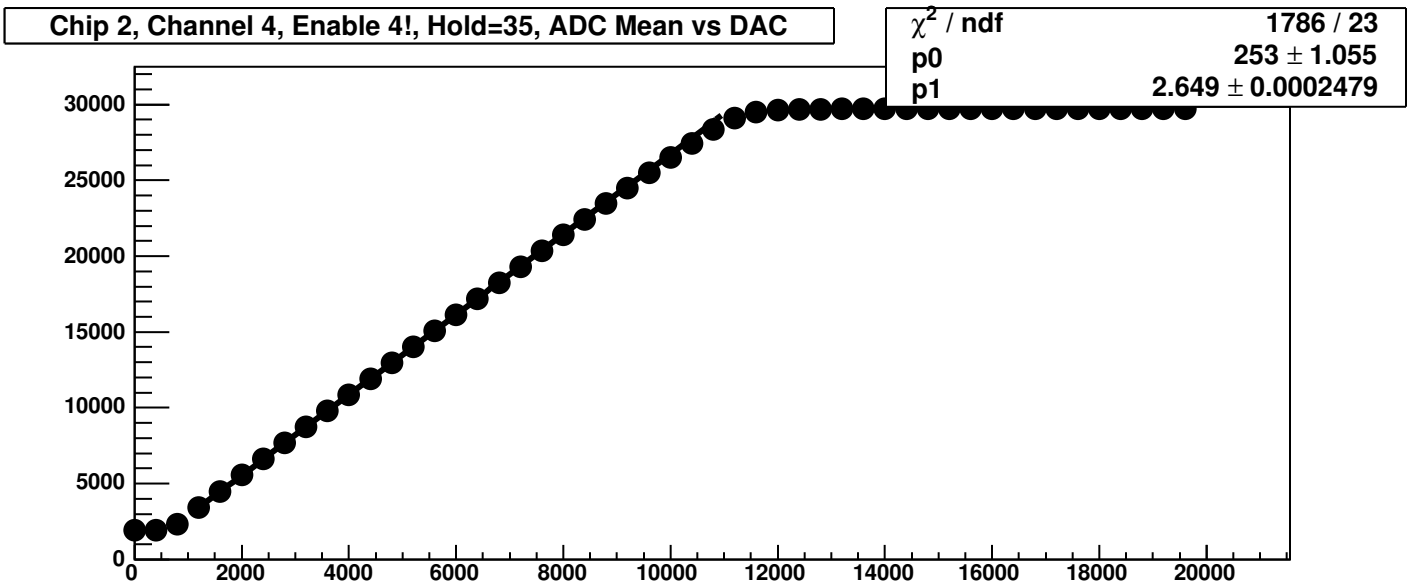


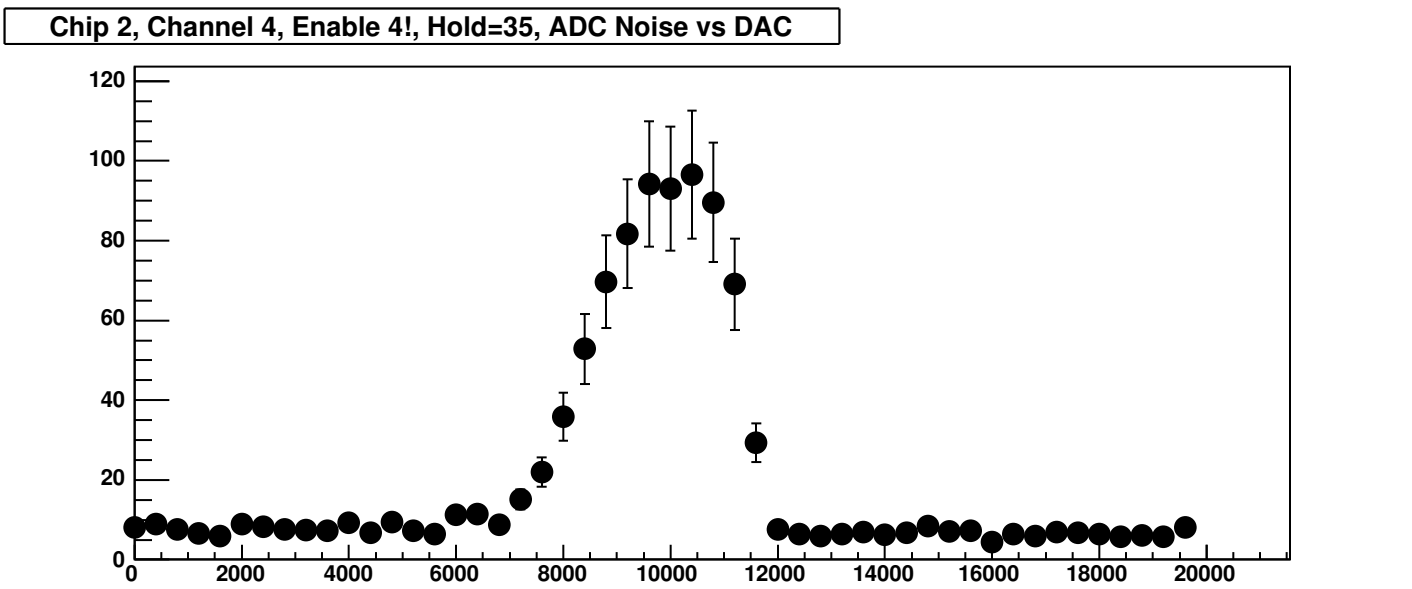
Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

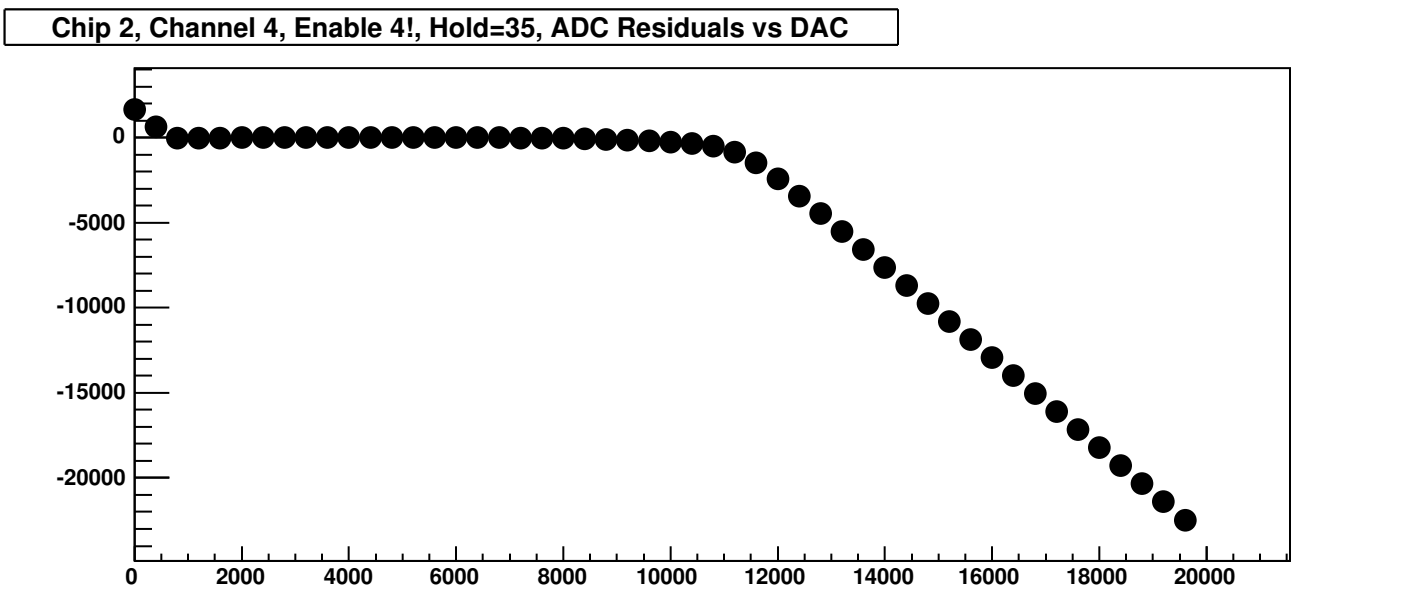


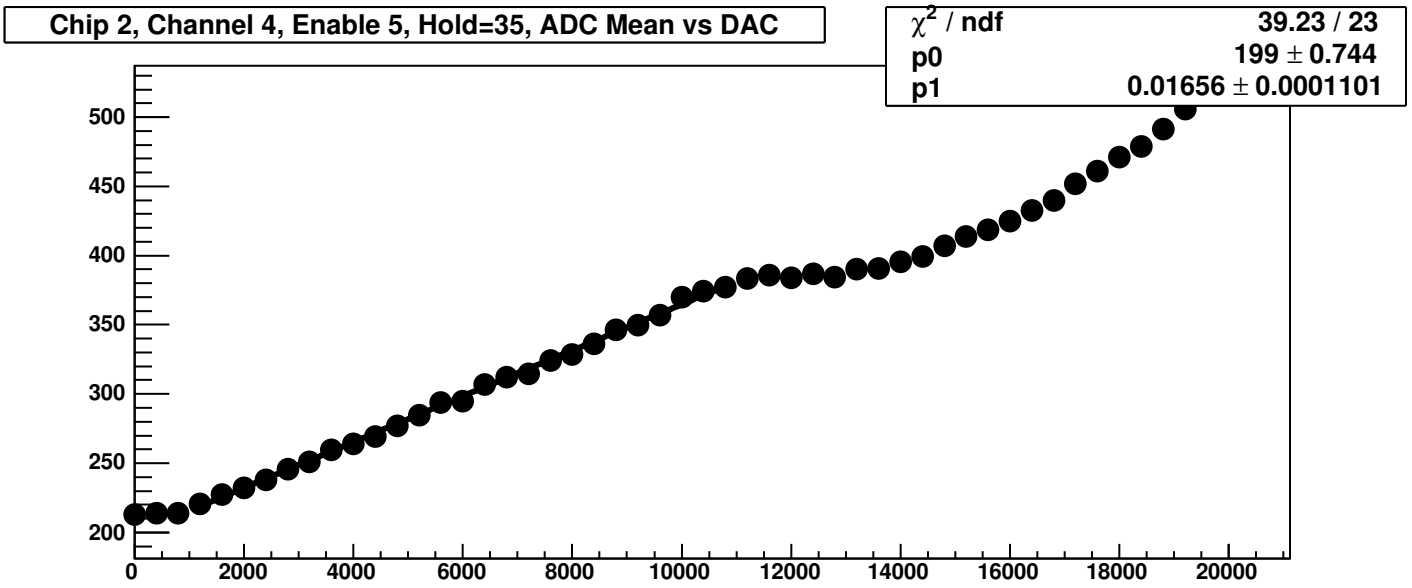
Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

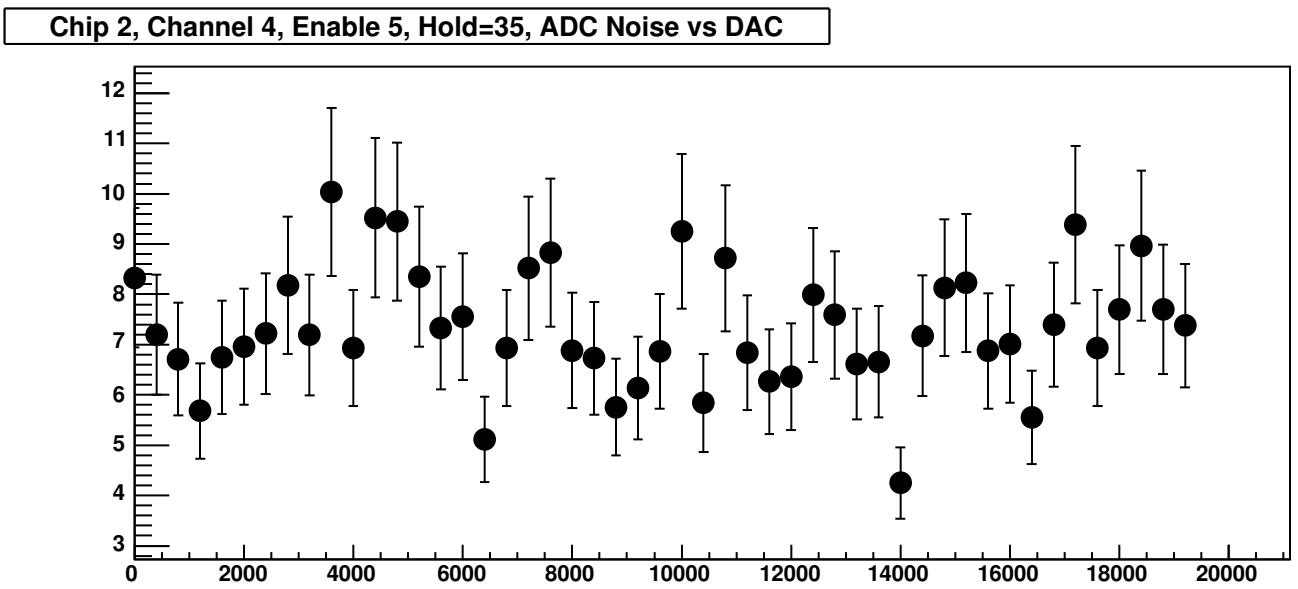


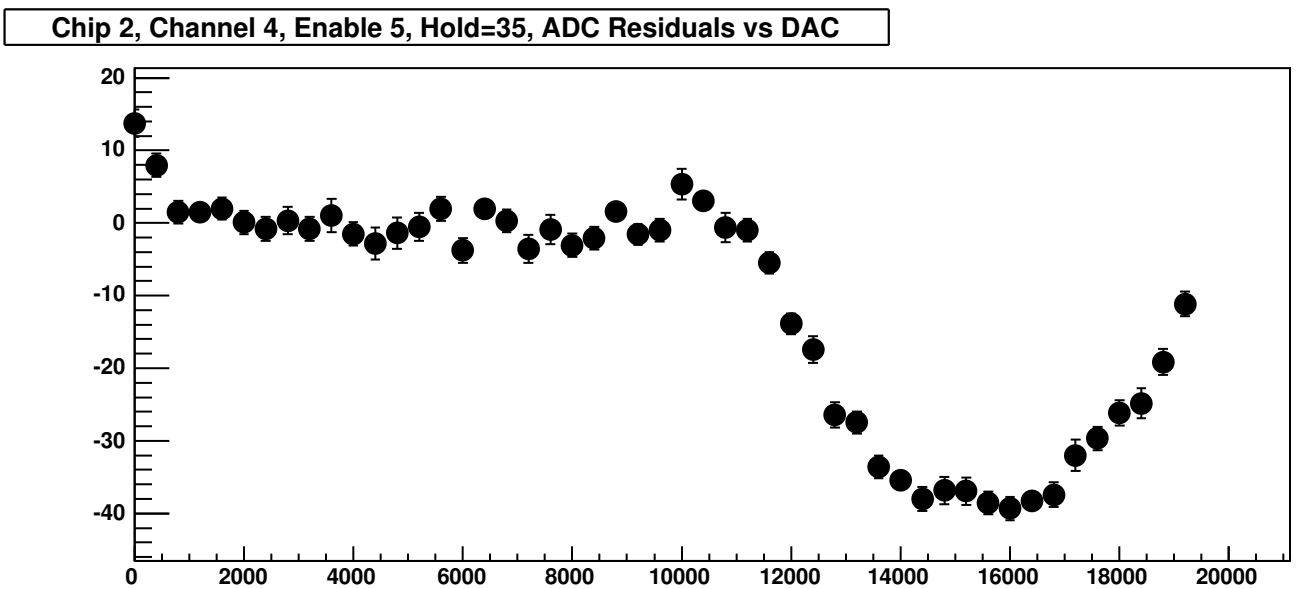

Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1786 / 23
p0 253 ± 1.055
p1 2.649 ± 0.0002479
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

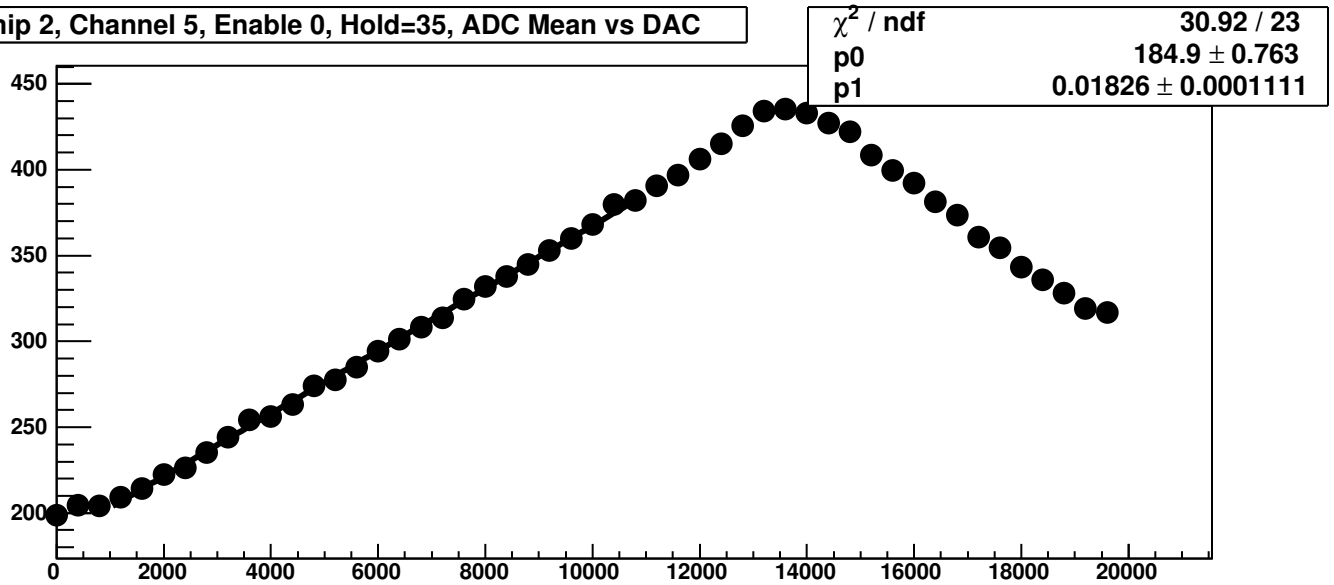

Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 39.23 / 23
p0 199 ± 0.744
p1 0.01656 ± 0.0001101
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

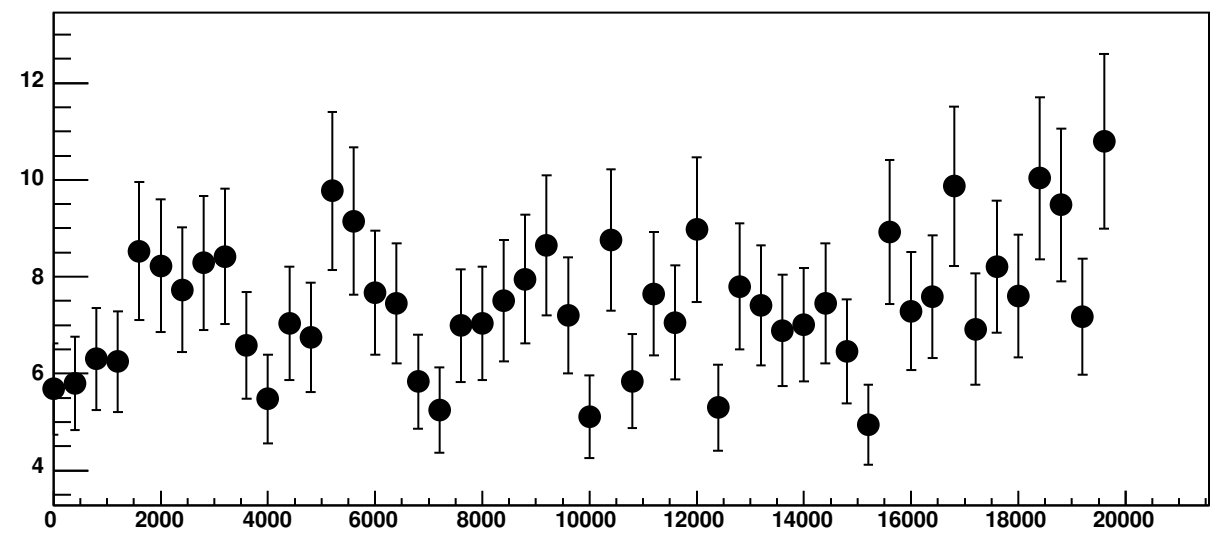

Chip 2, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

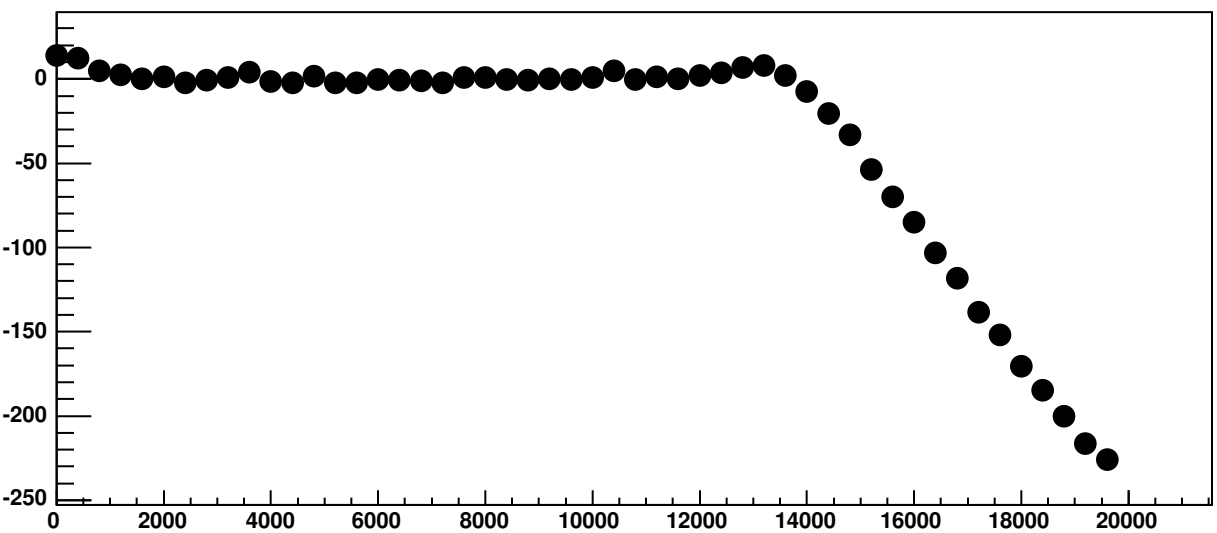
Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

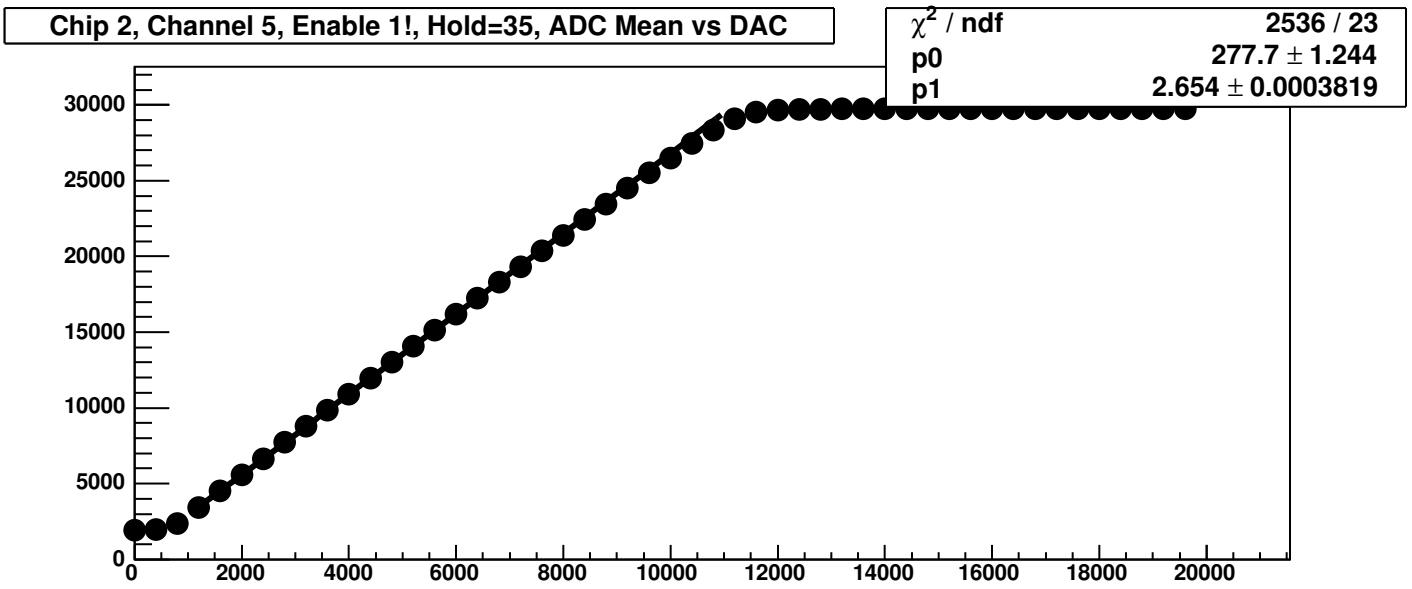


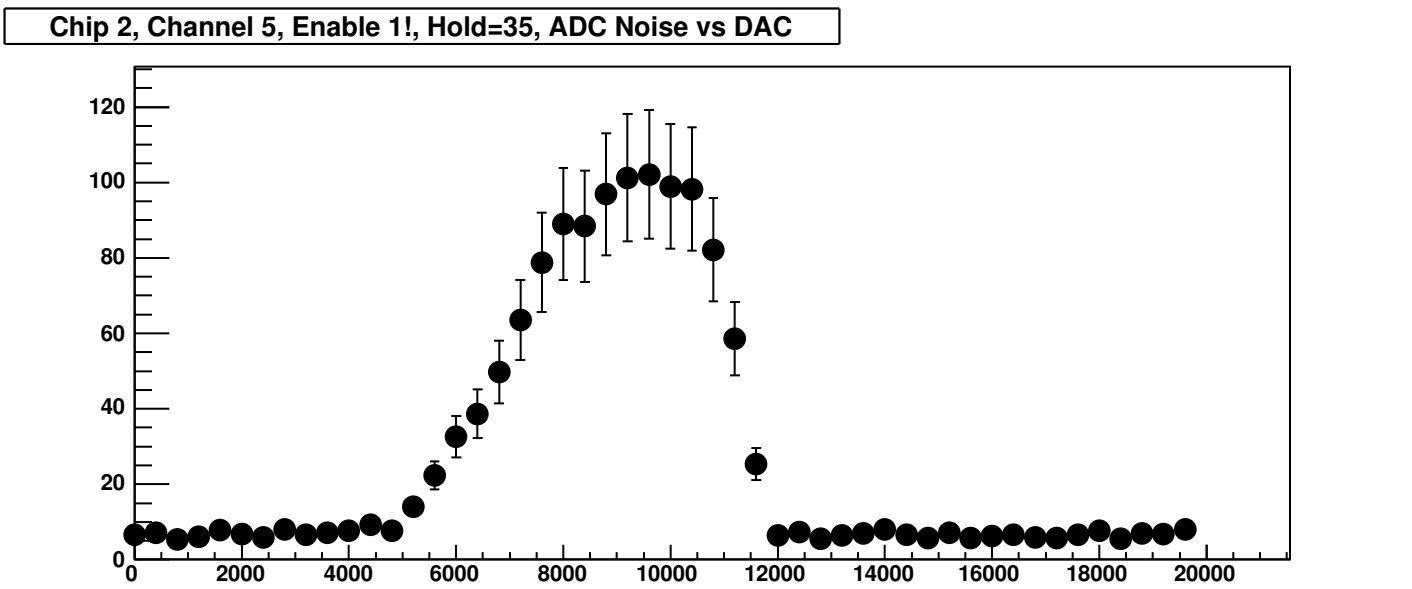
Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

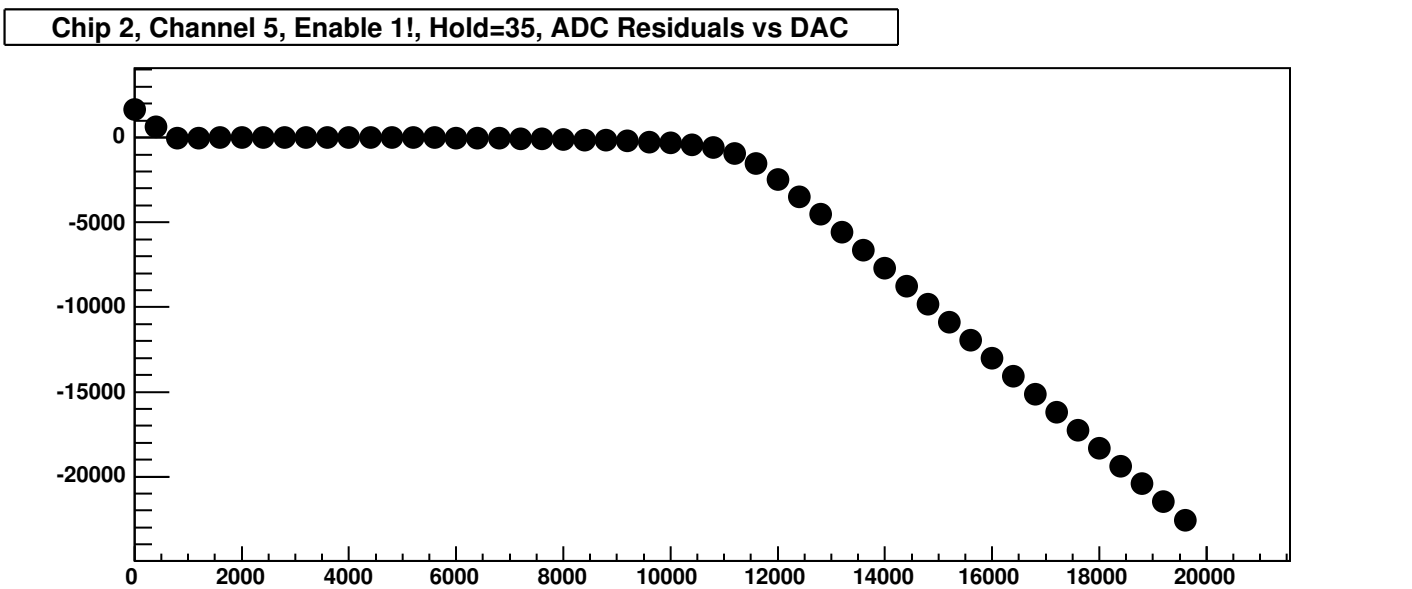


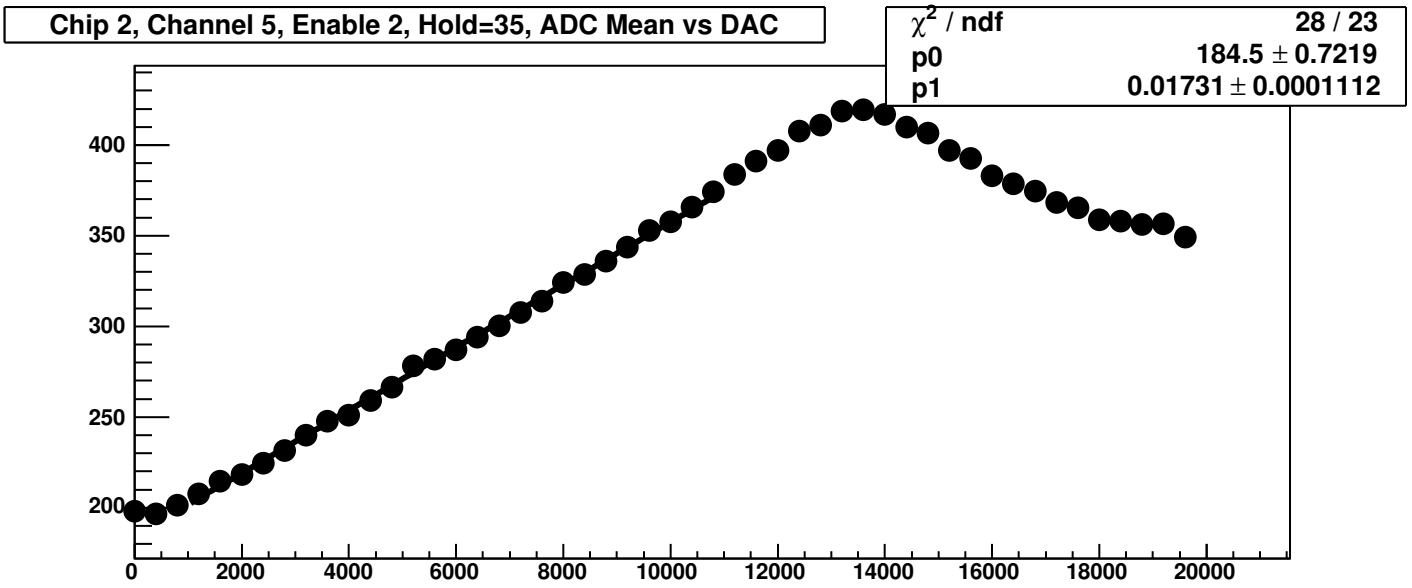
Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

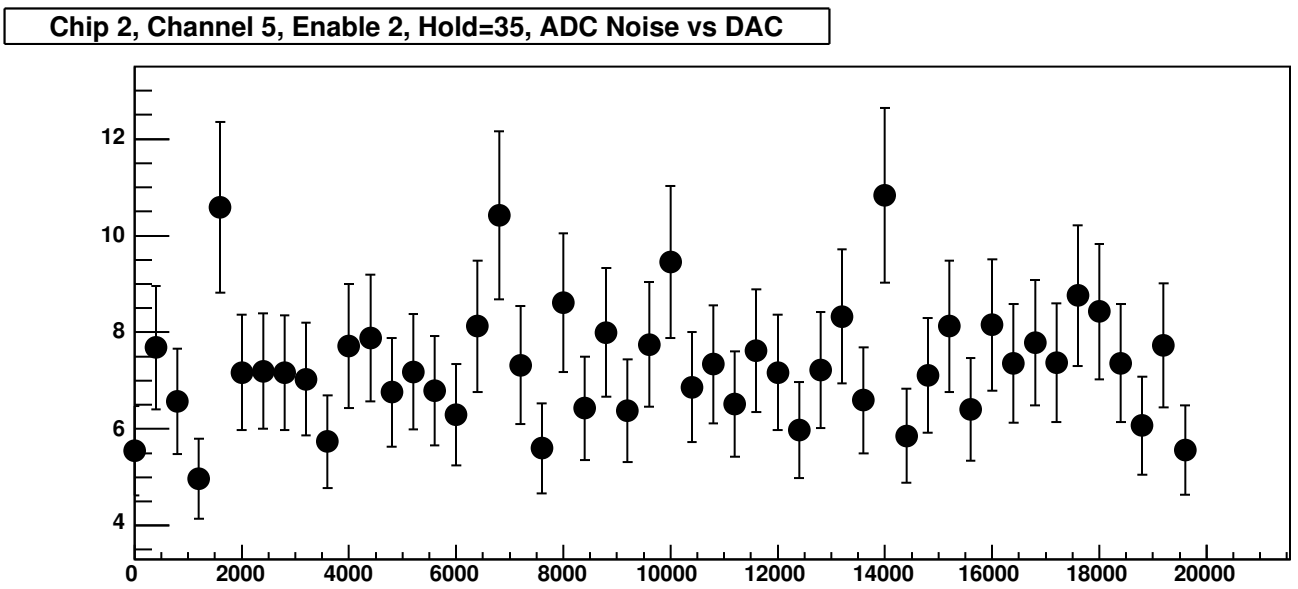


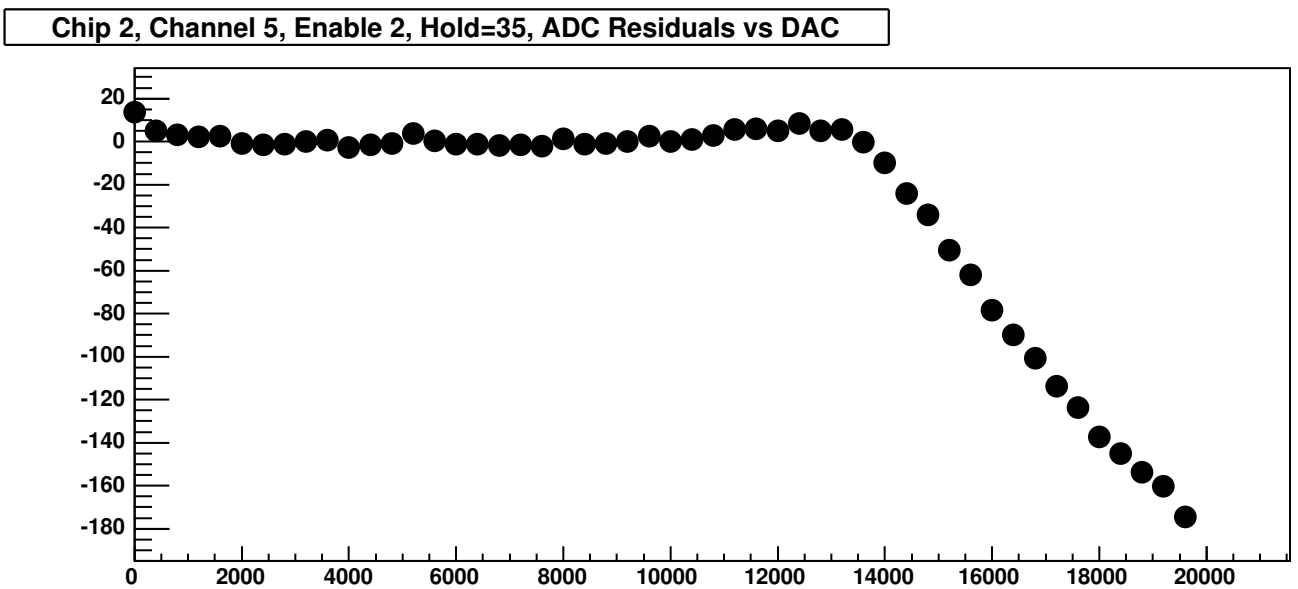

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 2536 / 23
p0 277.7 ± 1.244
p1 2.654 ± 0.0003819
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

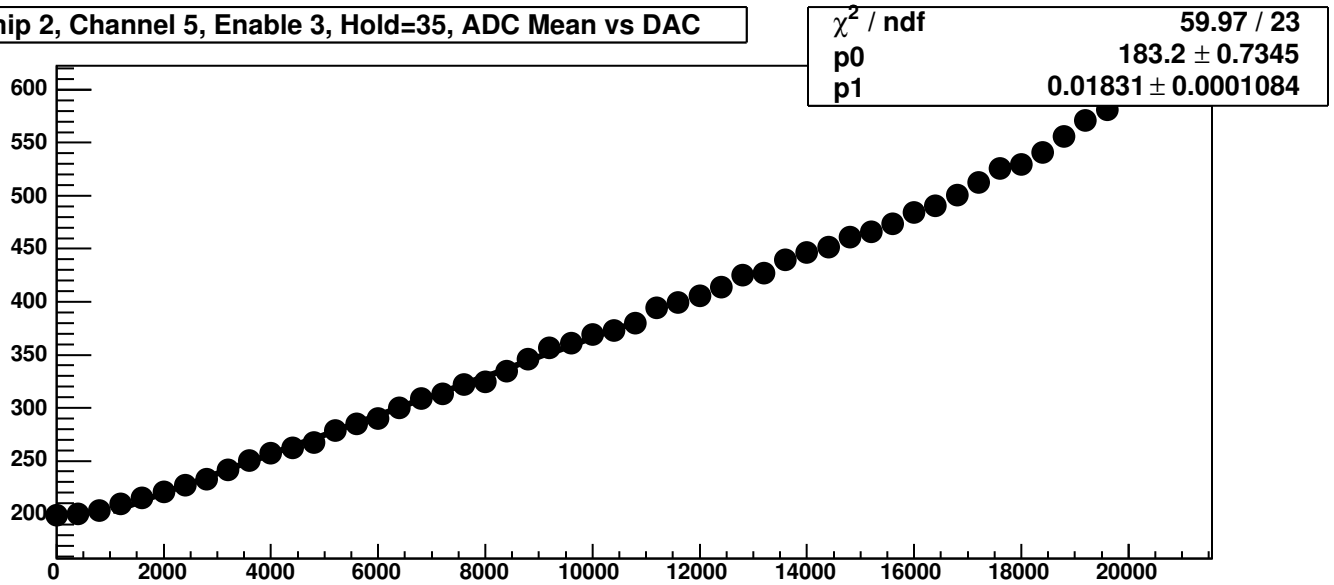

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 28 / 23
p0 184.5 ± 0.7219
p1 0.01731 ± 0.0001112
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

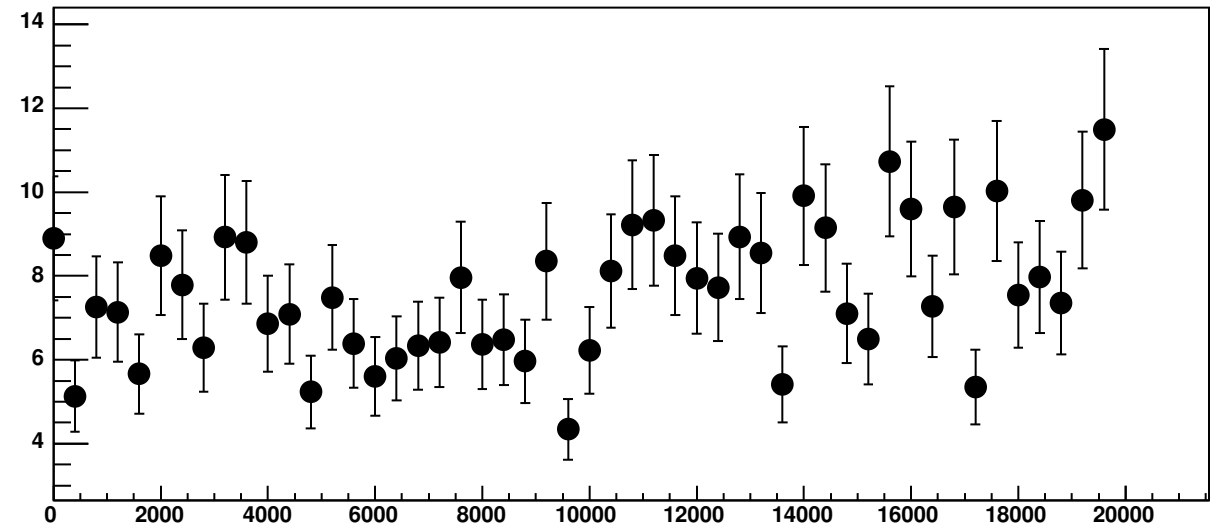

Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

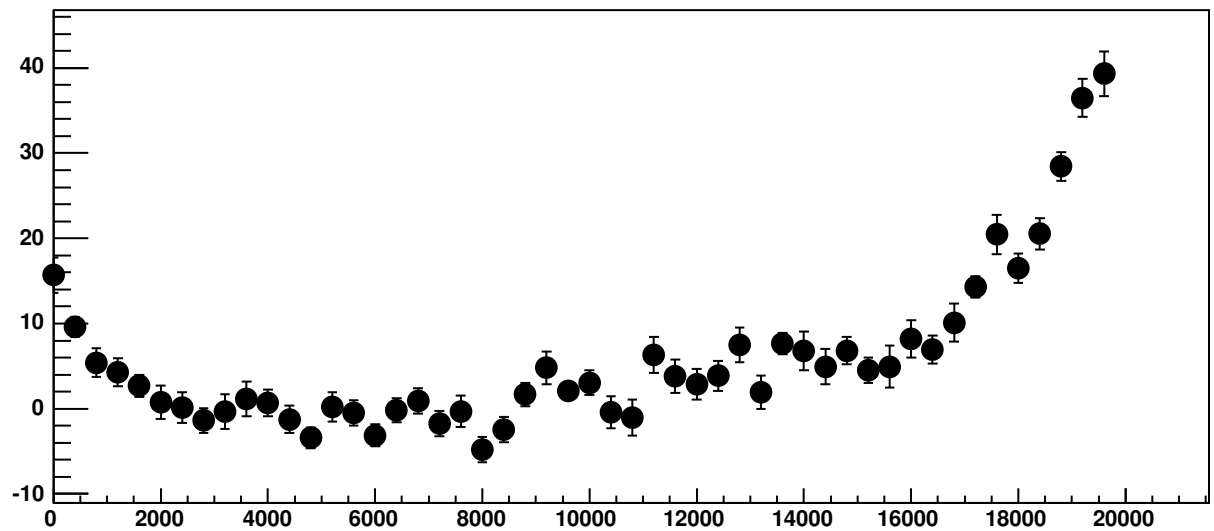
Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



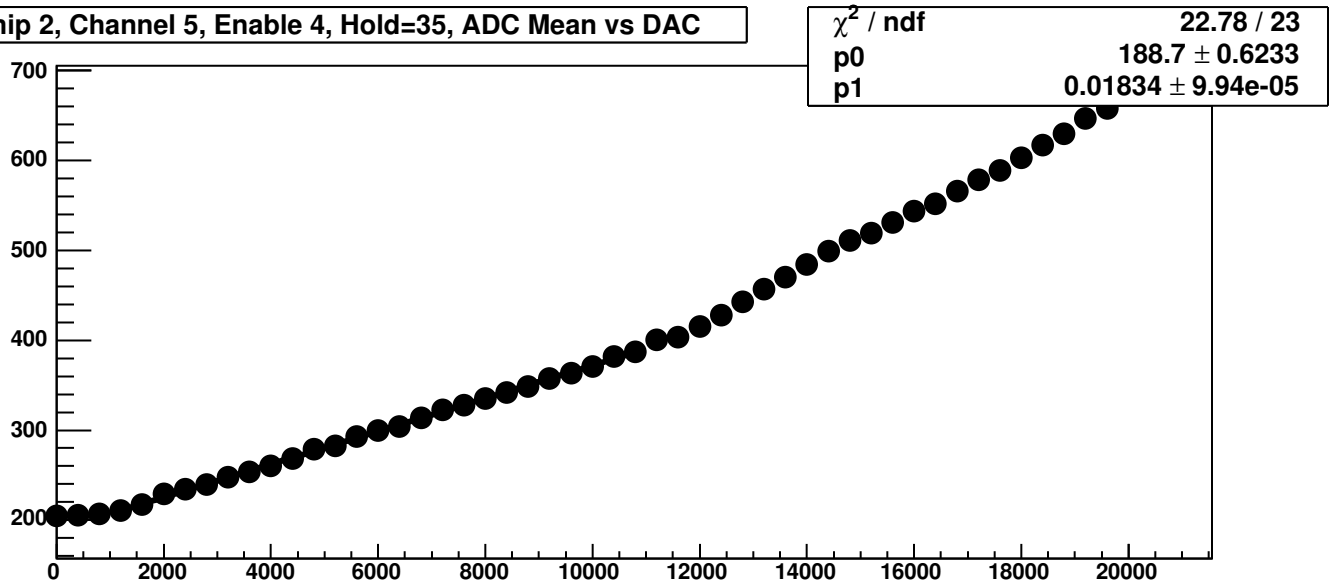
Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



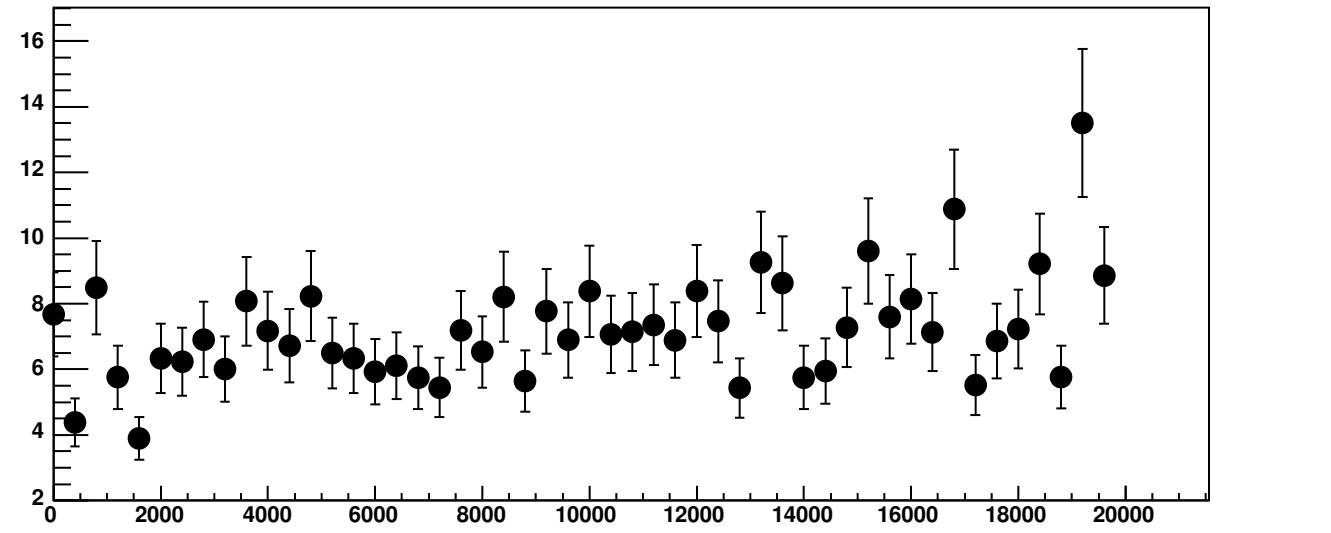
Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



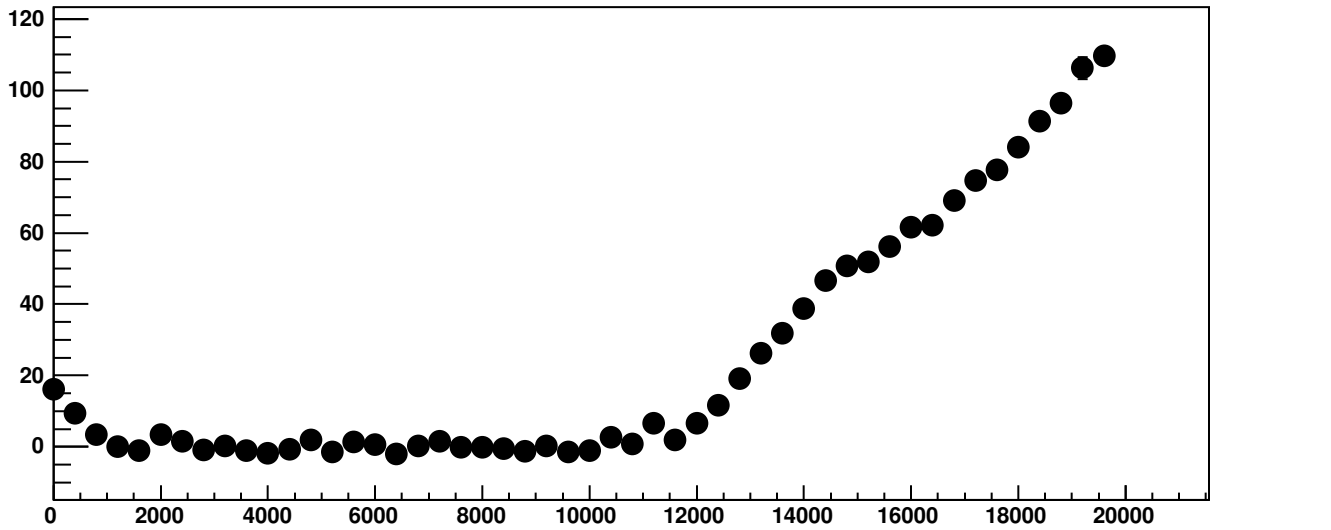
Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

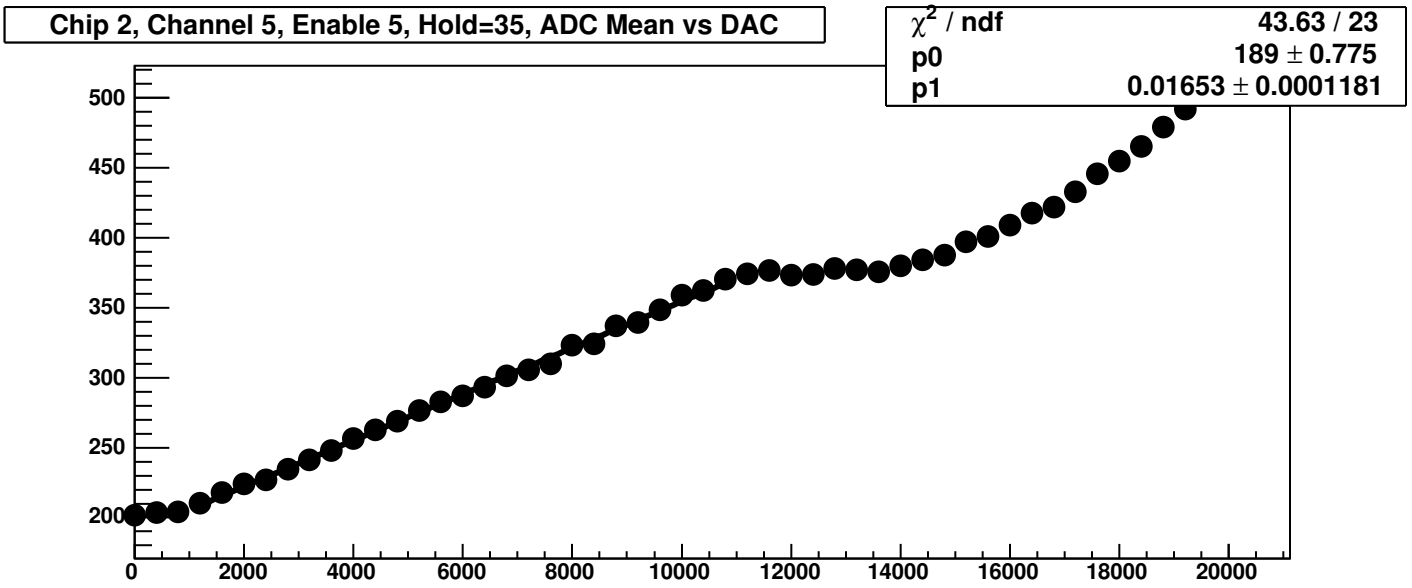


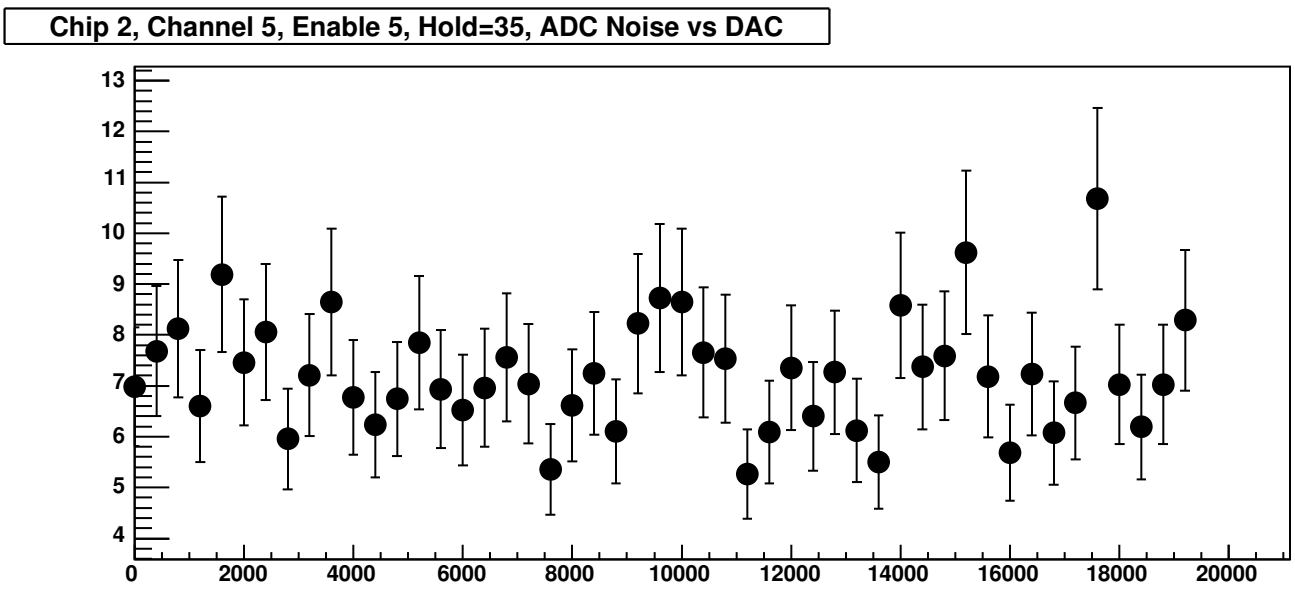
Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC

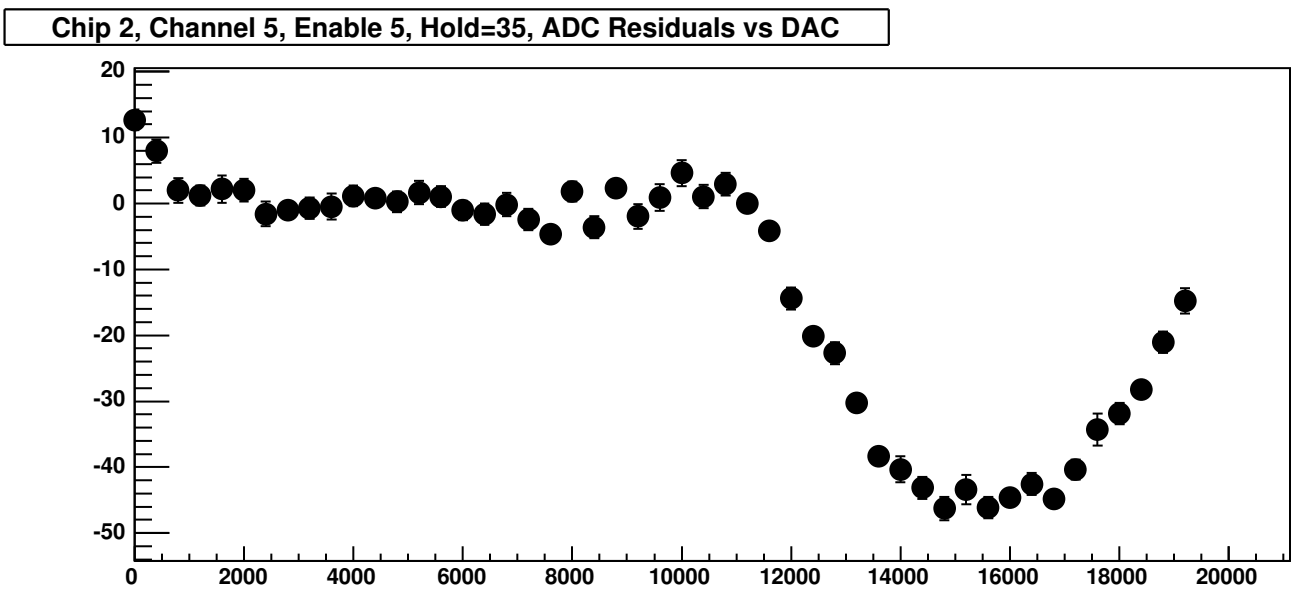


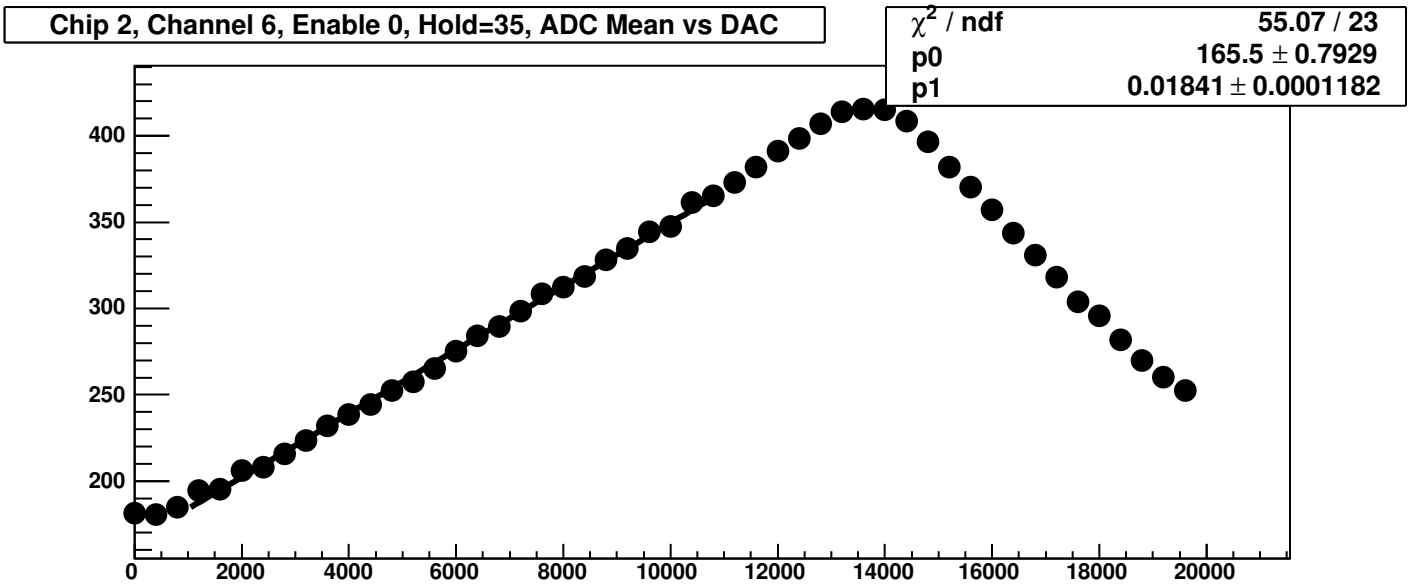
Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

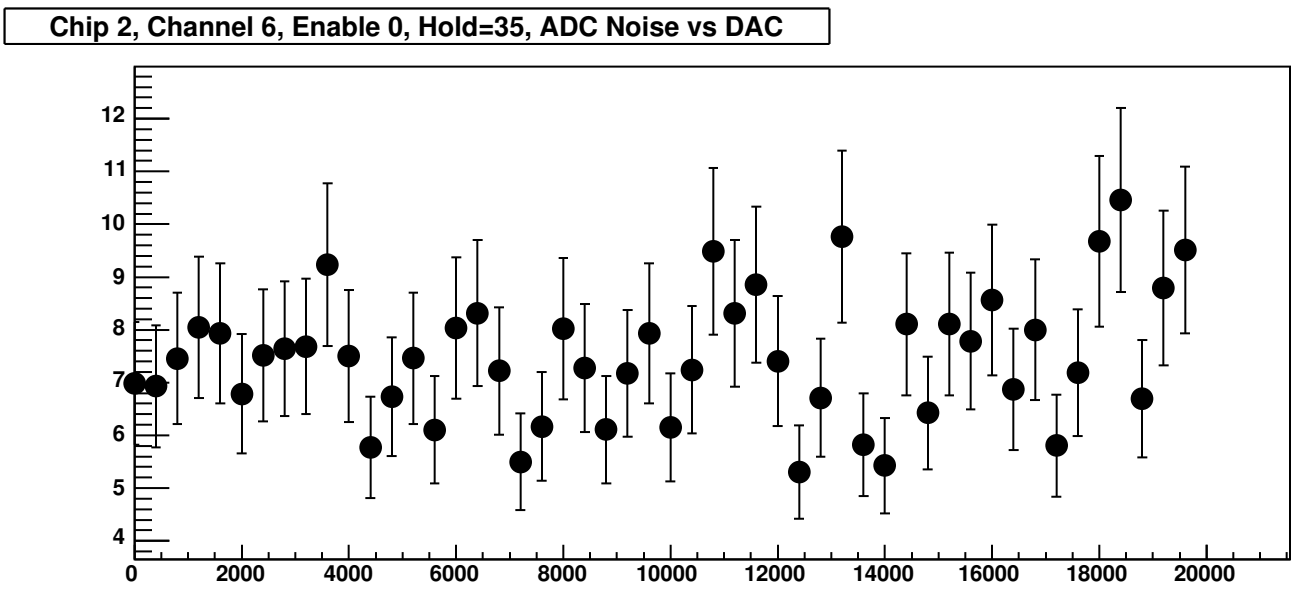


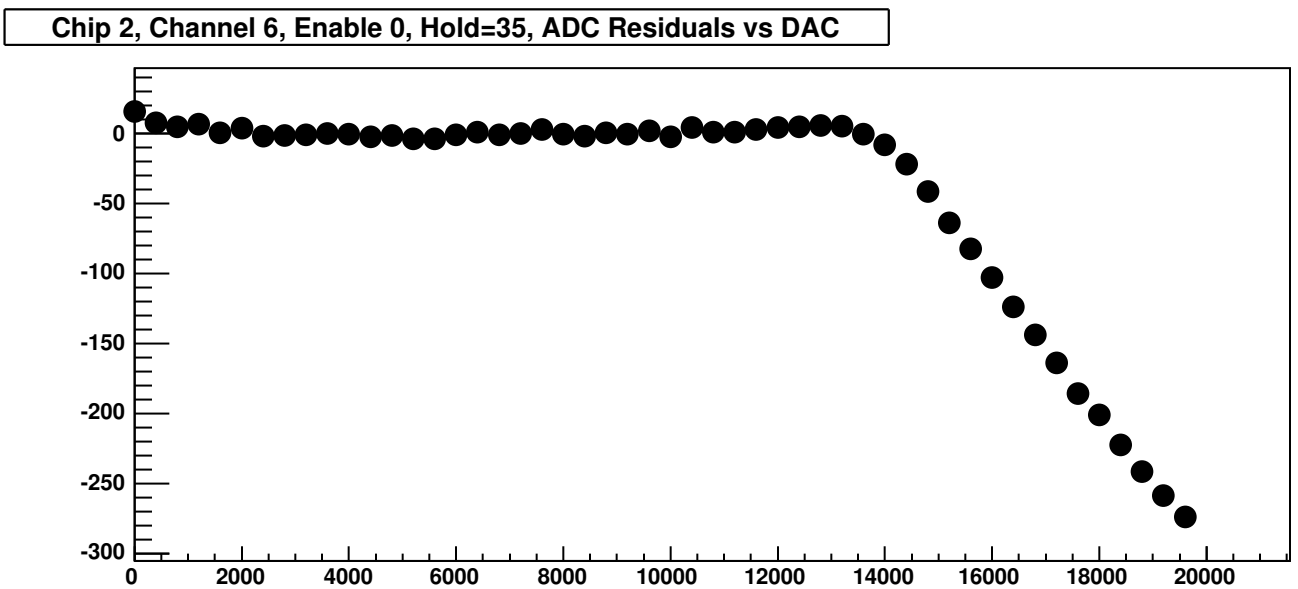

Chip 2, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 43.63 / 23
p0 189 ± 0.775
p1 0.01653 ± 0.0001181
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

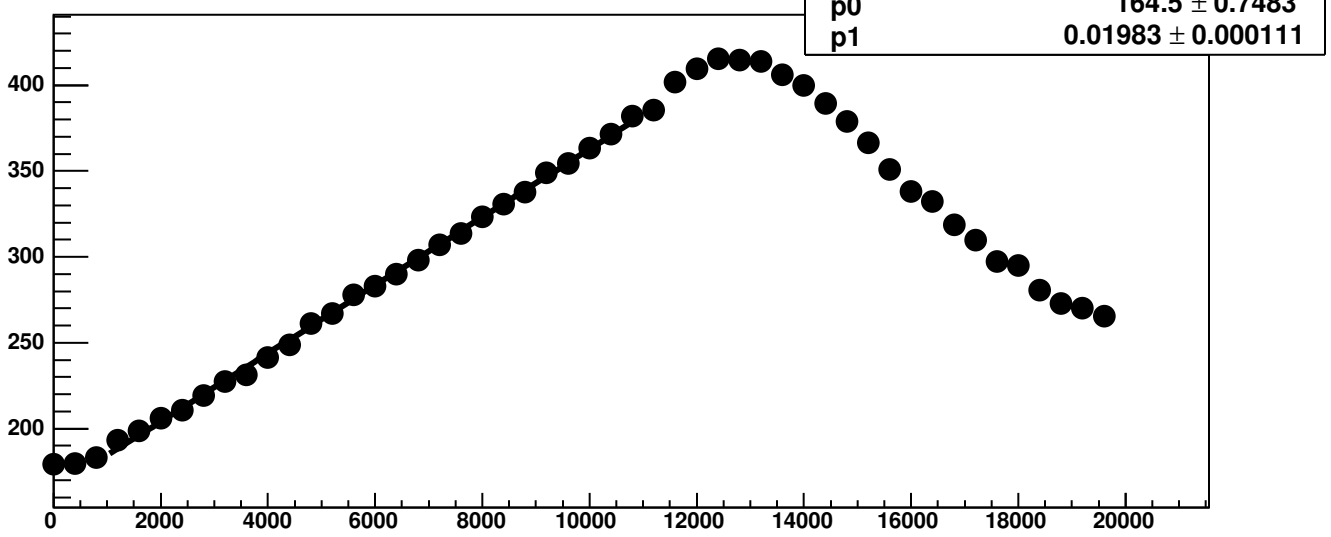

Chip 2, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 55.07 / 23
p0 165.5 ± 0.7929
p1 0.01841 ± 0.0001182
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

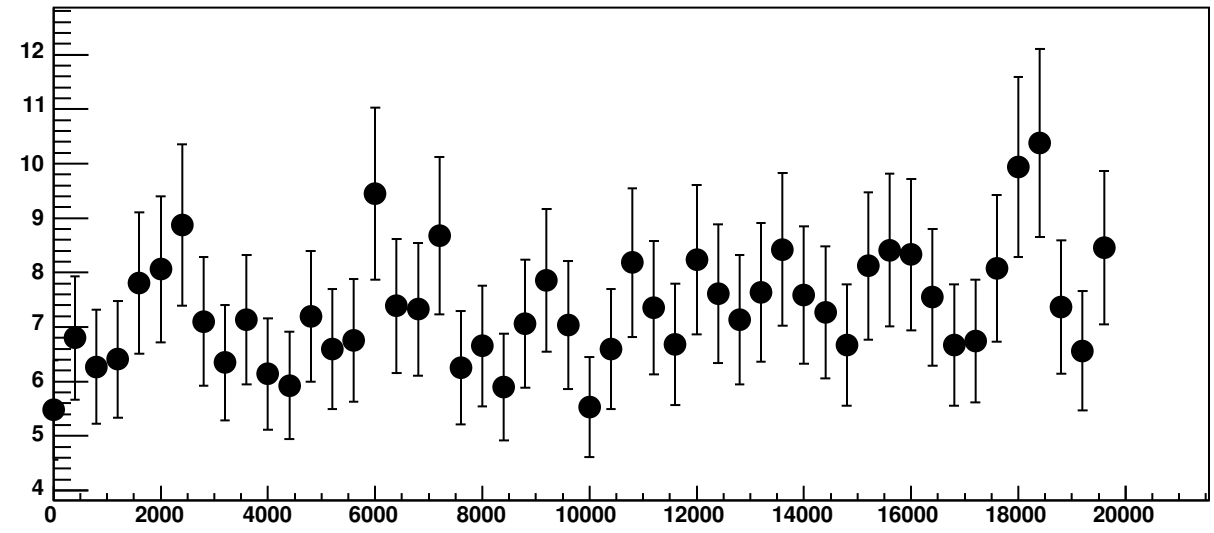

Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

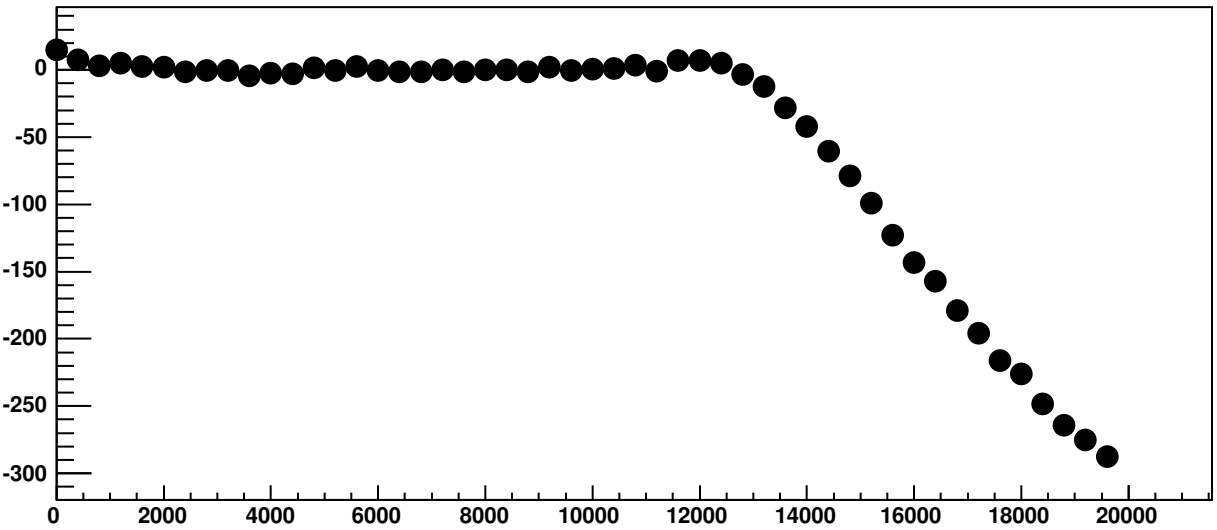
Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

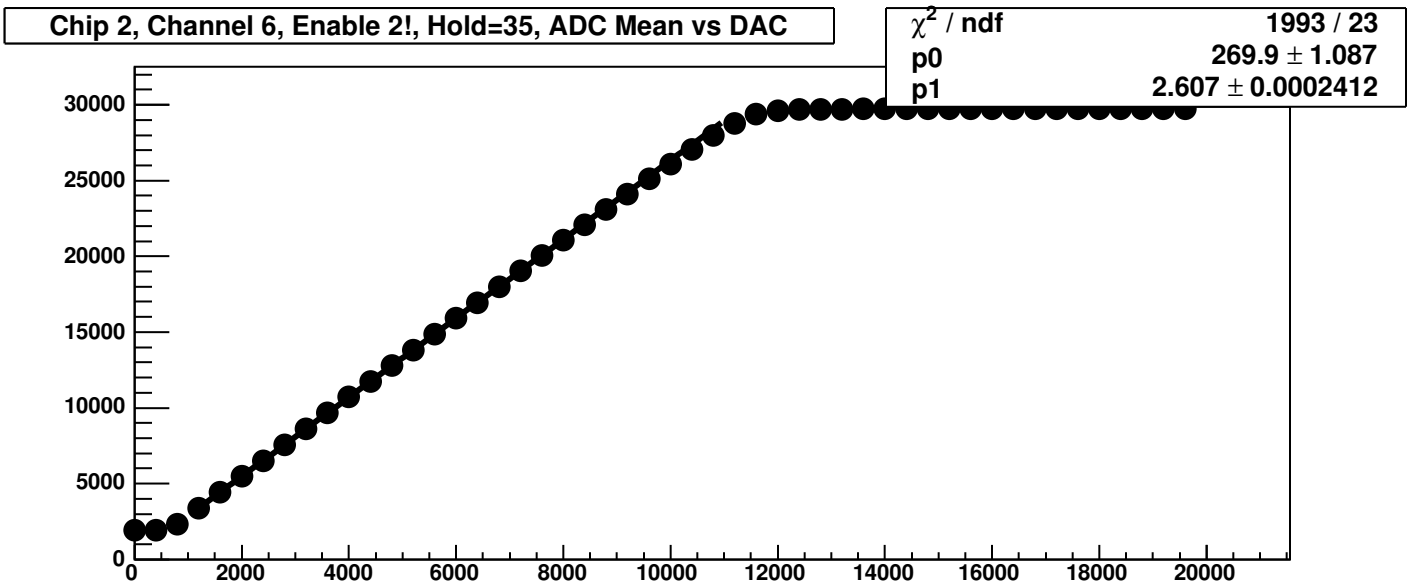


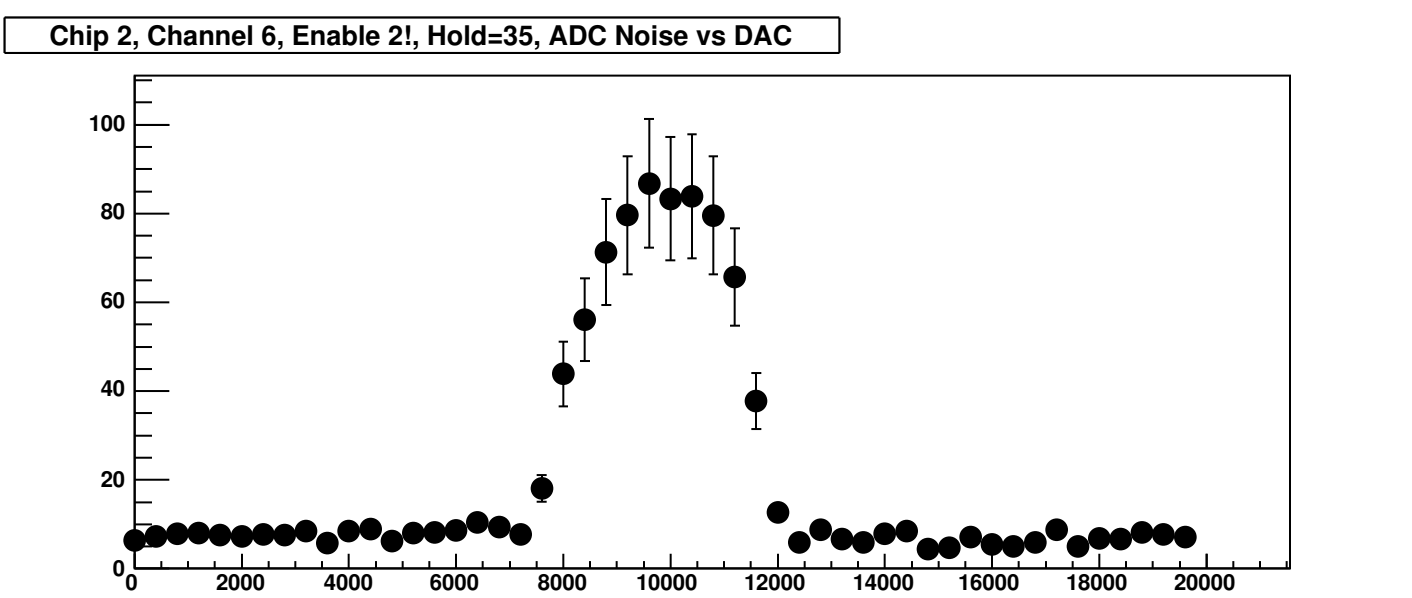
Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

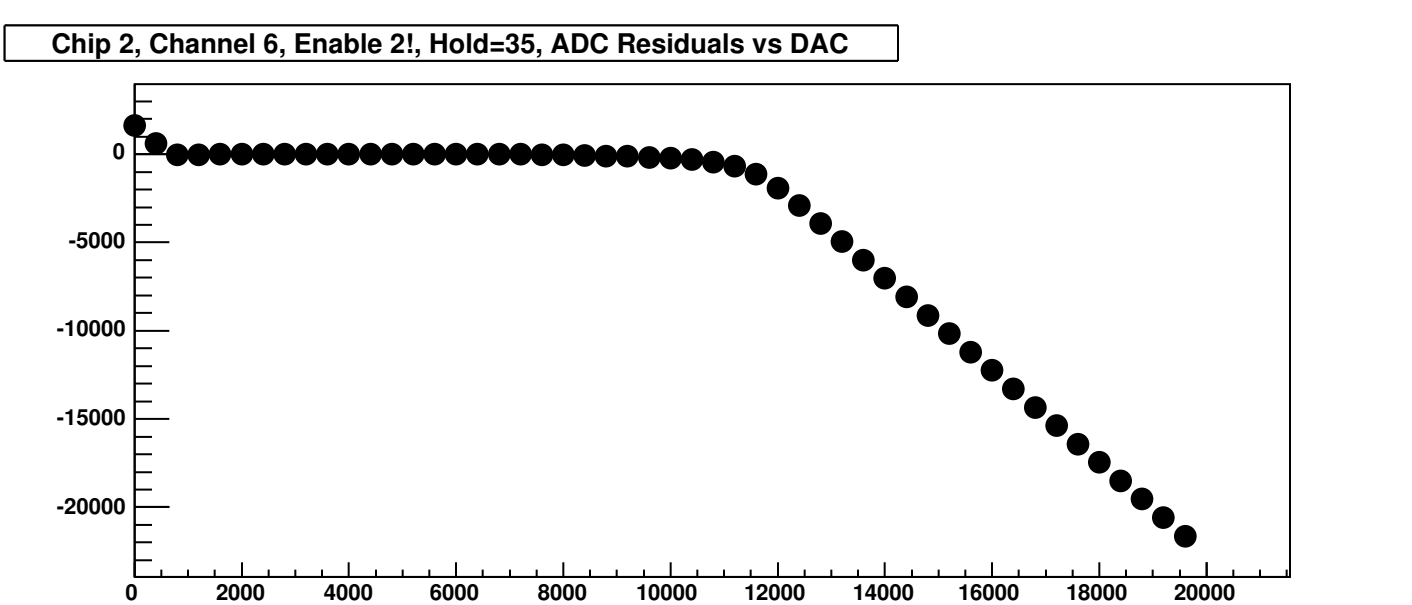


Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

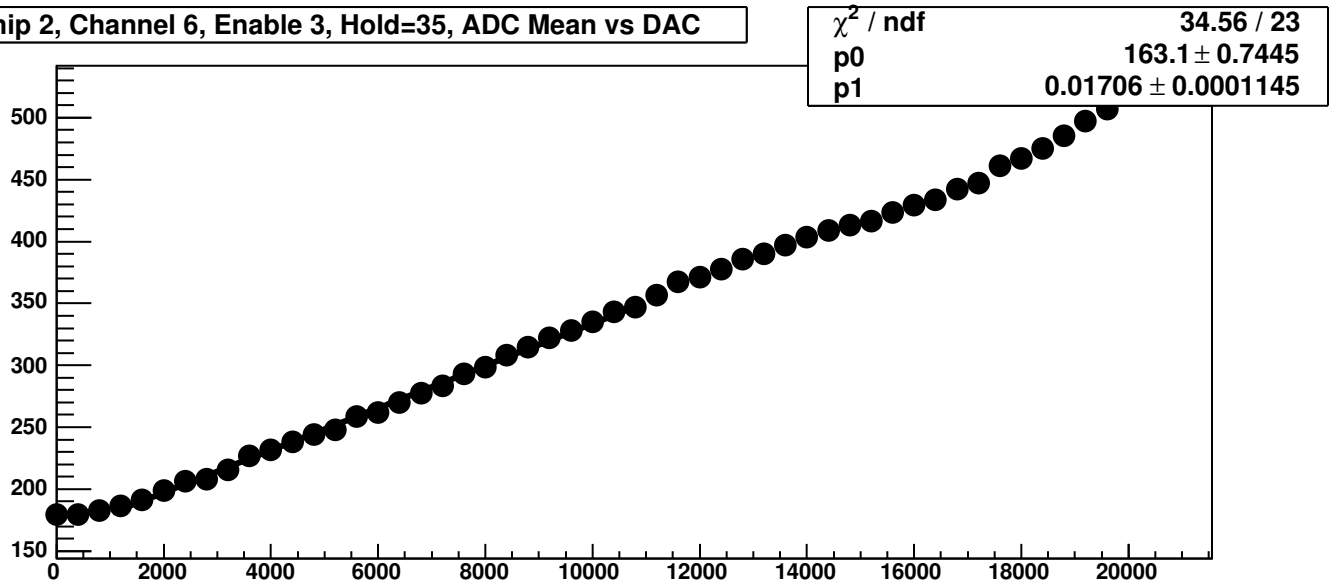



Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ^2 / ndf 1993 / 23
p0 269.9 ± 1.087
p1 2.607 ± 0.0002412
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

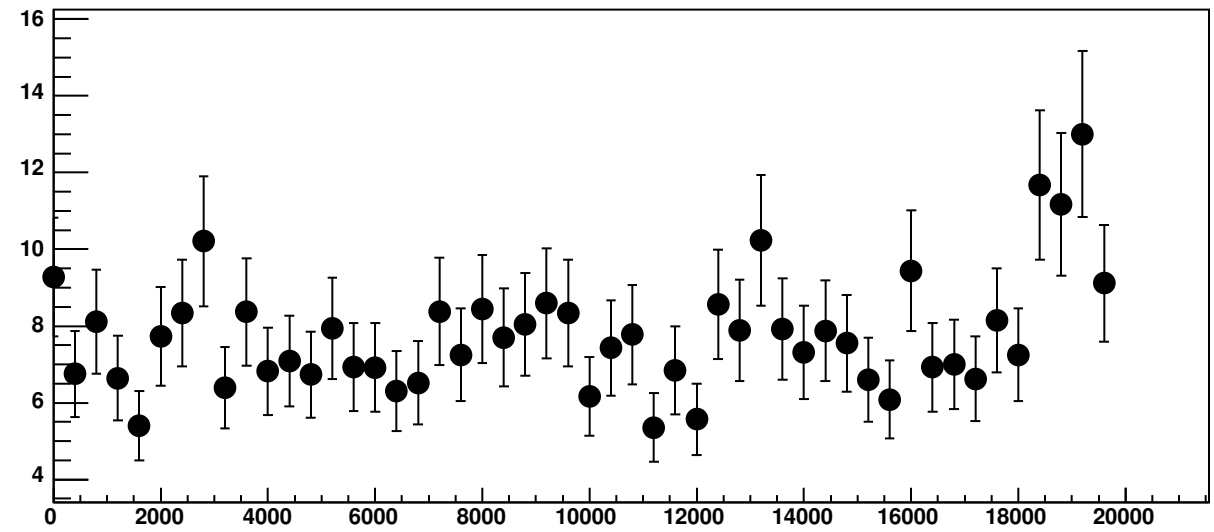

Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

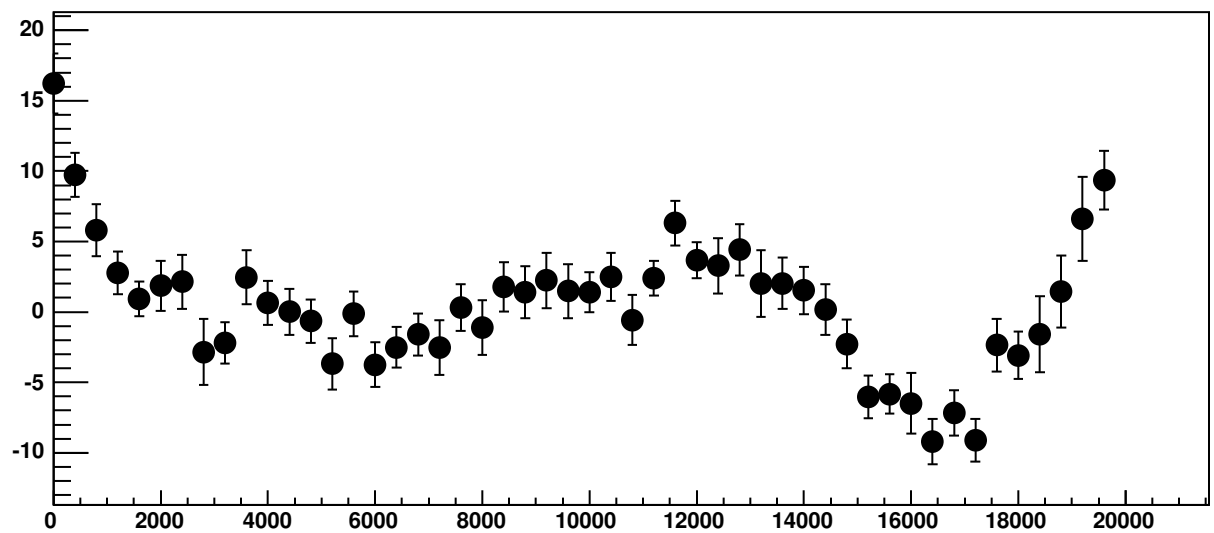
Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



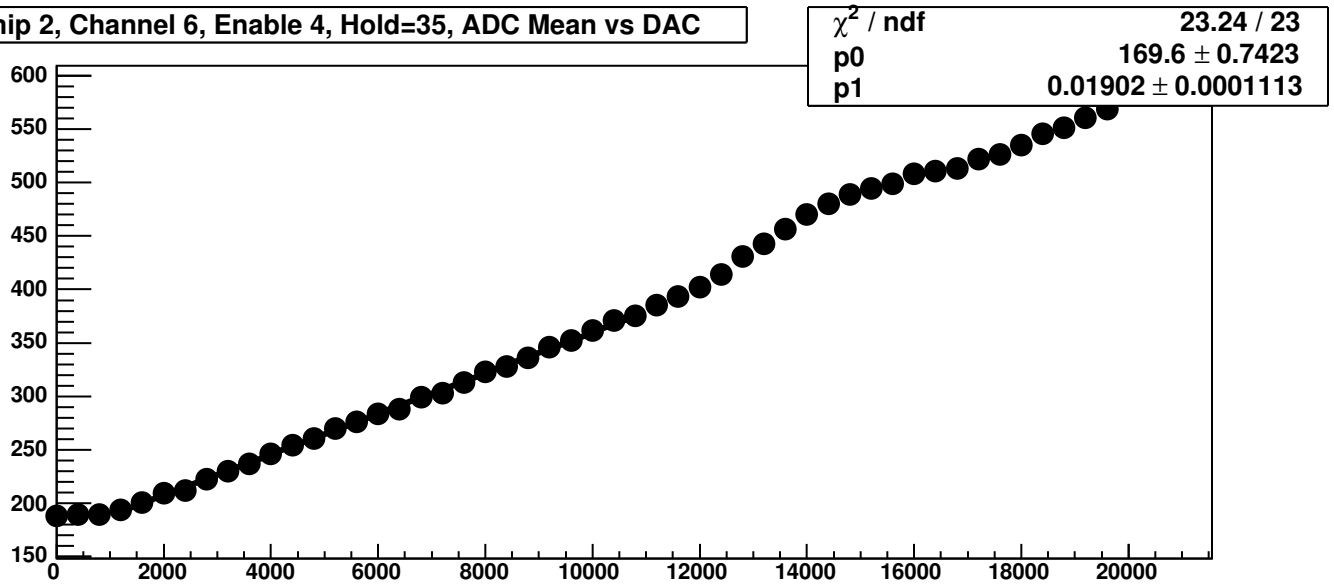
Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



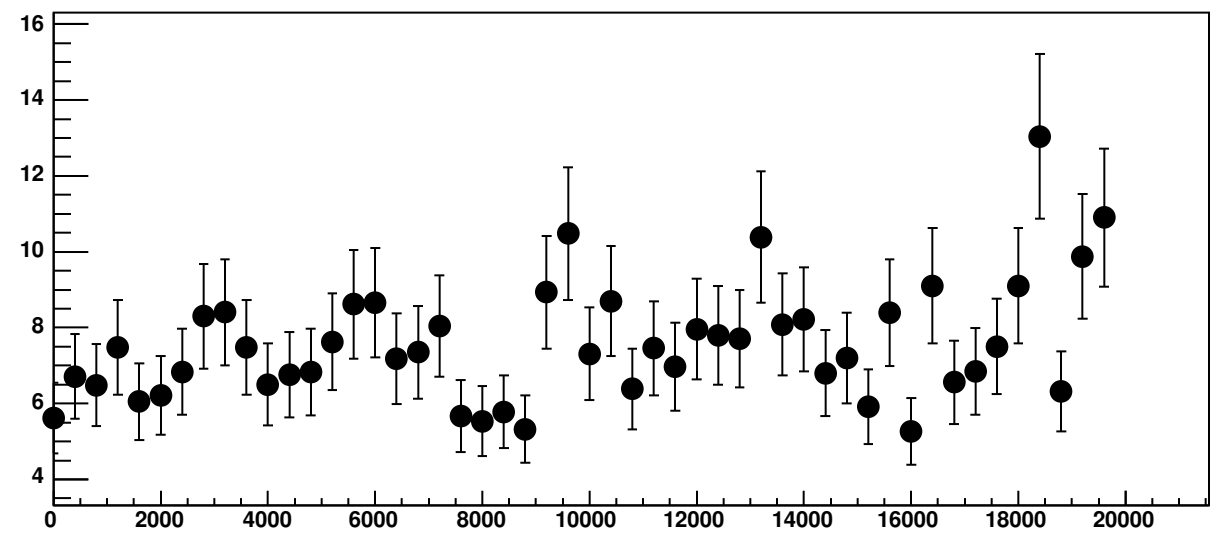
Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



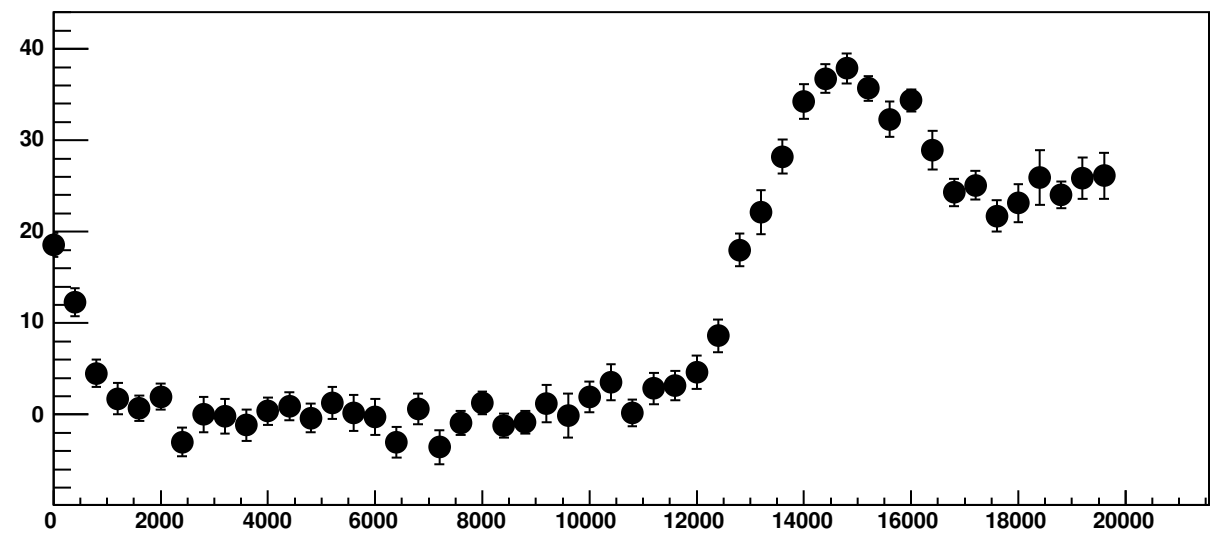
Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



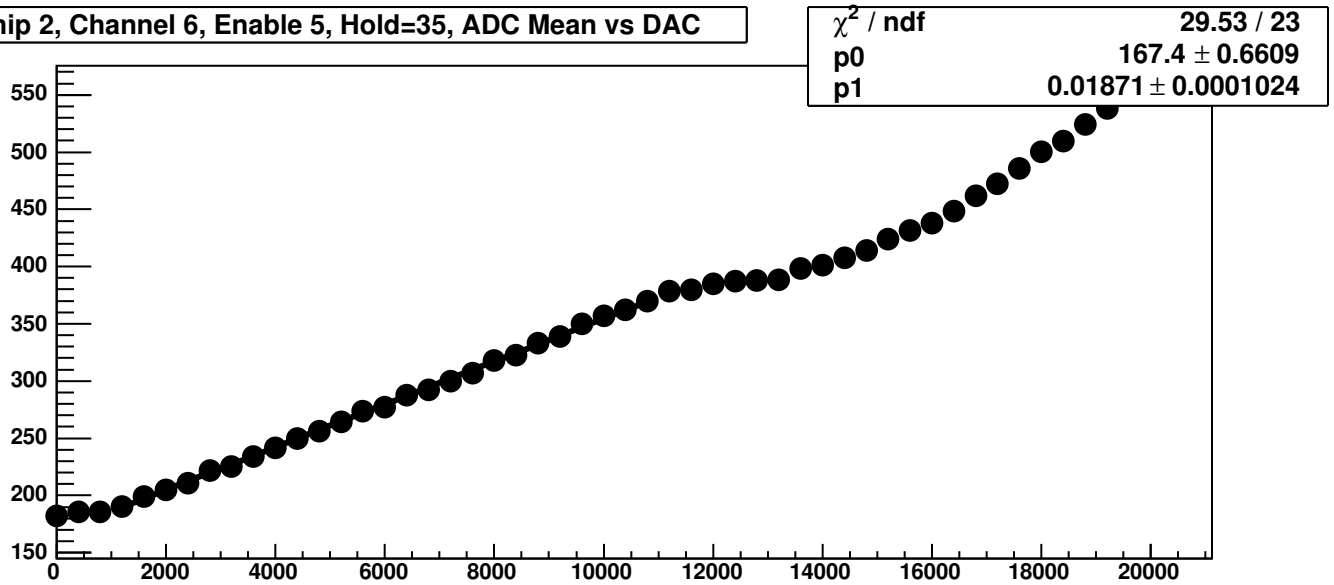
Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



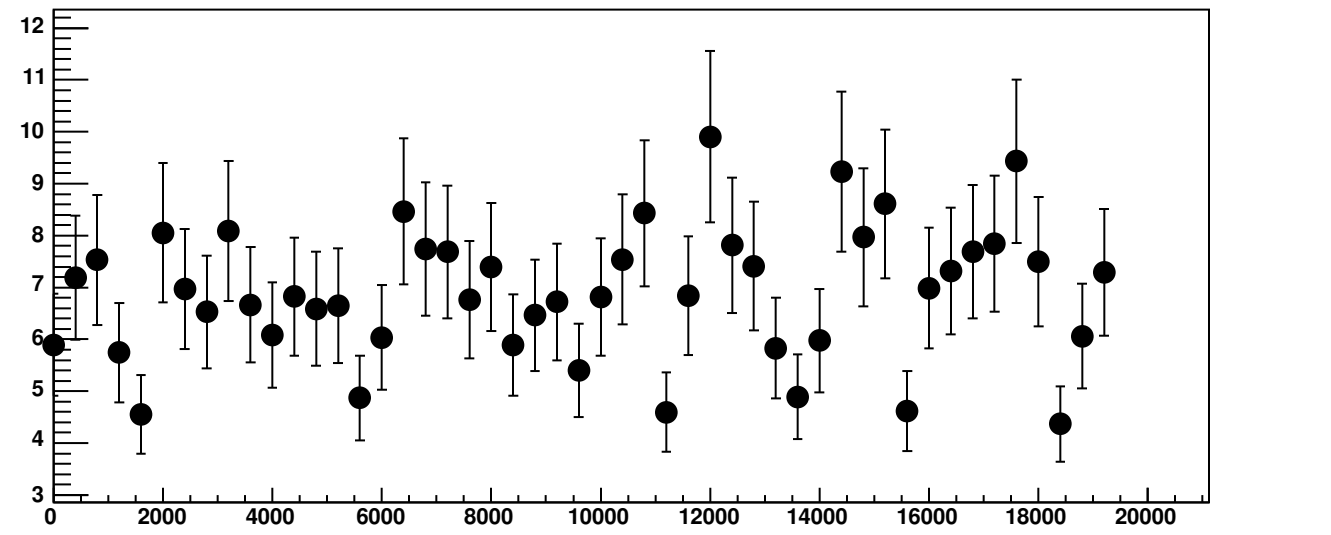
Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



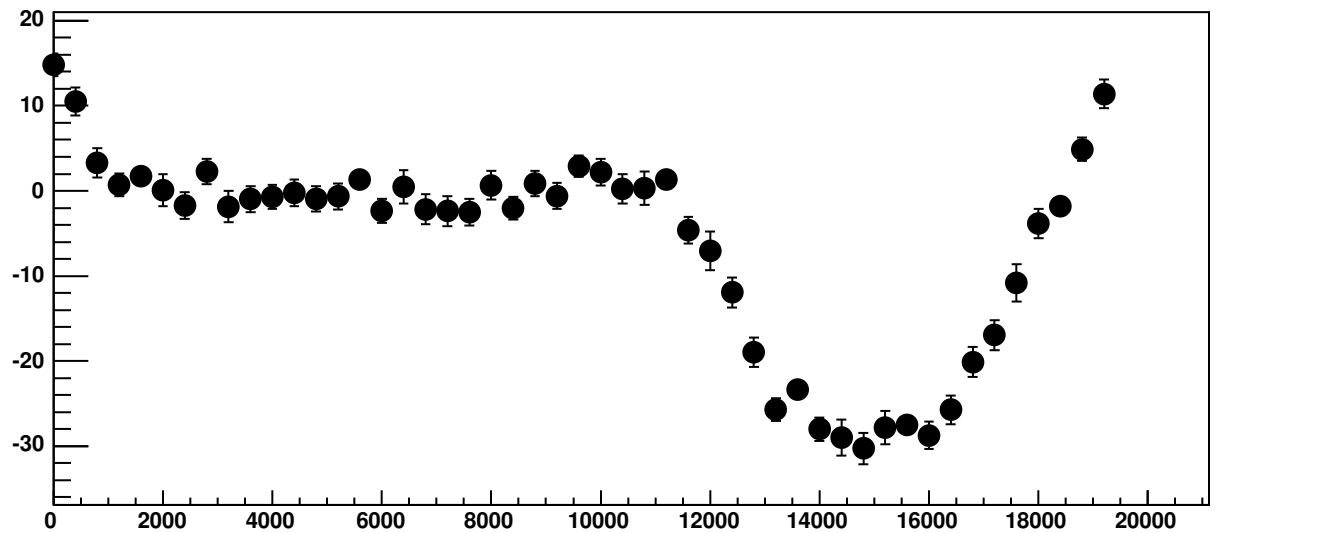
Chip 2, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

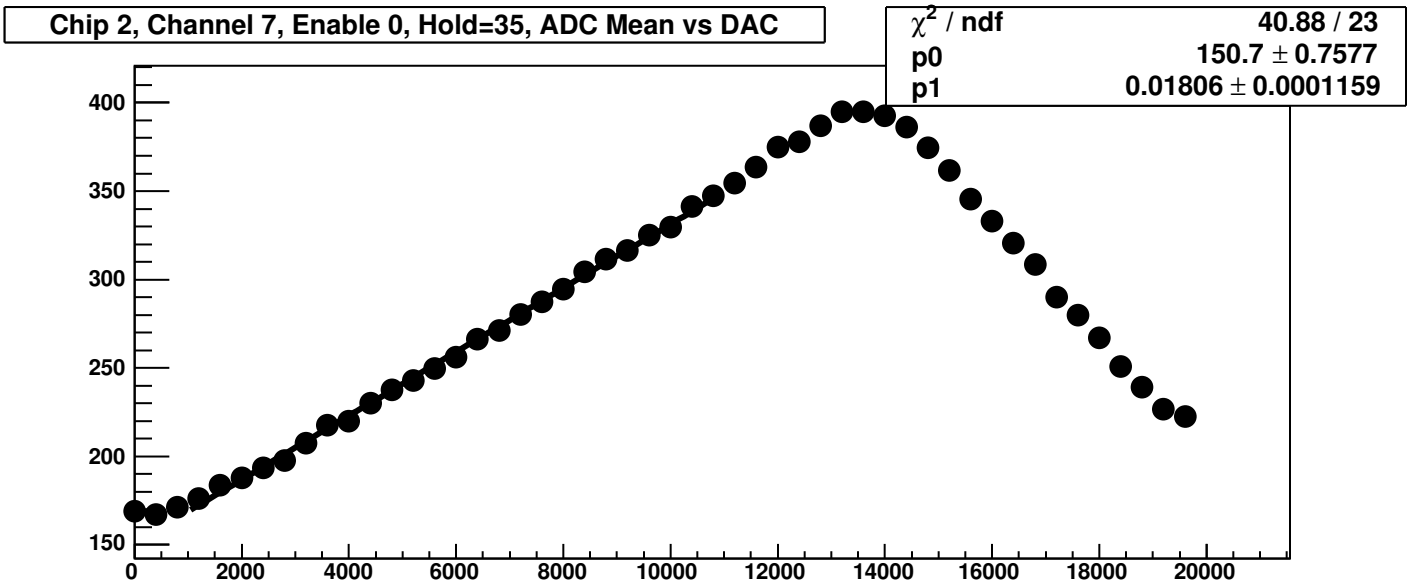


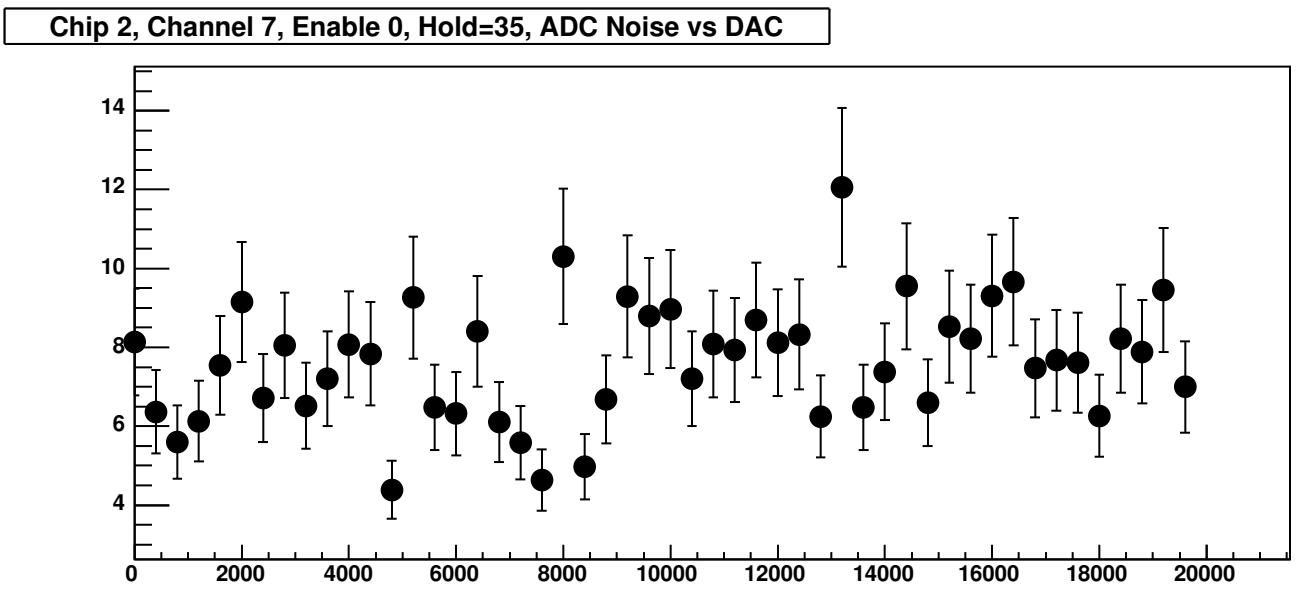
Chip 2, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC

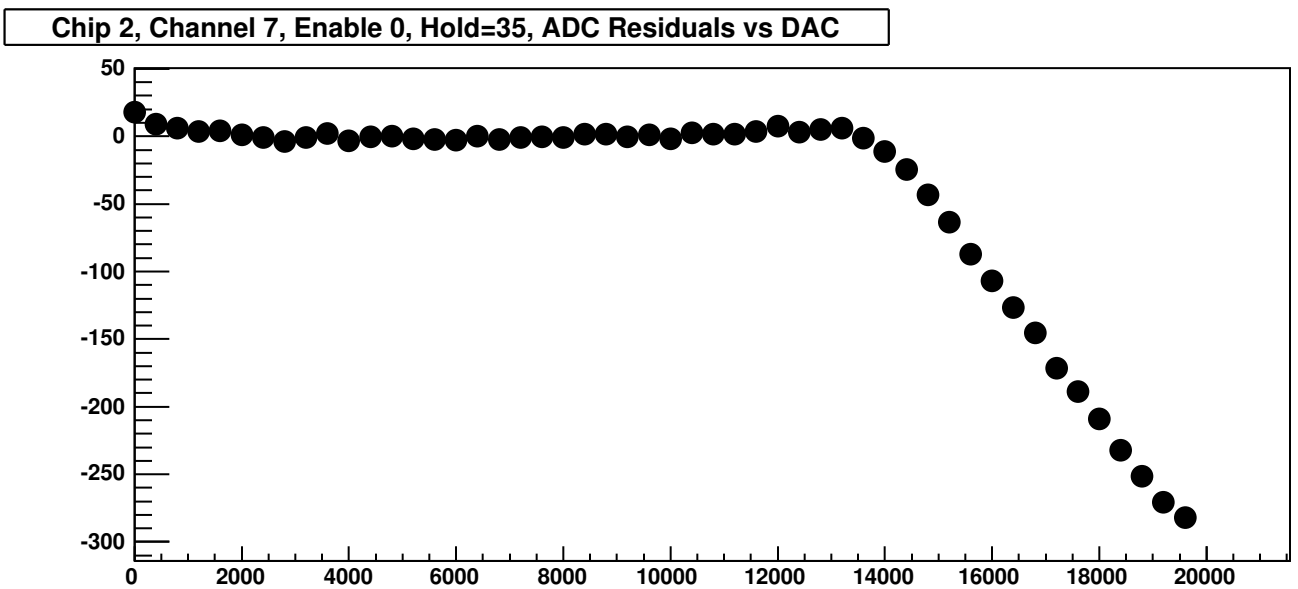


Chip 2, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

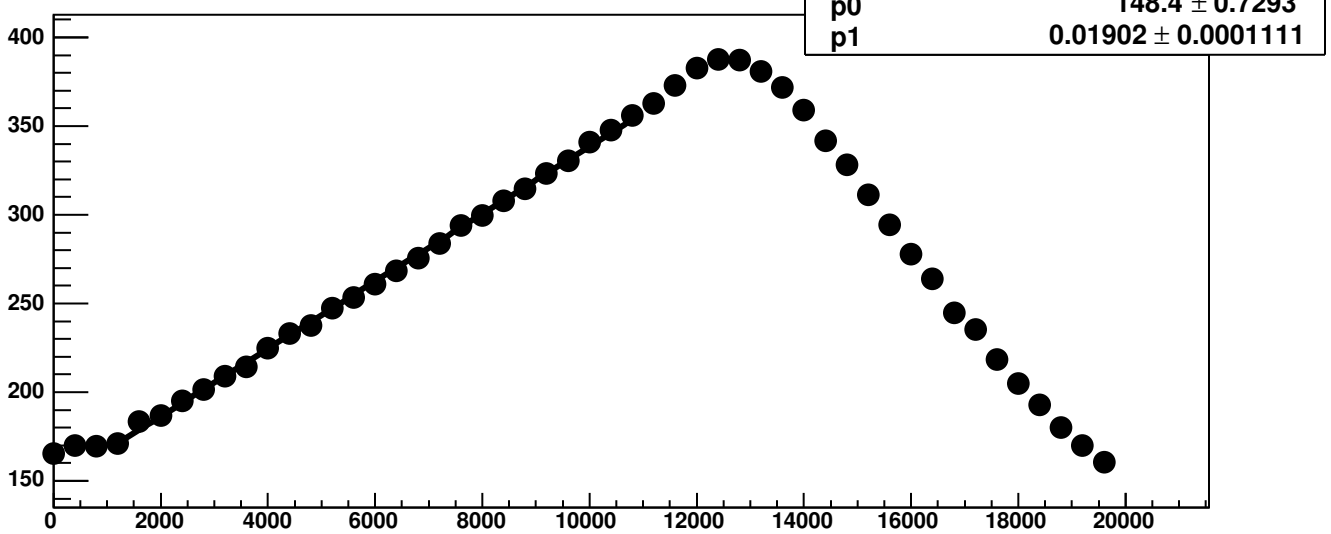



Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 40.88 / 23
p0 150.7 ± 0.7577
p1 0.01806 ± 0.0001159
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

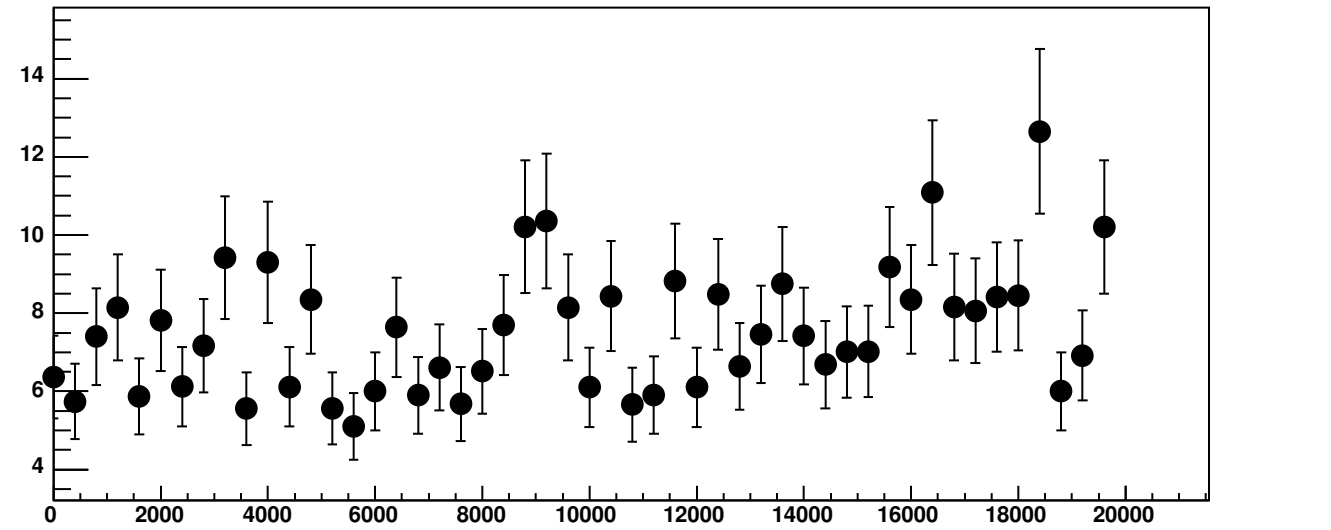

Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

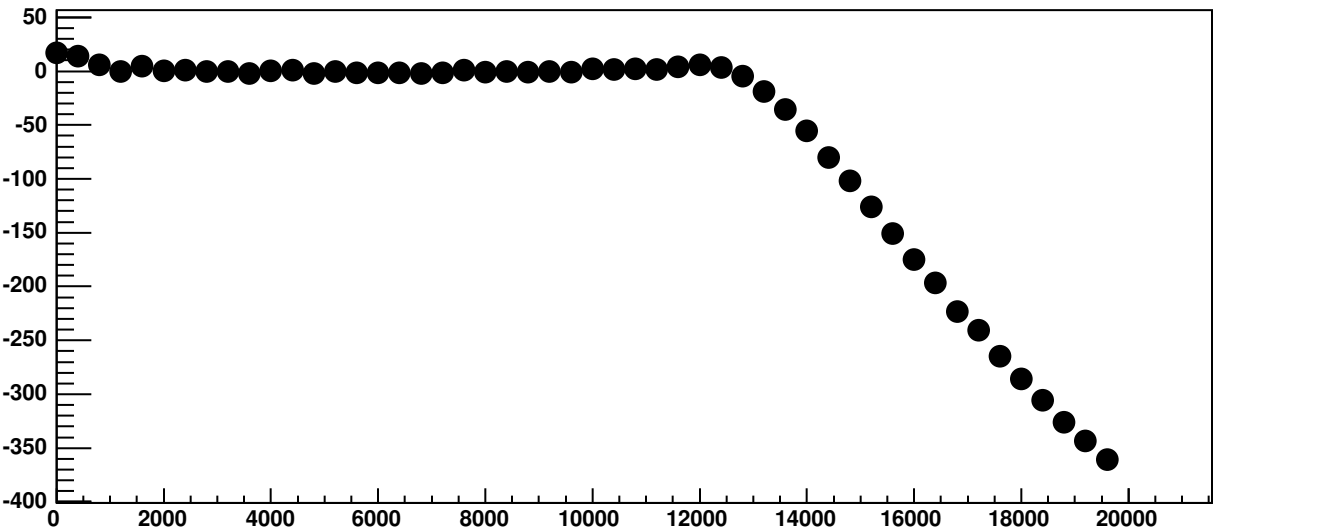
Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

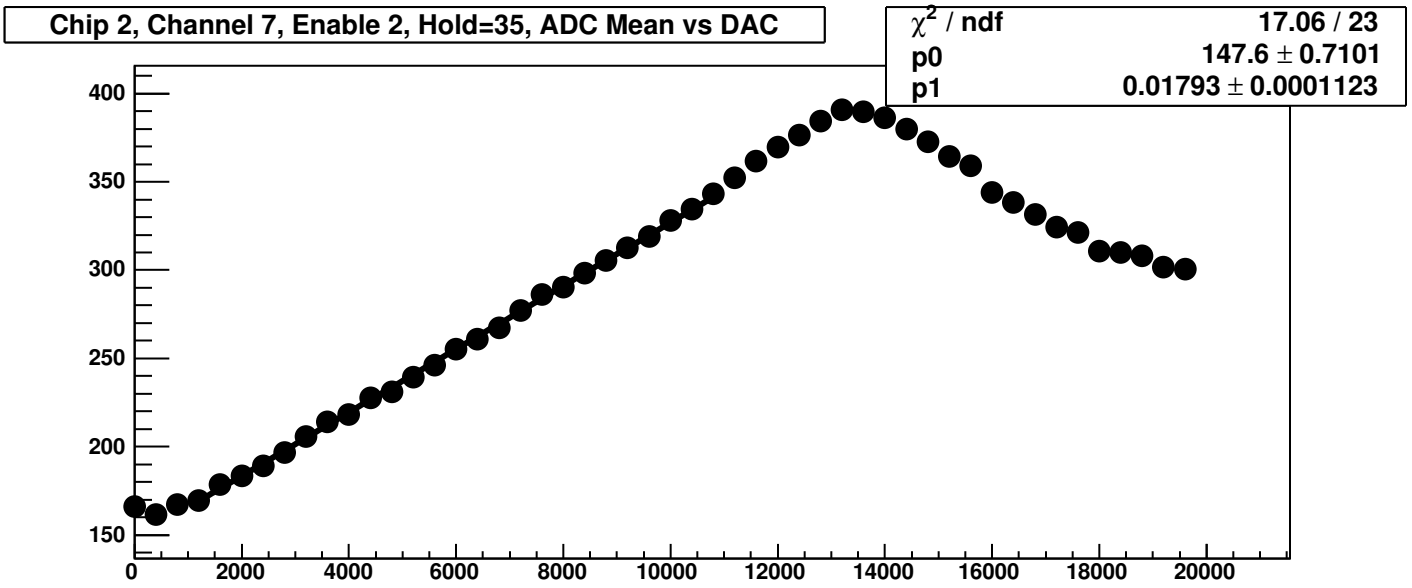


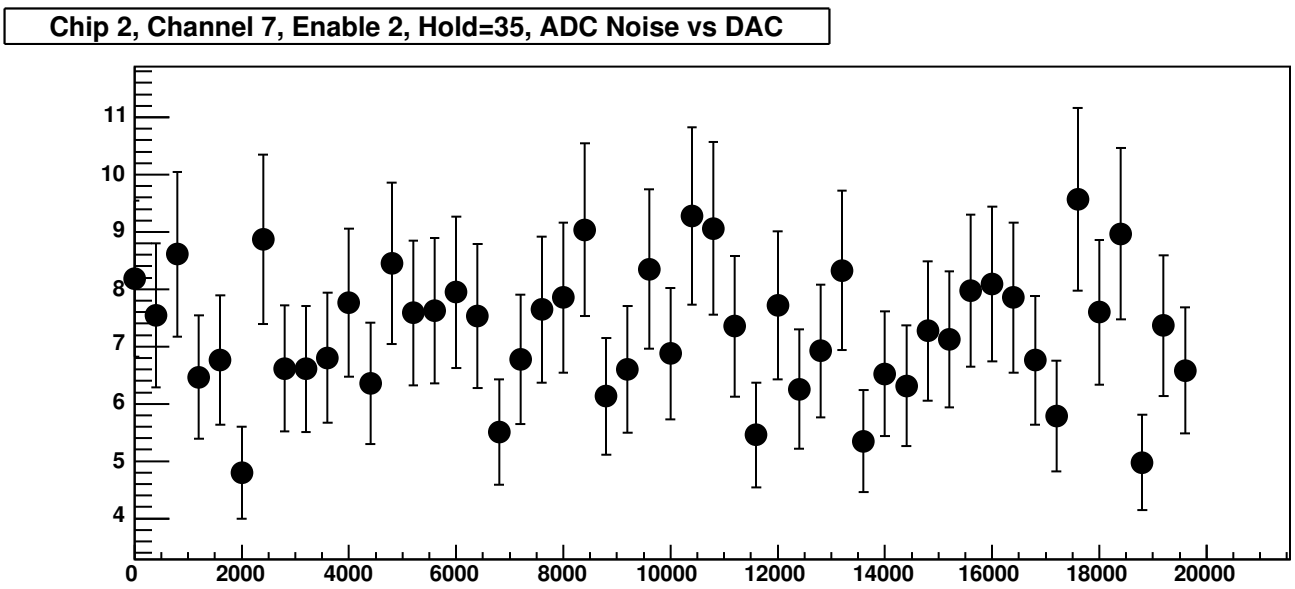
Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC

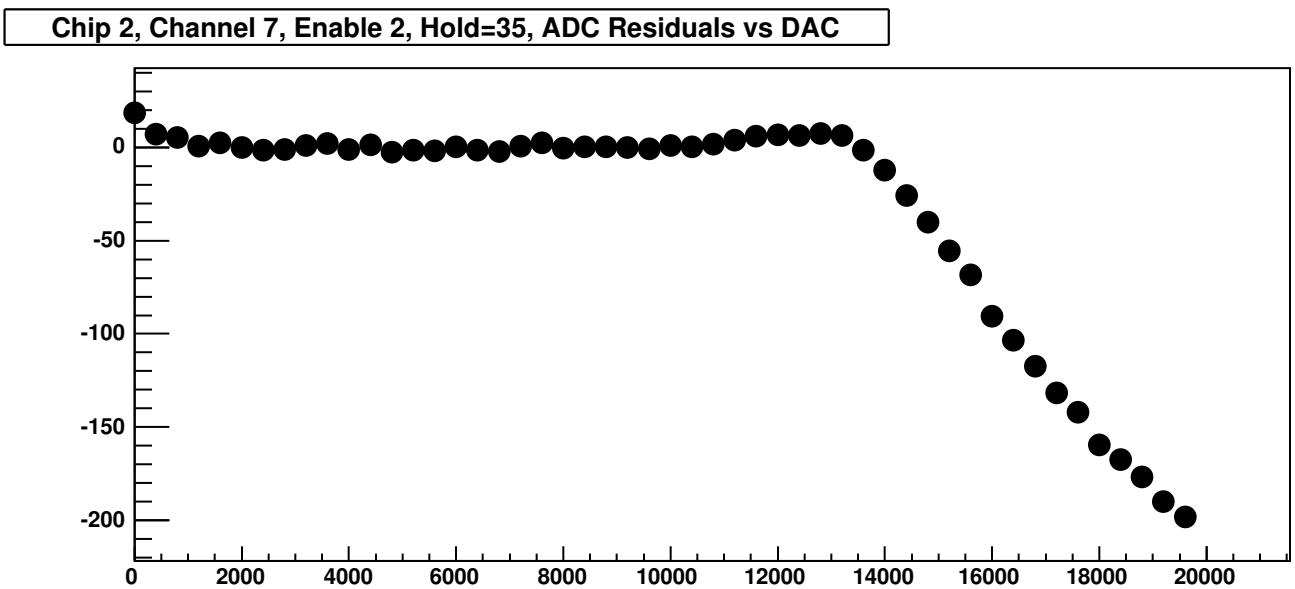


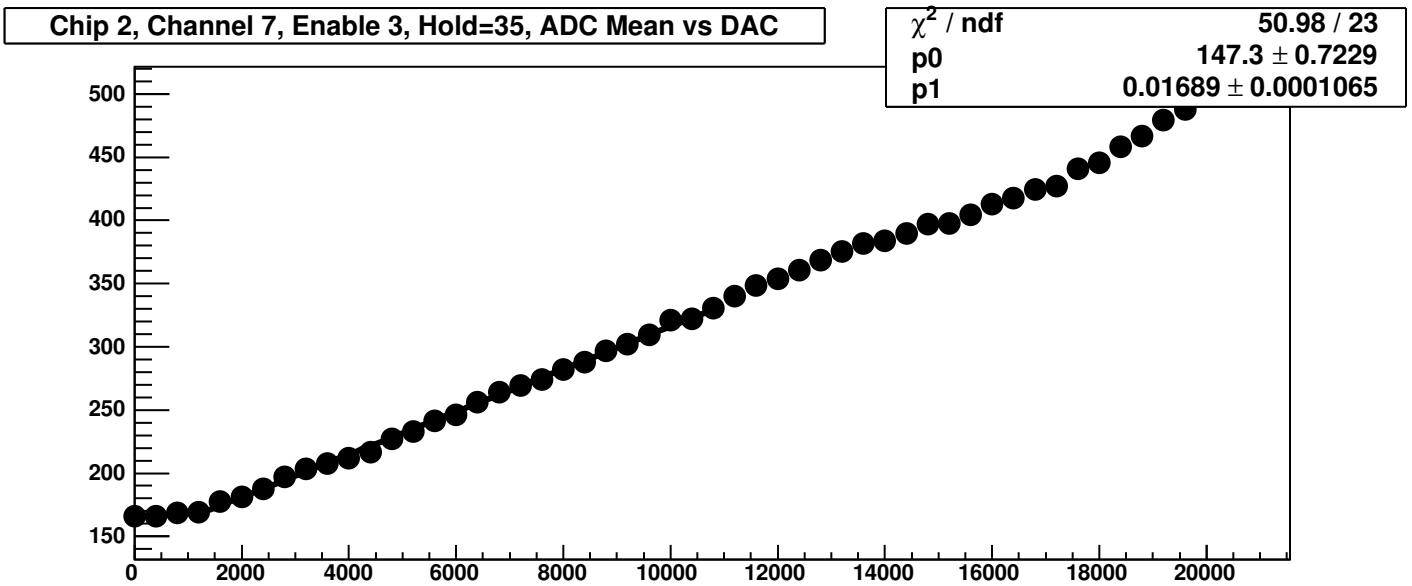
Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

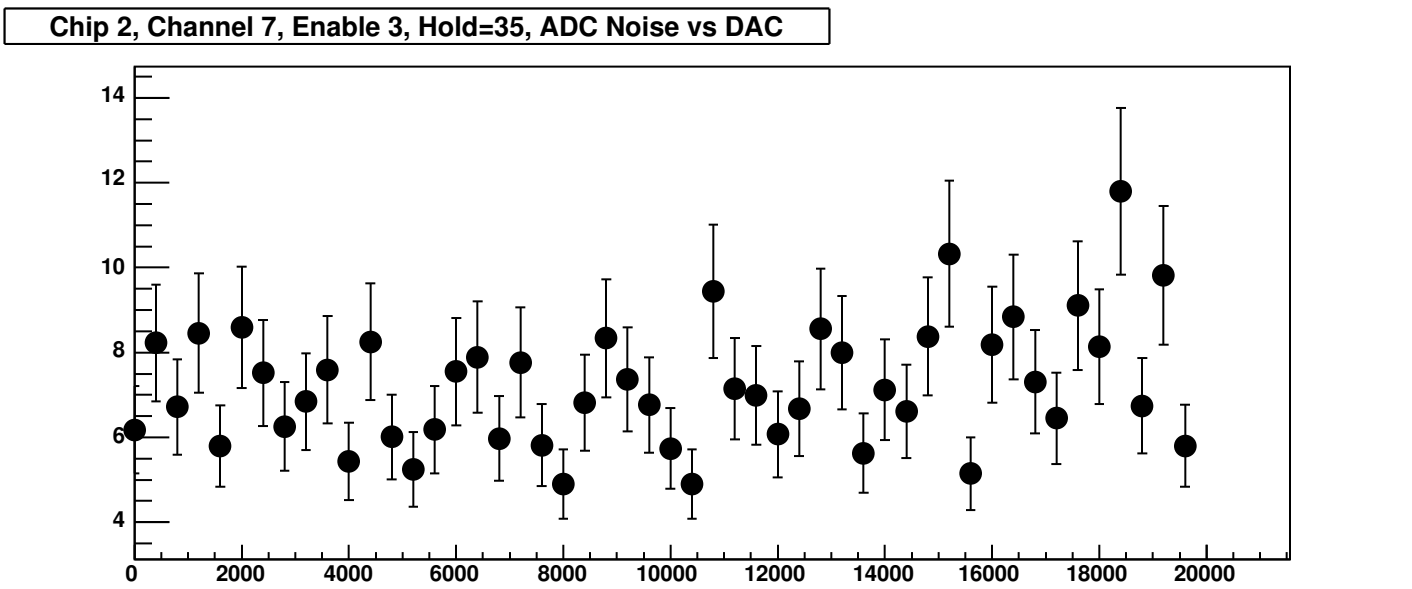


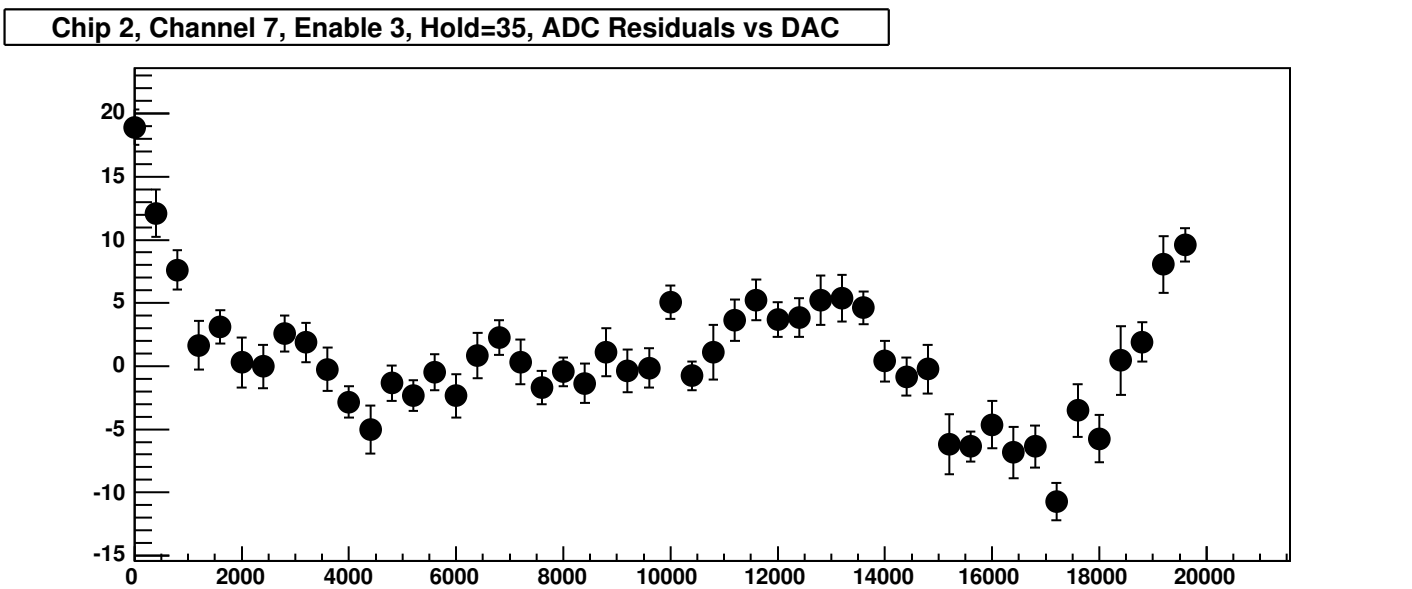

Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 17.06 / 23
p0 147.6 ± 0.7101
p1 0.01793 ± 0.0001123
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

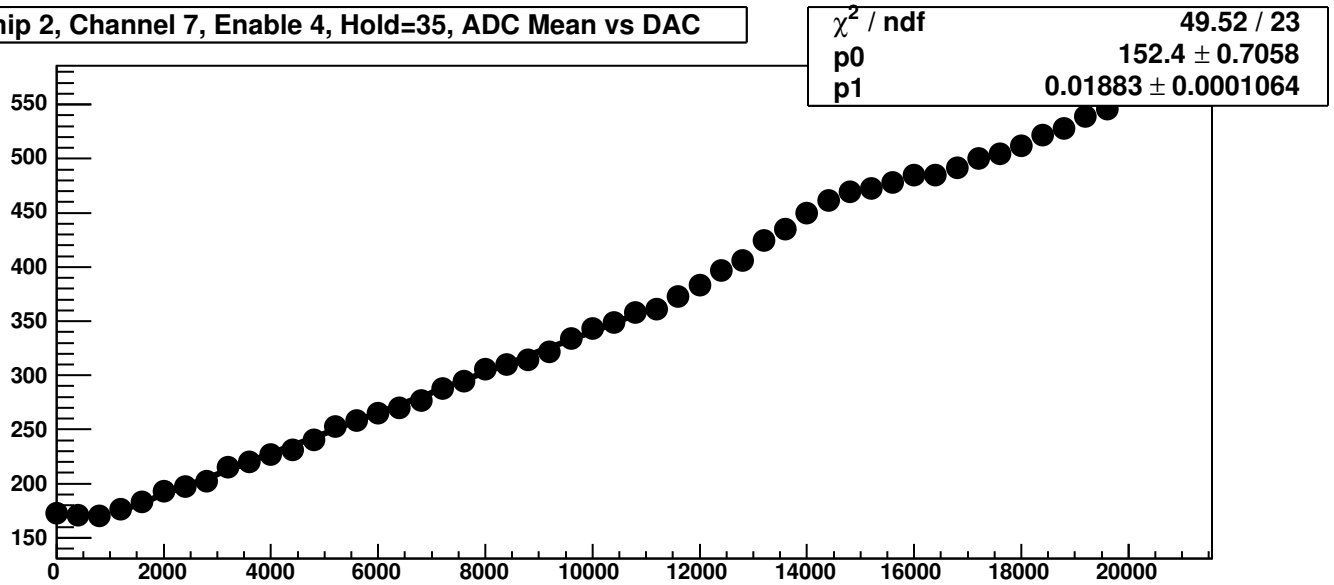

Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.98 / 23
p0 147.3 ± 0.7229
p1 0.01689 ± 0.0001065
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

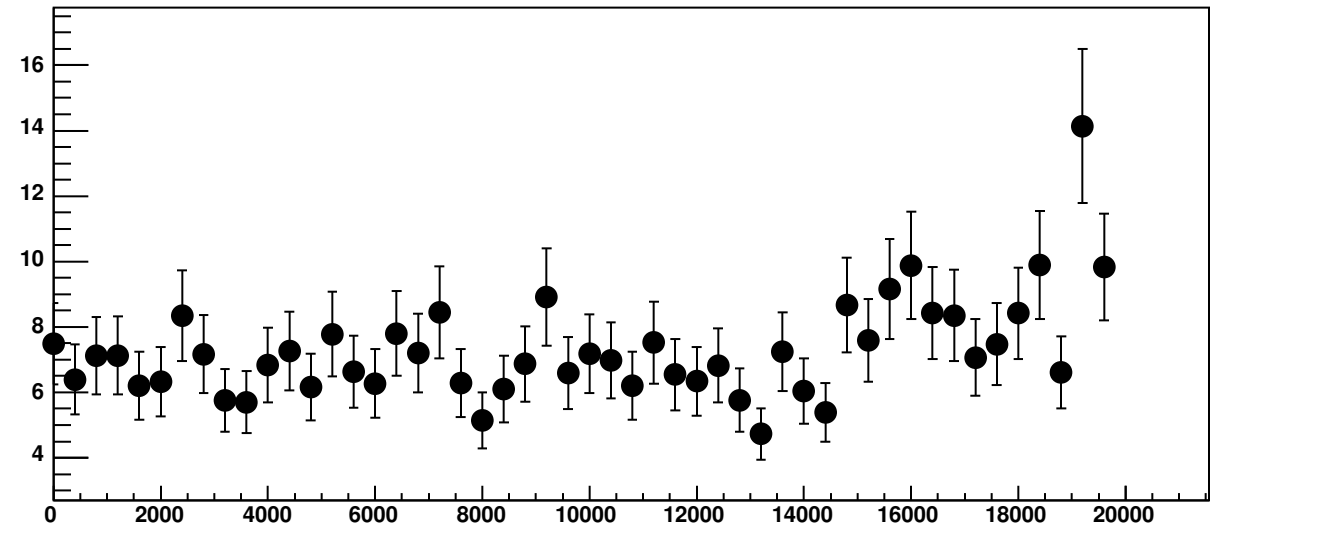

Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

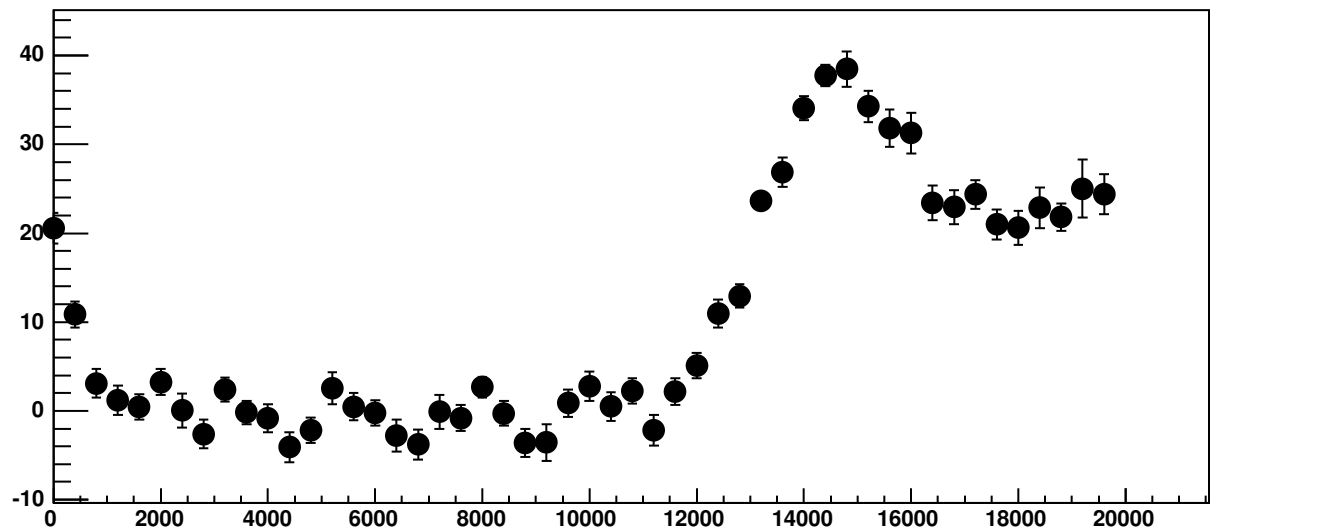
Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

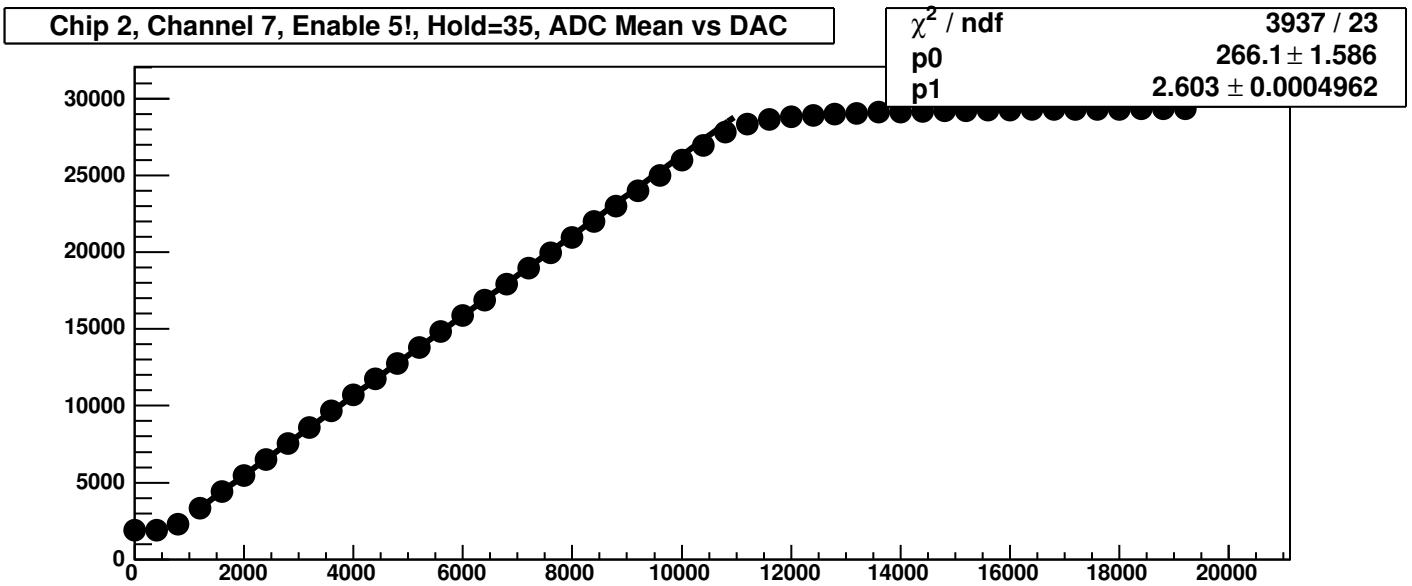


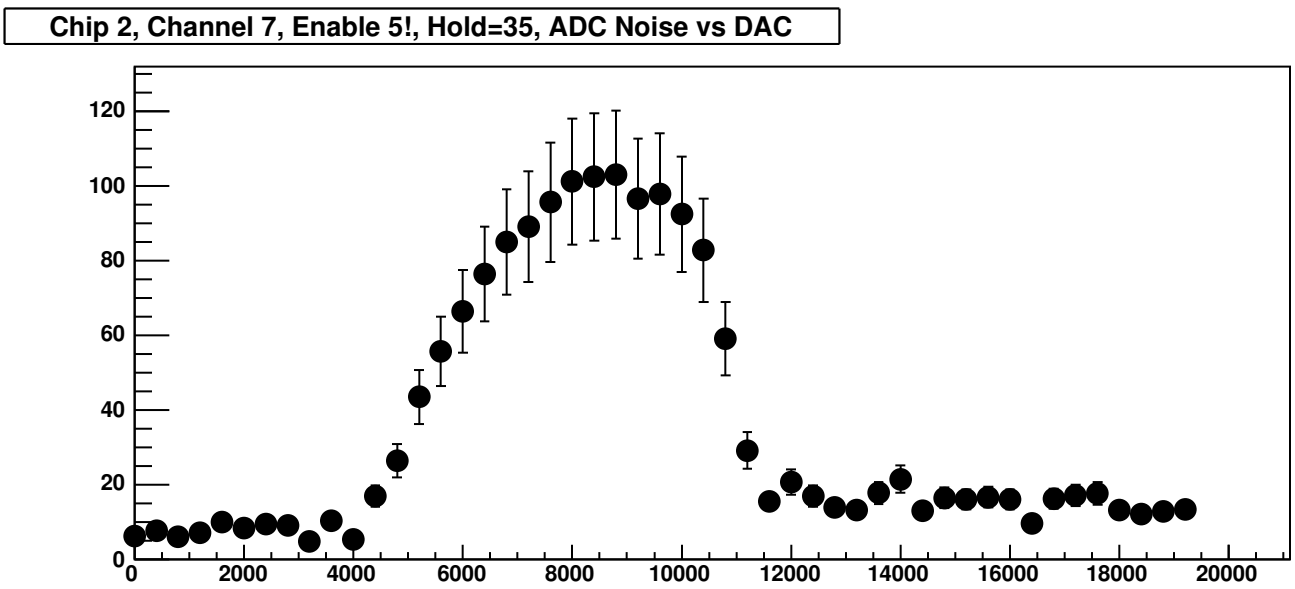
Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

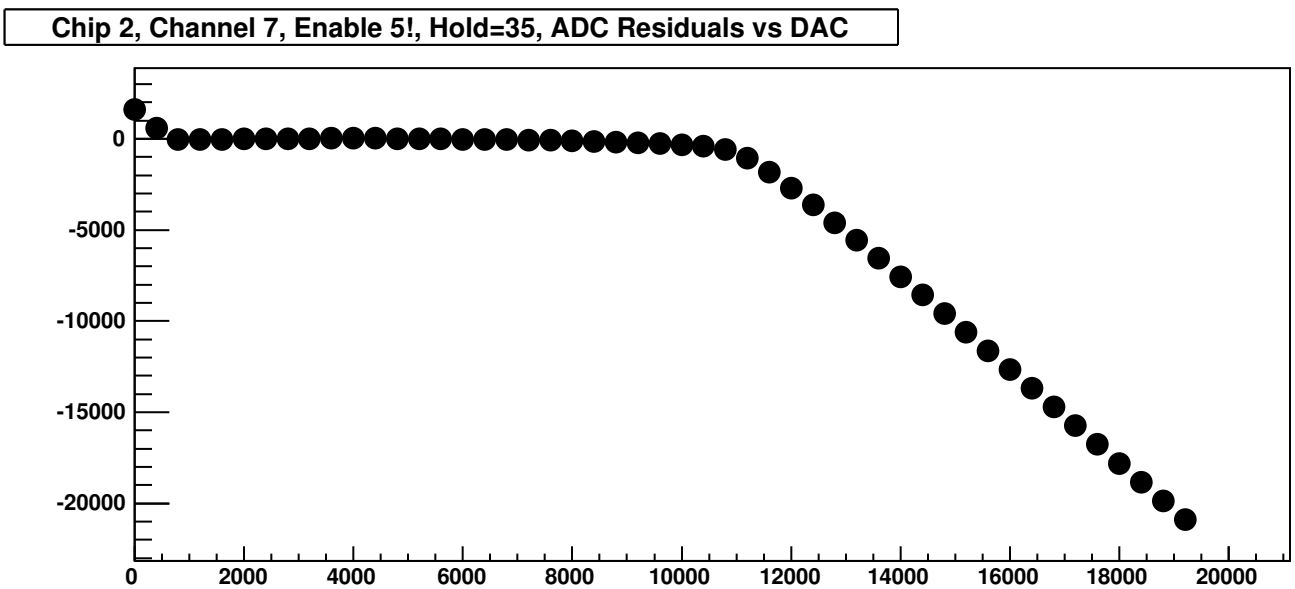


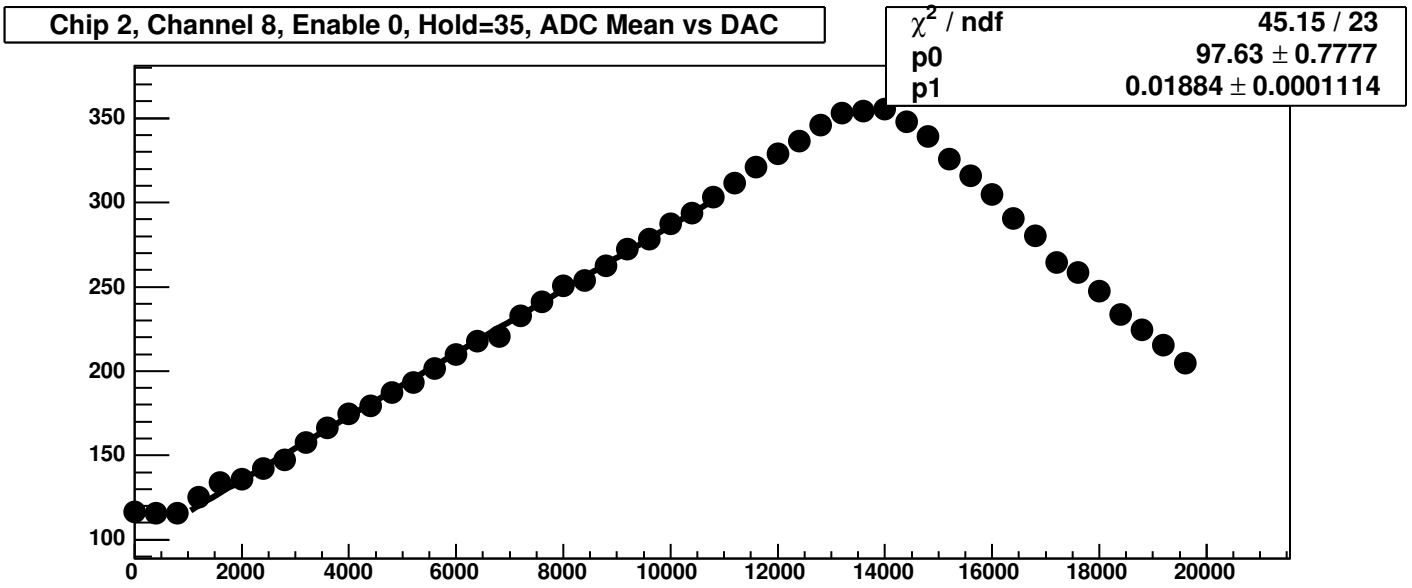
Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

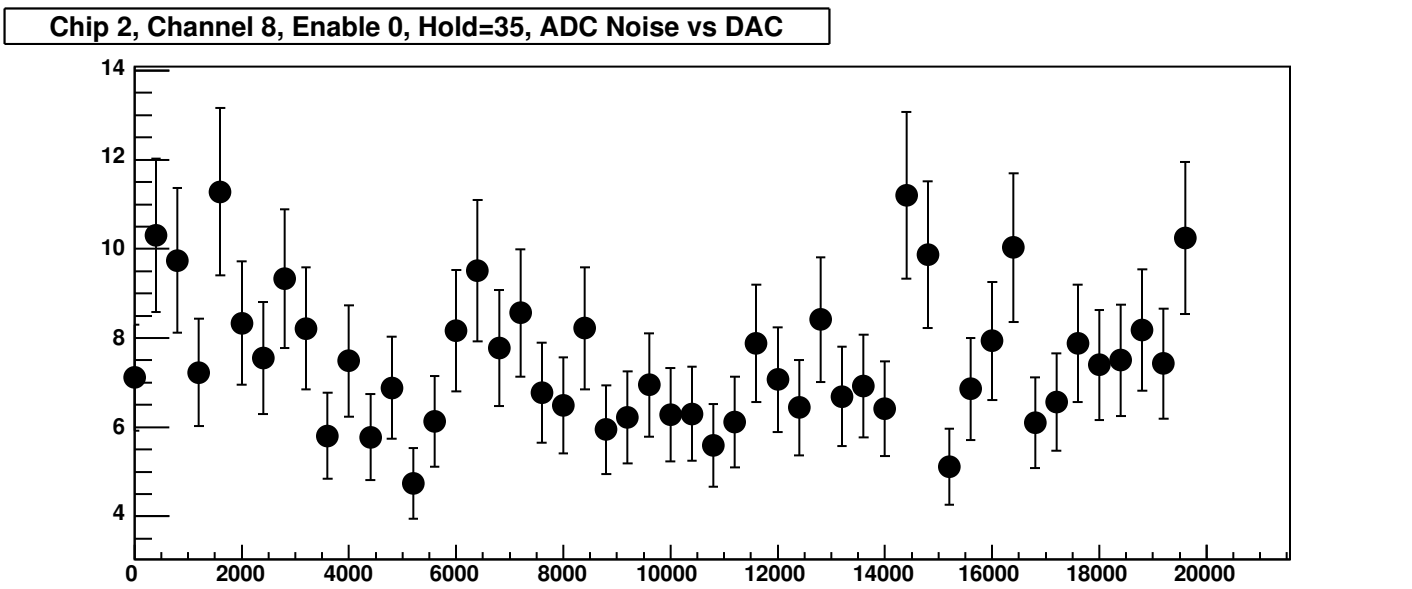


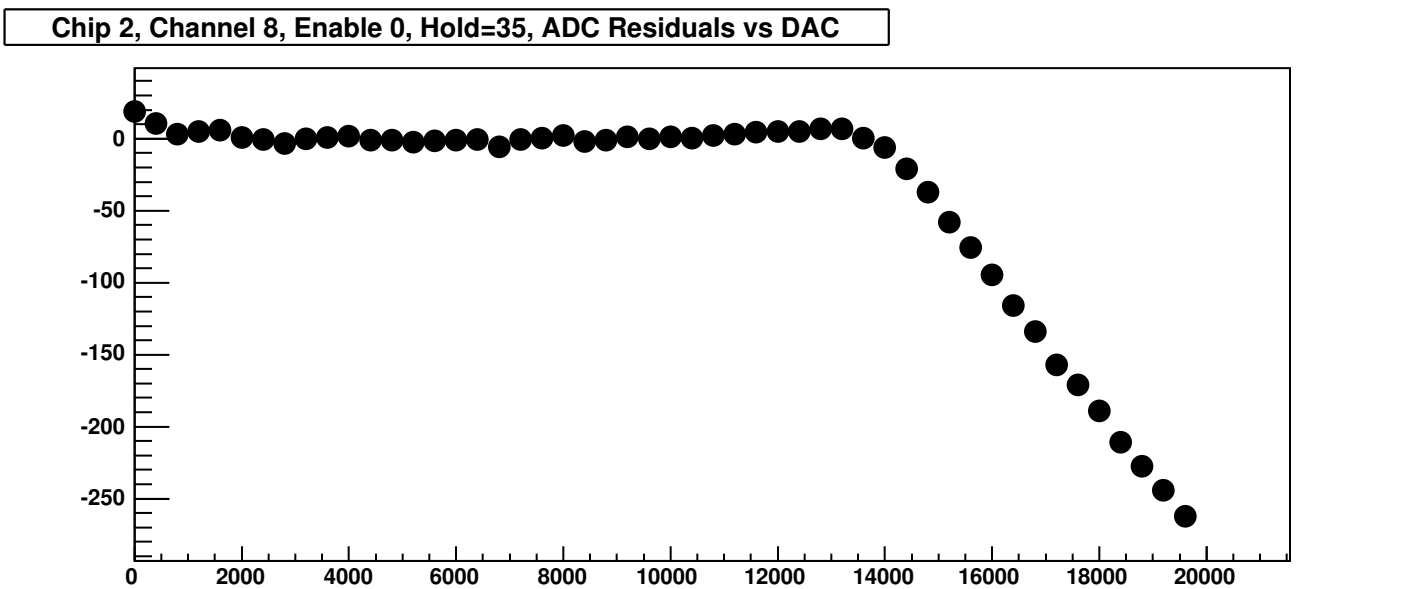

Chip 2, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3937 / 23
p0 266.1 ± 1.586
p1 2.603 ± 0.0004962
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

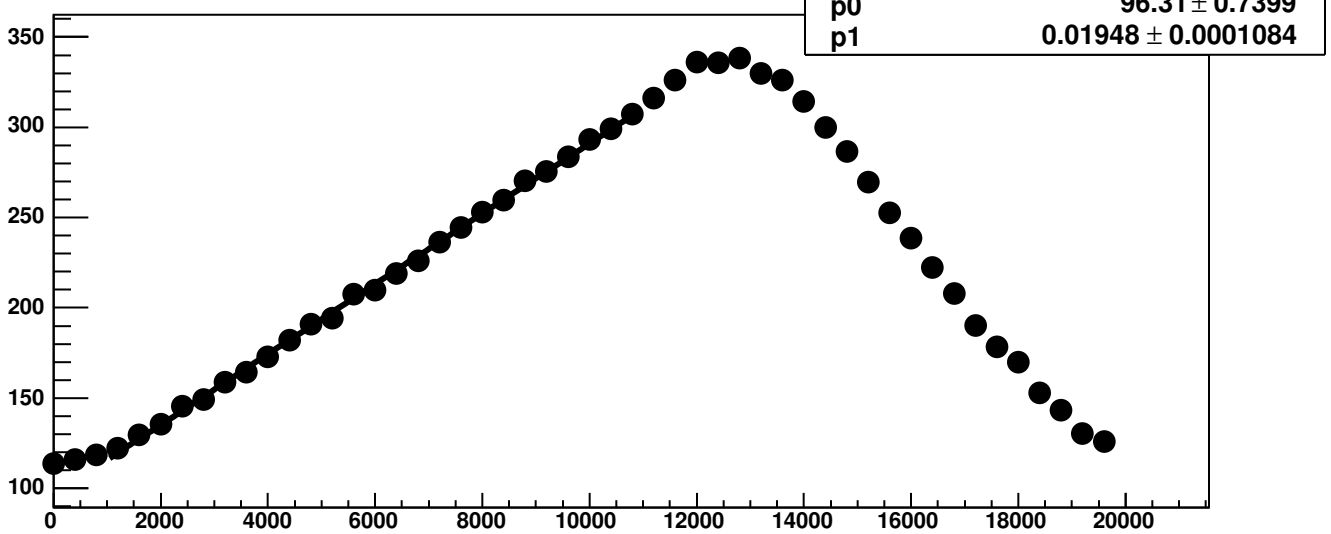

Chip 2, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 45.15 / 23
p0 97.63 ± 0.7777
p1 0.01884 ± 0.0001114
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

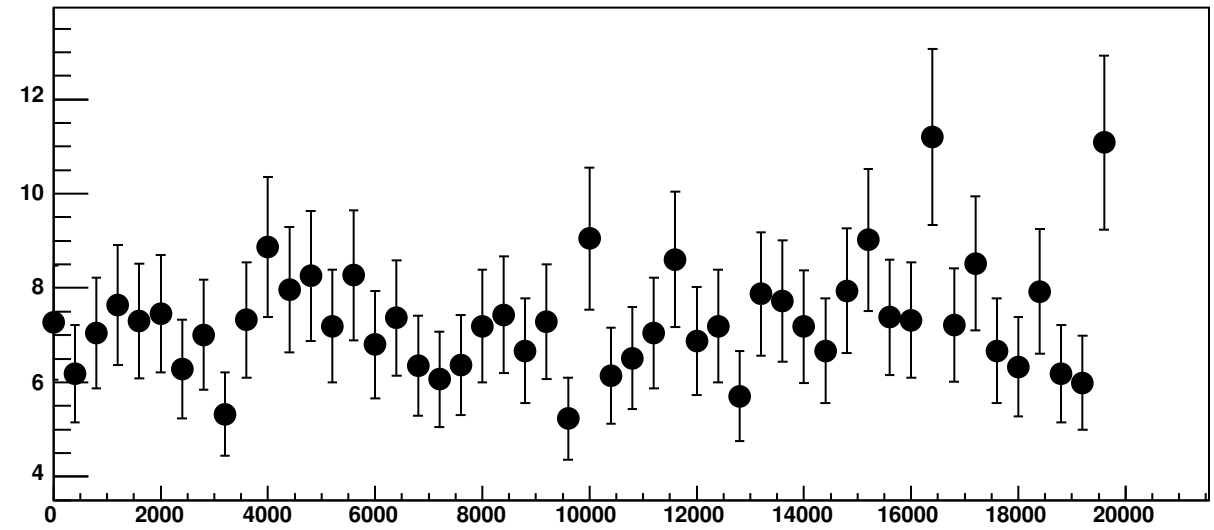

Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

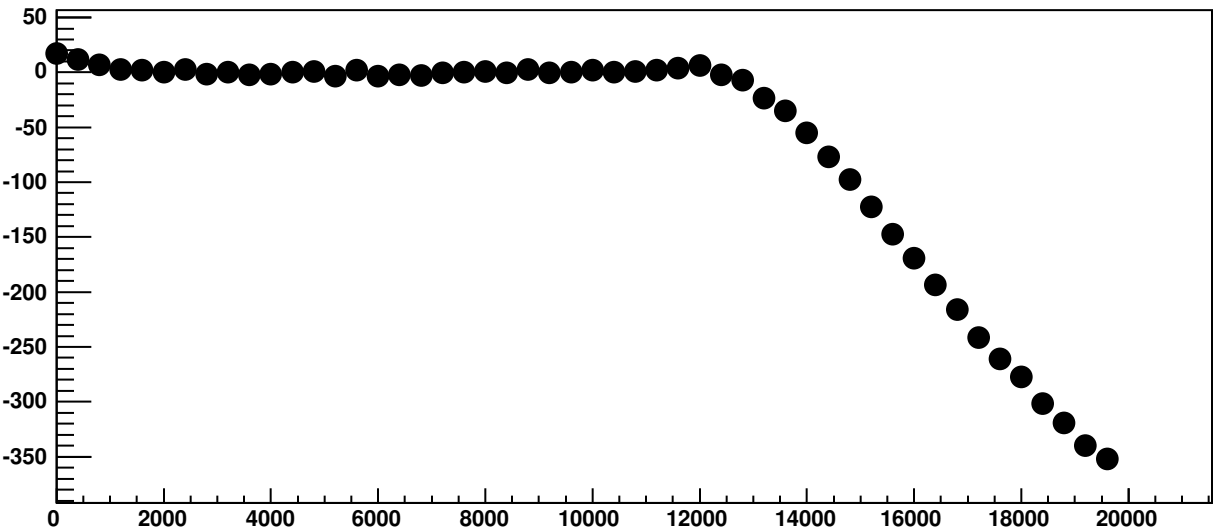
Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

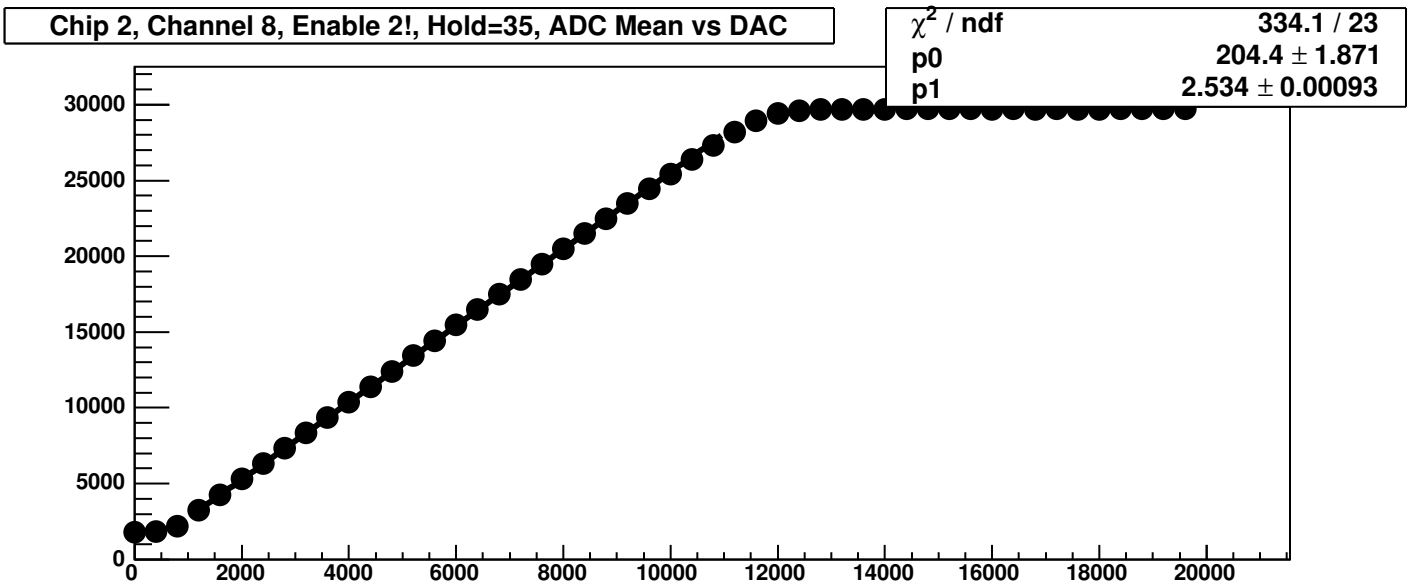


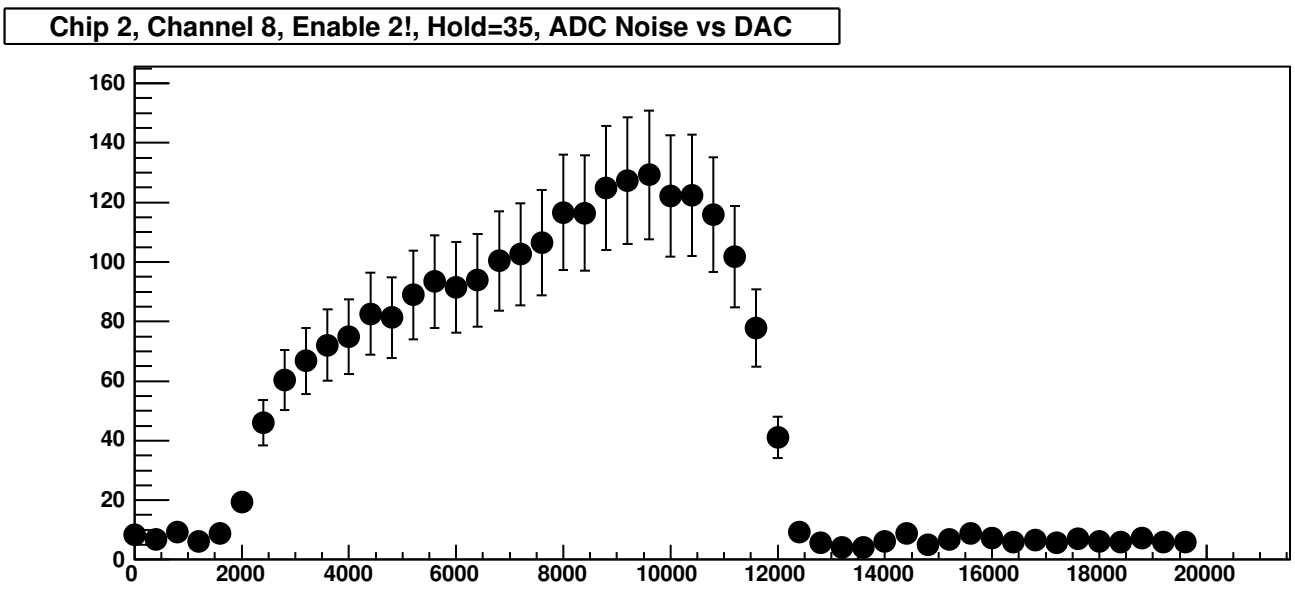
Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

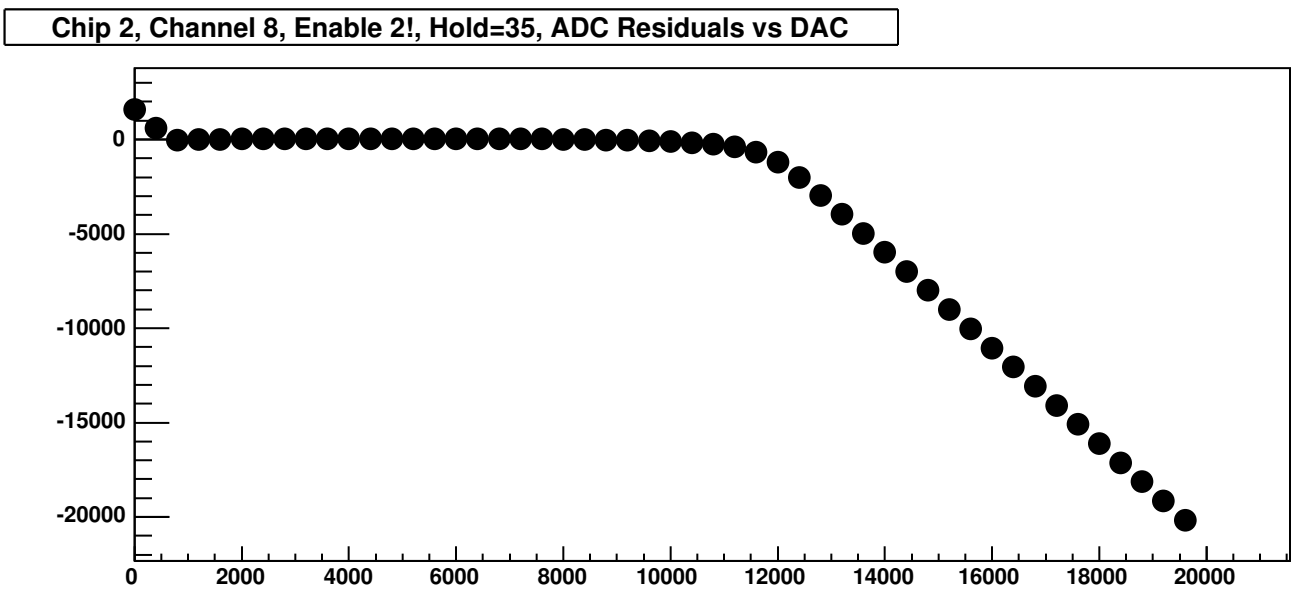


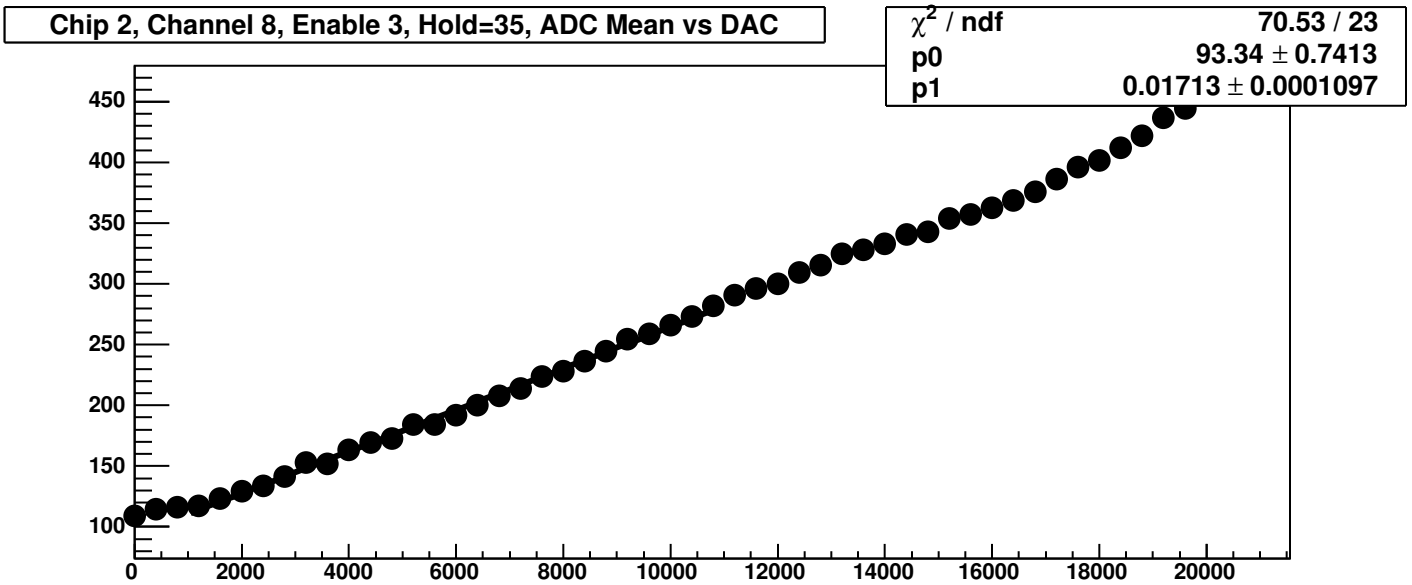
Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

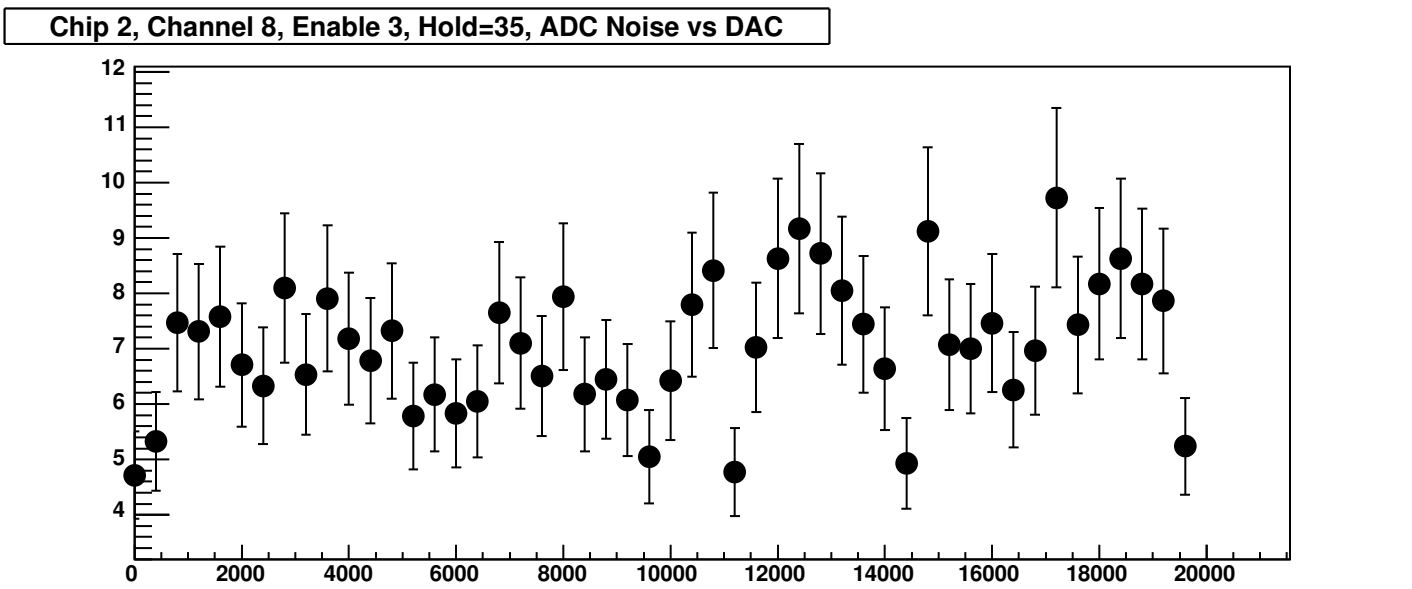


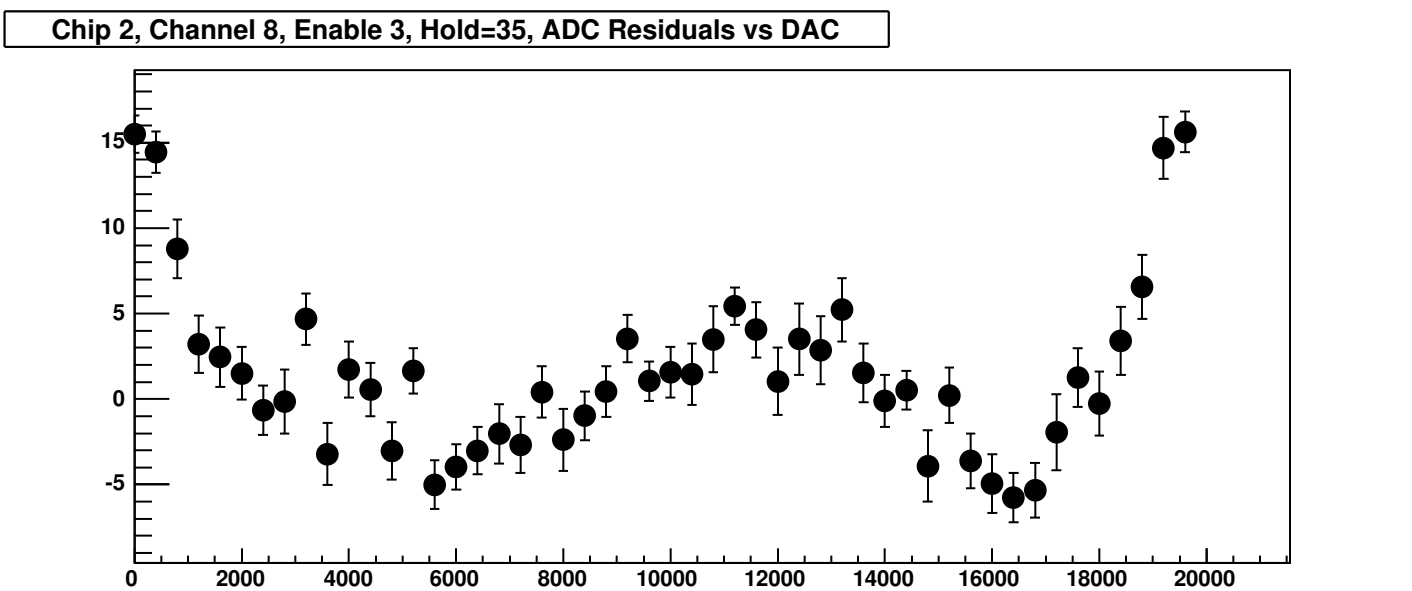

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 334.1 / 23
p0 204.4 ± 1.871
p1 2.534 ± 0.00093
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

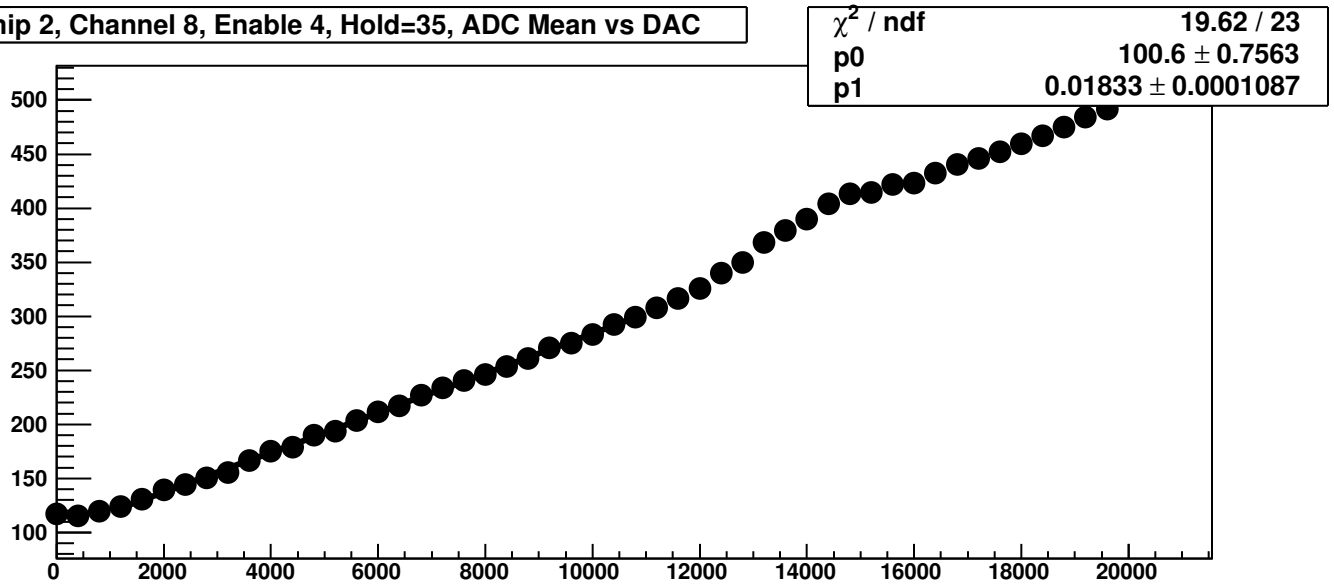

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ^2 / ndf 70.53 / 23
p0 93.34 ± 0.7413
p1 0.01713 ± 0.0001097
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

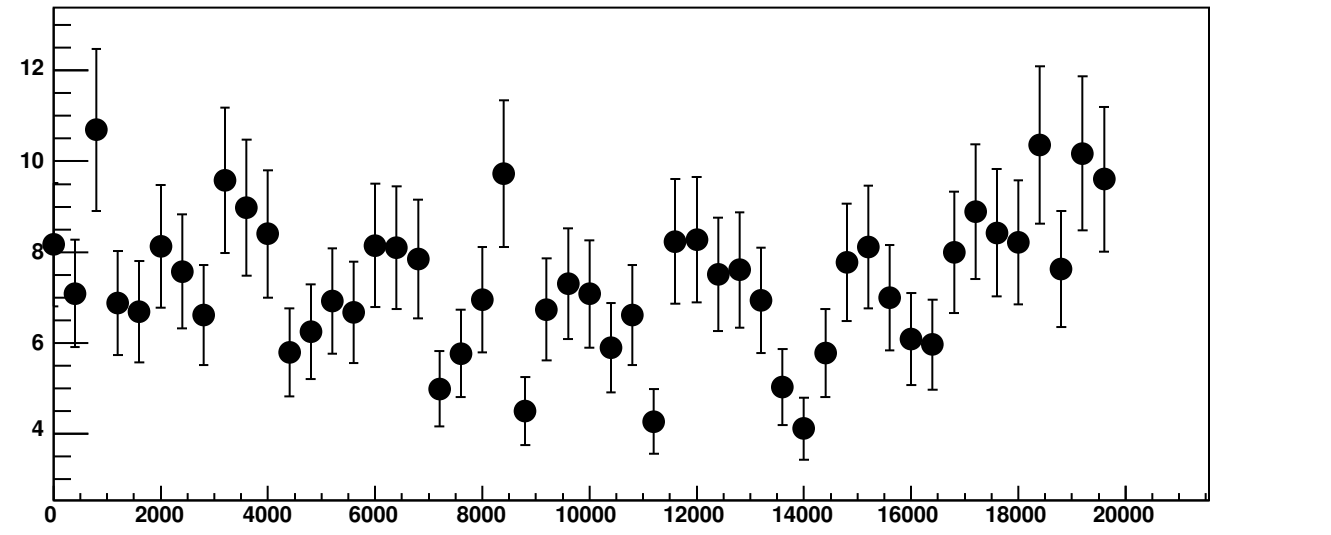

Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

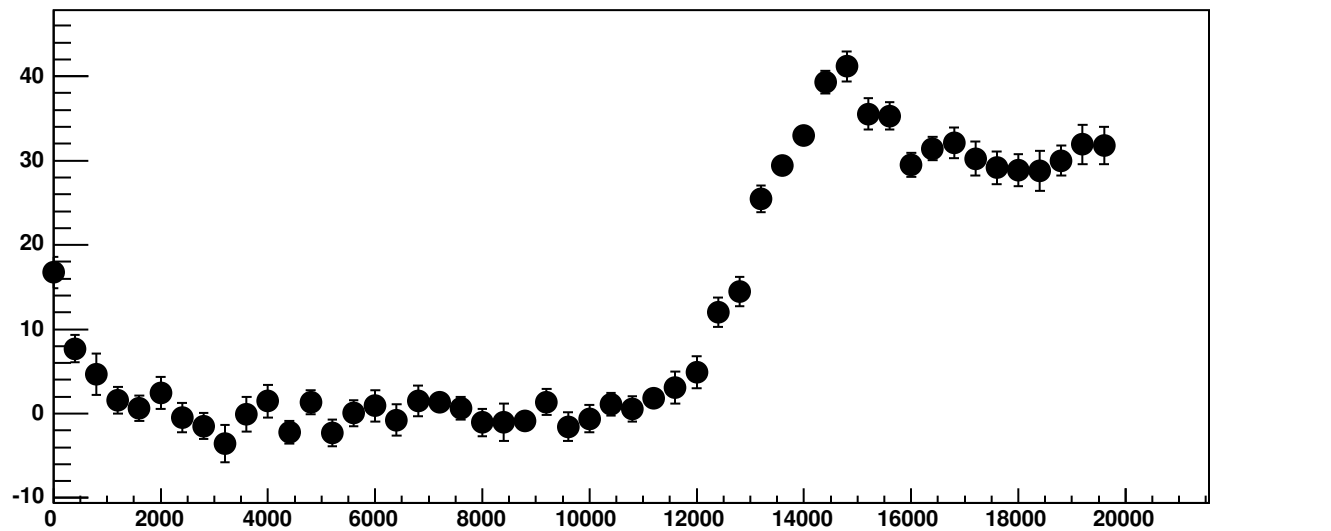
Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

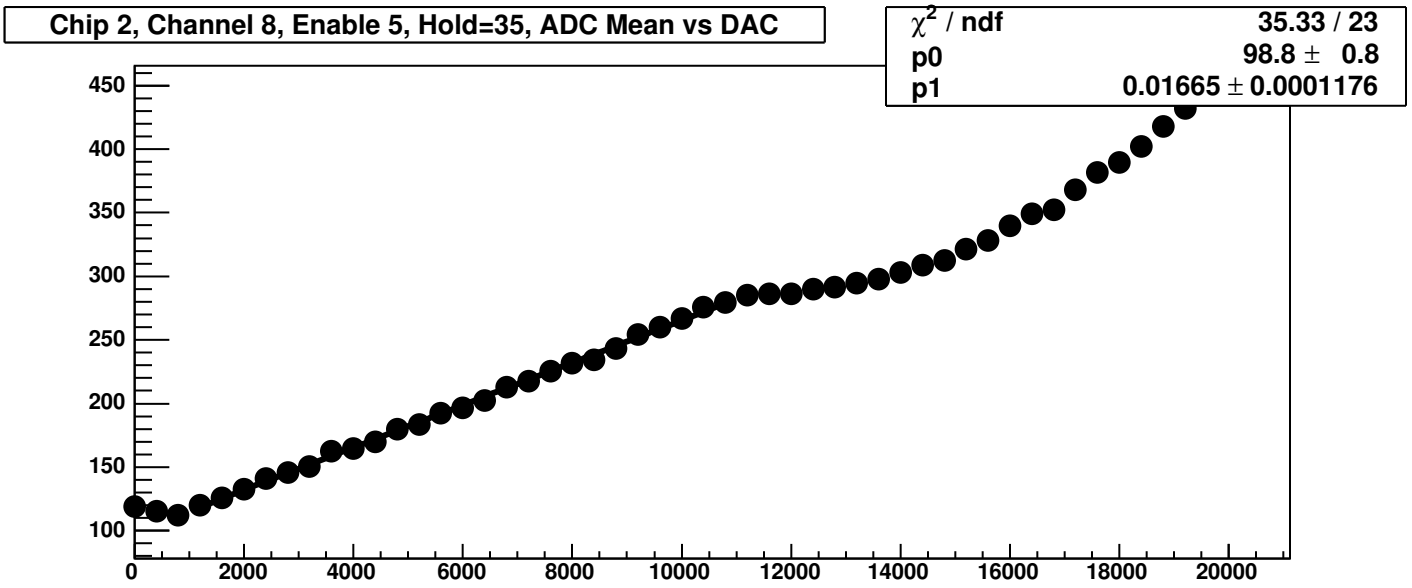


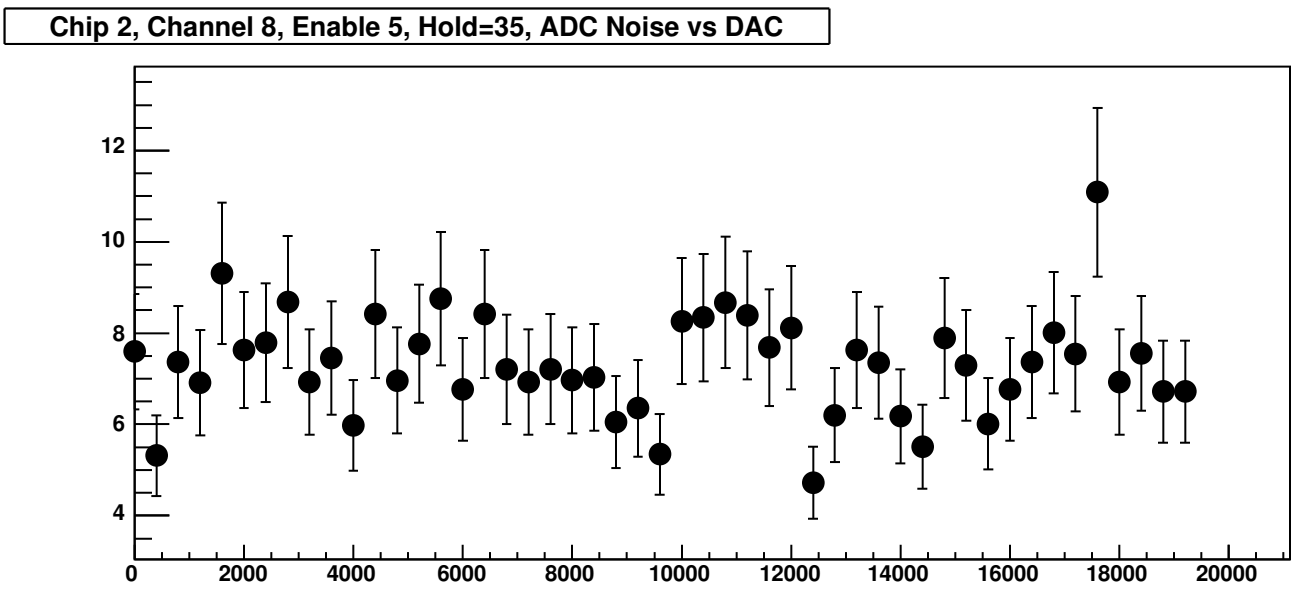
Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

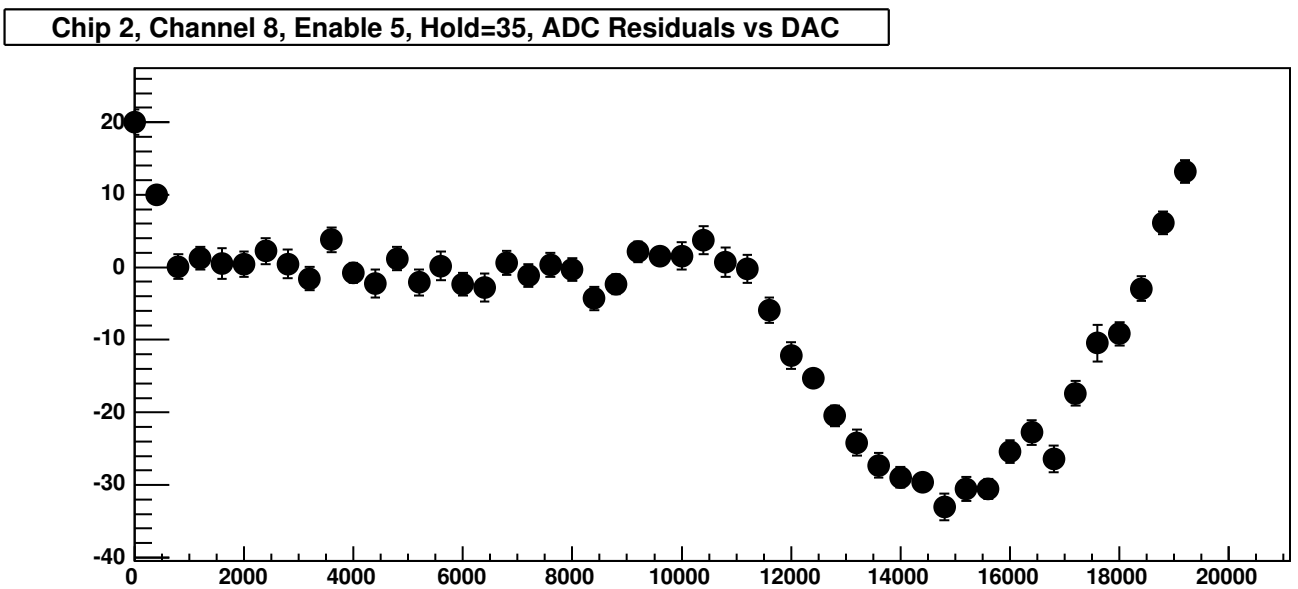


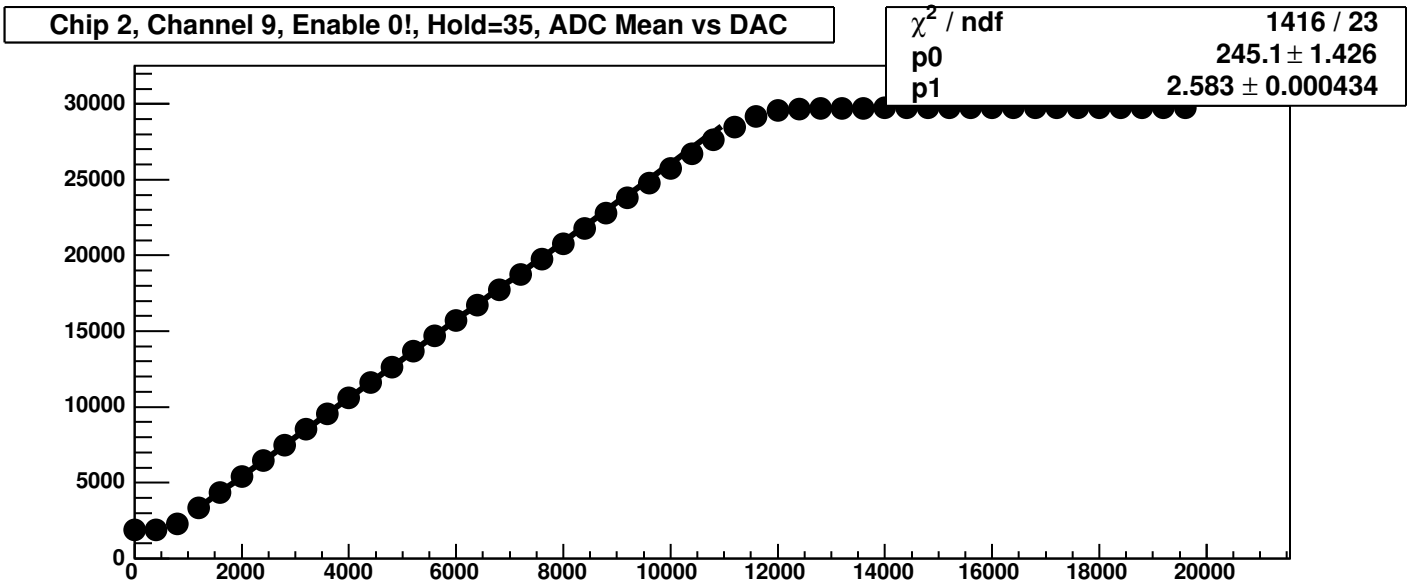
Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

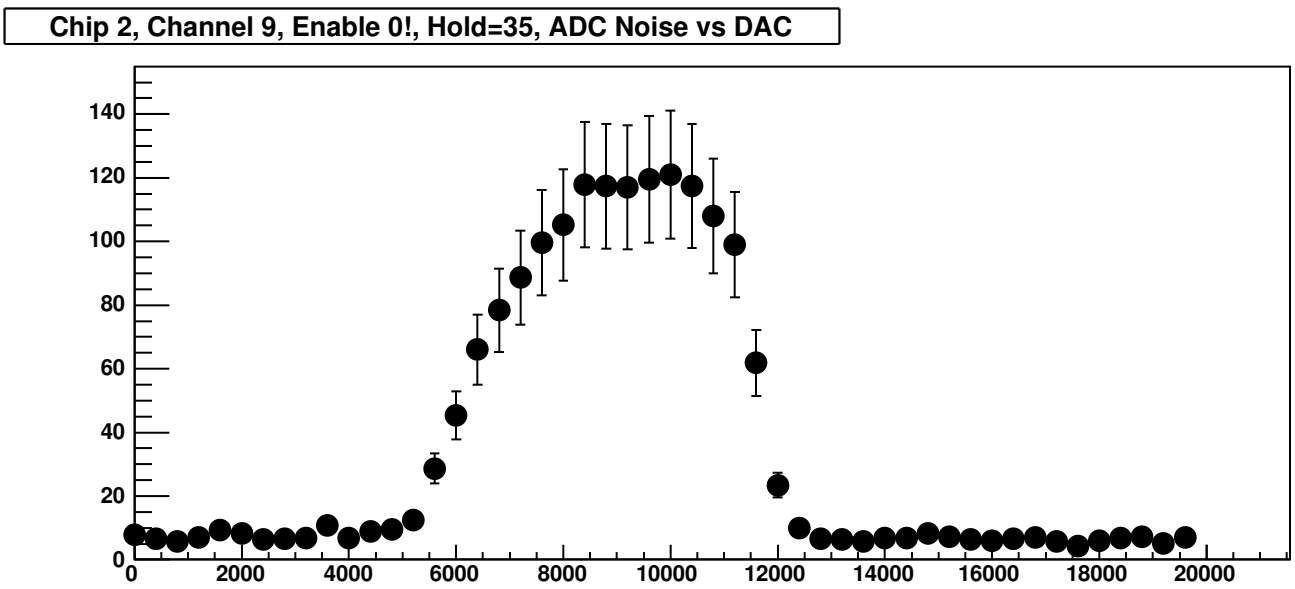


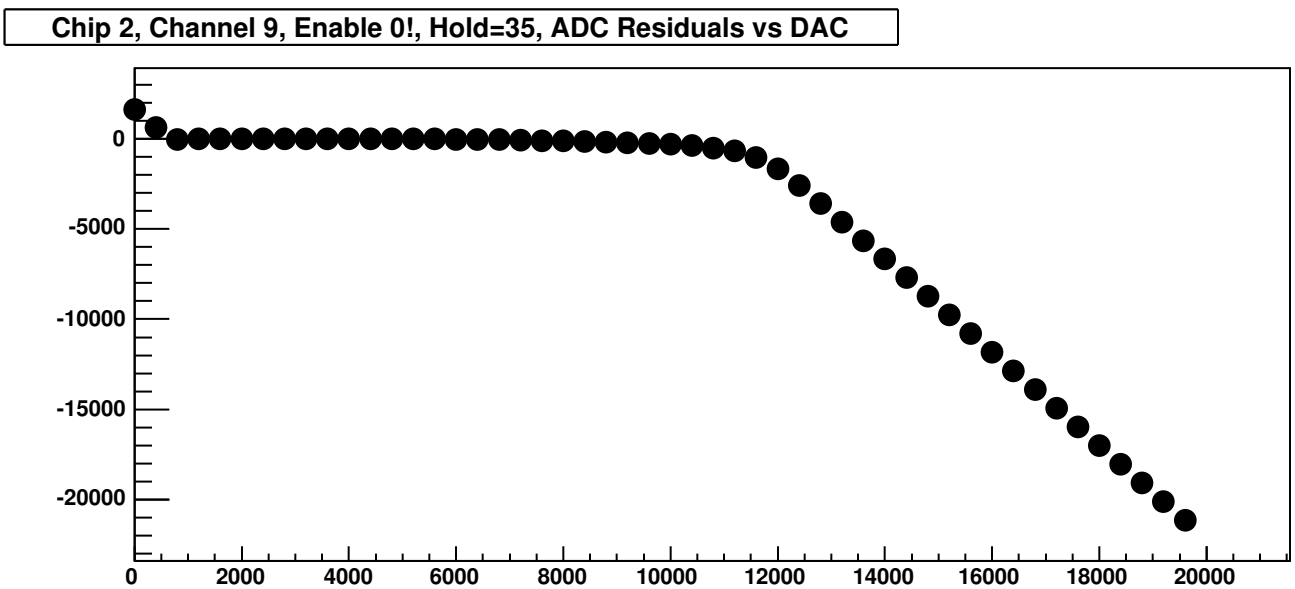

Chip 2, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 35.33 / 23
p0 98.8 ± 0.8
p1 0.01665 ± 0.0001176
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

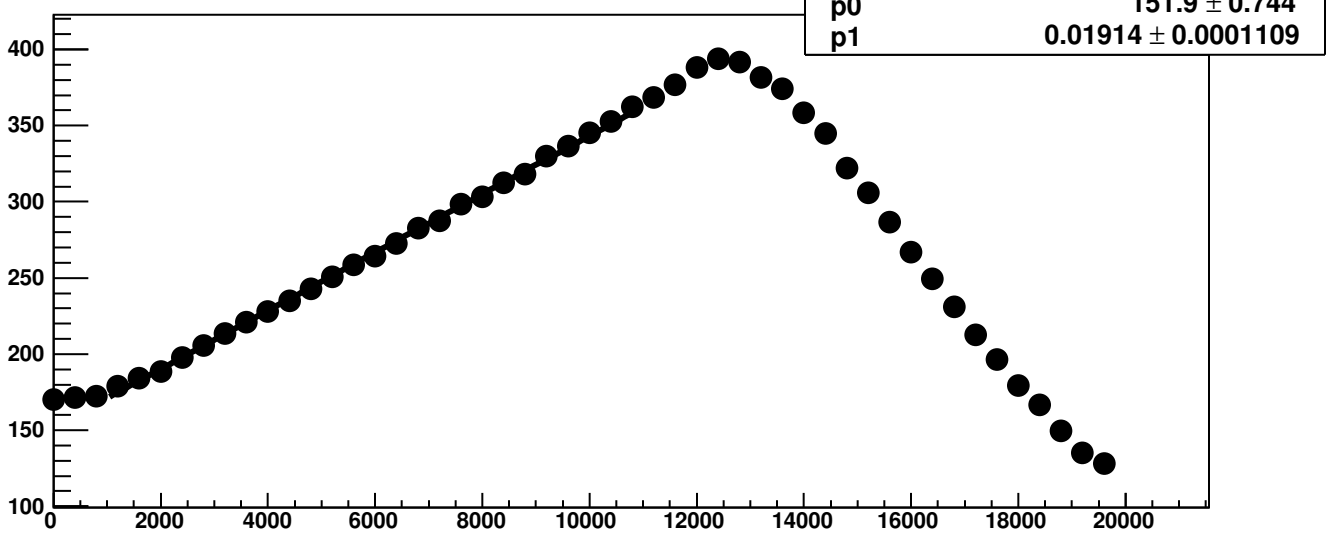

Chip 2, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1416 / 23
p0 245.1 ± 1.426
p1 2.583 ± 0.000434
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

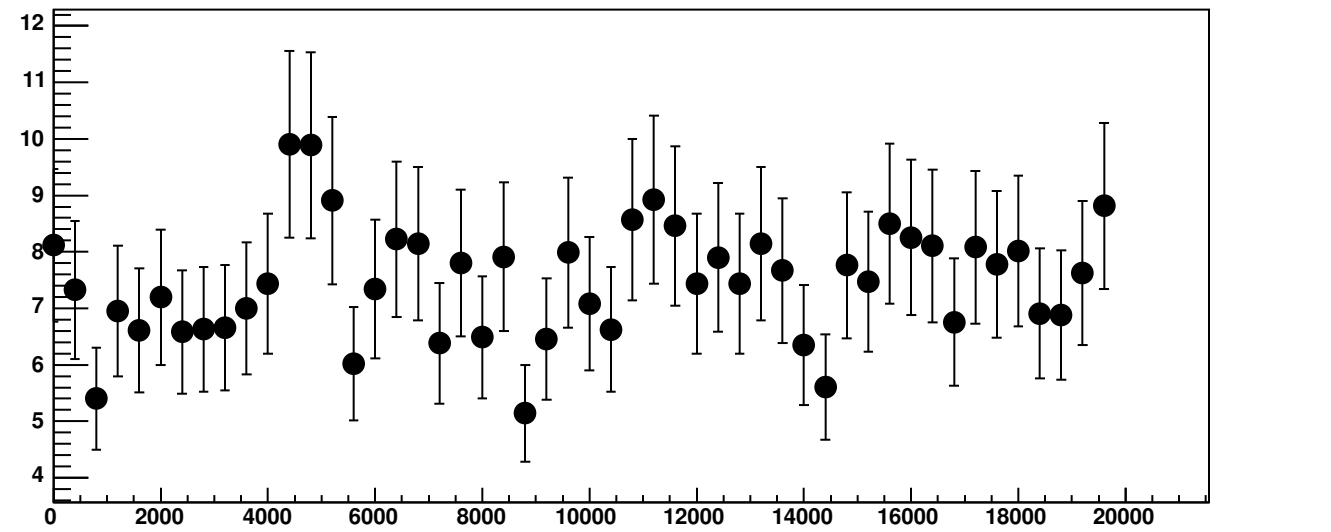

Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

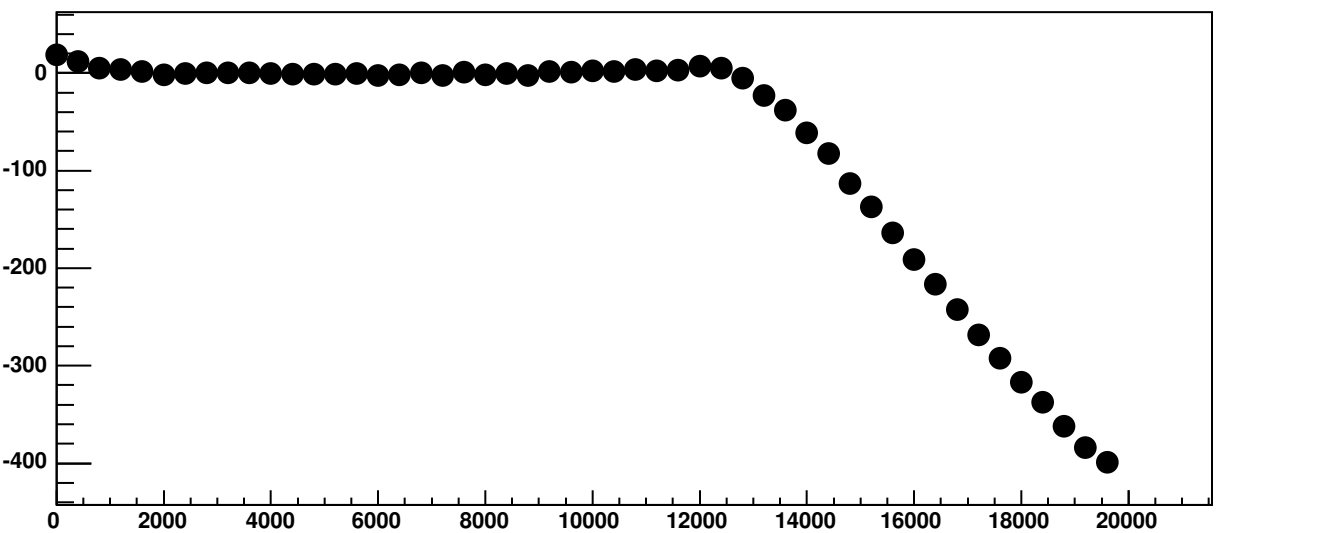
Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



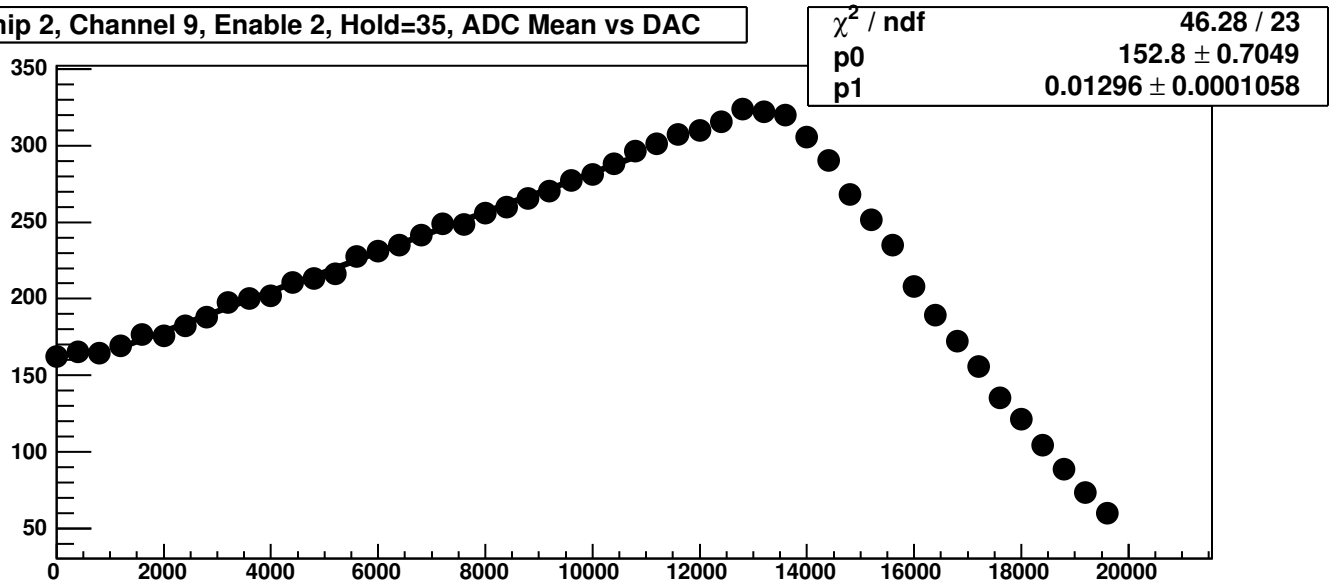
Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



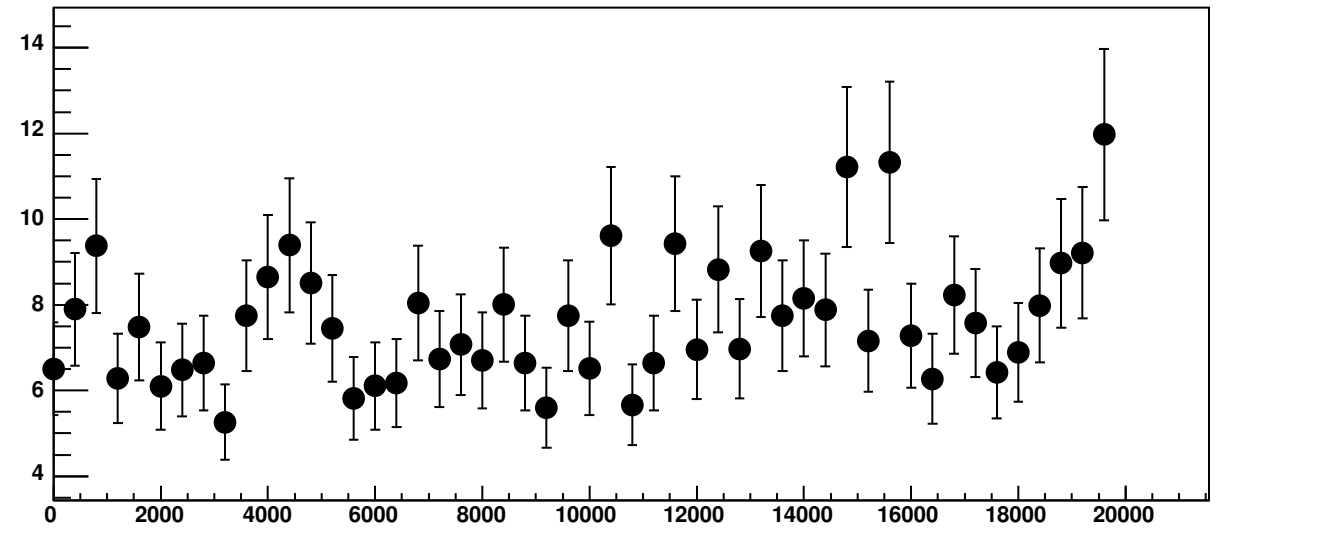
Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



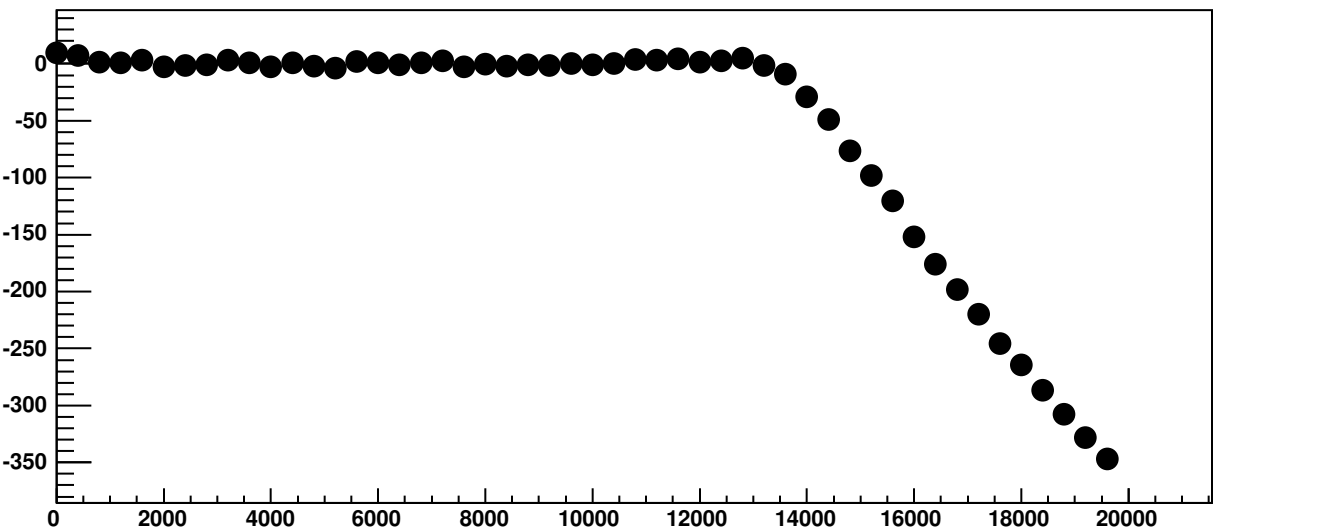
Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

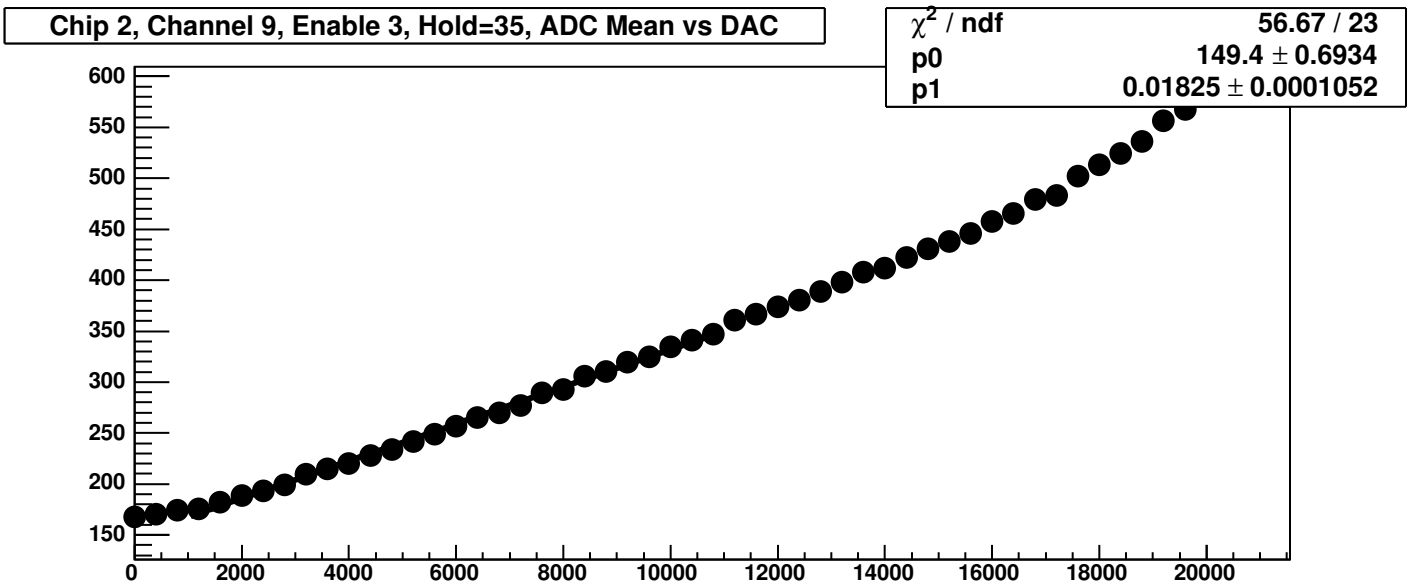


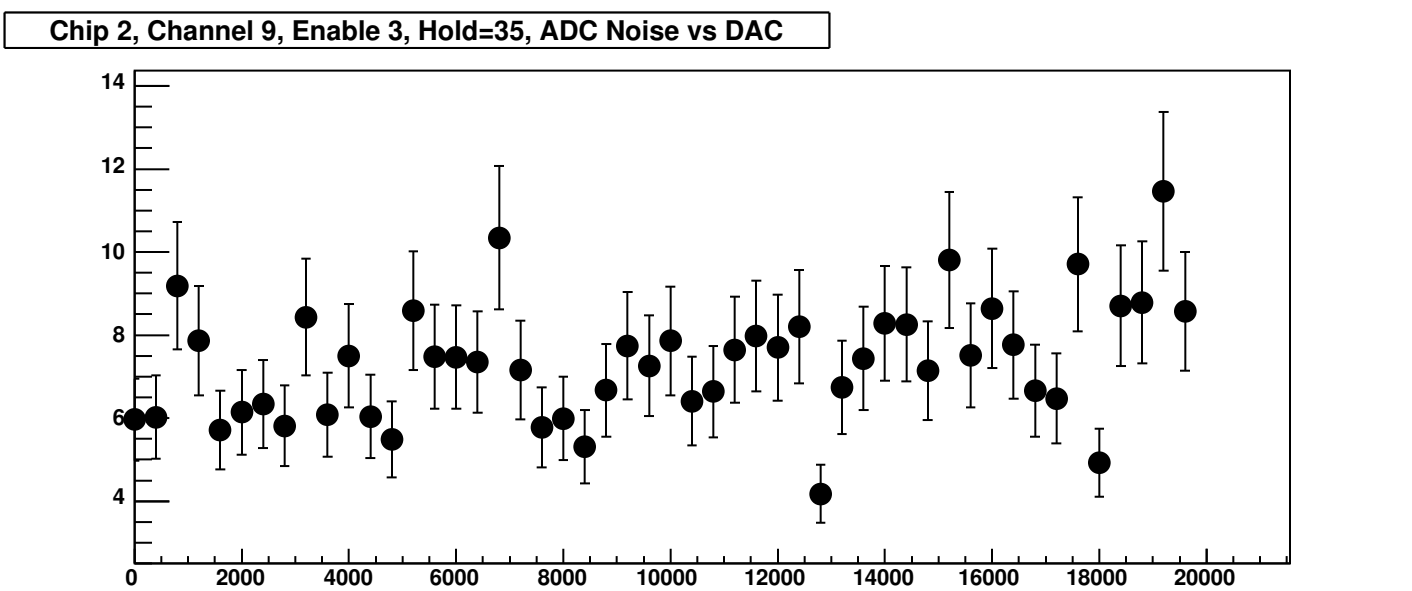
Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

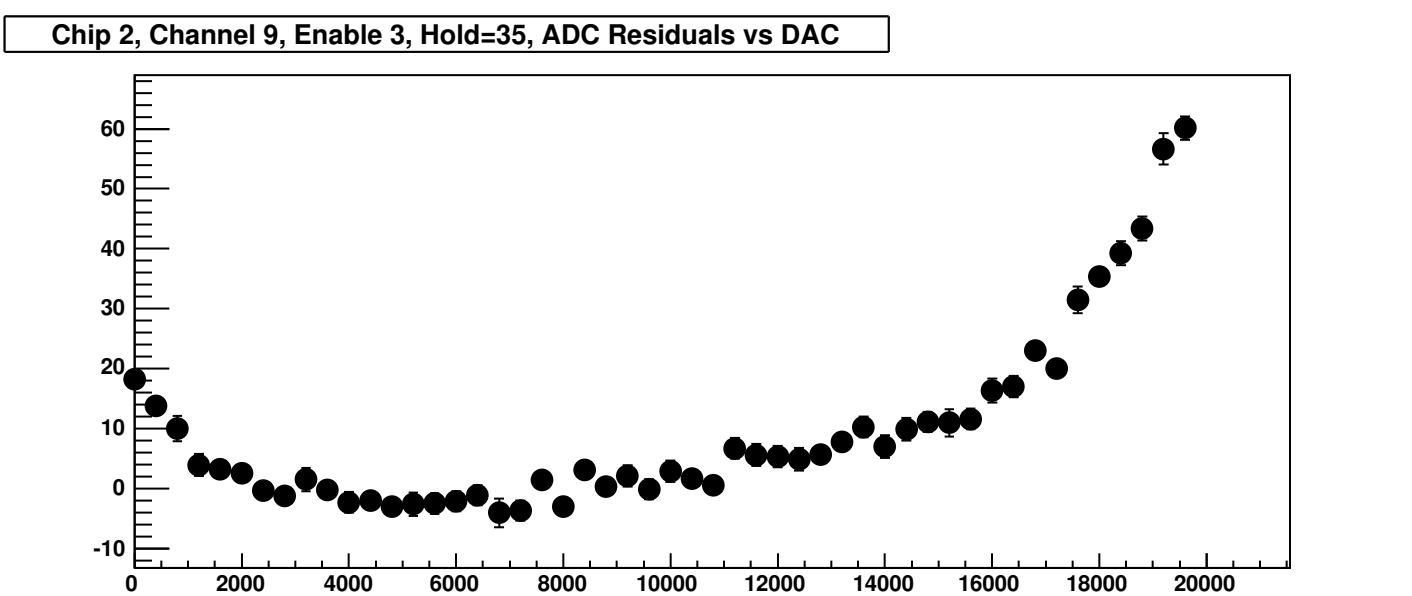


Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

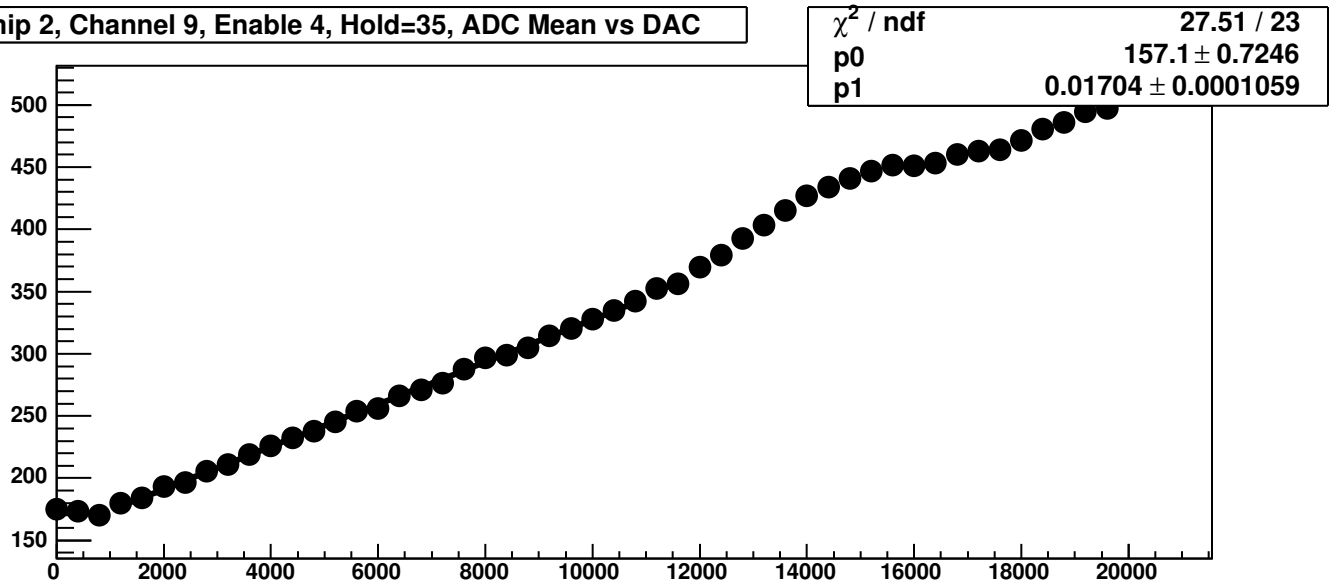



Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 56.67 / 23
p0 149.4 ± 0.6934
p1 0.01825 ± 0.0001052
600
550
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

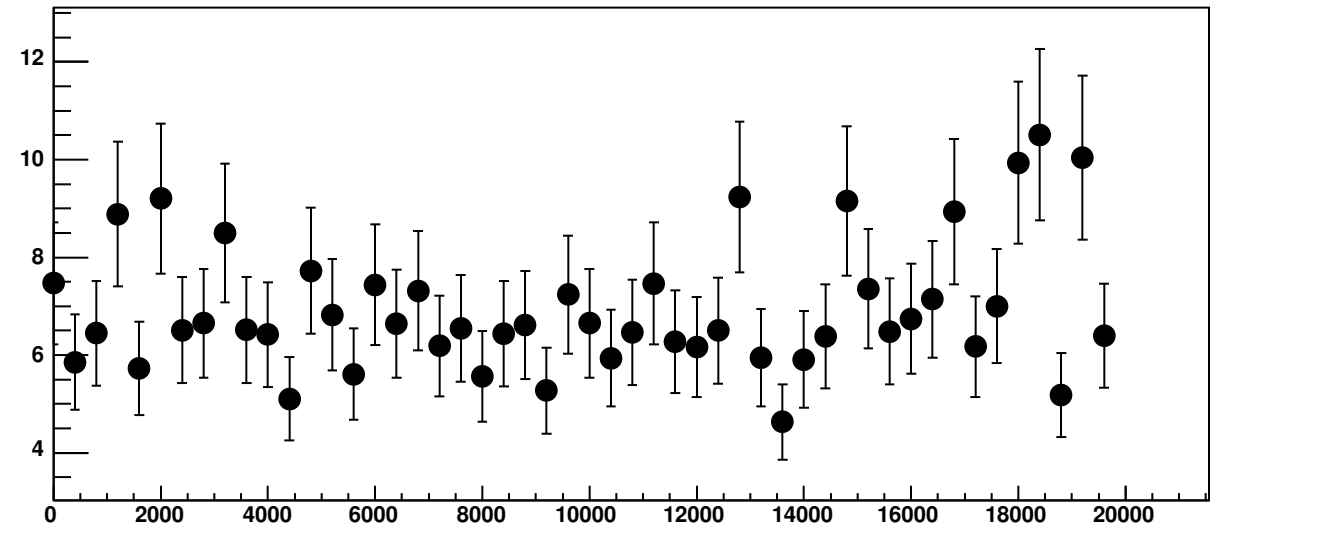

Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

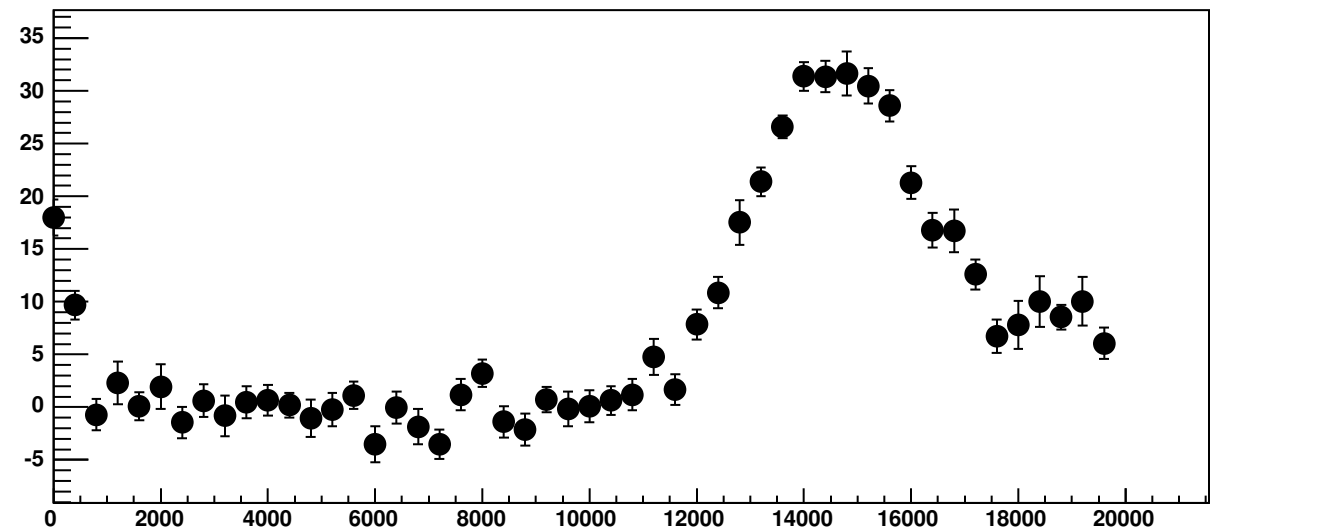
Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

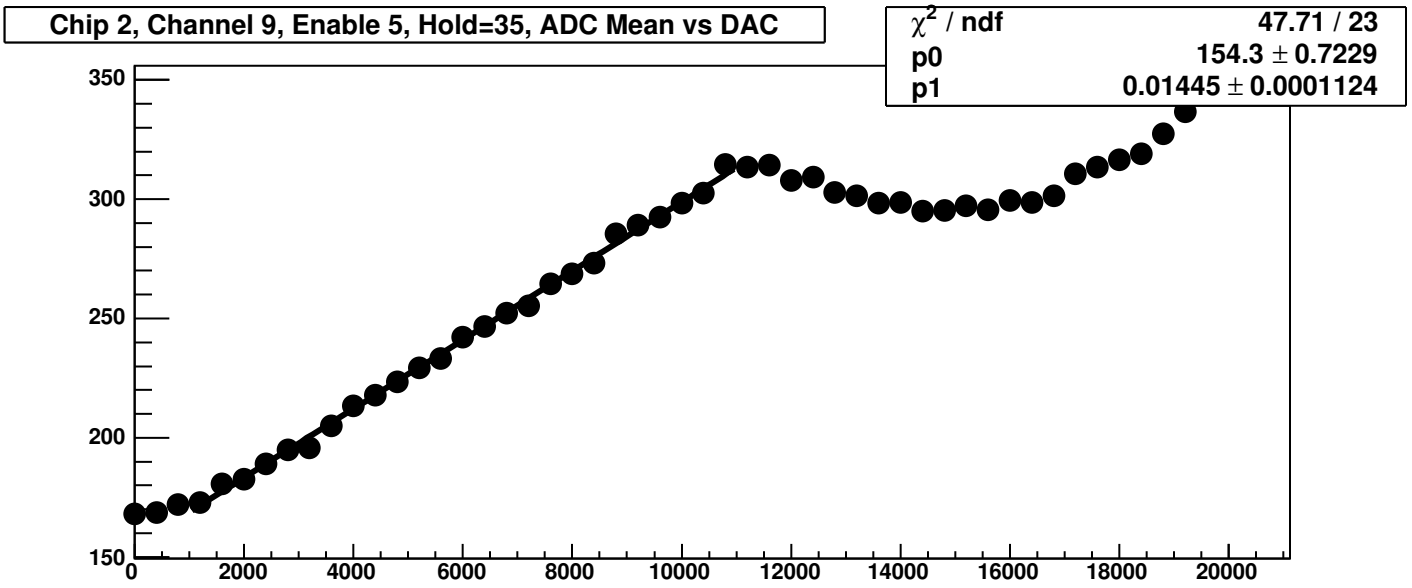


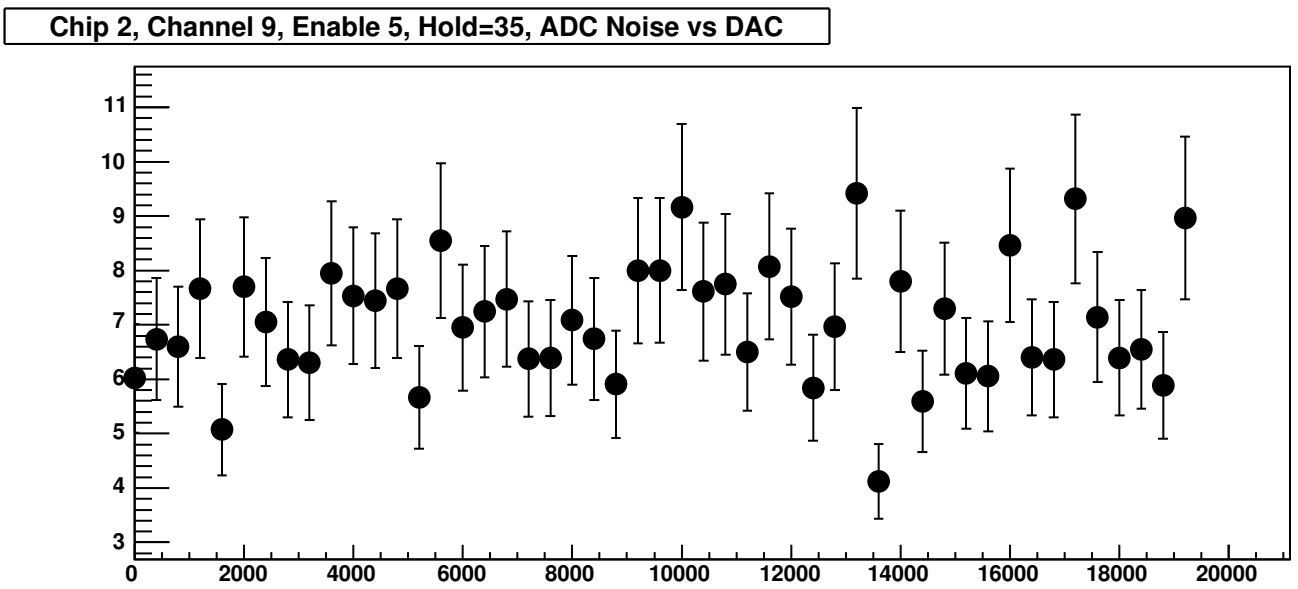
Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

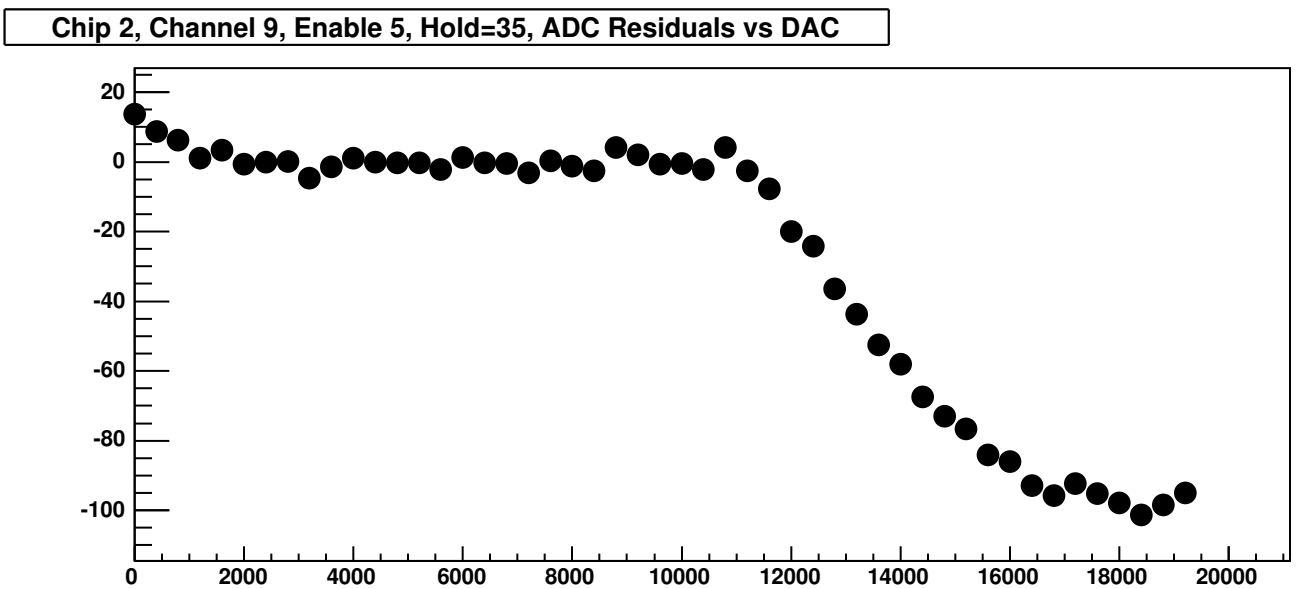


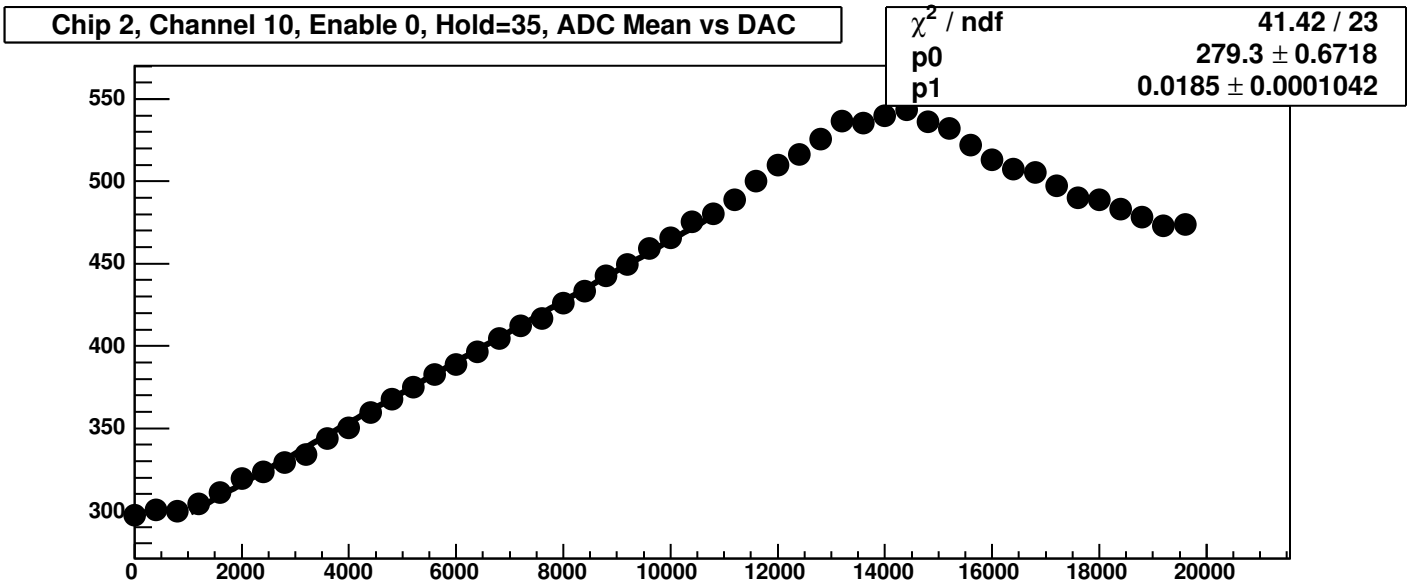
Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

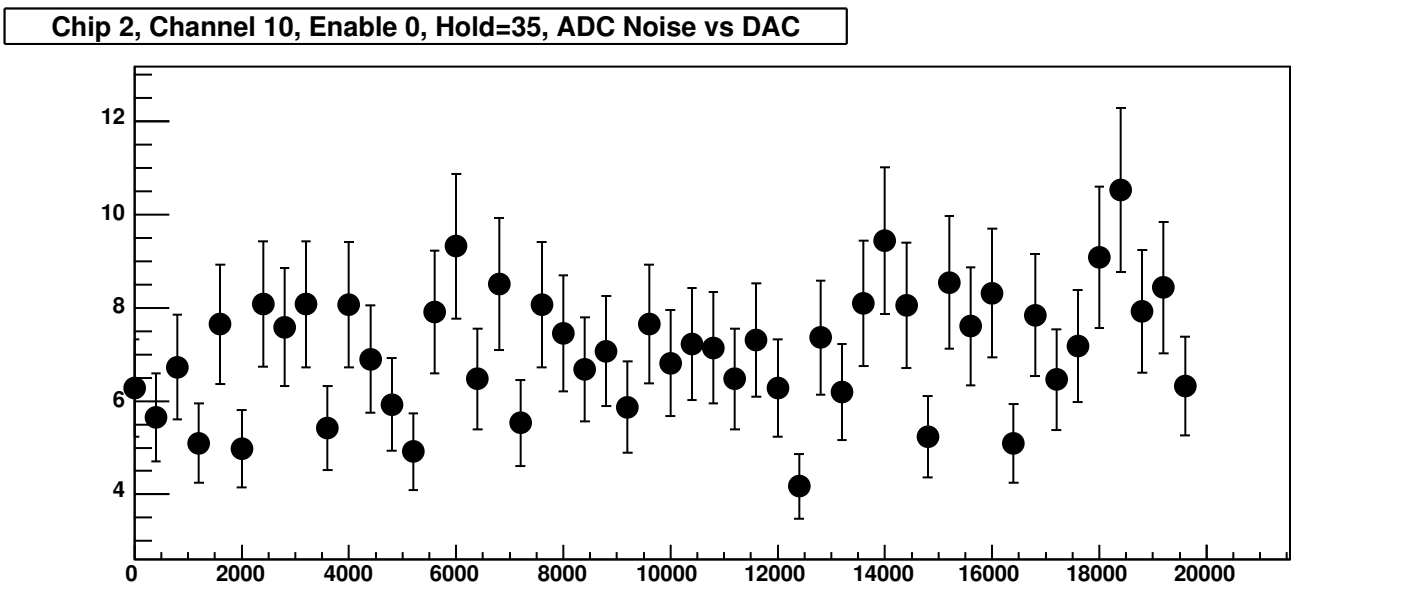


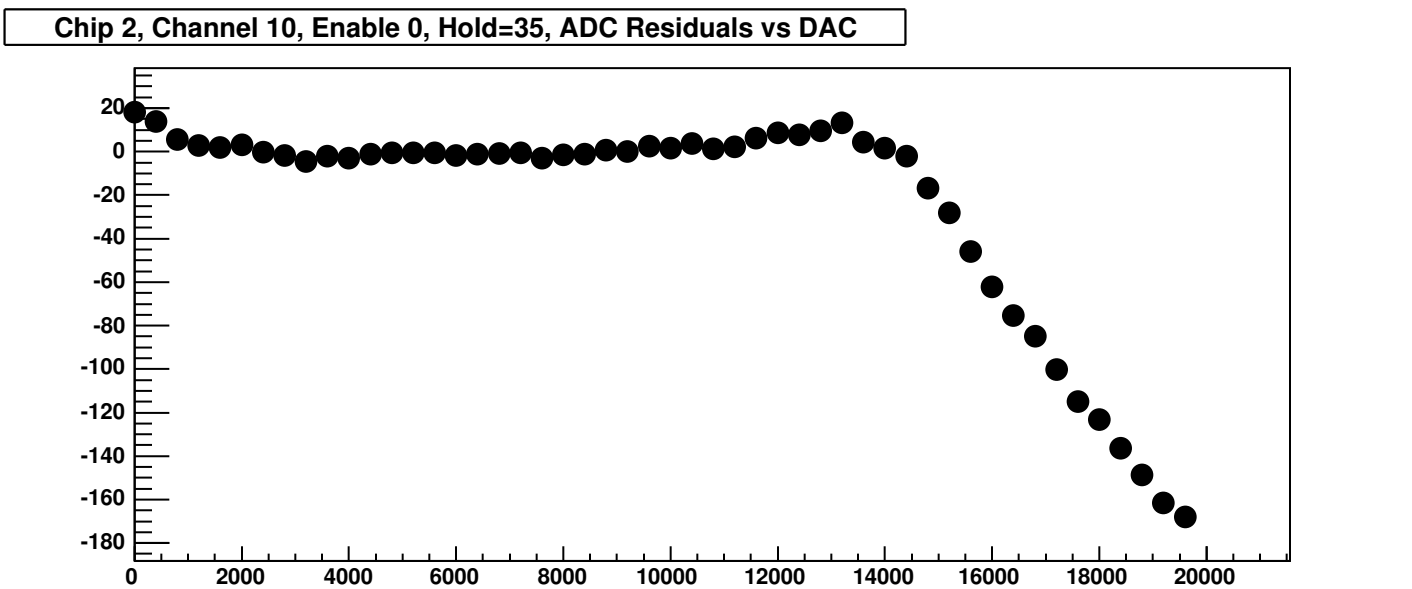

Chip 2, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 47.71 / 23
p0 154.3 ± 0.7229
p1 0.01445 ± 0.0001124
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

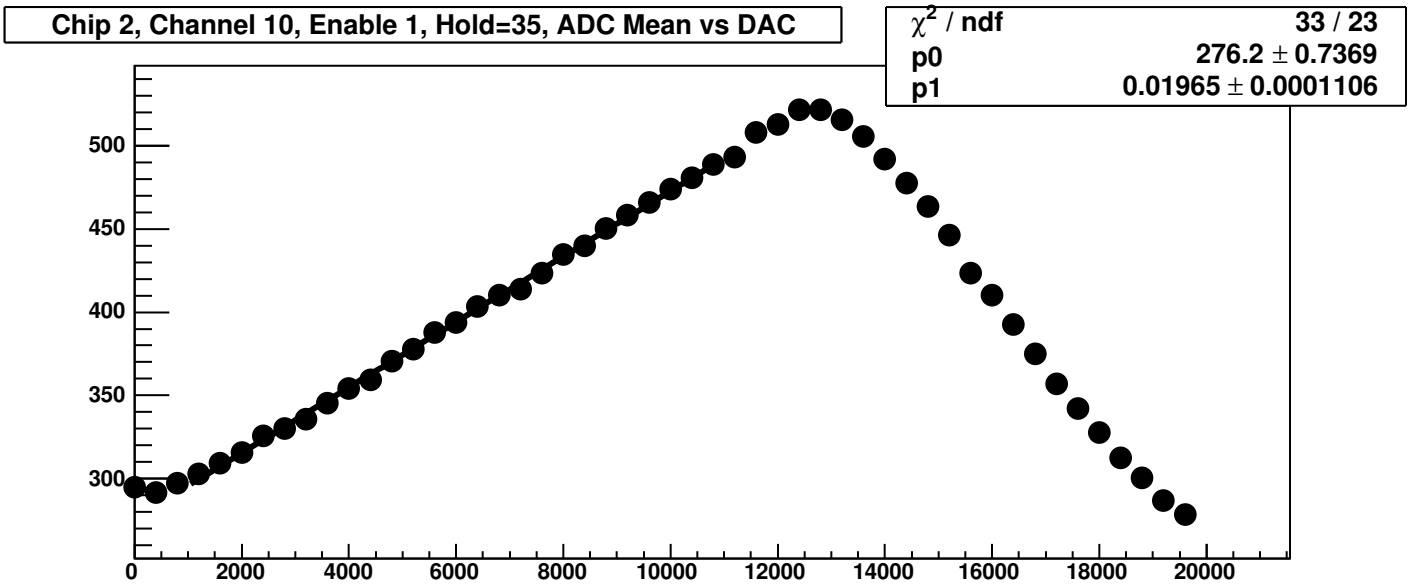

Chip 2, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

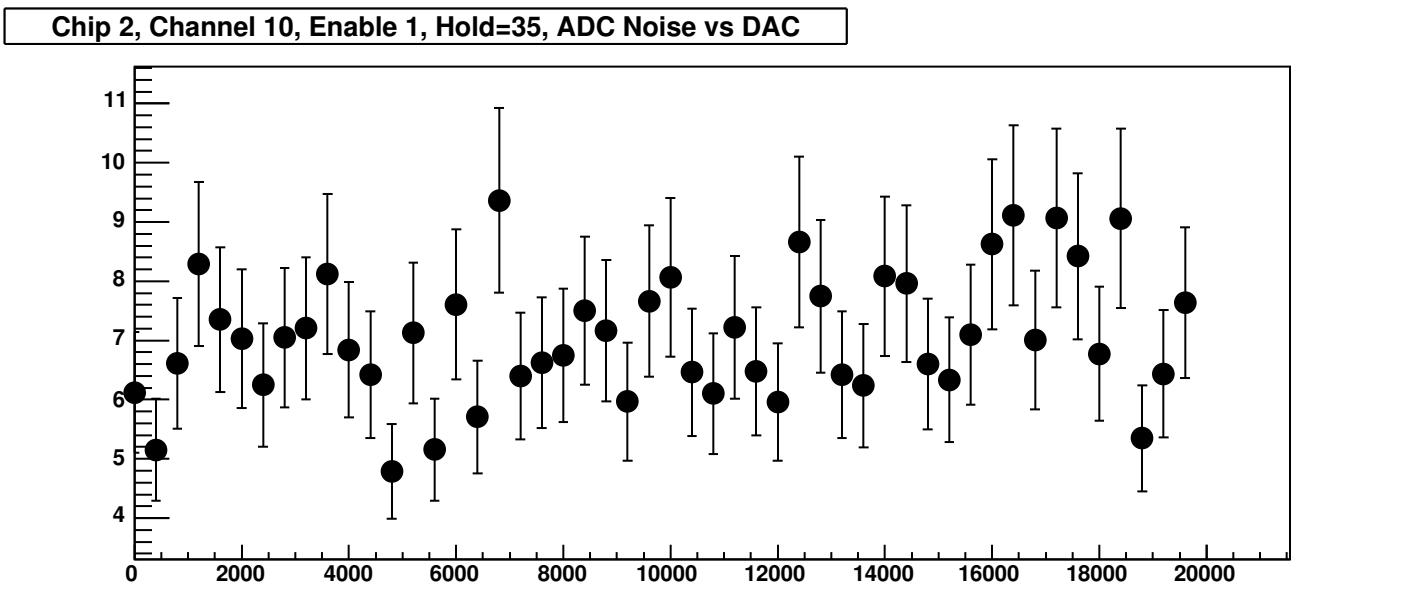

Chip 2, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

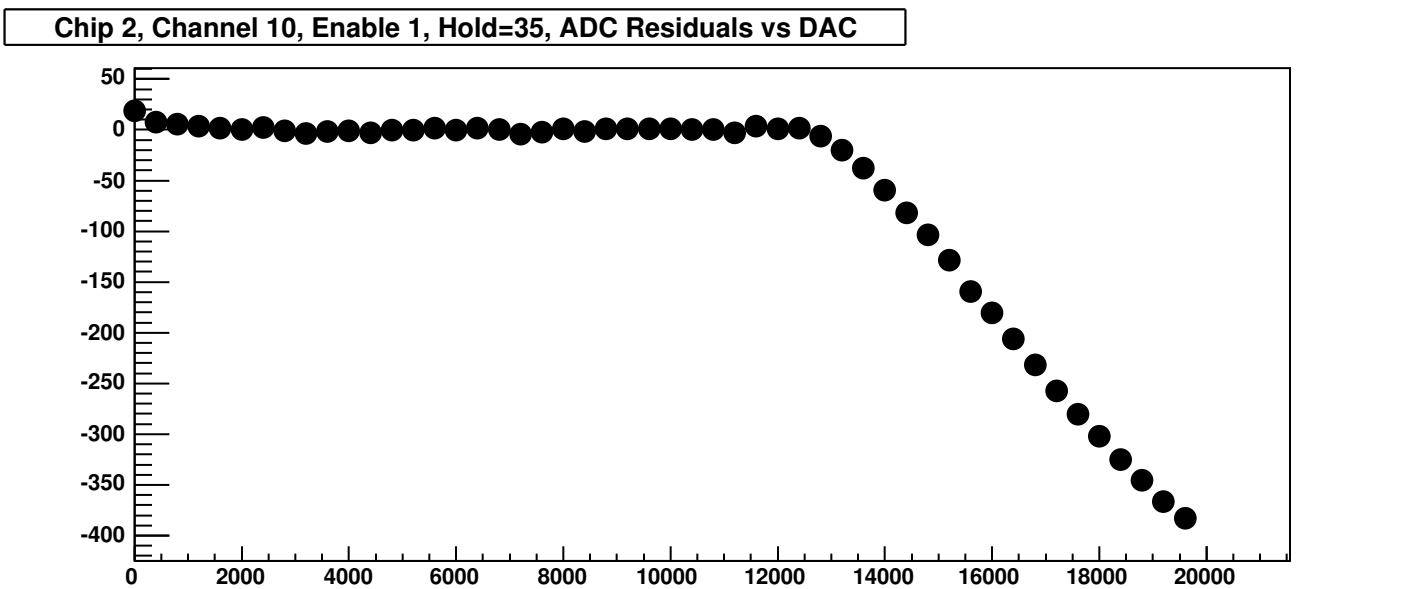

Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ^2 / ndf 41.42 / 23
p0 279.3 ± 0.6718
p1 0.0185 ± 0.0001042
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

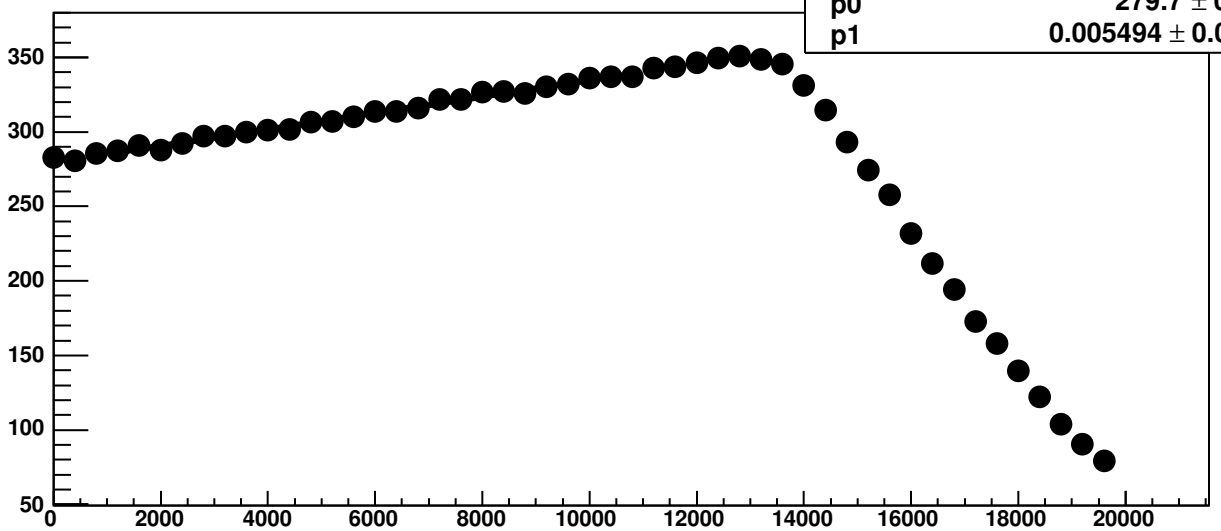

Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33 / 23
p0 276.2 ± 0.7369
p1 0.01965 ± 0.0001106
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

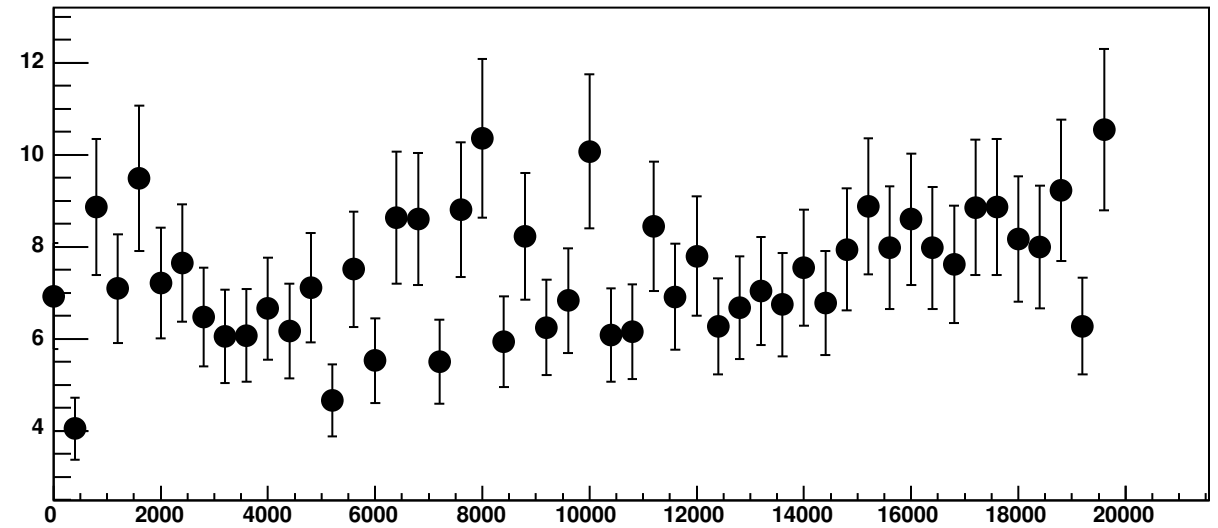

Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

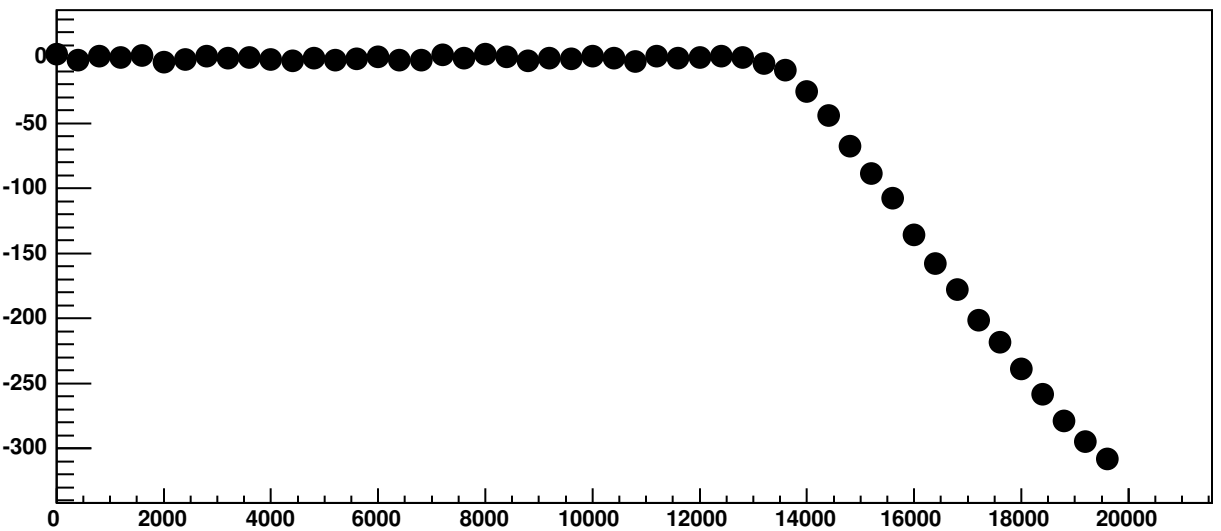
χ^2 / ndf	23.88 / 23
p0	279.7 ± 0.7335
p1	0.005494 ± 0.000111

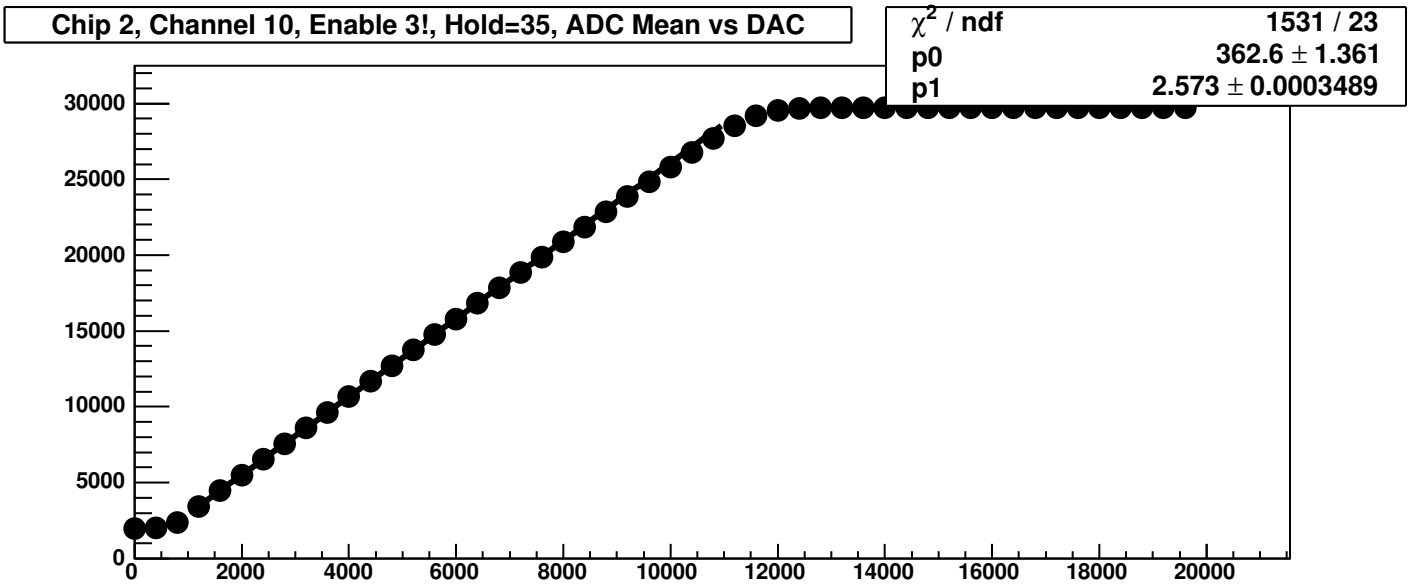


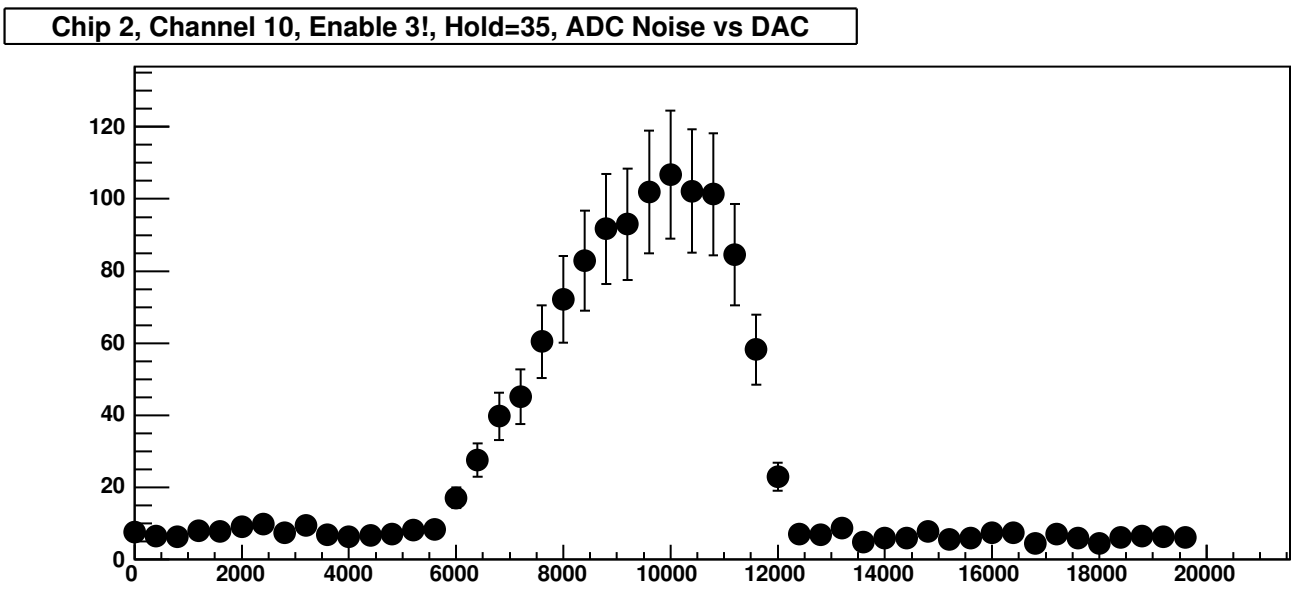
Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

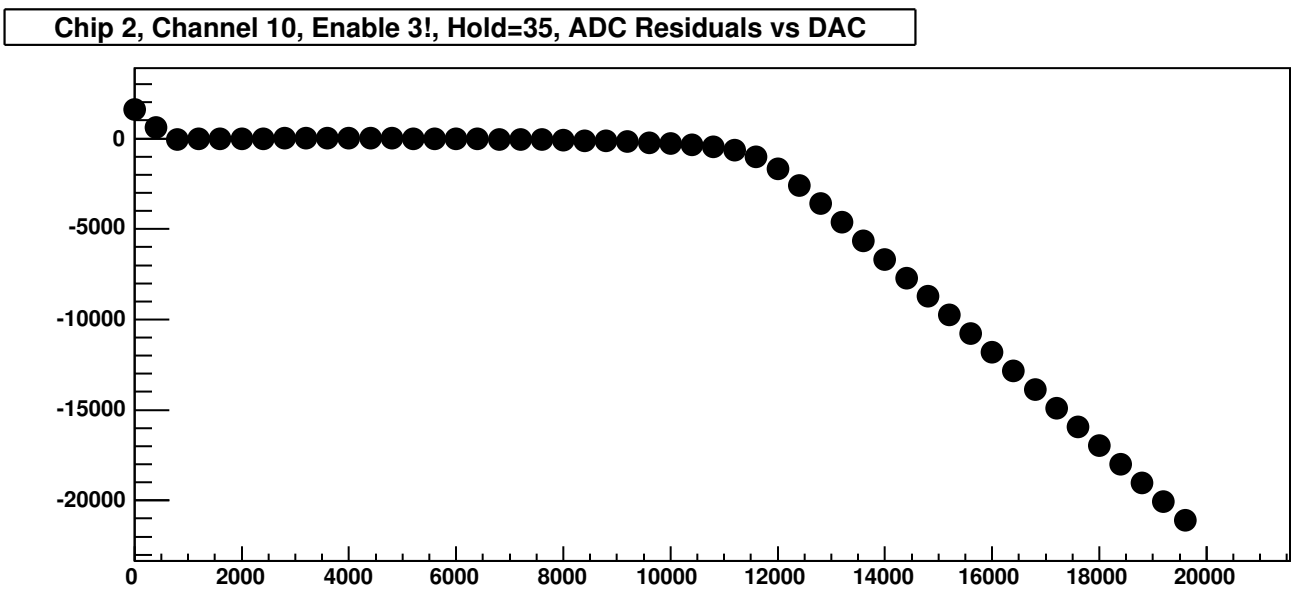


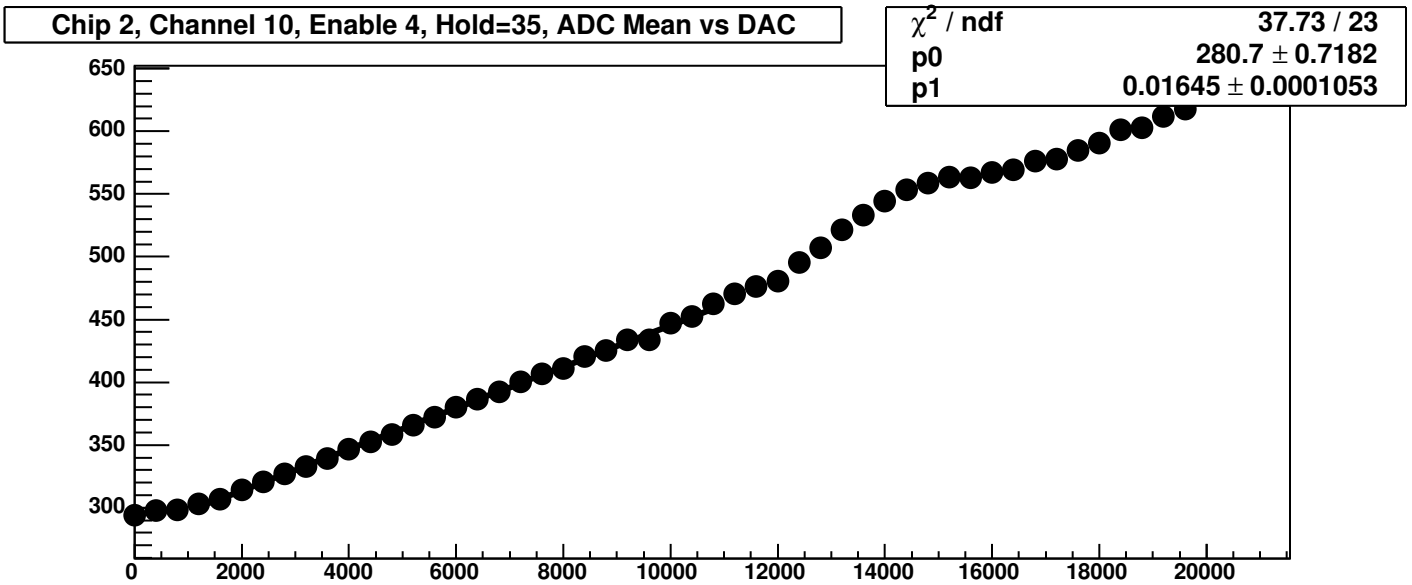
Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

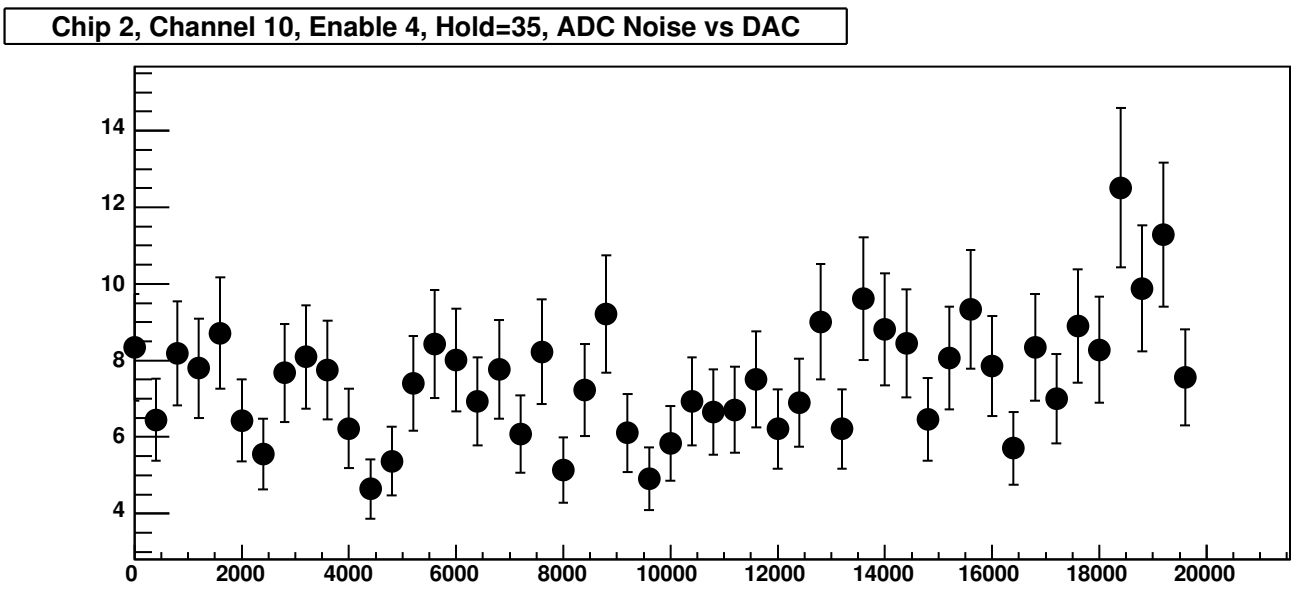


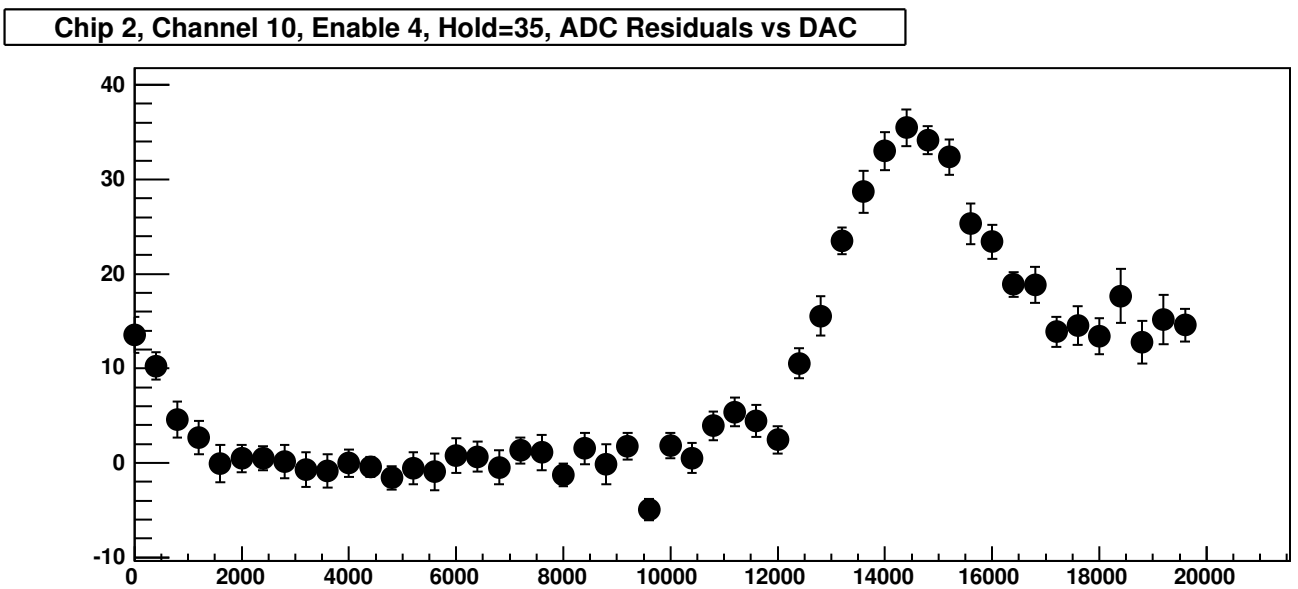

Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1531 / 23
p0 362.6 ± 1.361
p1 2.573 ± 0.0003489
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

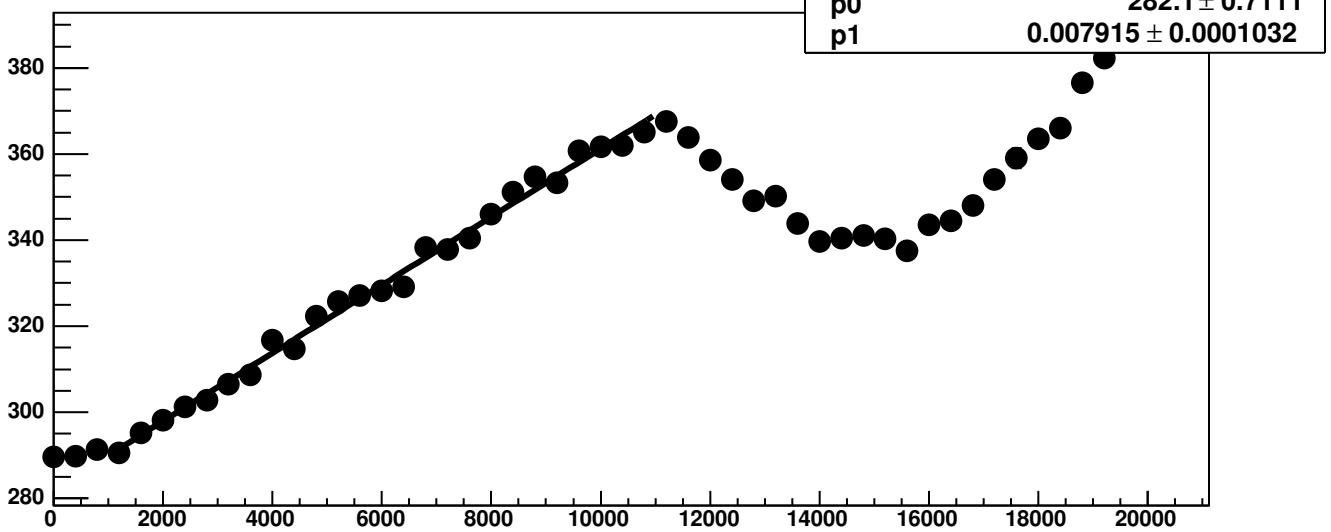

Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.73 / 23
p0 280.7 ± 0.7182
p1 0.01645 ± 0.0001053
650
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

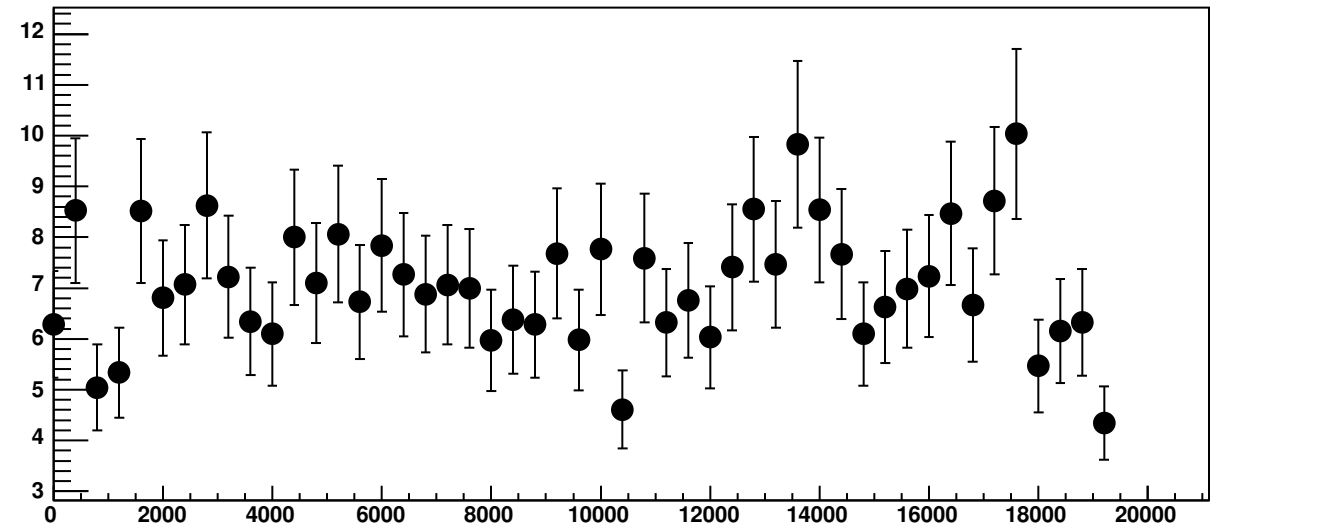

Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

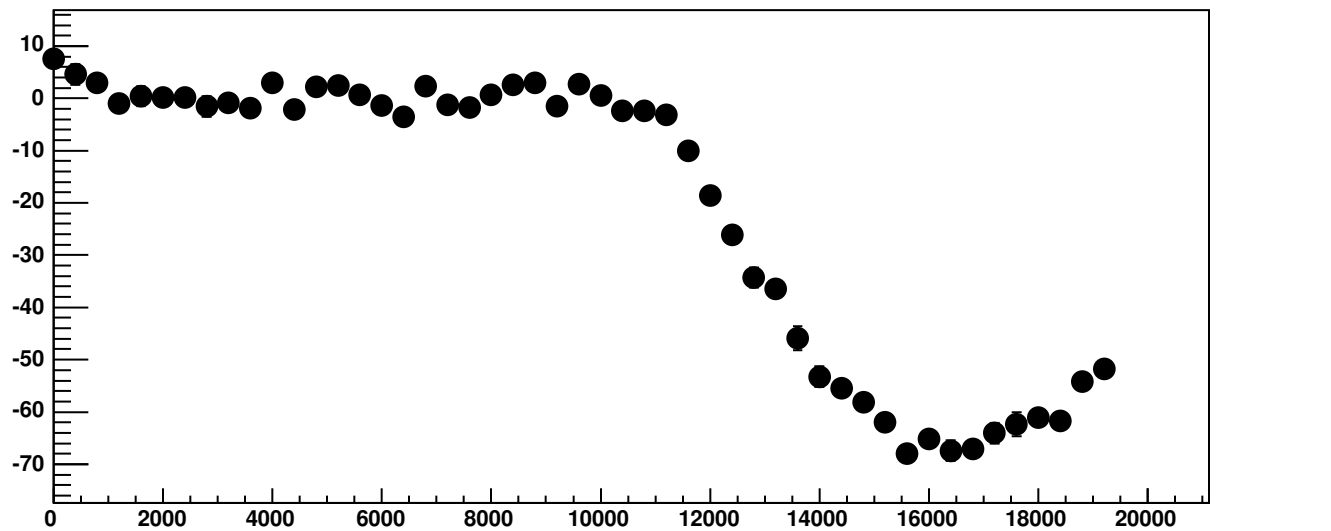
Chip 2, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

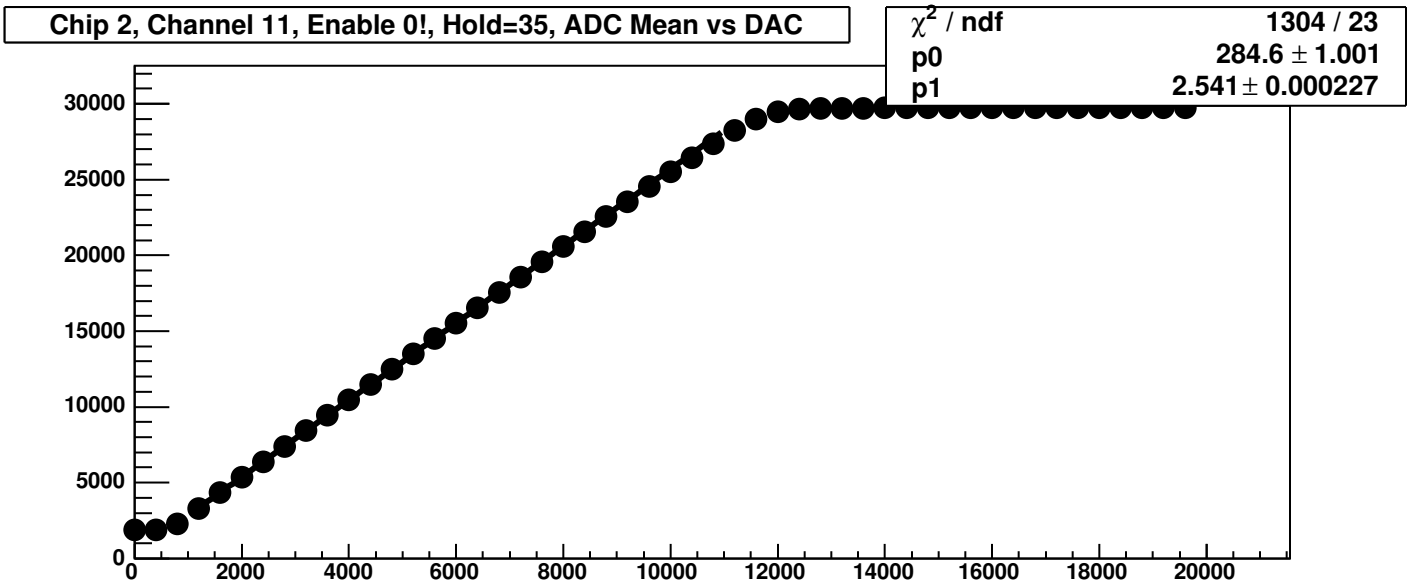


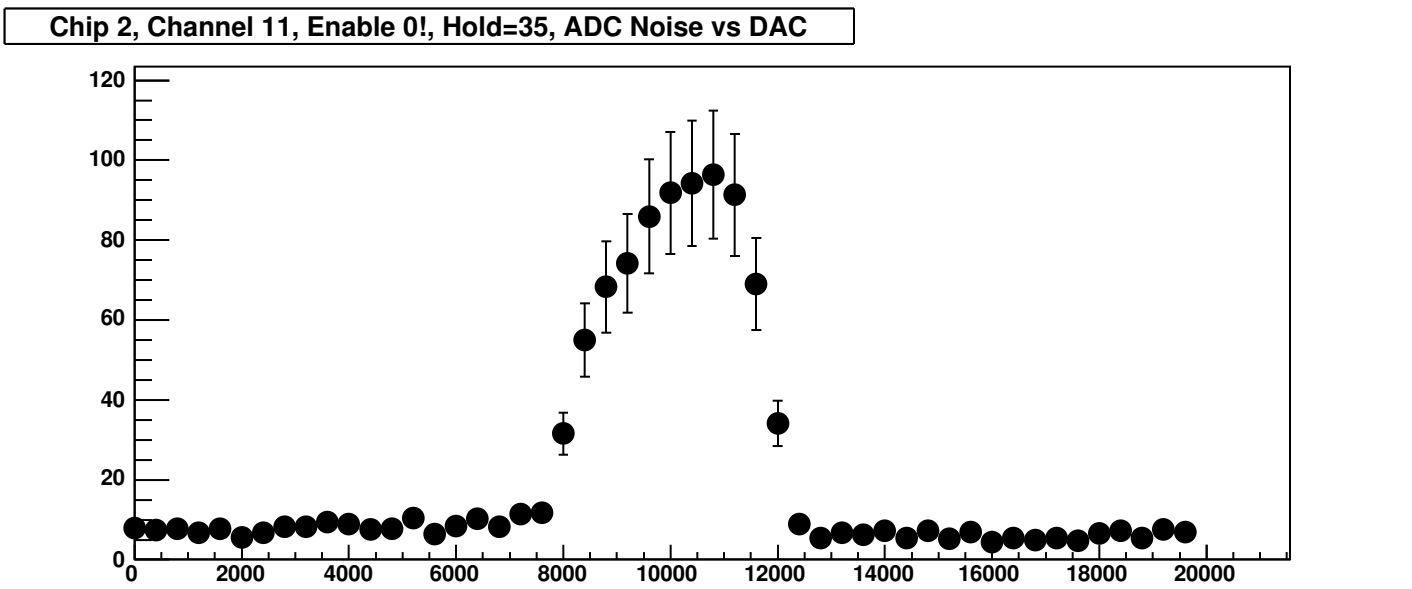
Chip 2, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

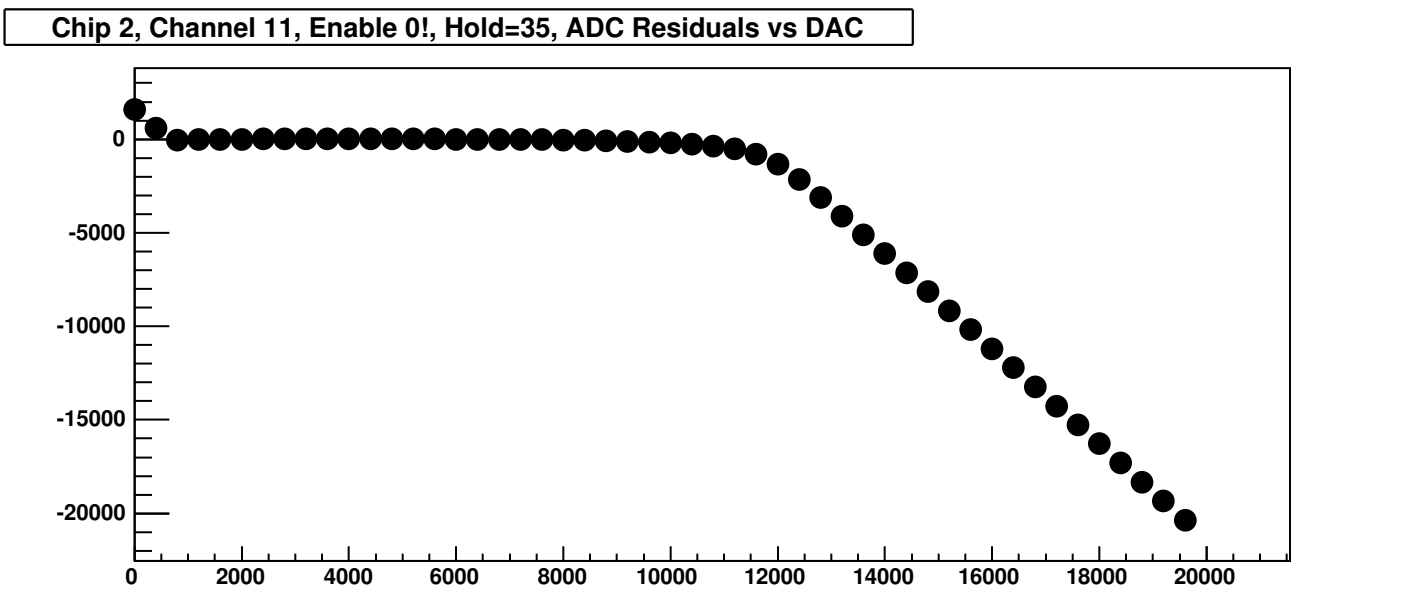


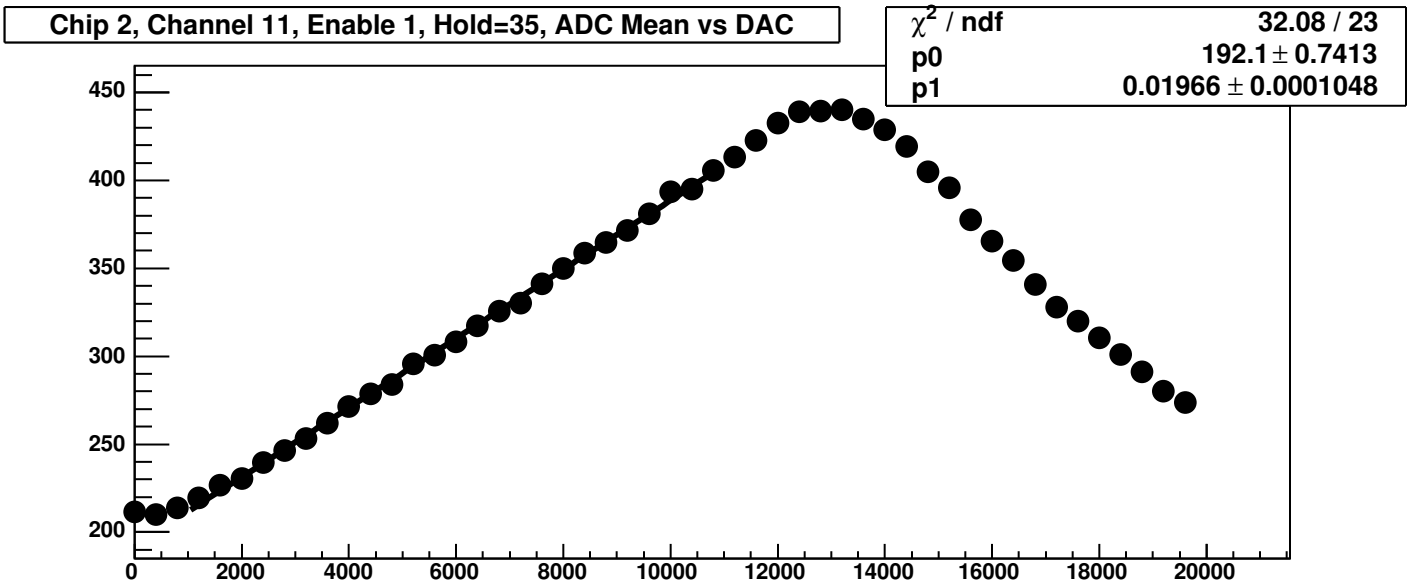
Chip 2, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

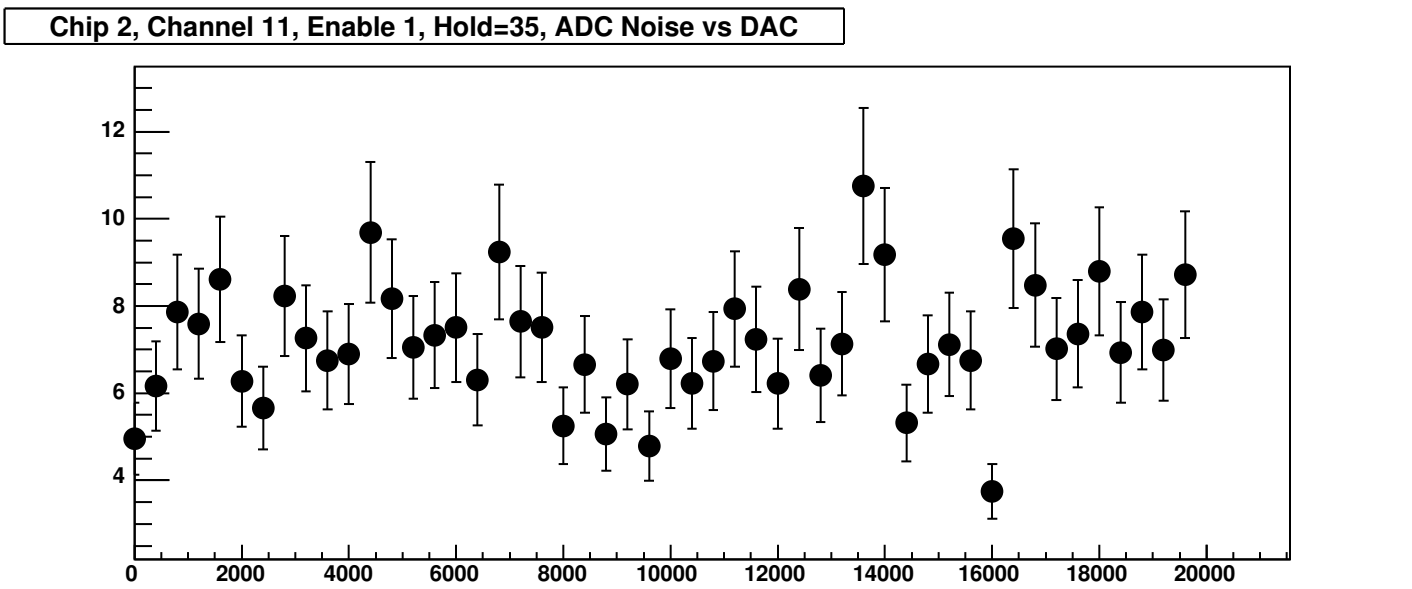


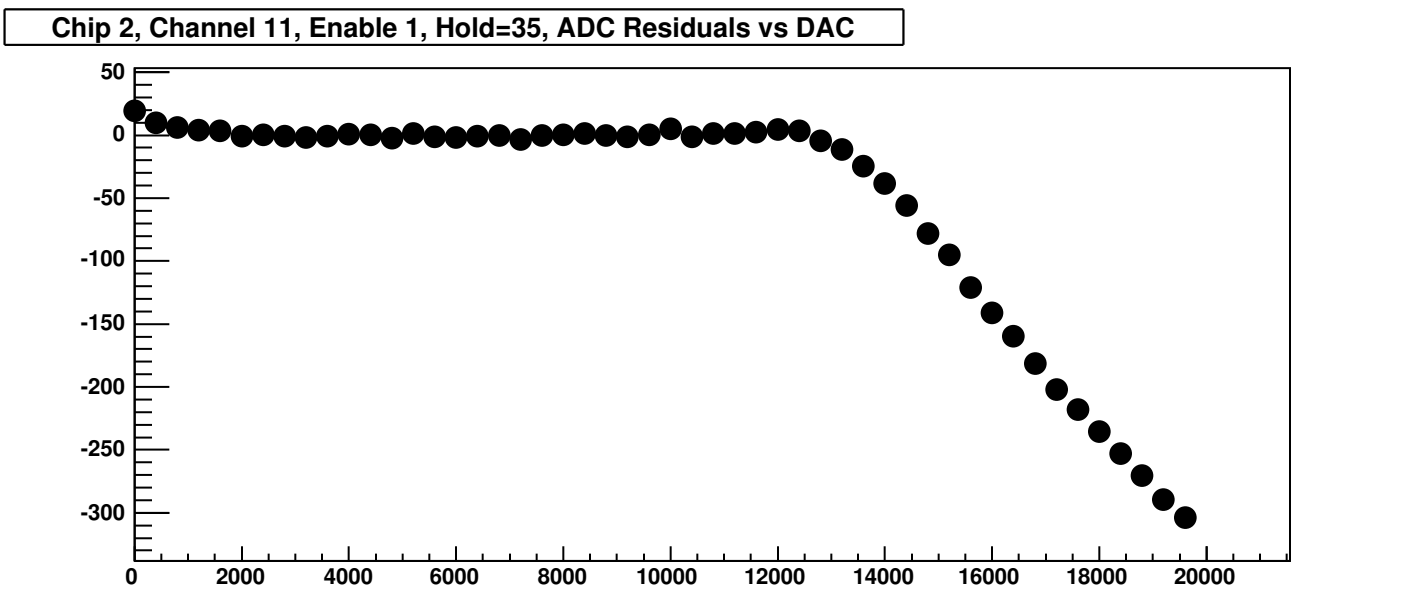

Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1304 / 23
p0 284.6 ± 1.001
p1 2.541 ± 0.000227
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

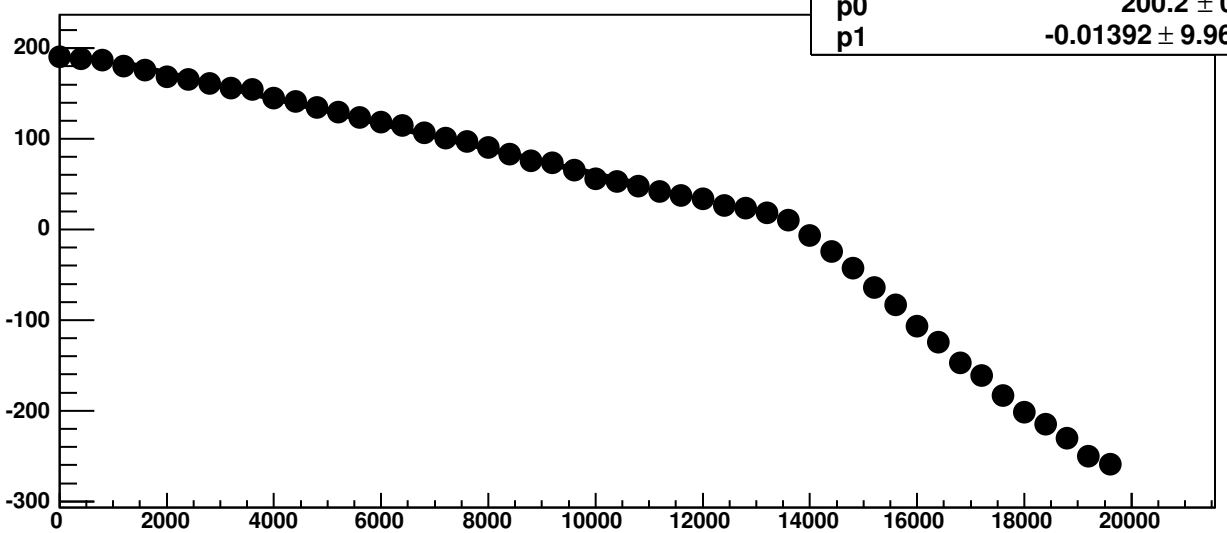

Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.08 / 23
p0 192.1 ± 0.7413
p1 0.01966 ± 0.0001048
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

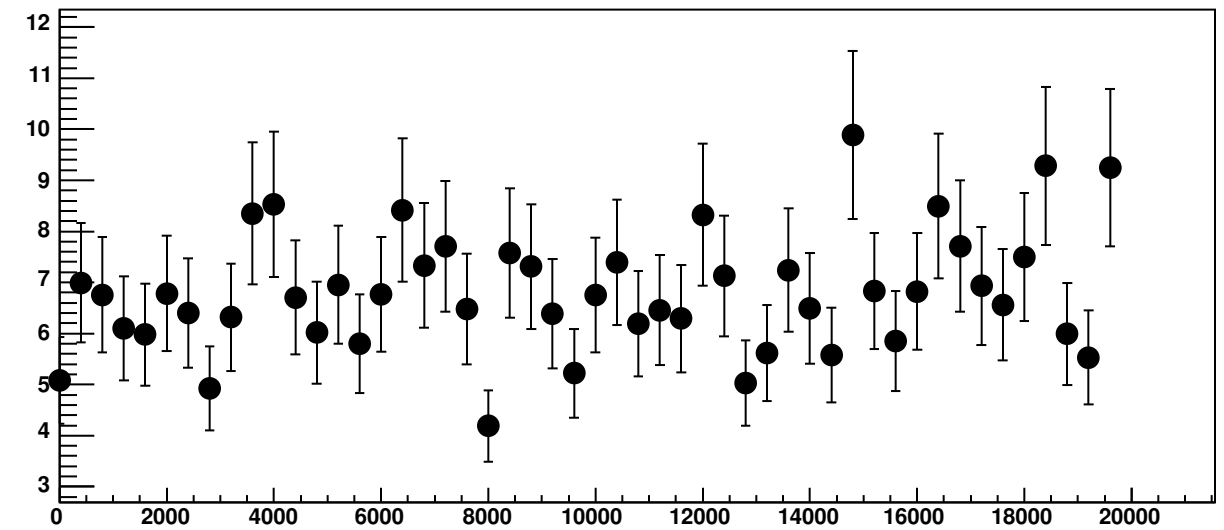

Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

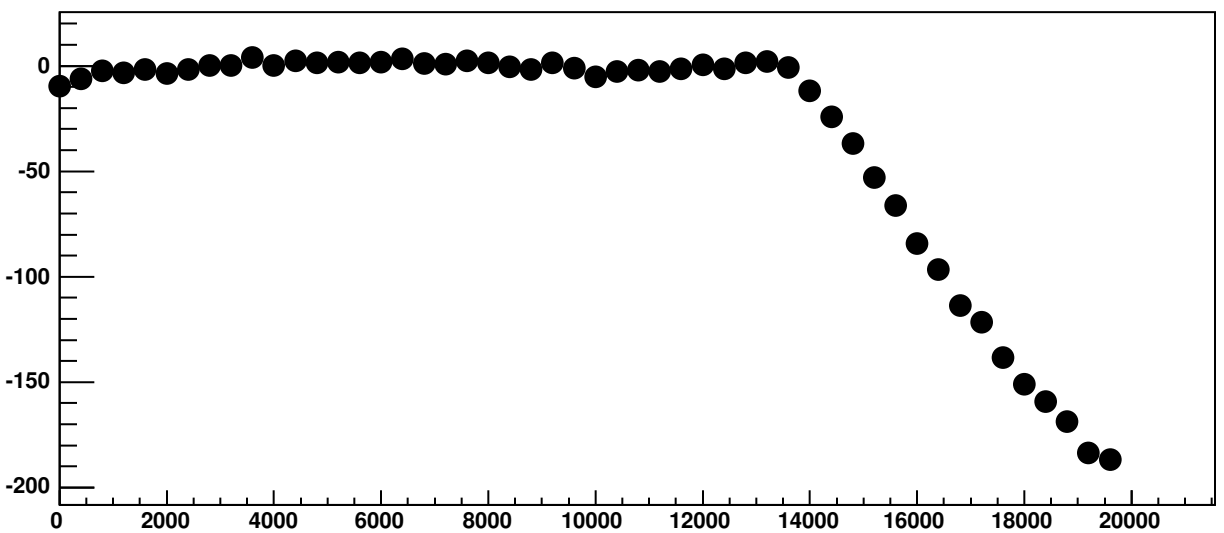
χ^2 / ndf	51.49 / 23
p0	200.2 ± 0.6663
p1	-0.01392 ± 9.962e-05



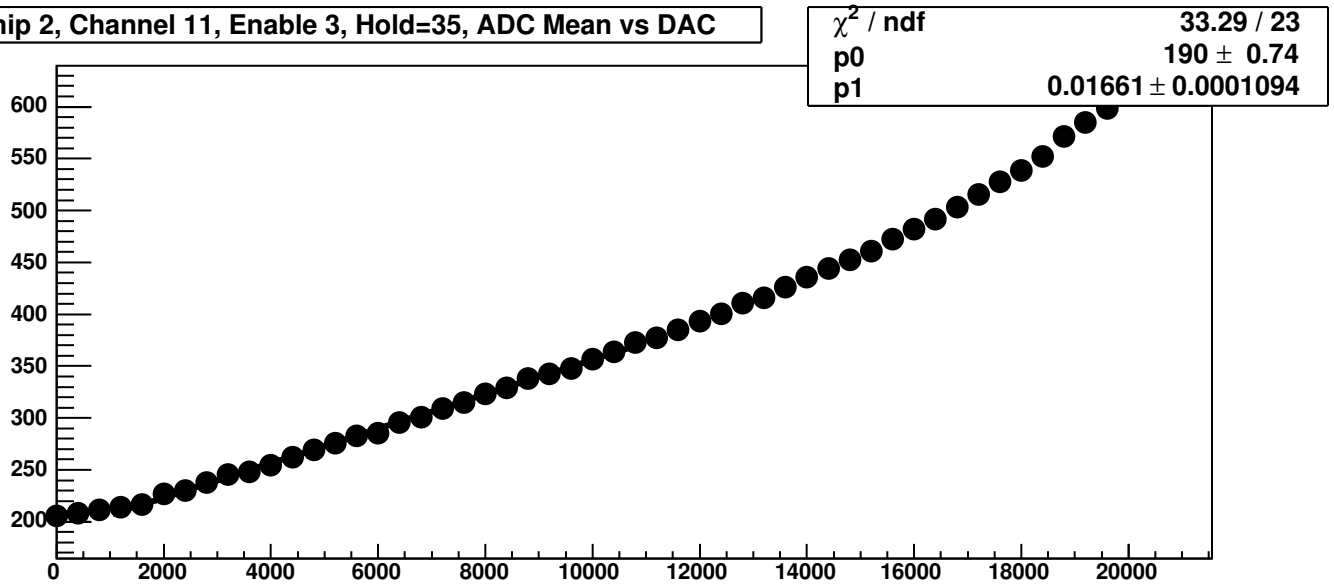
Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



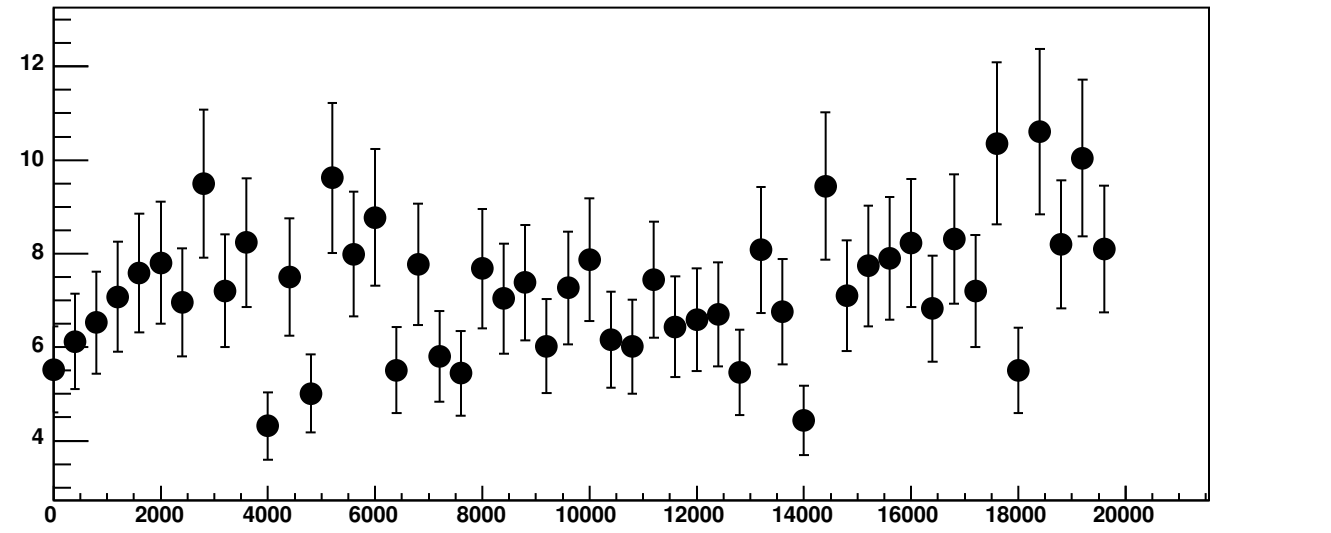
Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



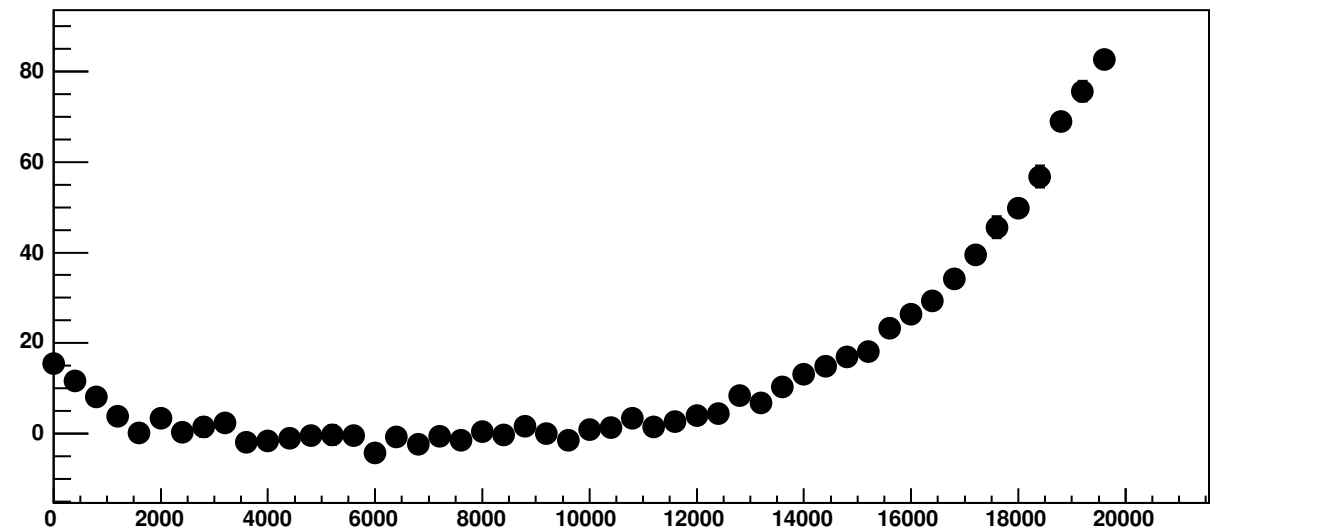
Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC

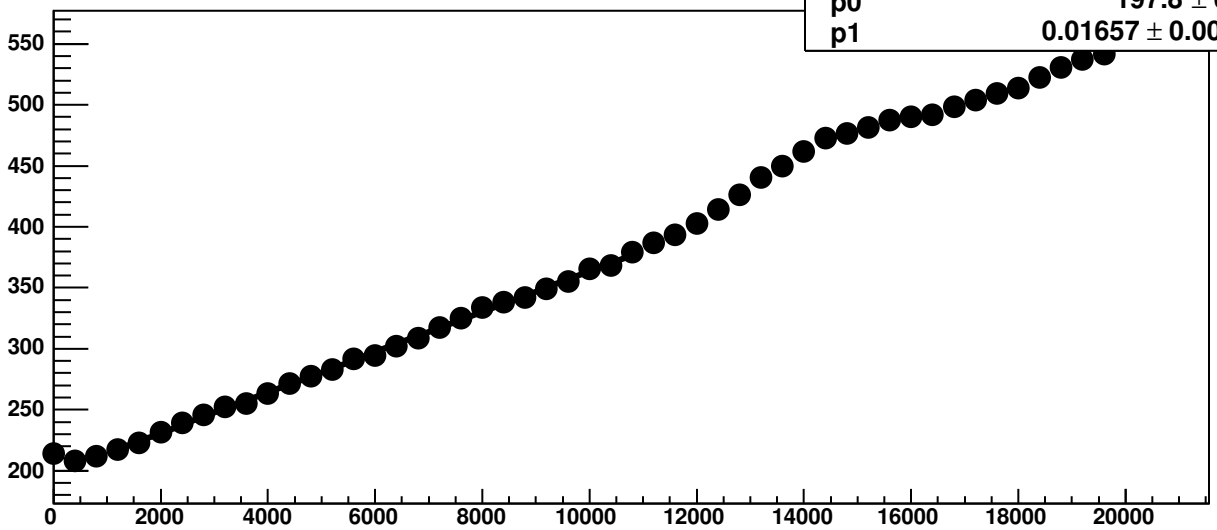


Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

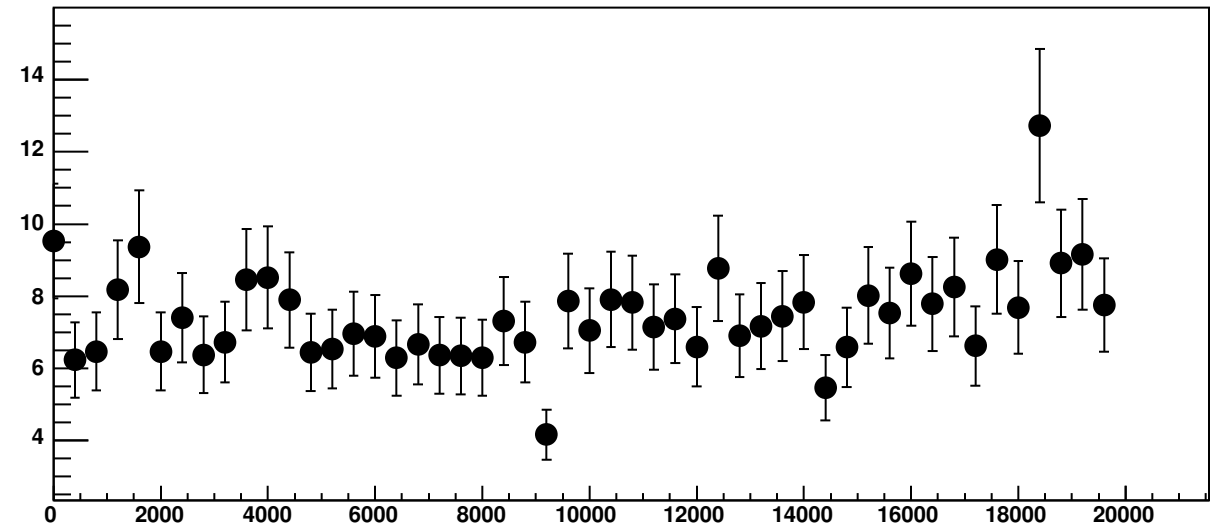


Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

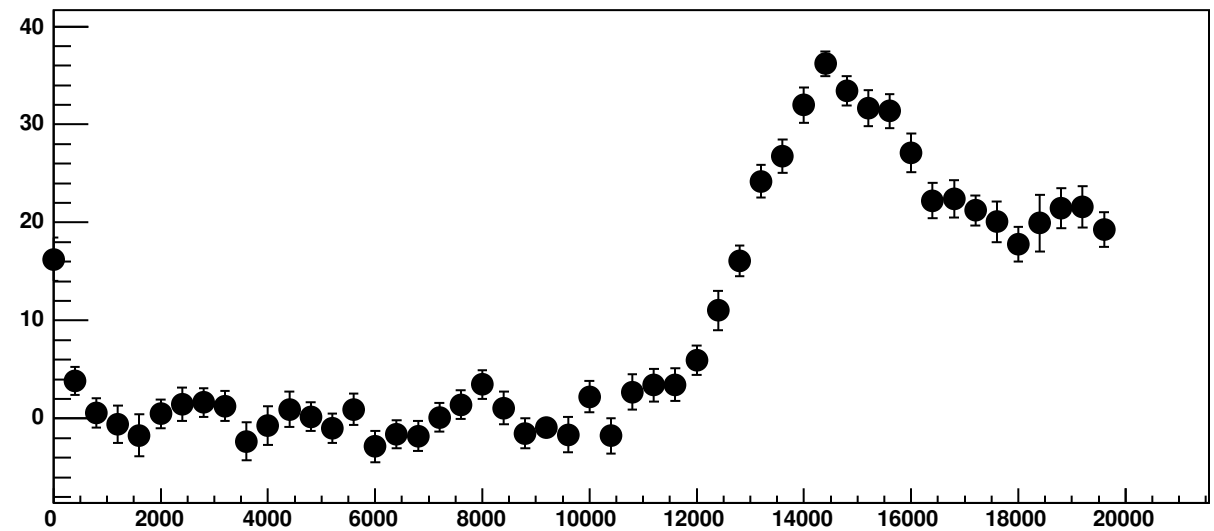
χ^2 / ndf	27.1 / 23
p0	197.8 $\pm$ 0.7792
p1	0.01657 $\pm$ 0.0001133



Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

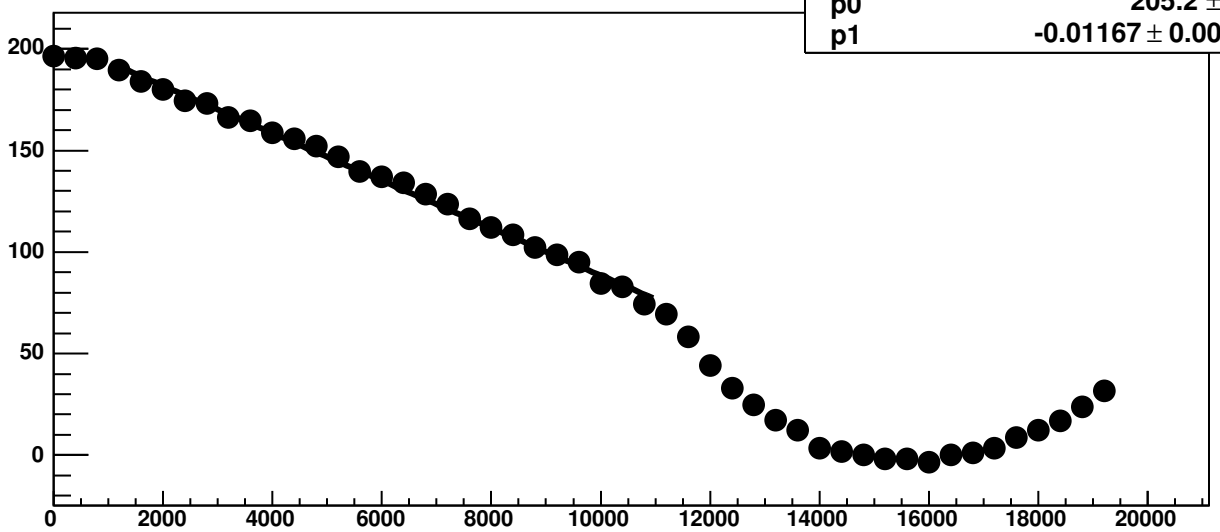


Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

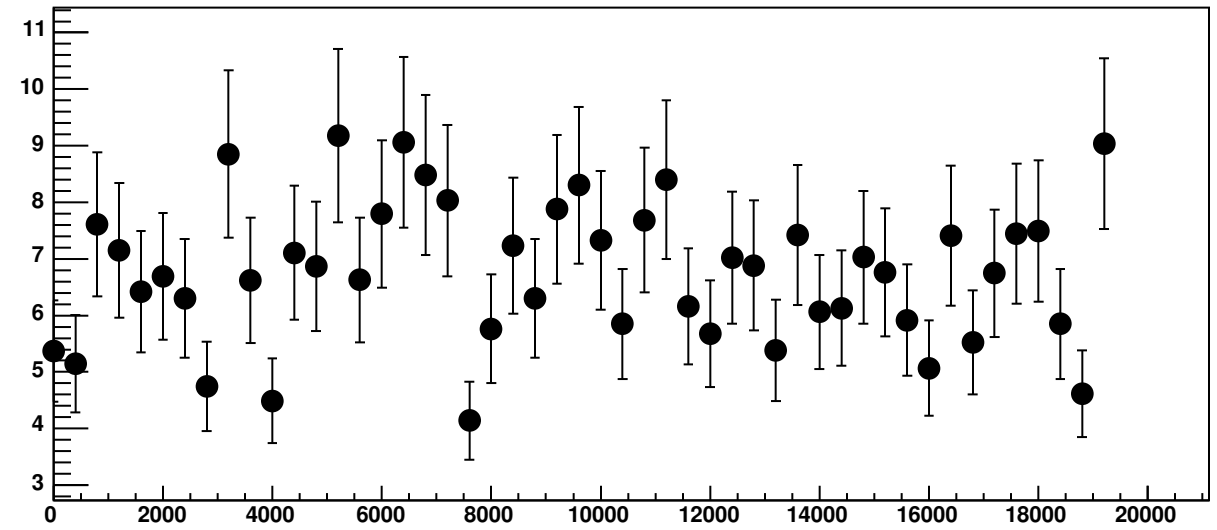


Chip 2, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

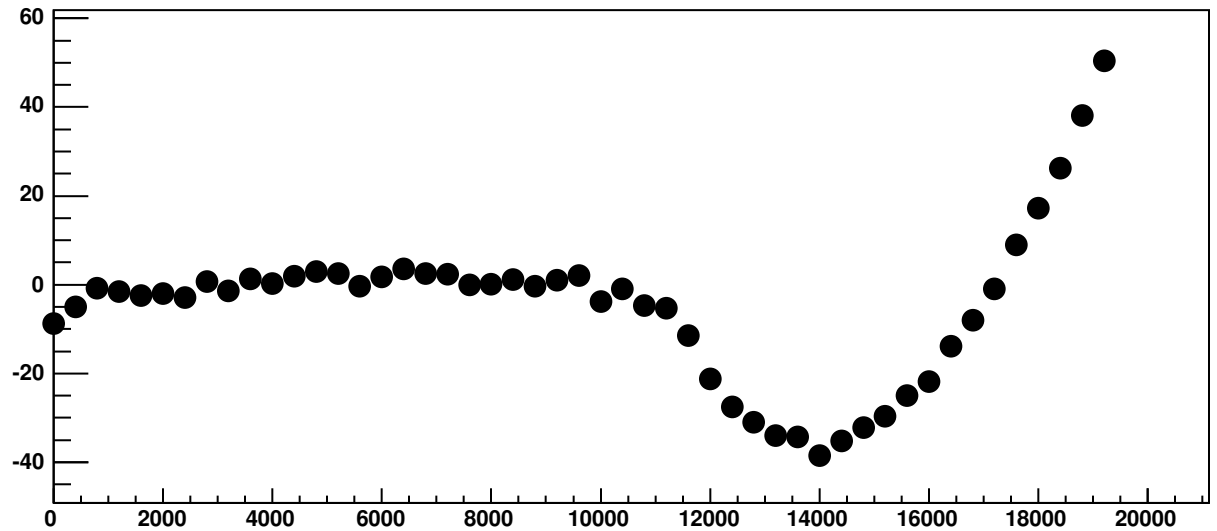
χ^2 / ndf	39.1 / 23
p0	205.2 ± 0.682
p1	-0.01167 ± 0.0001049



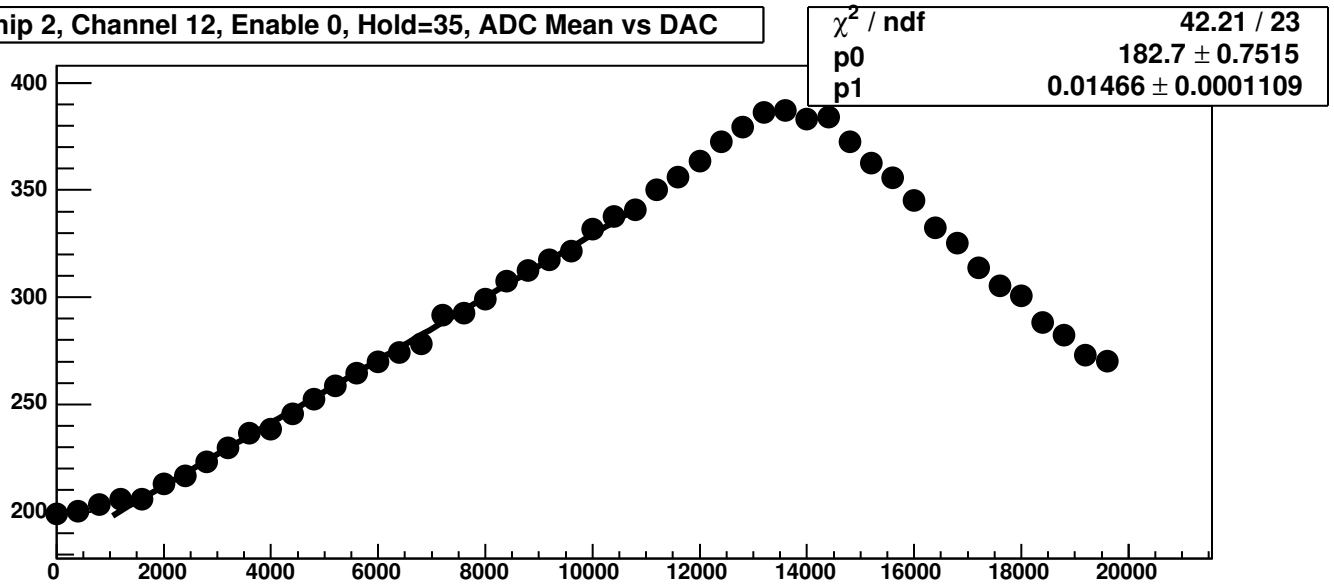
Chip 2, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



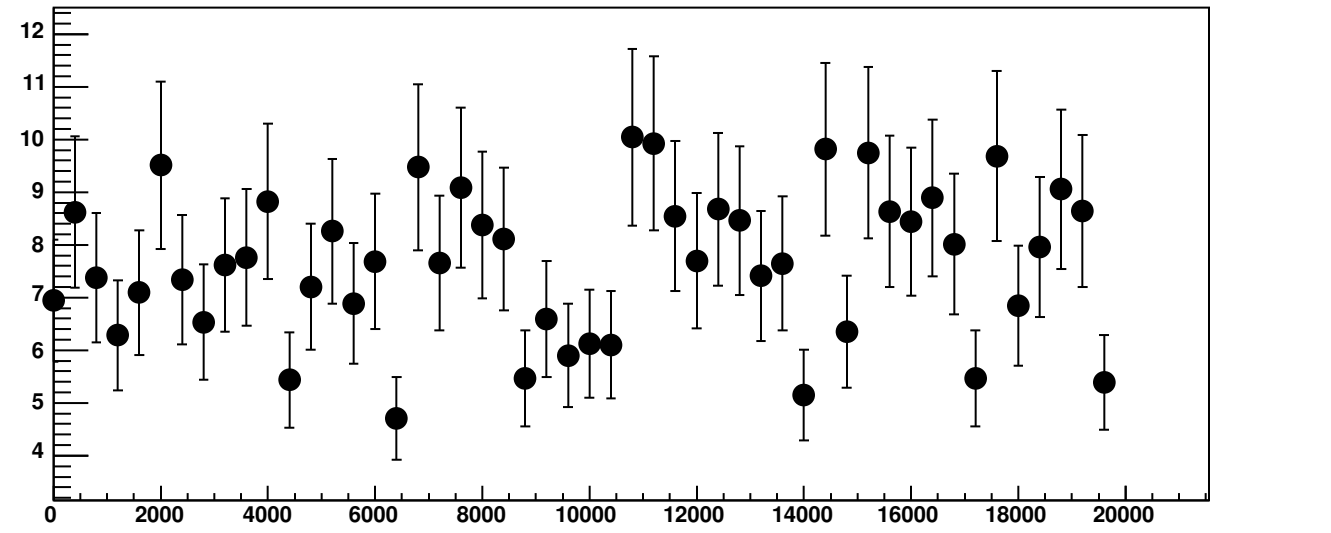
Chip 2, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



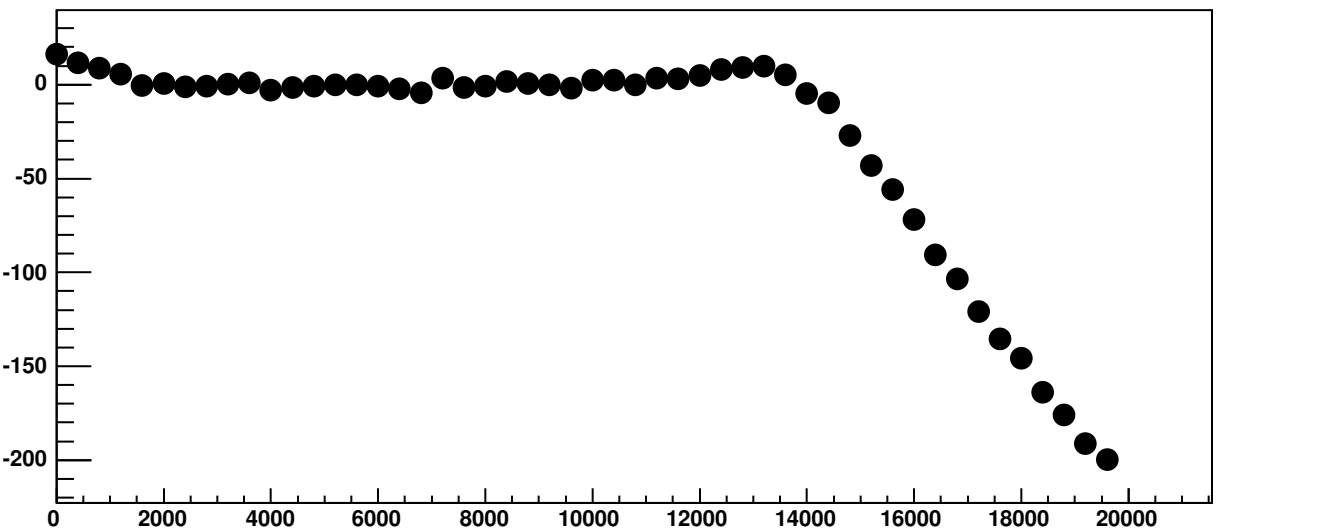
Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

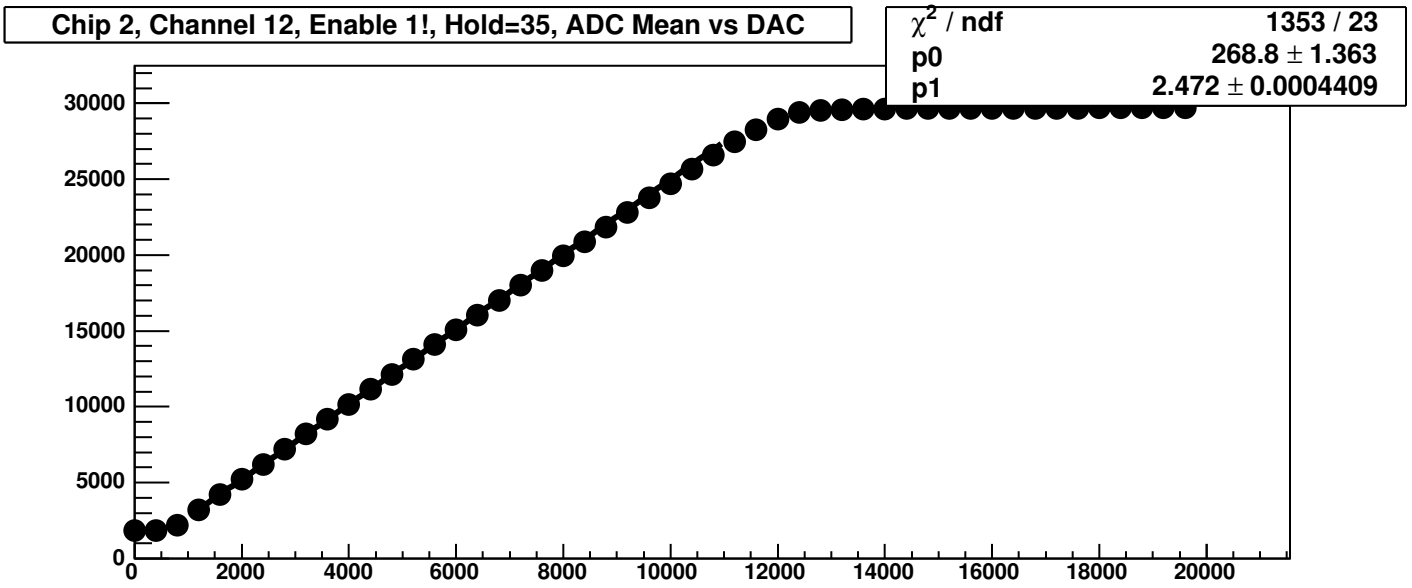


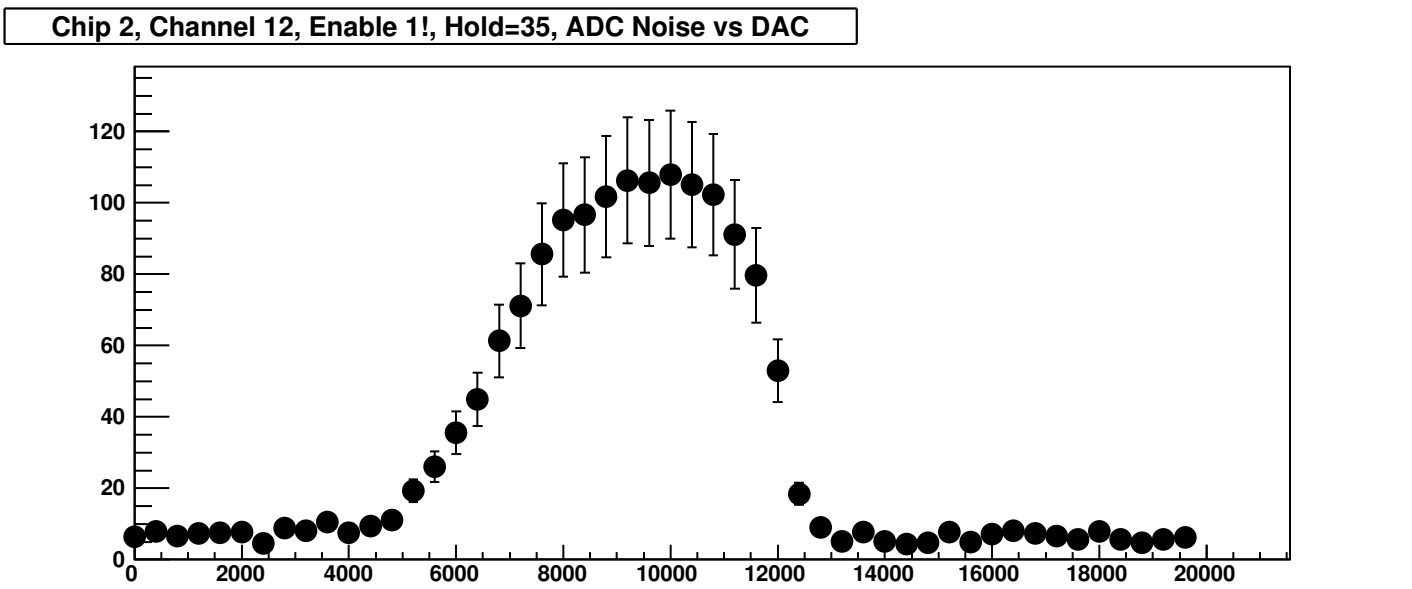
Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

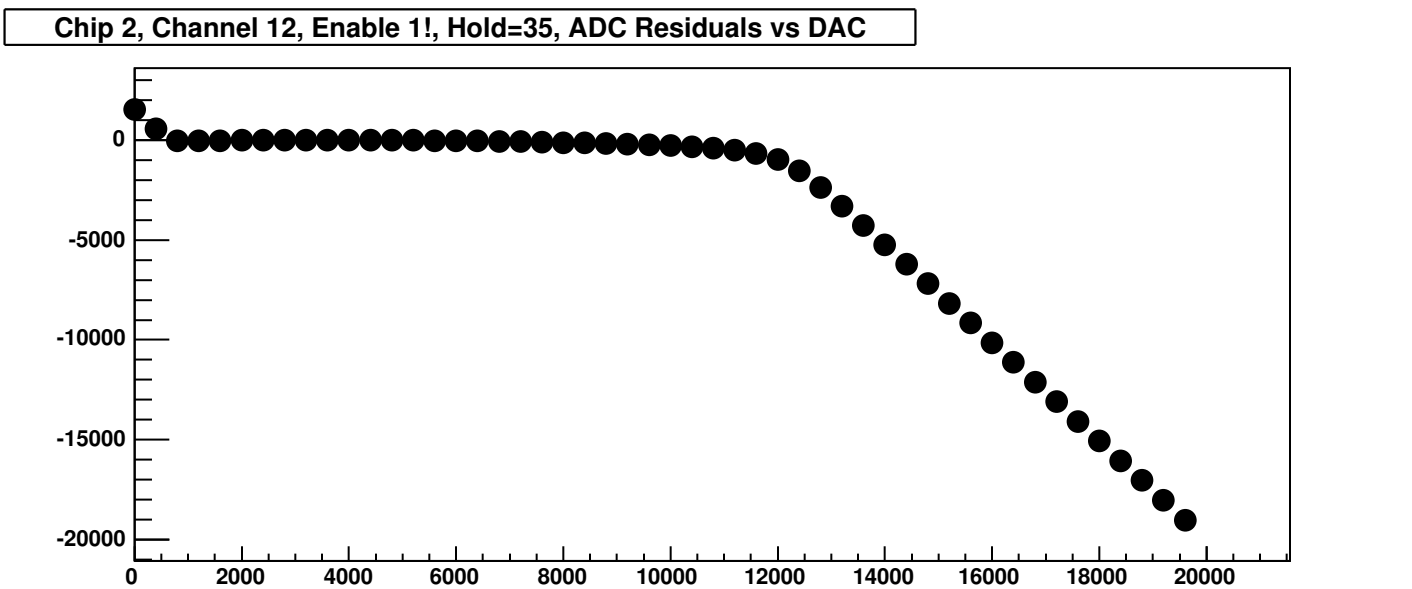


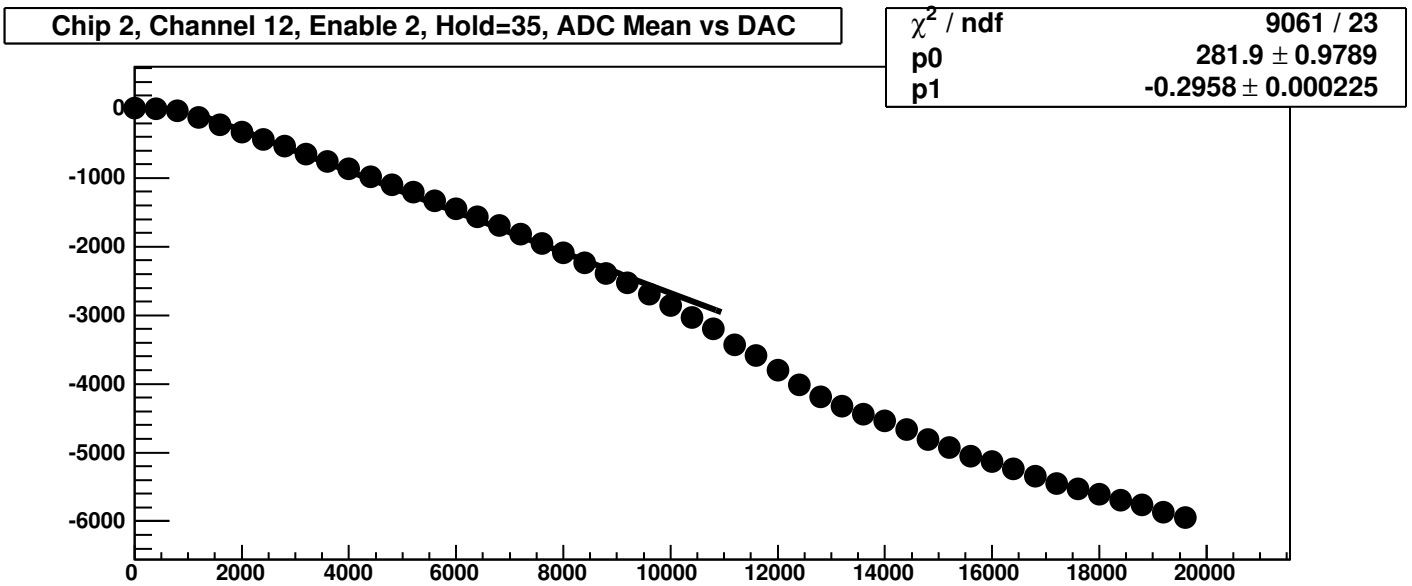
Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

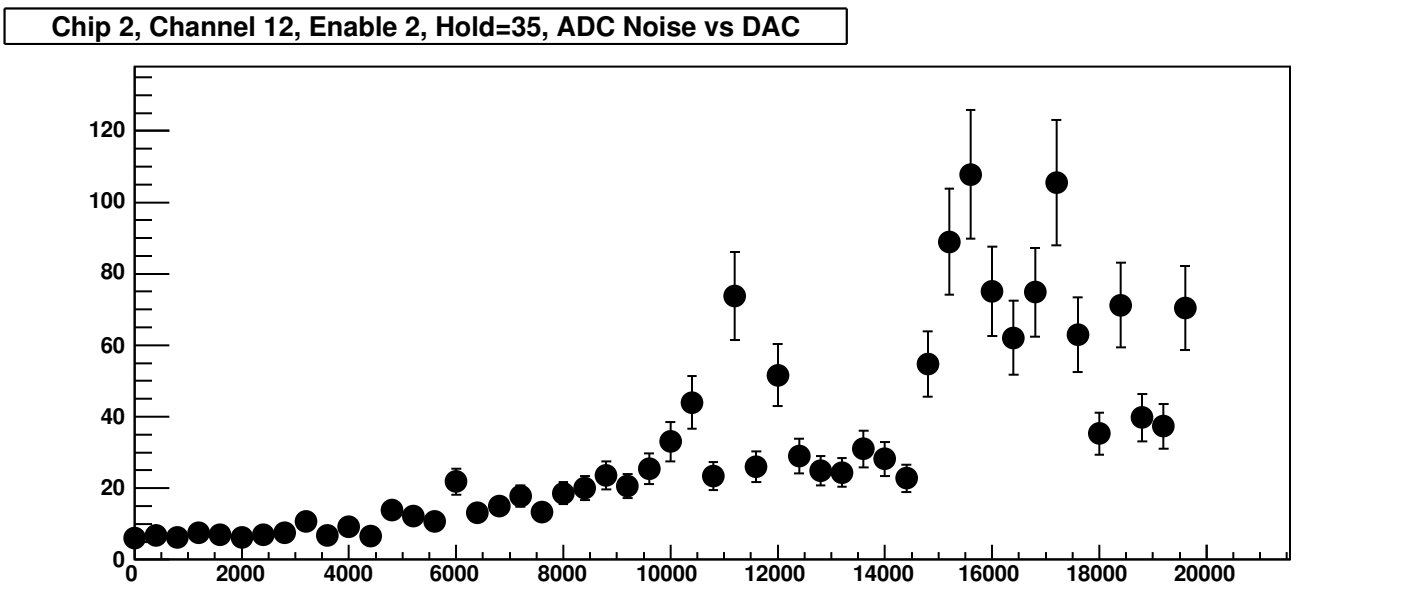


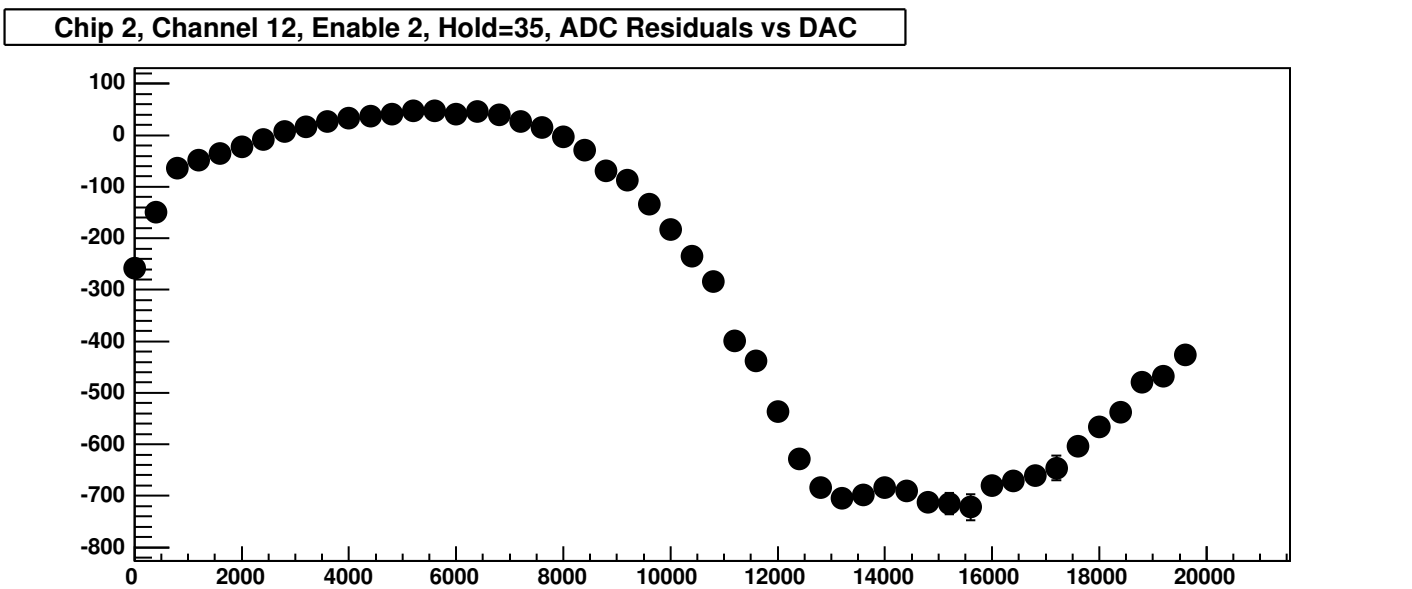

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1353 / 23
p0 268.8 ± 1.363
p1 2.472 ± 0.0004409
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

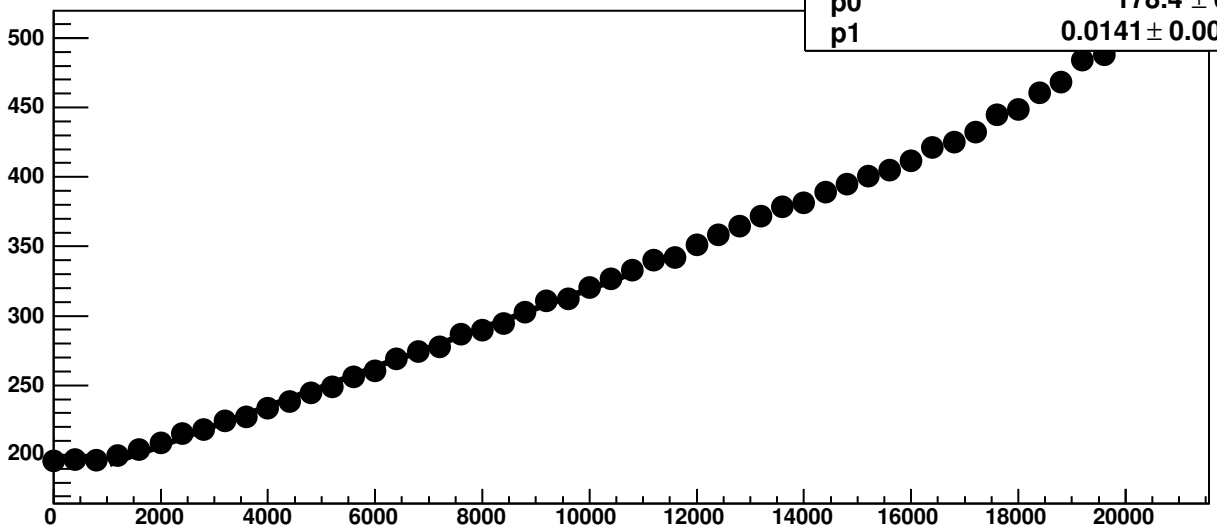

Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 9061 / 23
p0 281.9 ± 0.9789
p1 -0.2958 ± 0.000225
0
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

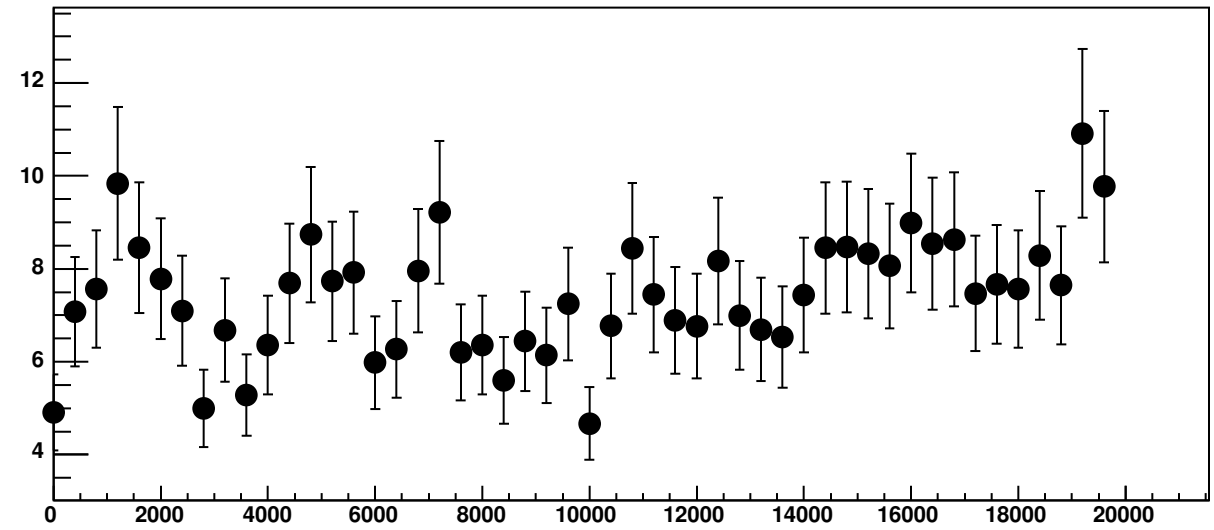

Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
-500
-600
-700
-800
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

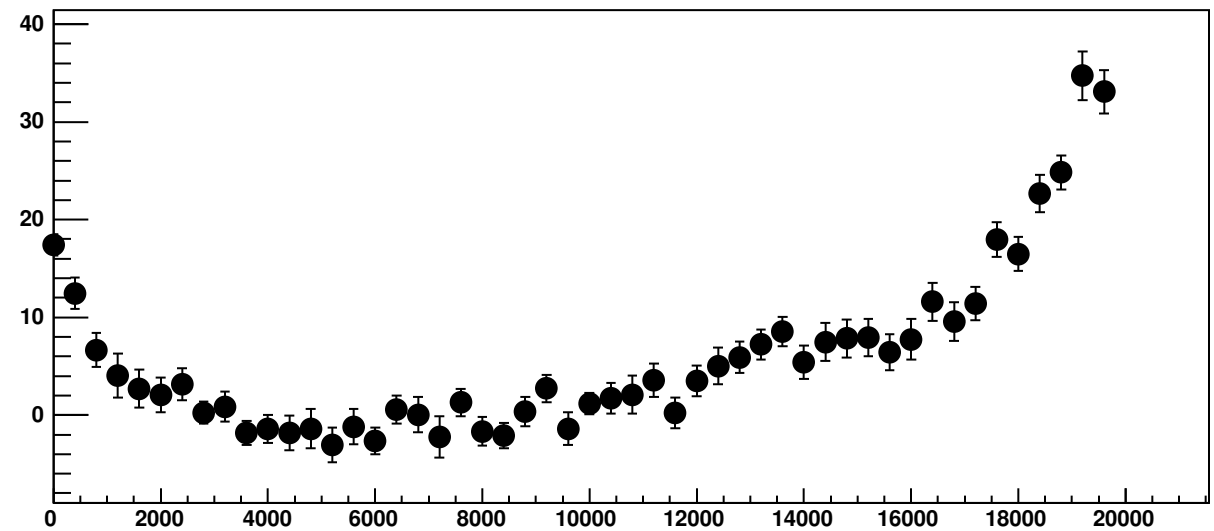
χ^2 / ndf	36.53 / 23
p0	178.4 ± 0.7389
p1	0.0141 ± 0.0001078



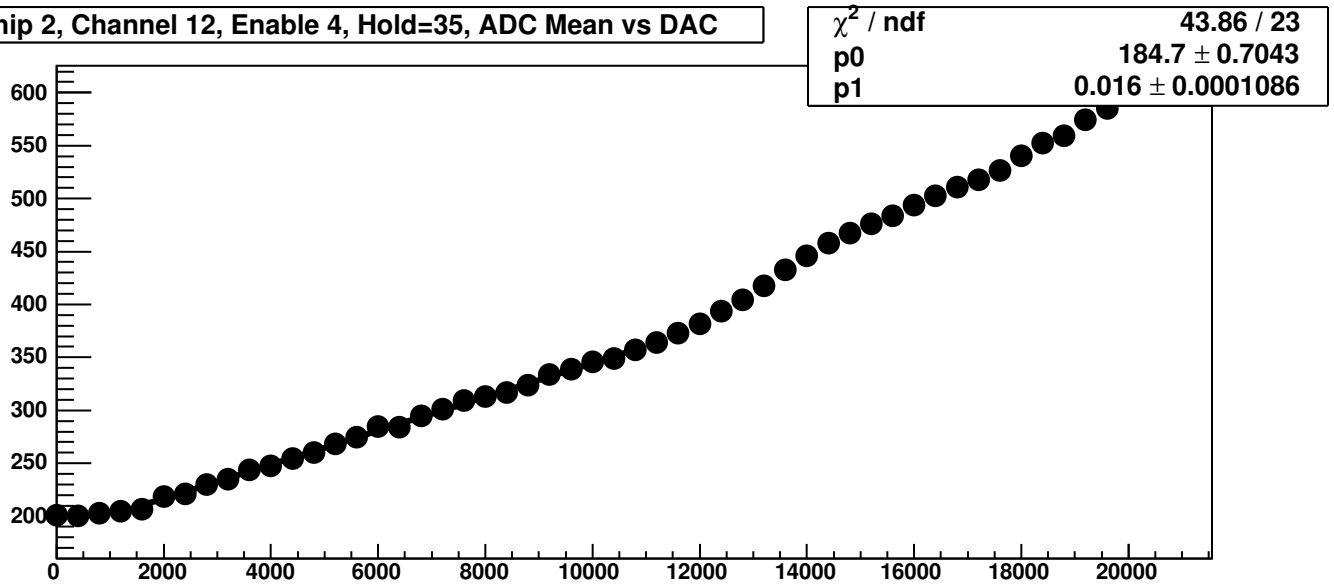
Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



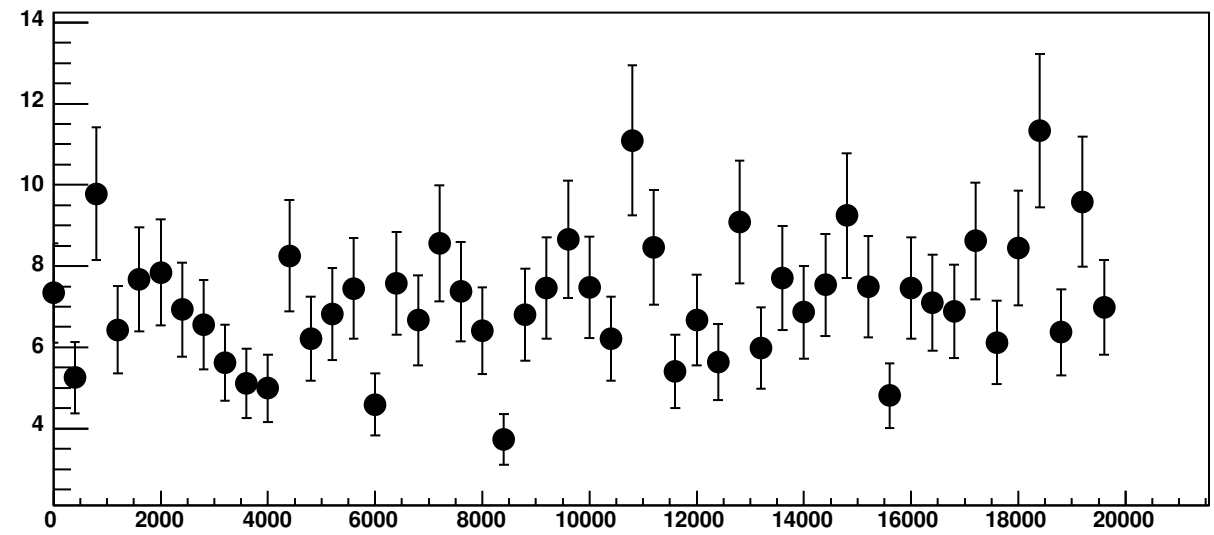
Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



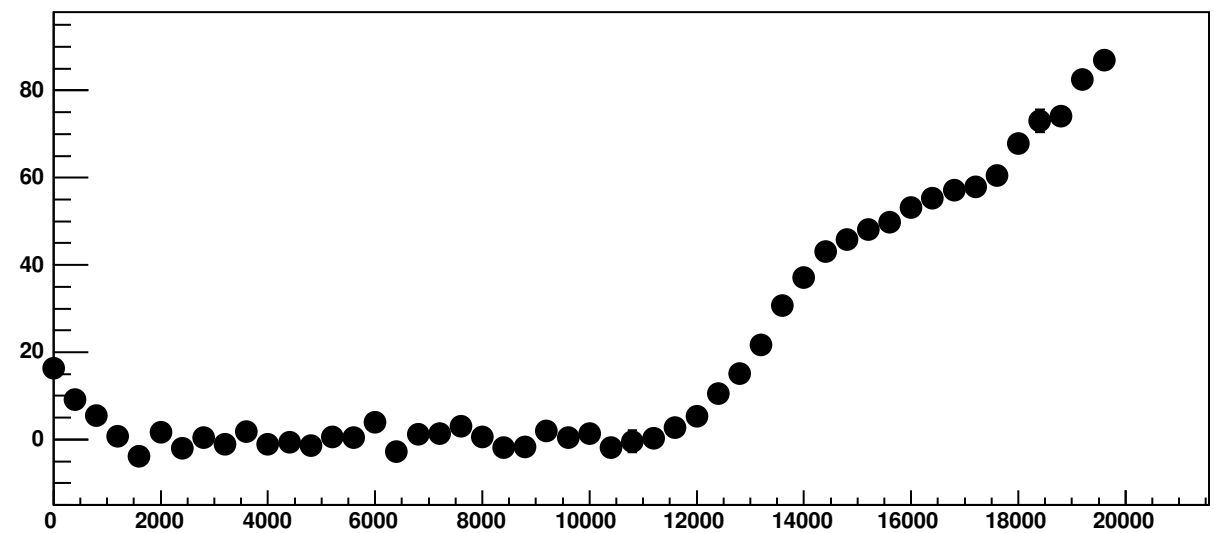
Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

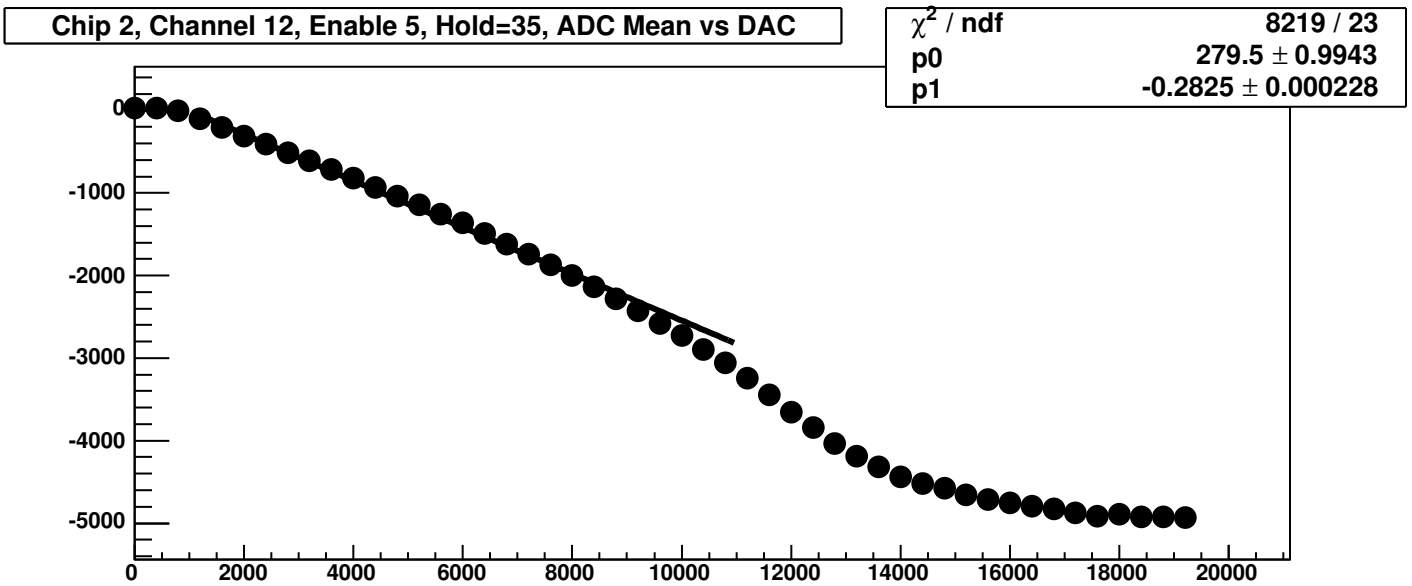


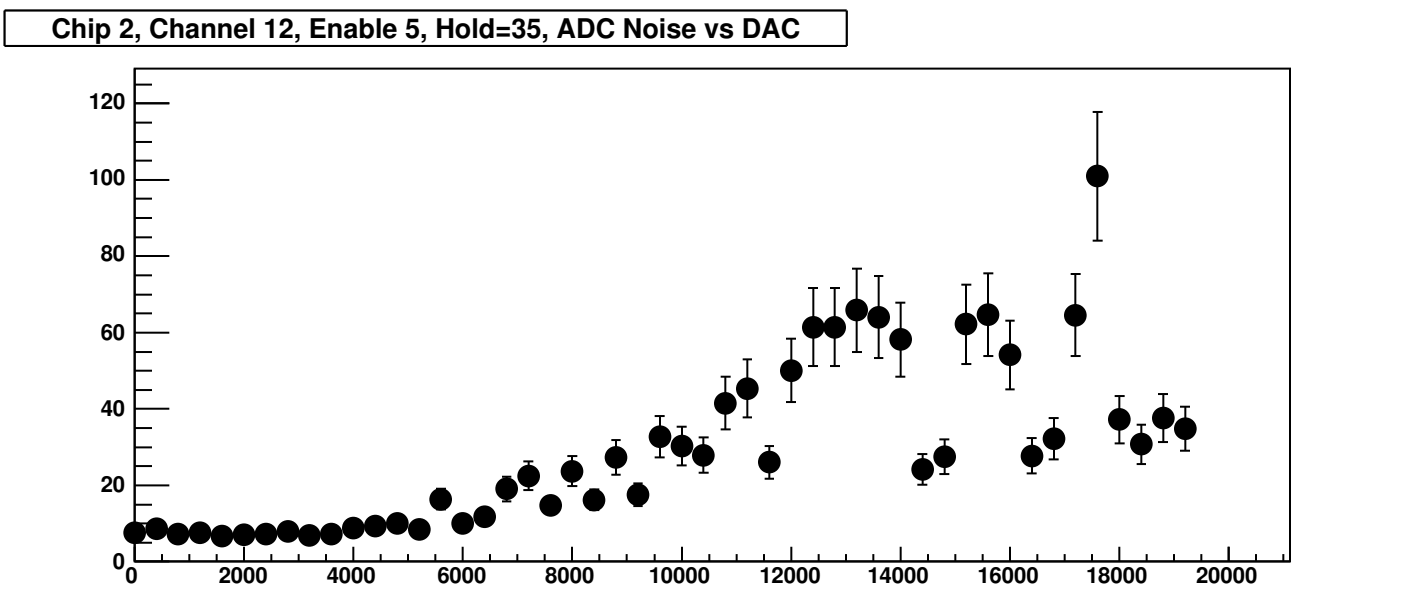
Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC

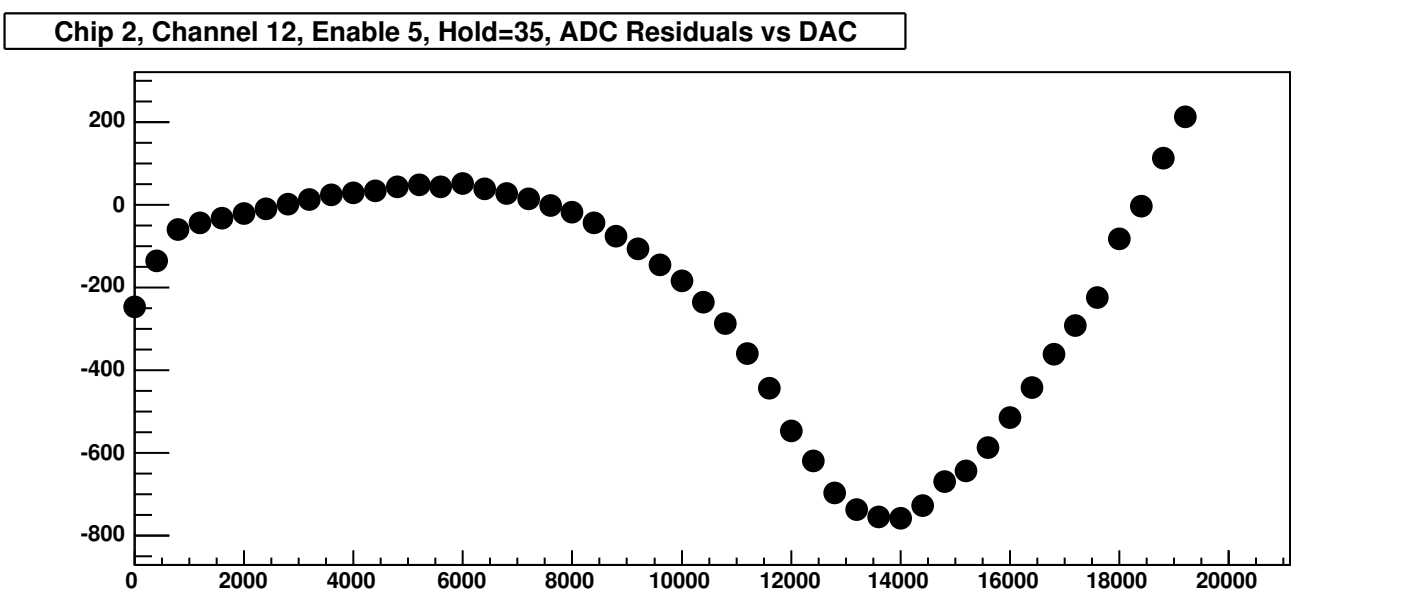


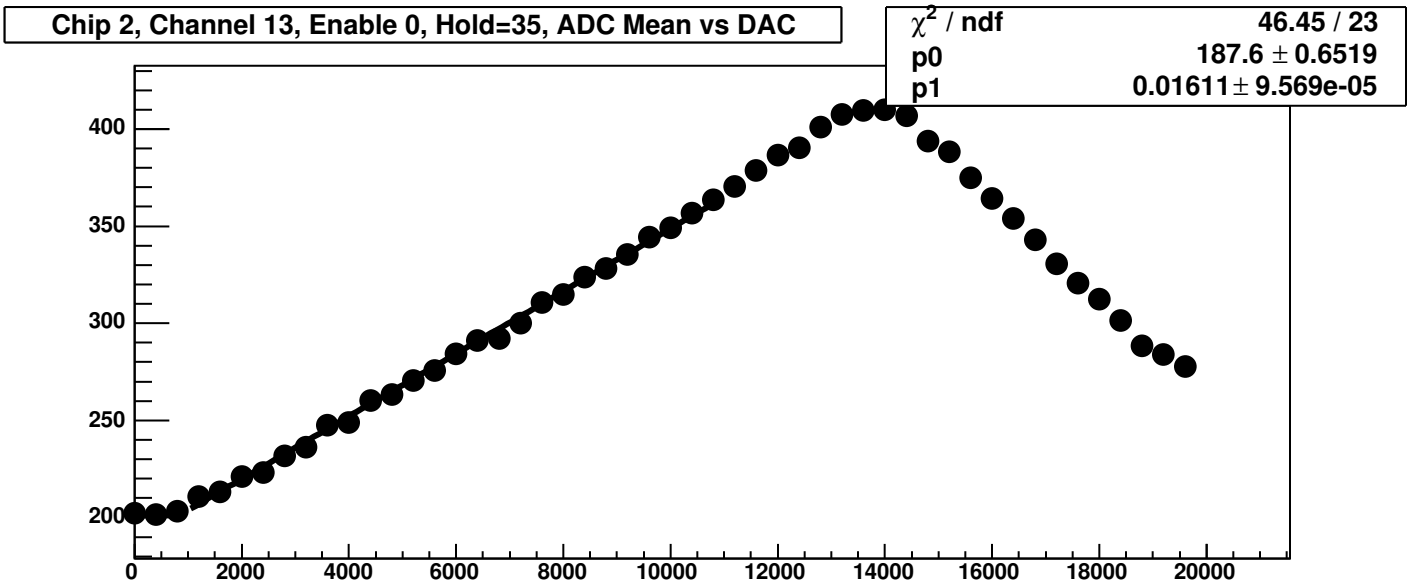
Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

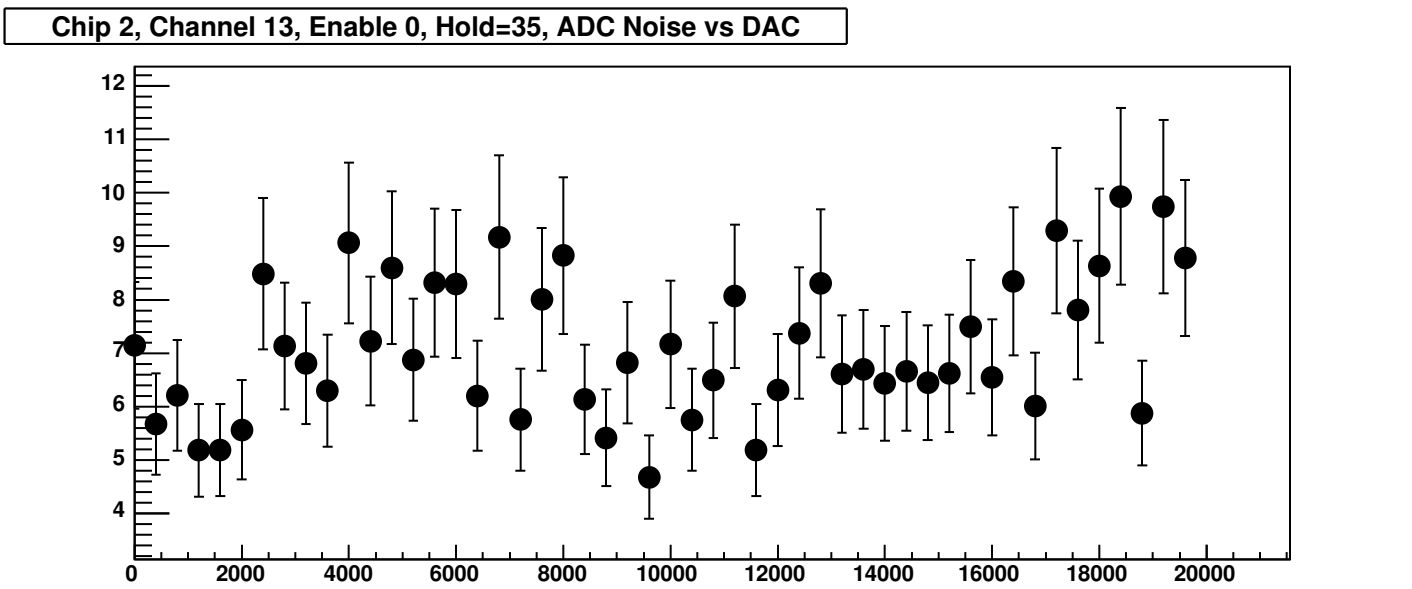


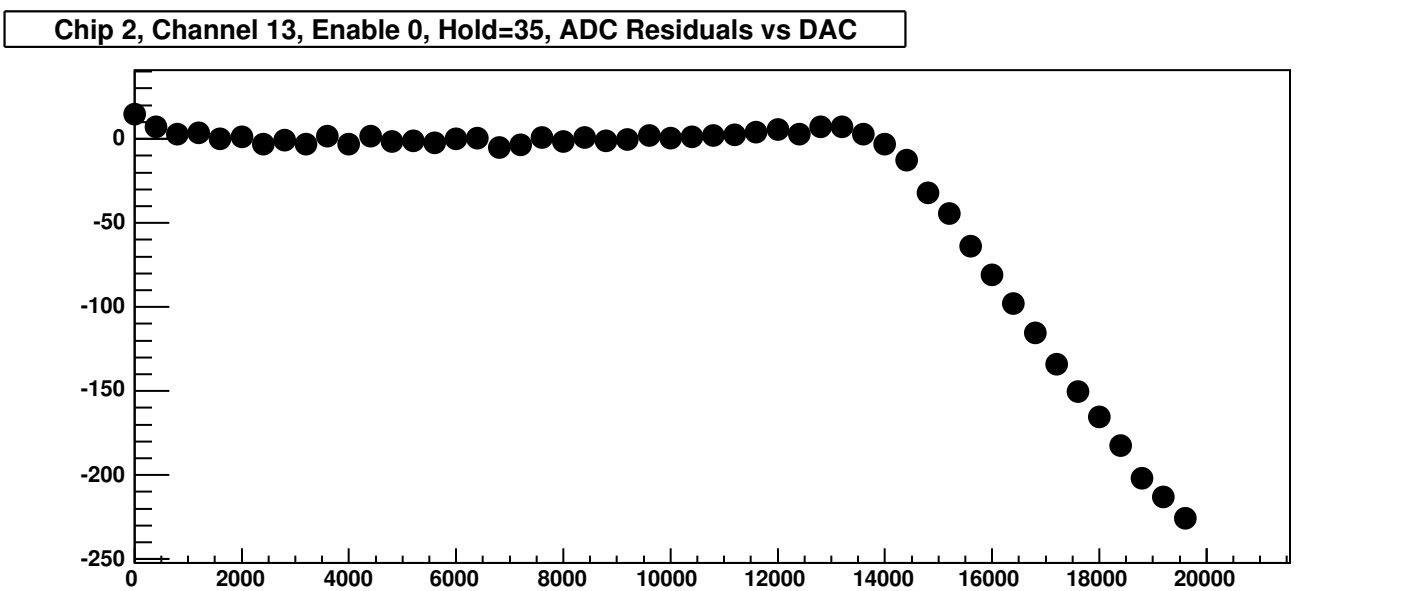

Chip 2, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 8219 / 23
p0 279.5 ± 0.9943
p1 -0.2825 ± 0.000228
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

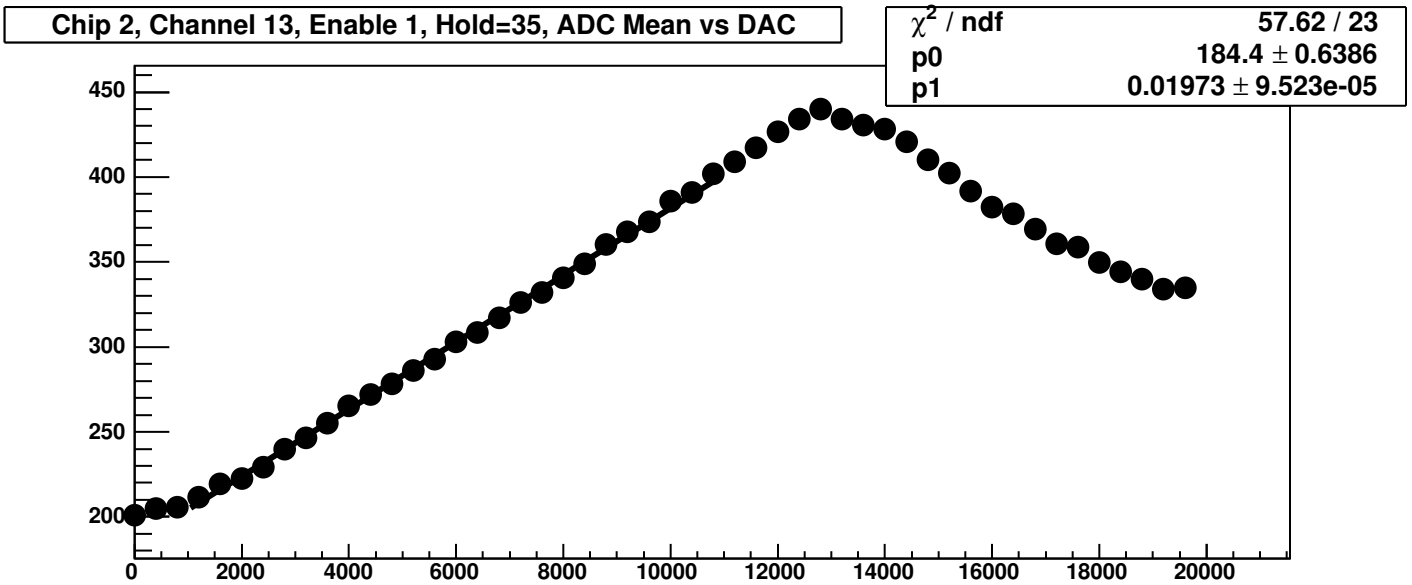

Chip 2, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

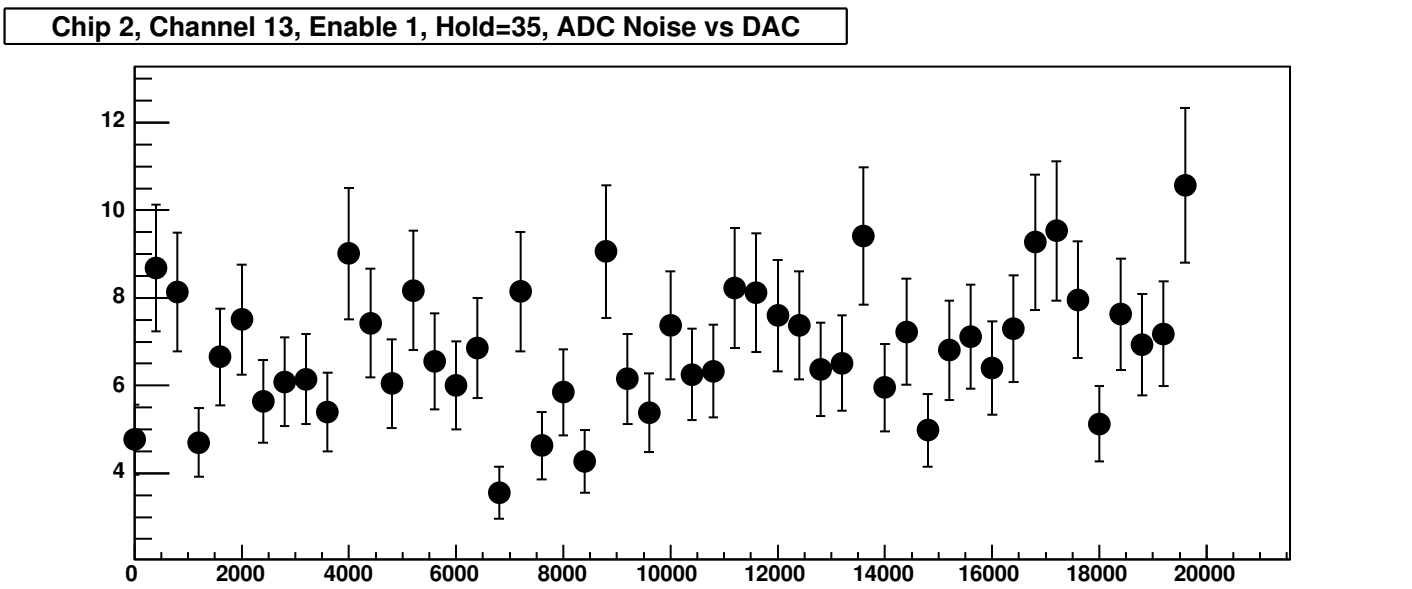

Chip 2, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

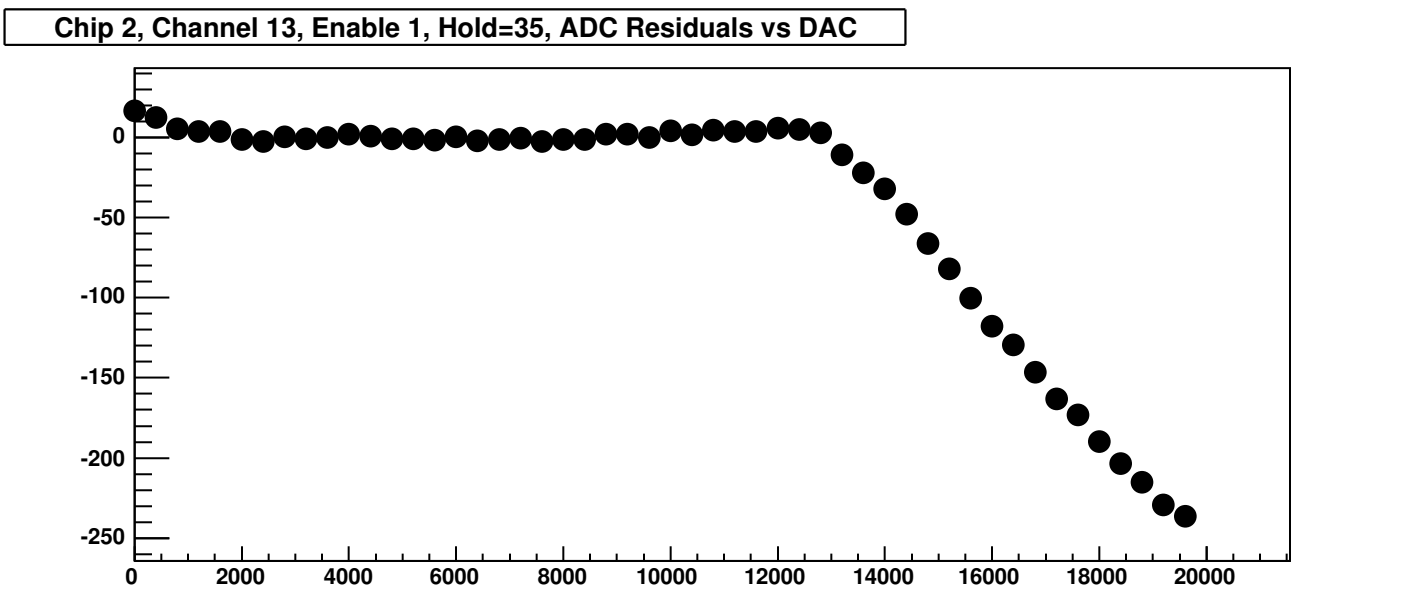

Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 46.45 / 23
p0 187.6 ± 0.6519
p1 0.01611 ± 9.569e-05
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

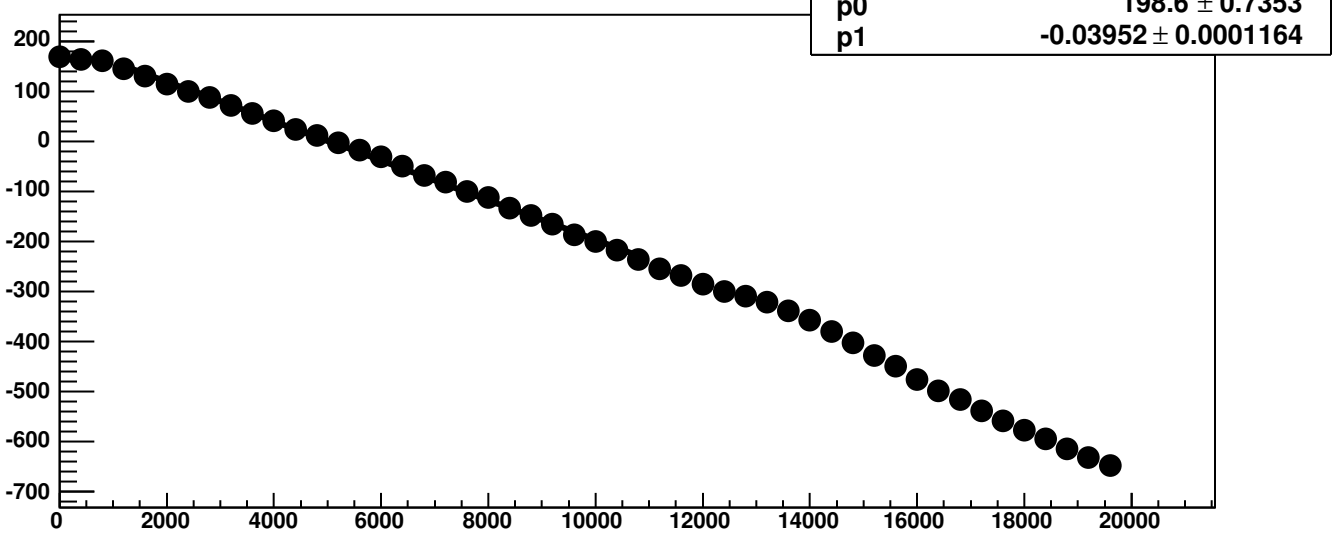

Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 57.62 / 23
p0 184.4 ± 0.6386
p1 0.01973 ± 9.523e-05
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

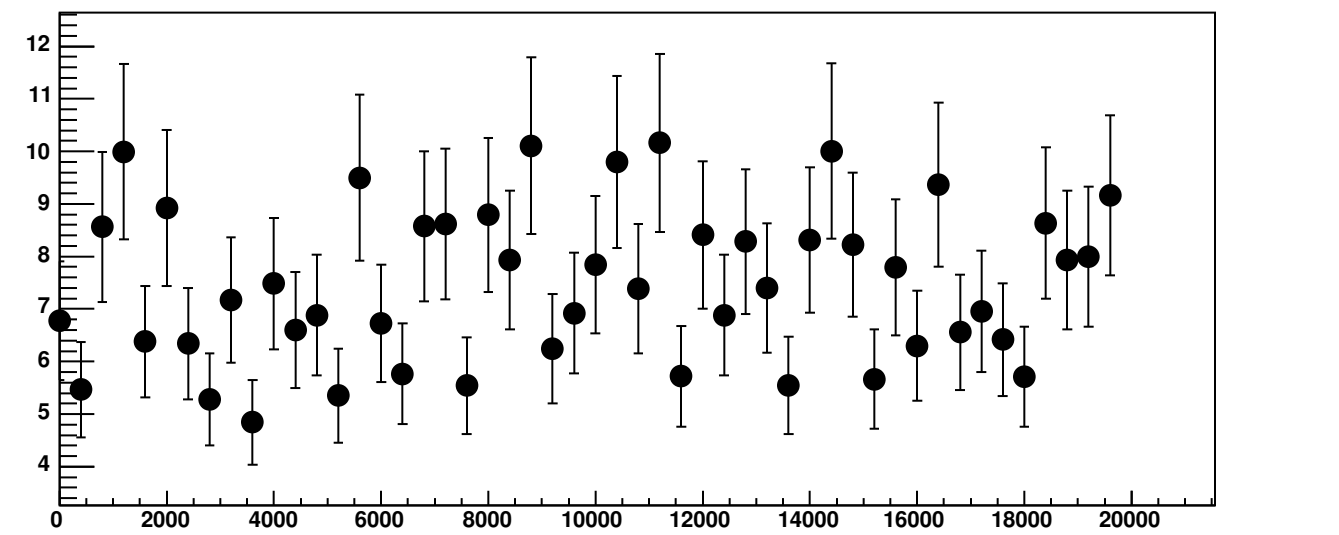

Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

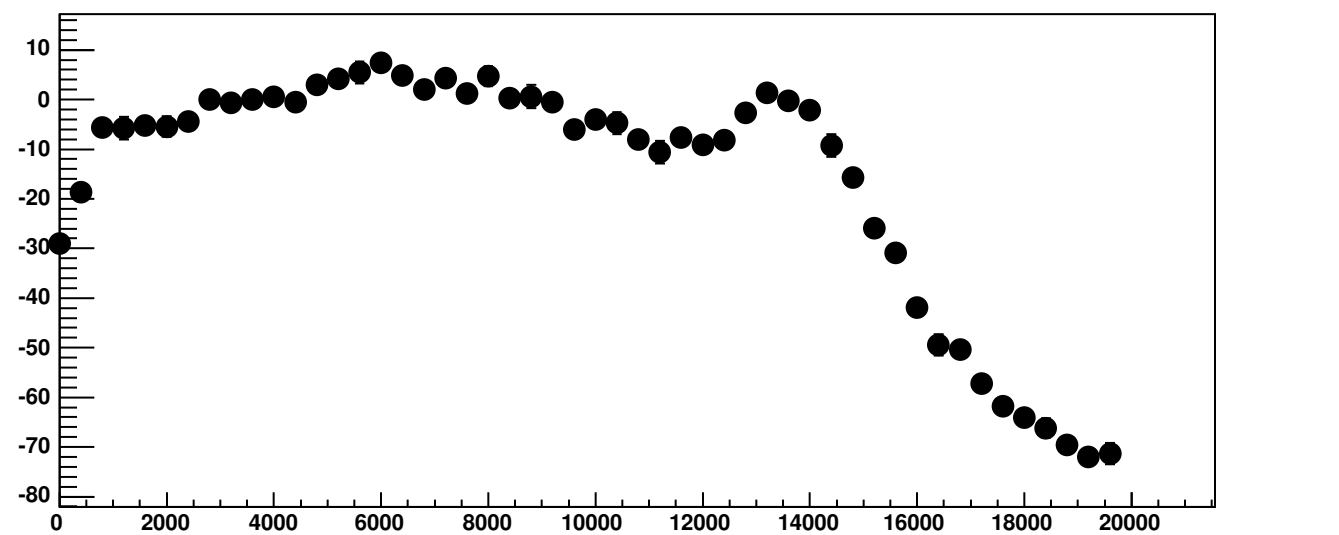
Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

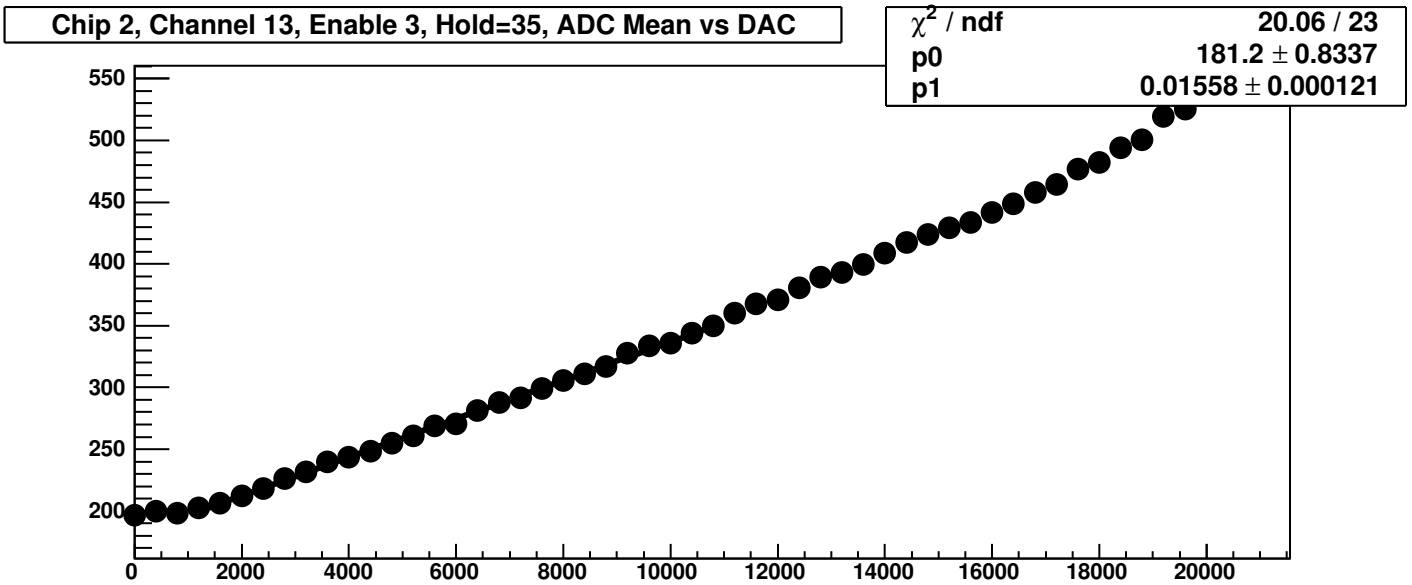


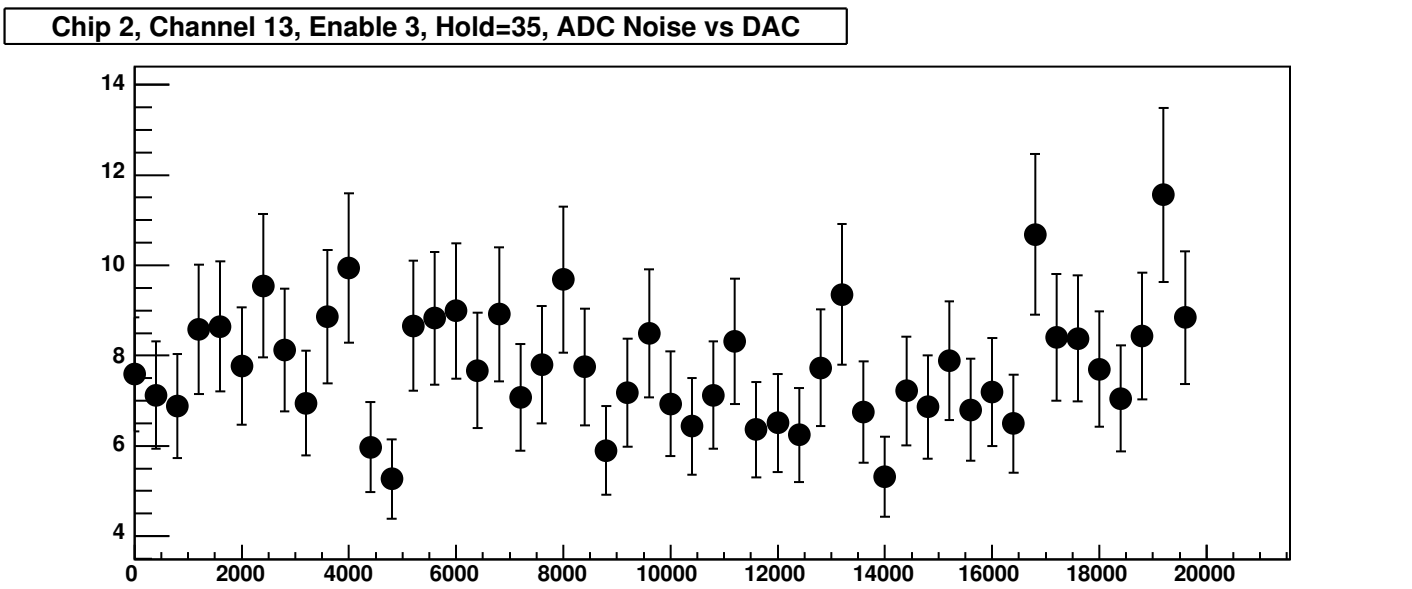
Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC

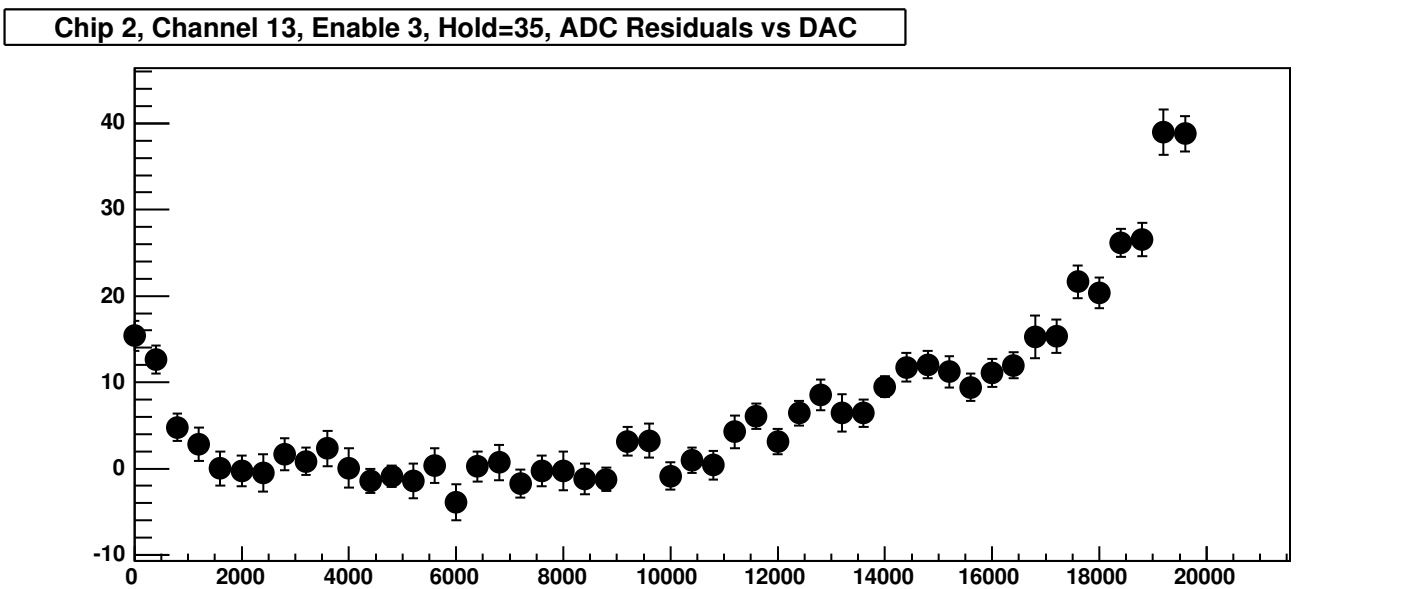


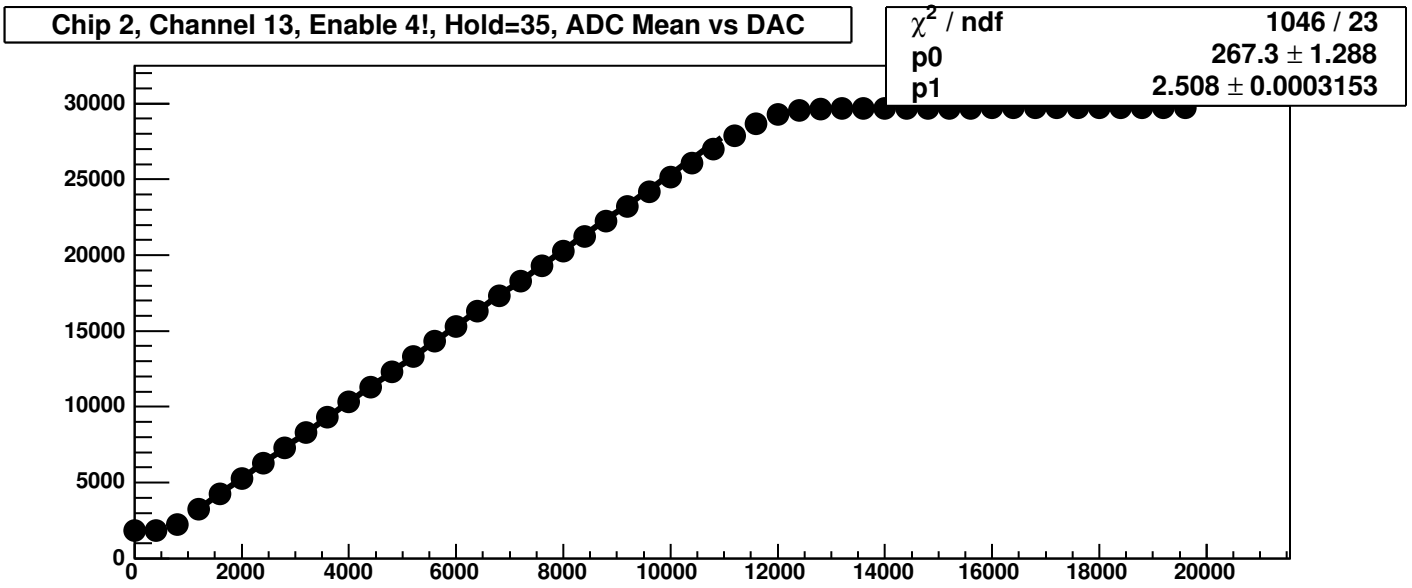
Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

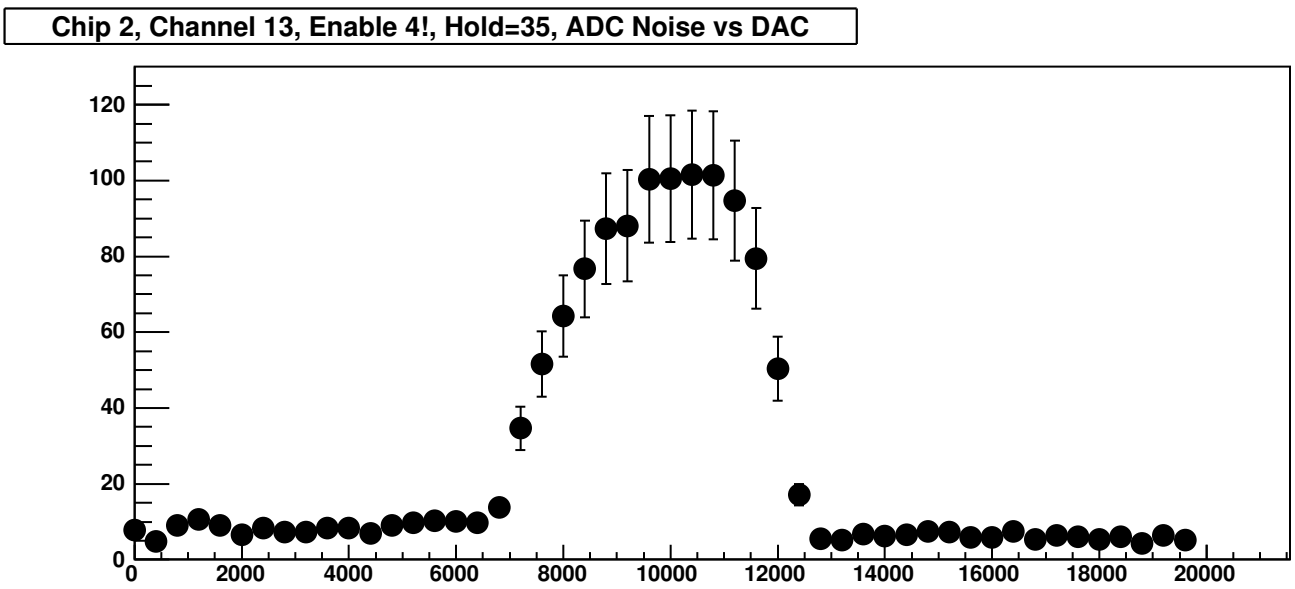


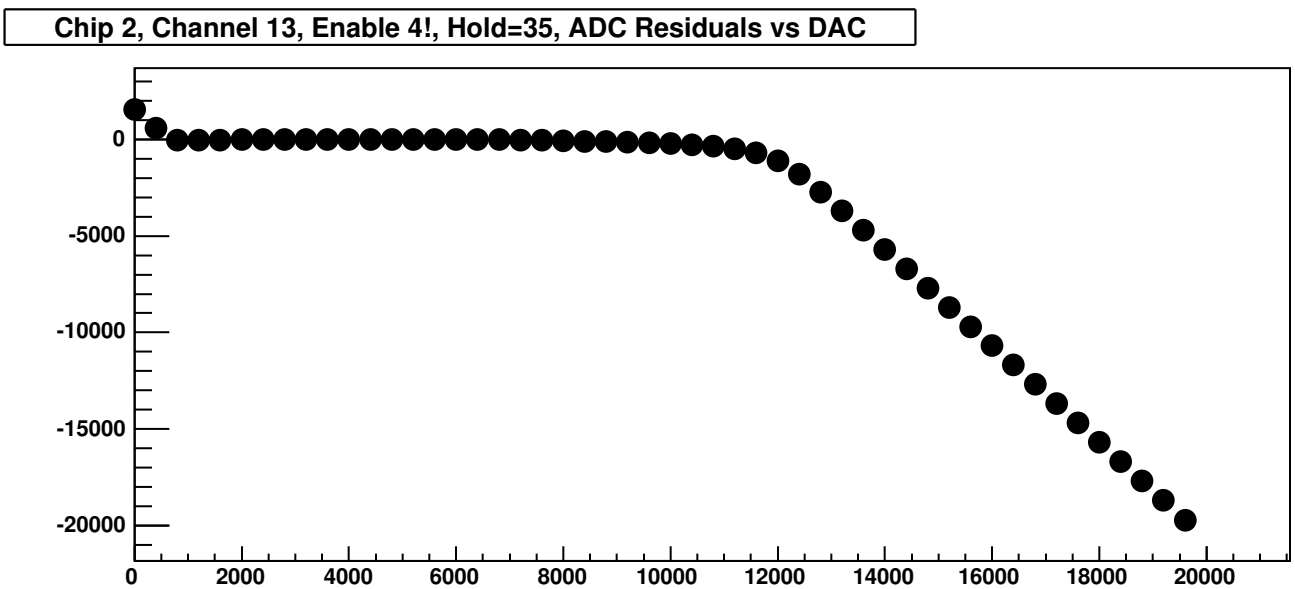

Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.06 / 23
p0 181.2 ± 0.8337
p1 0.01558 ± 0.000121
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

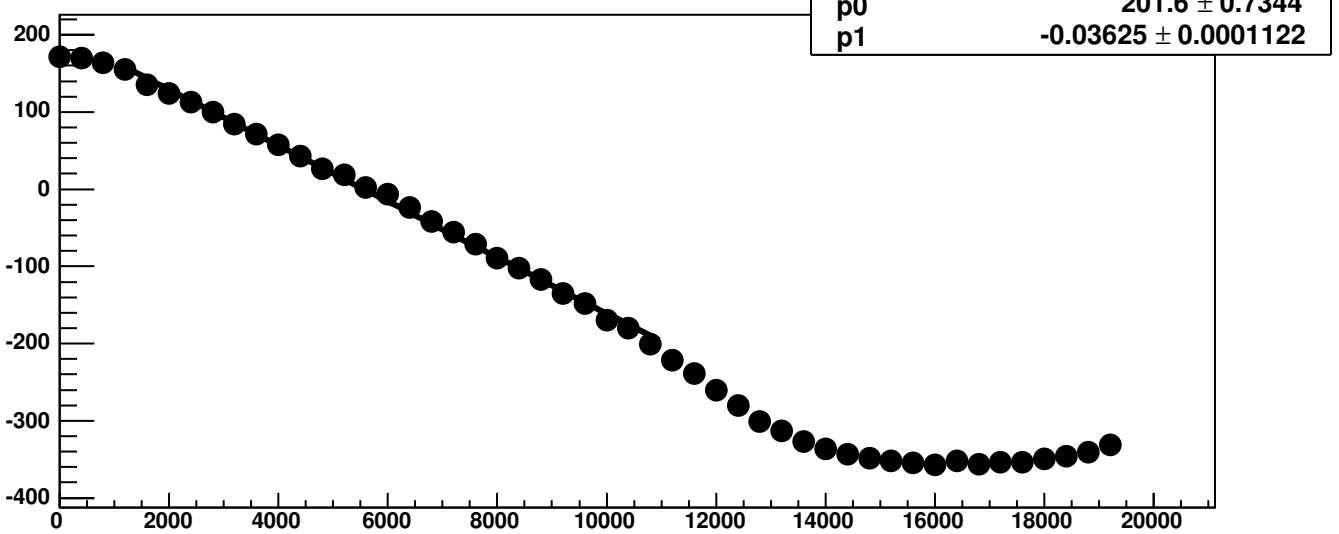

Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1046 / 23
p0 267.3 ± 1.288
p1 2.508 ± 0.0003153
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

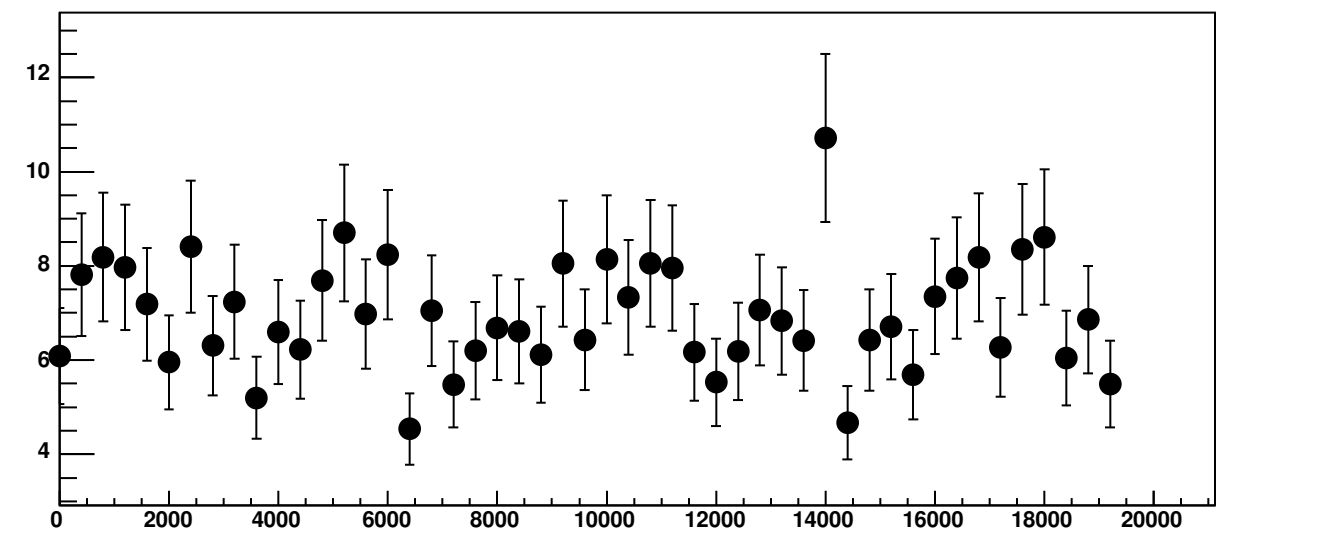

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

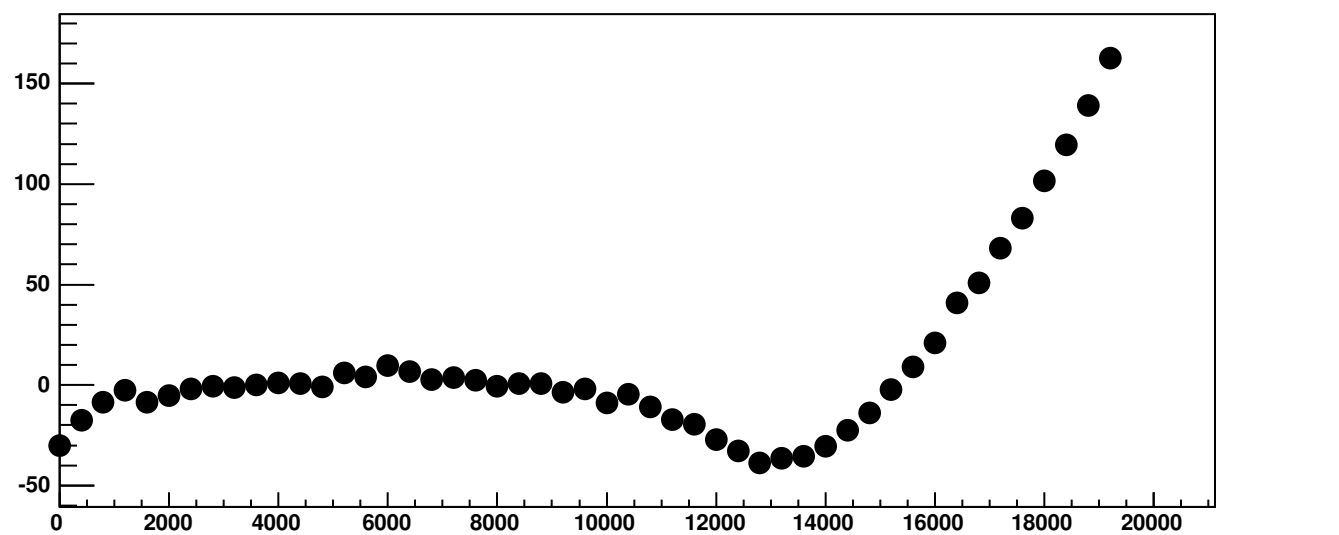
Chip 2, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

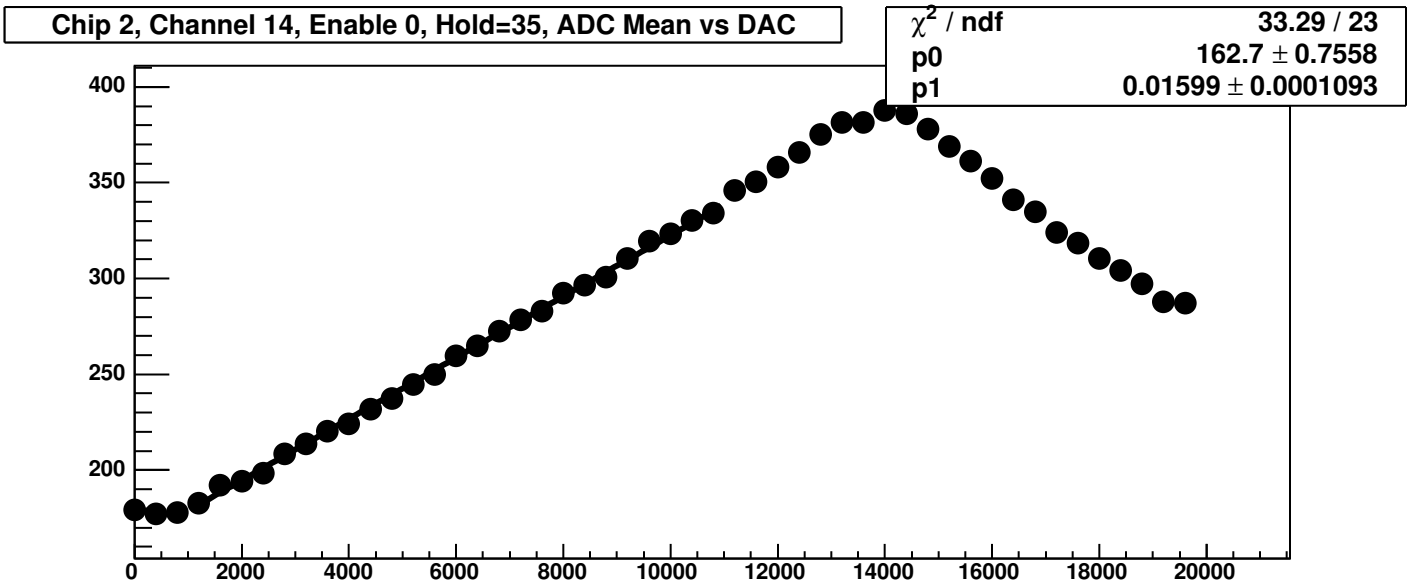


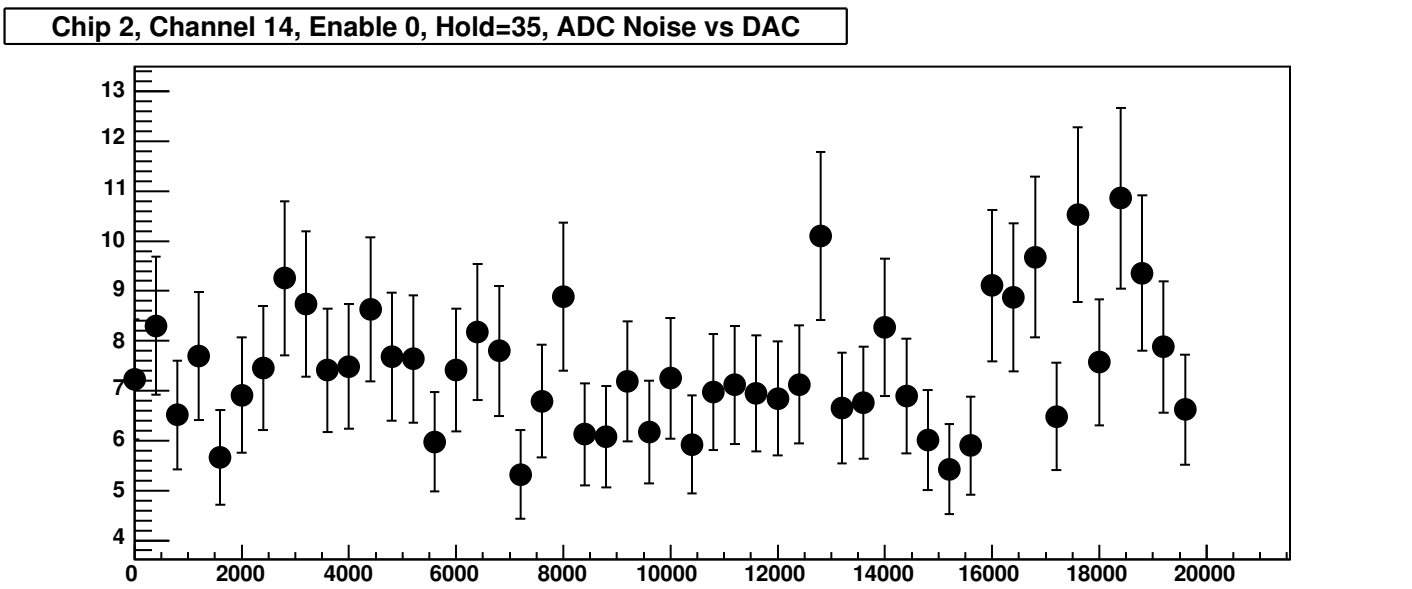
Chip 2, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

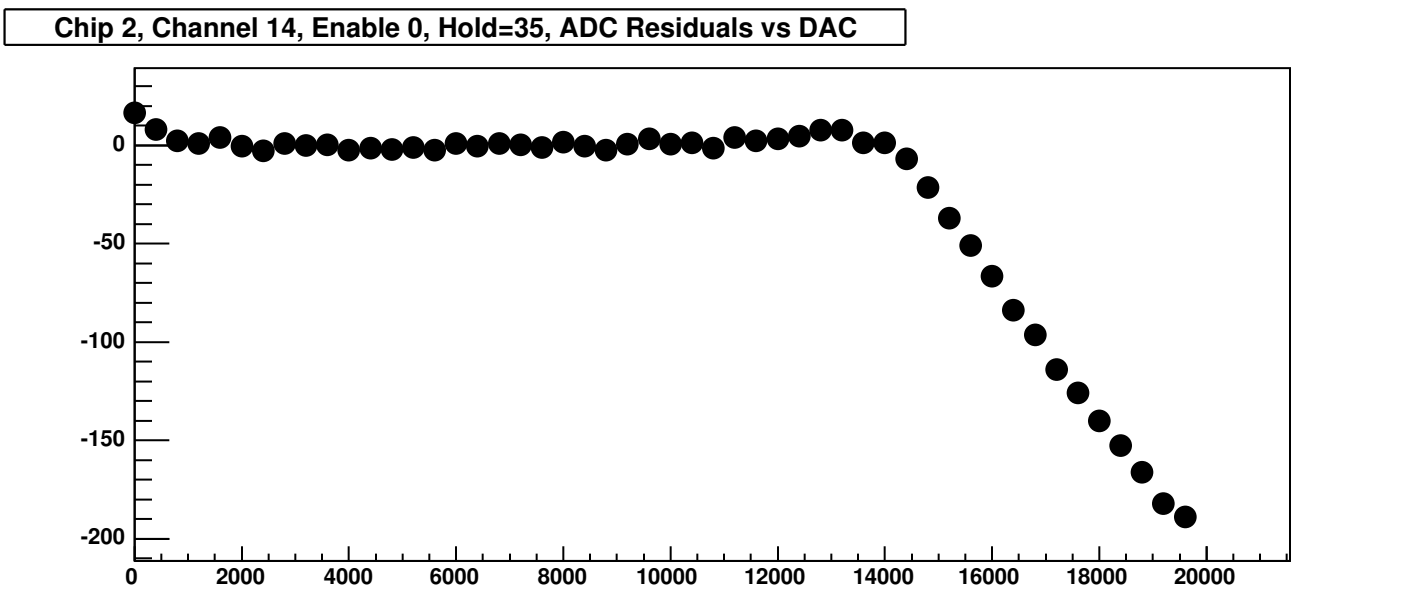


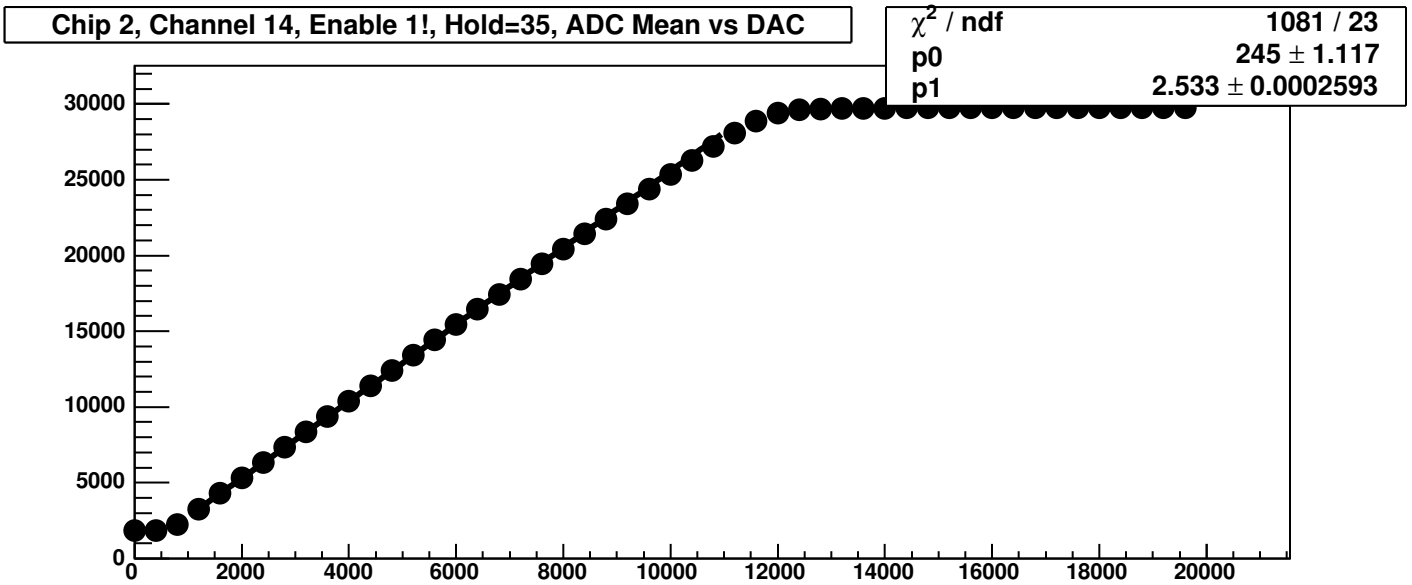
Chip 2, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

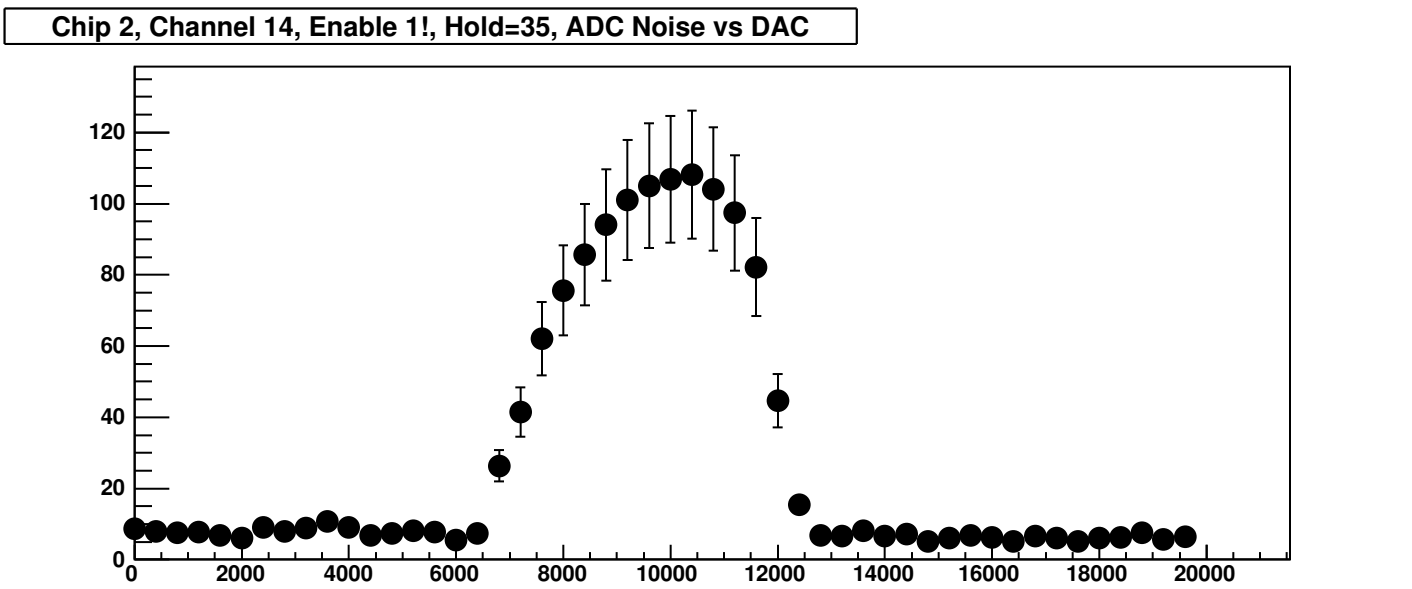


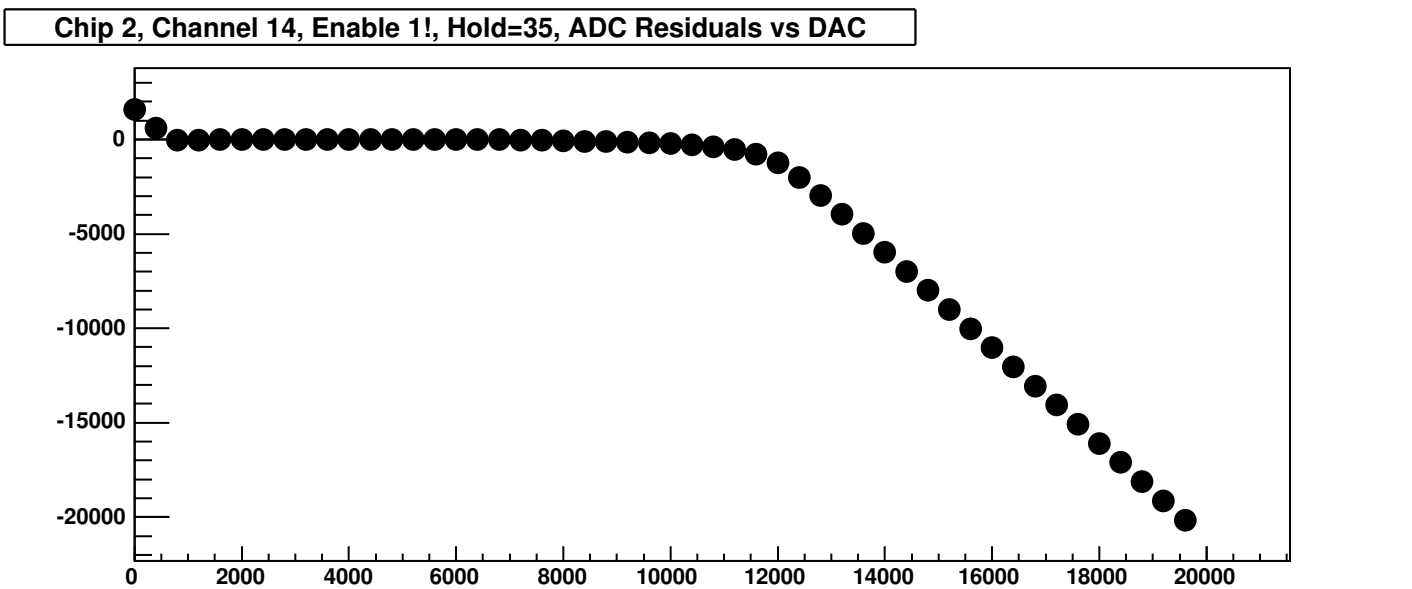

Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 33.29 / 23
p0 162.7 ± 0.7558
p1 0.01599 ± 0.0001093
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

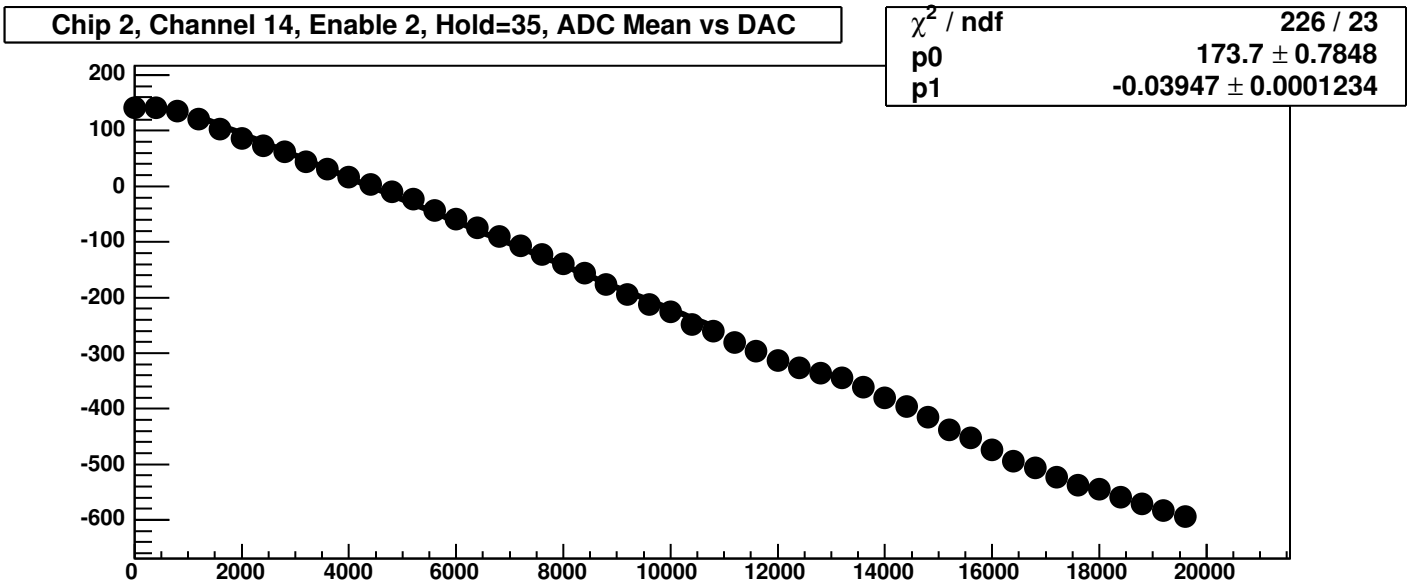

Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

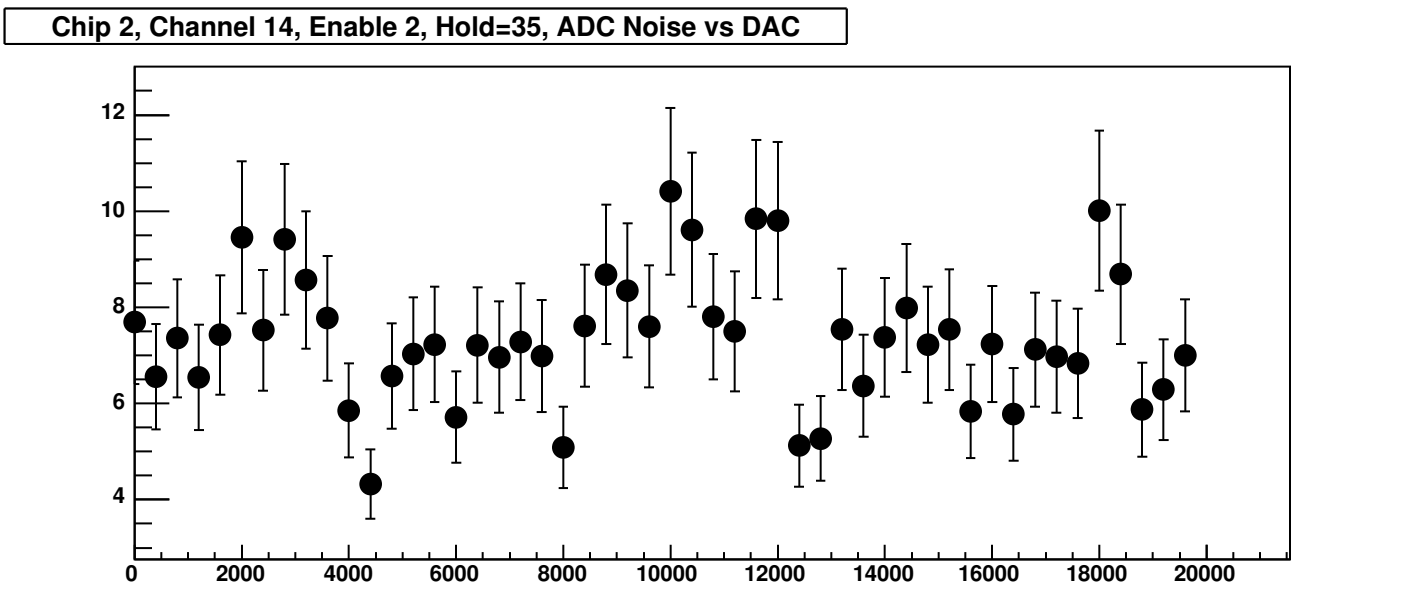

Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

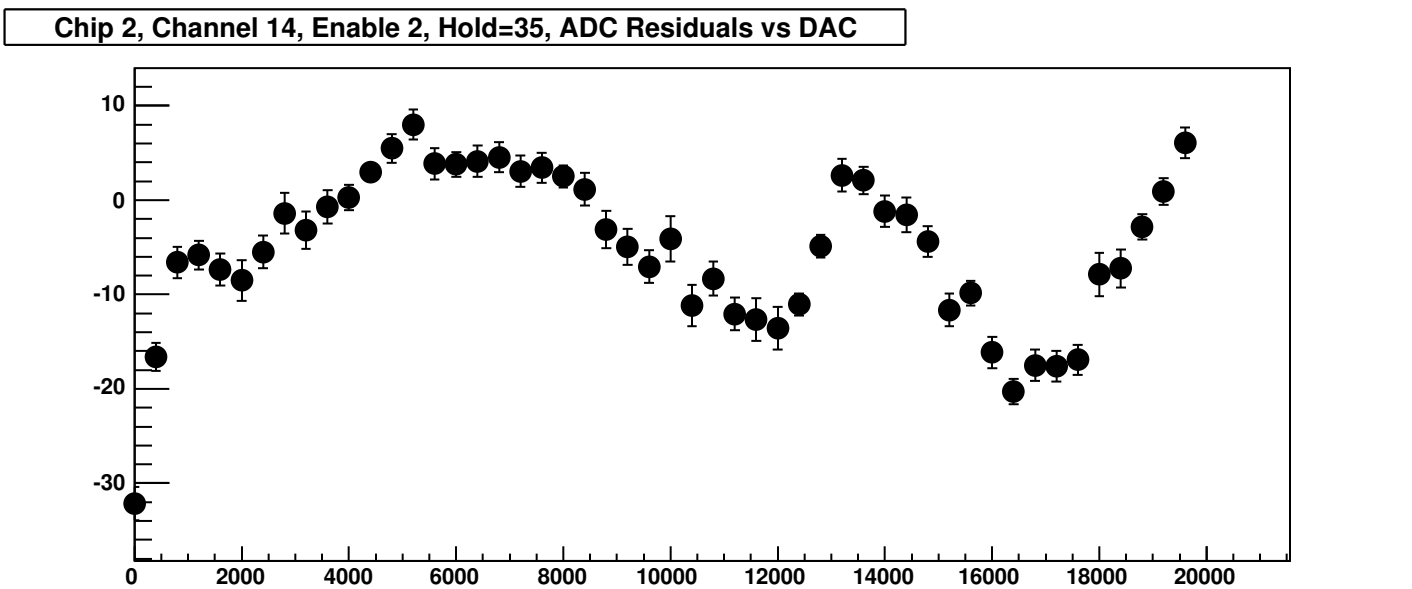

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1081 / 23
p0 245 ± 1.117
p1 2.533 ± 0.0002593
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

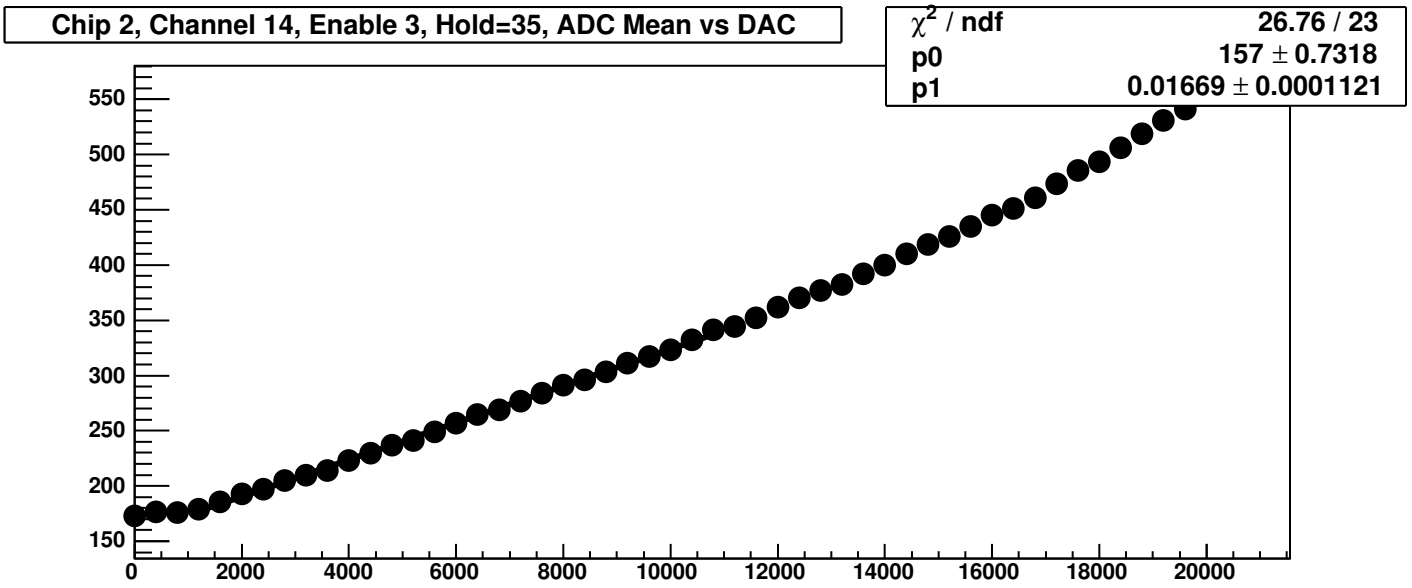

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

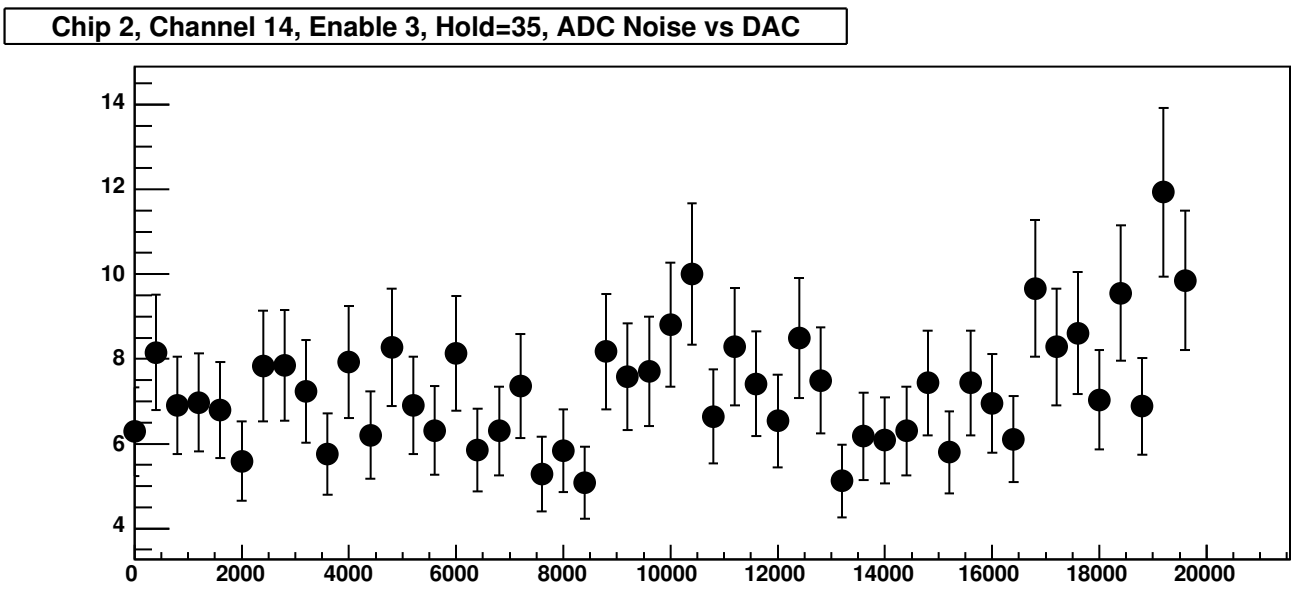

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

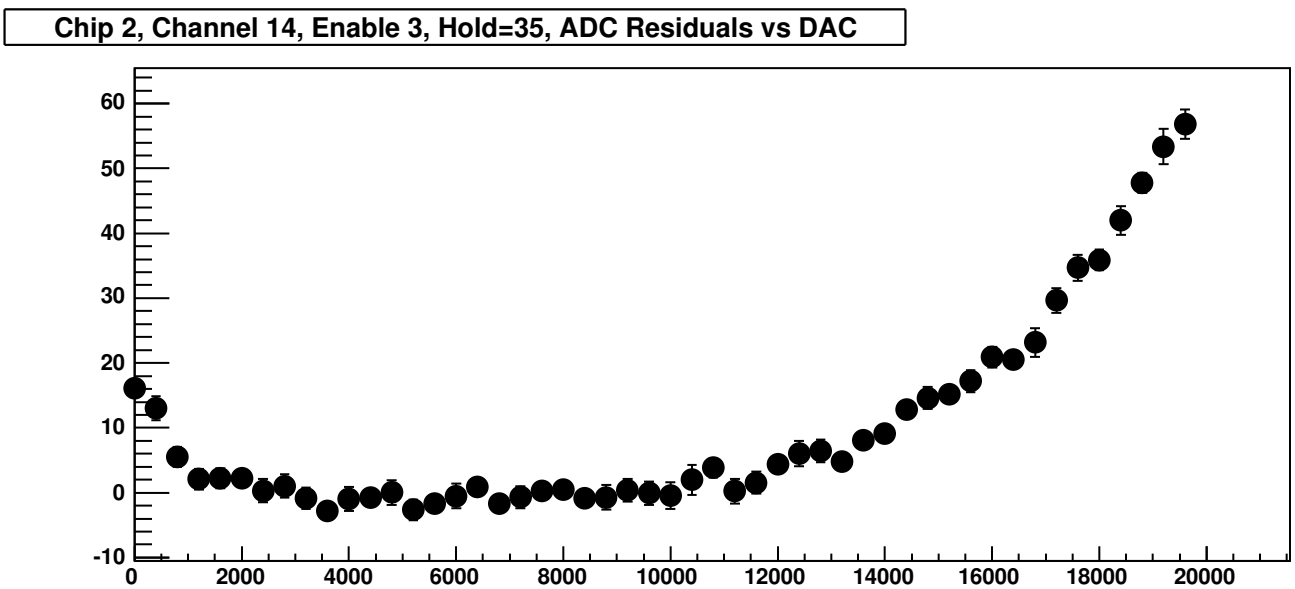

Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 226 / 23
p0 173.7 ± 0.7848
p1 -0.03947 ± 0.0001234
200
100
0
-100
-200
-300
-400
-500
-600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

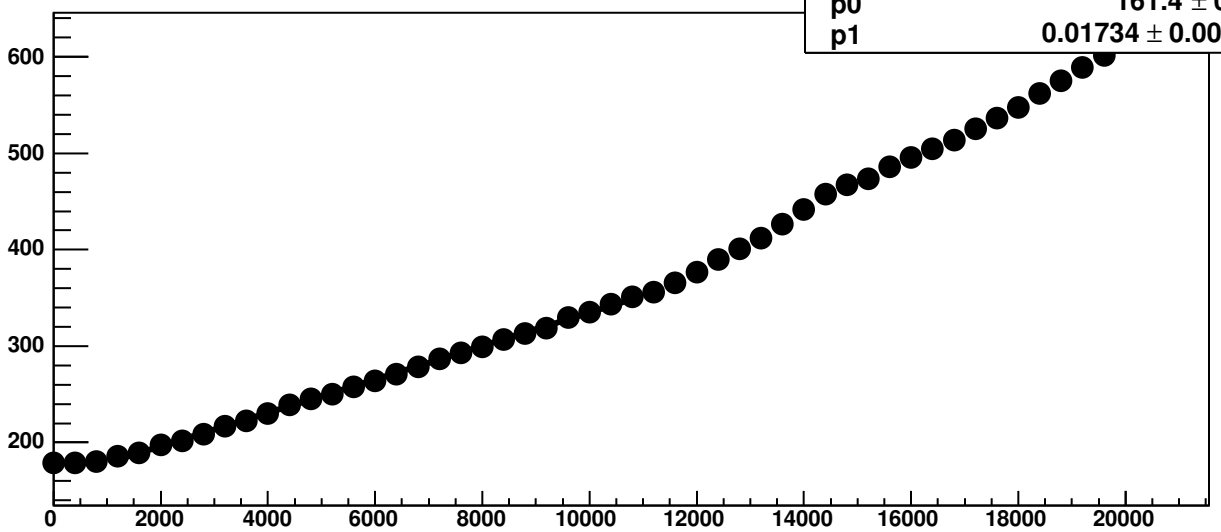

Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.76 / 23
p0 157 ± 0.7318
p1 0.01669 ± 0.0001121
550
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

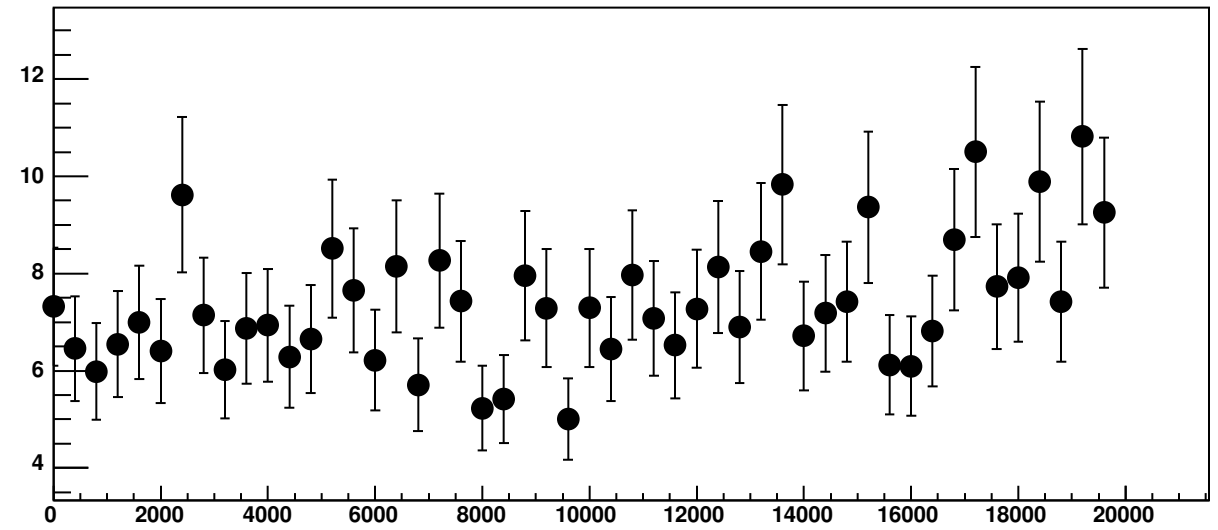

Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

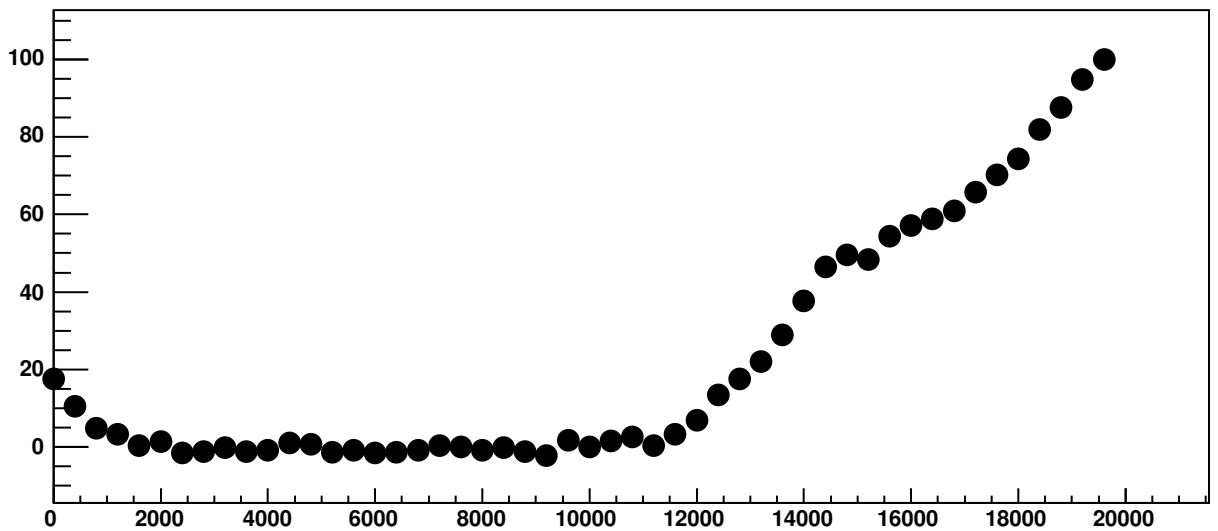
χ^2 / ndf	19.27 / 23
p0	161.4 $\pm$ 0.7273
p1	0.01734 $\pm$ 0.0001072

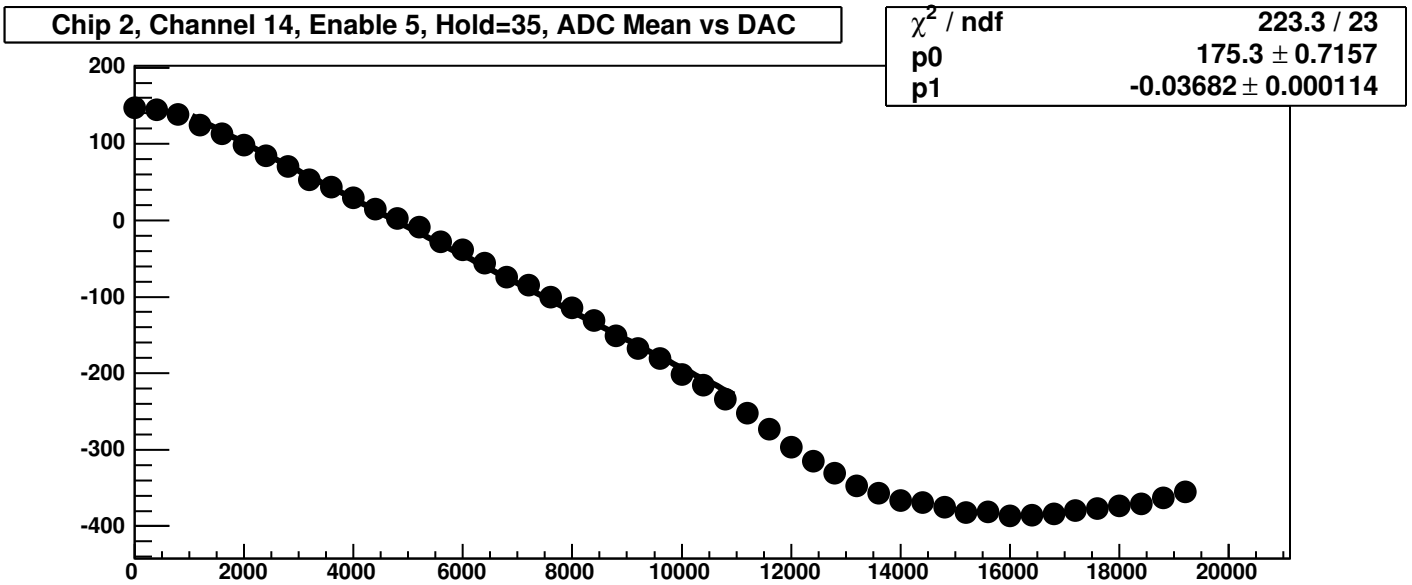


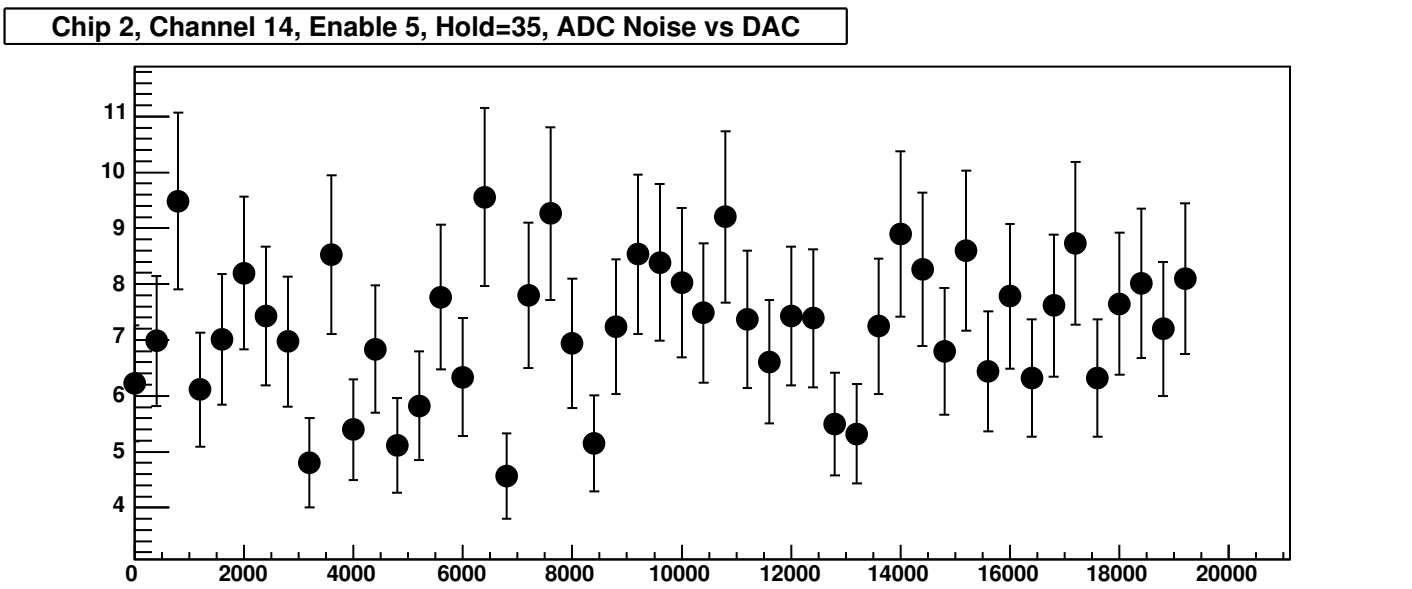
Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC

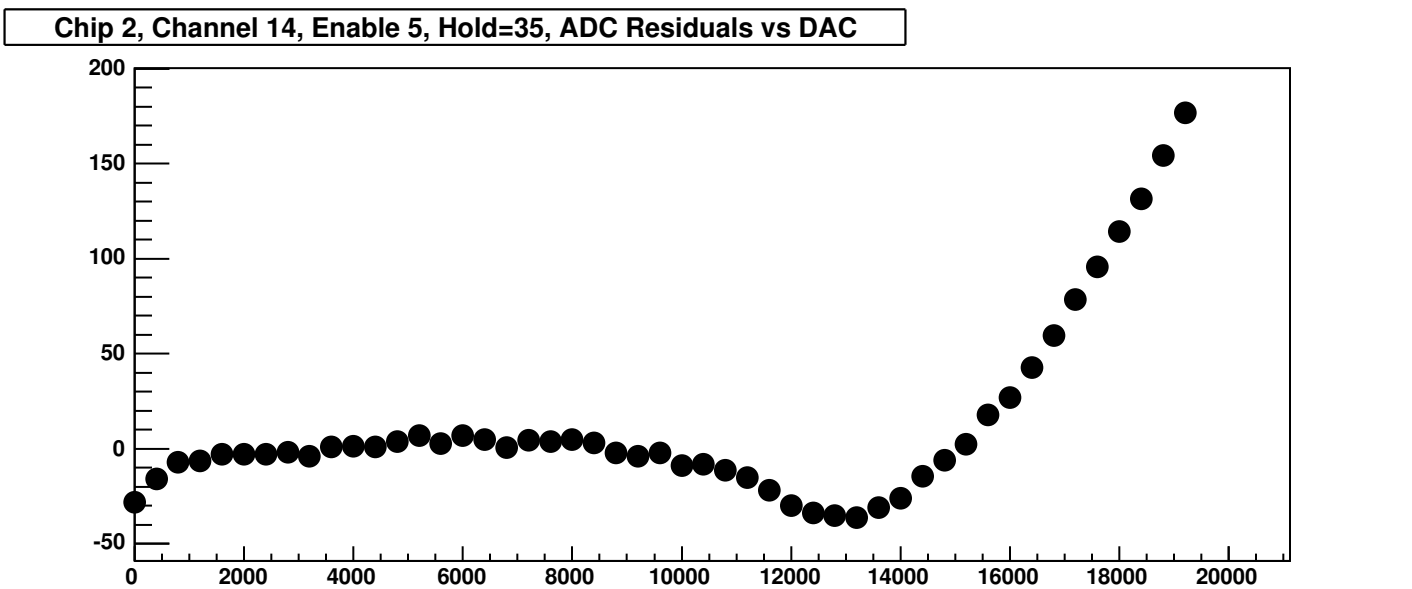


Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

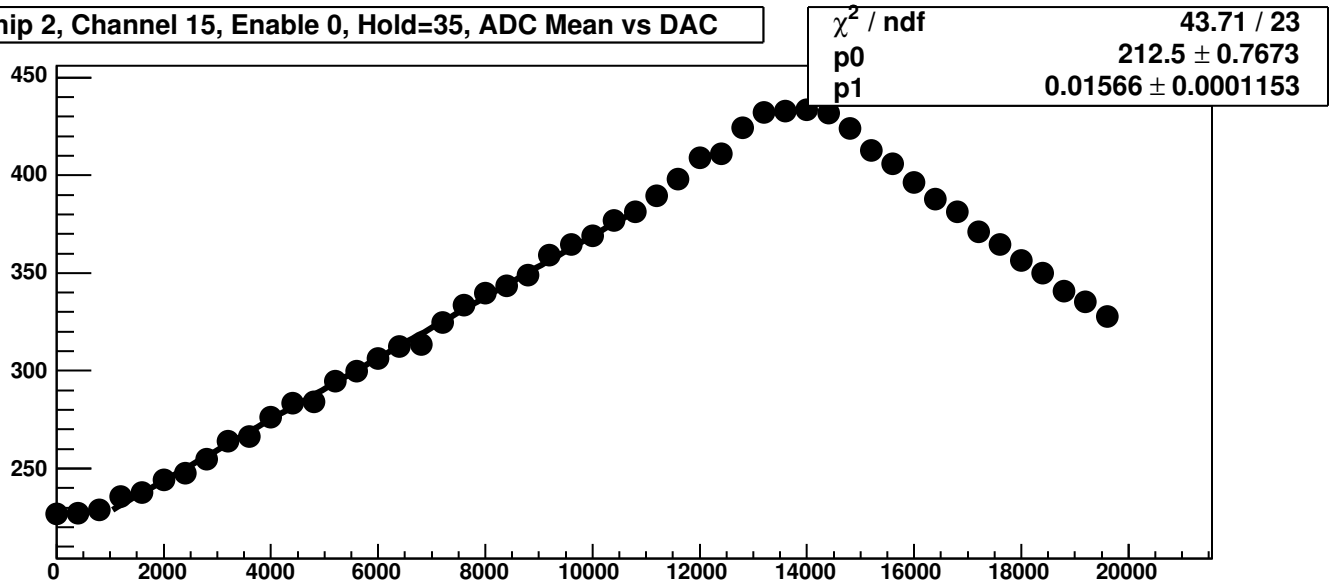



Chip 2, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 223.3 / 23
p0 175.3 ± 0.7157
p1 -0.03682 ± 0.000114
200
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

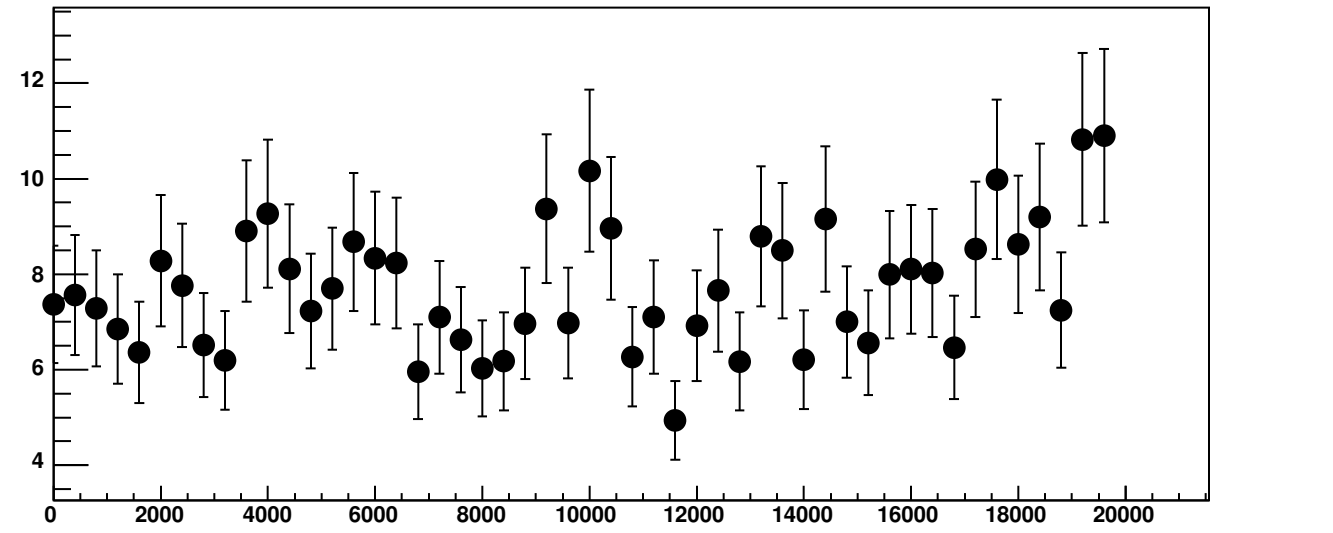

Chip 2, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

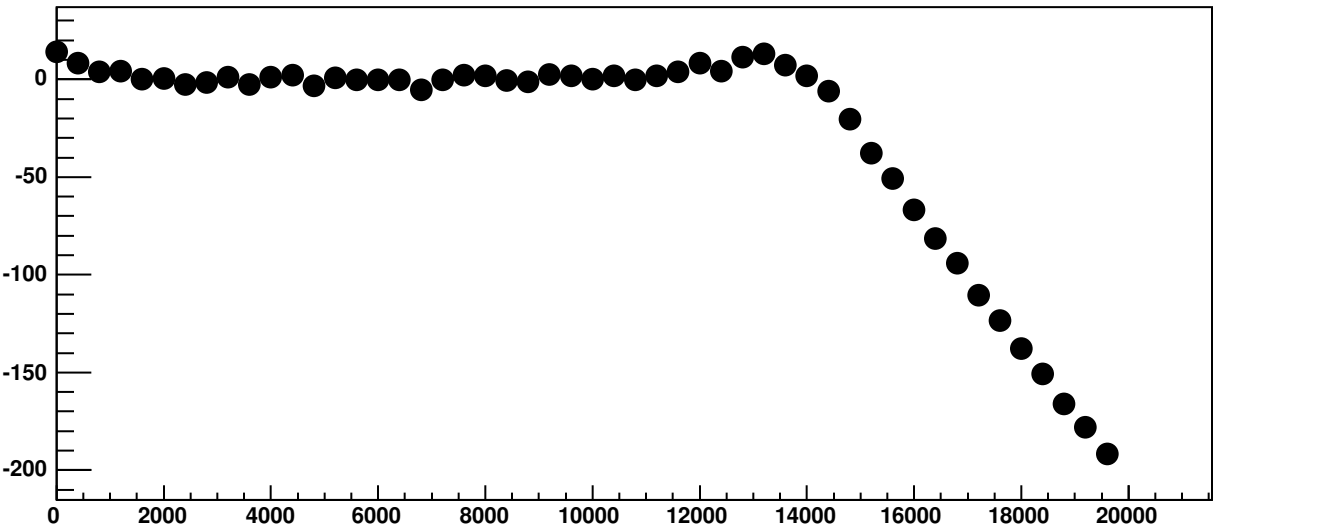
Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

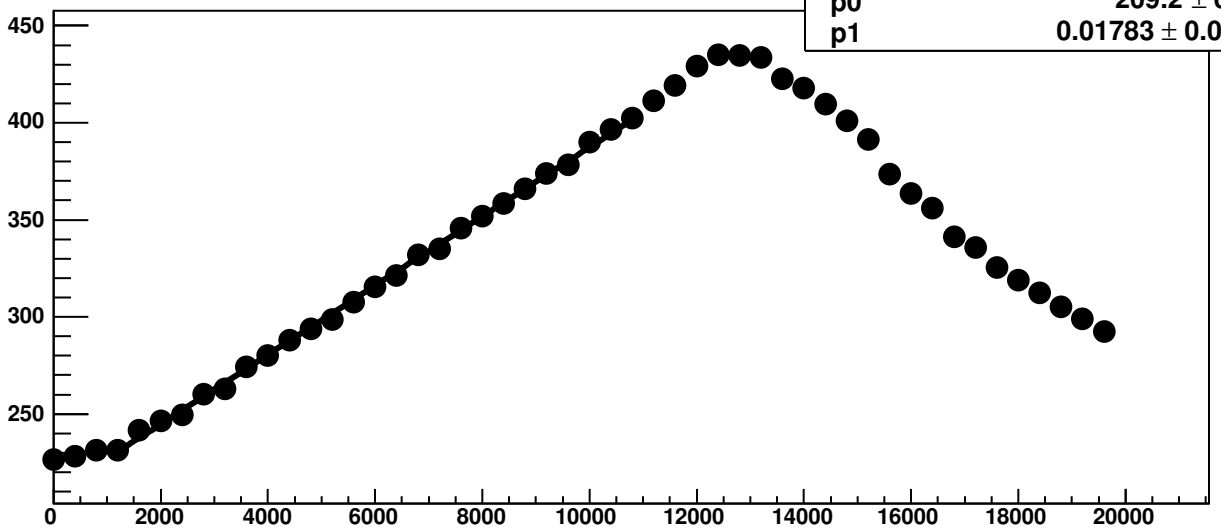


Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

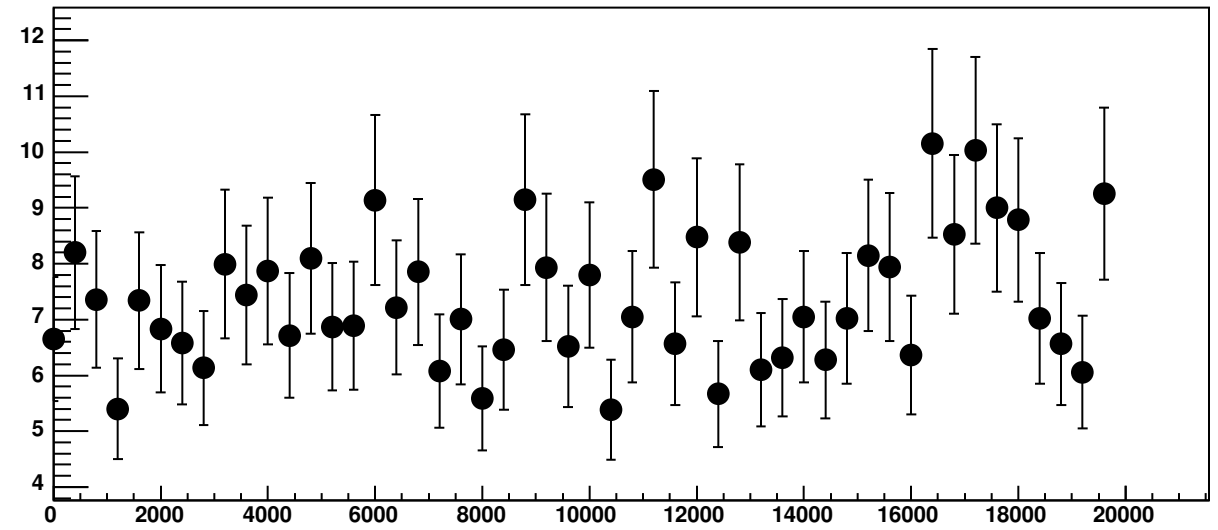


Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

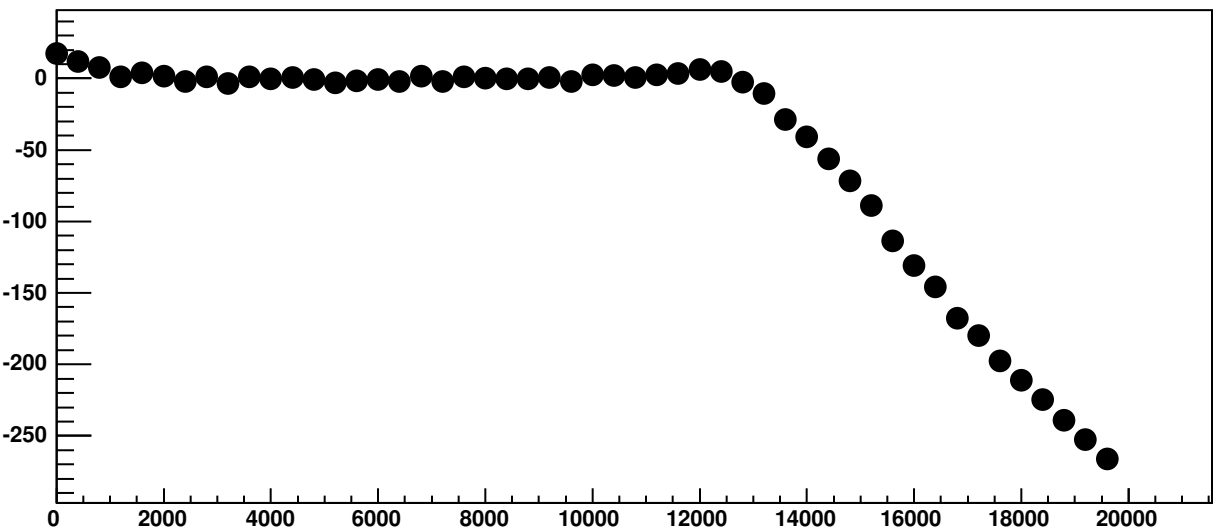
χ^2 / ndf	31.36 / 23
p0	209.2 ± 0.7032
p1	0.01783 ± 0.000105

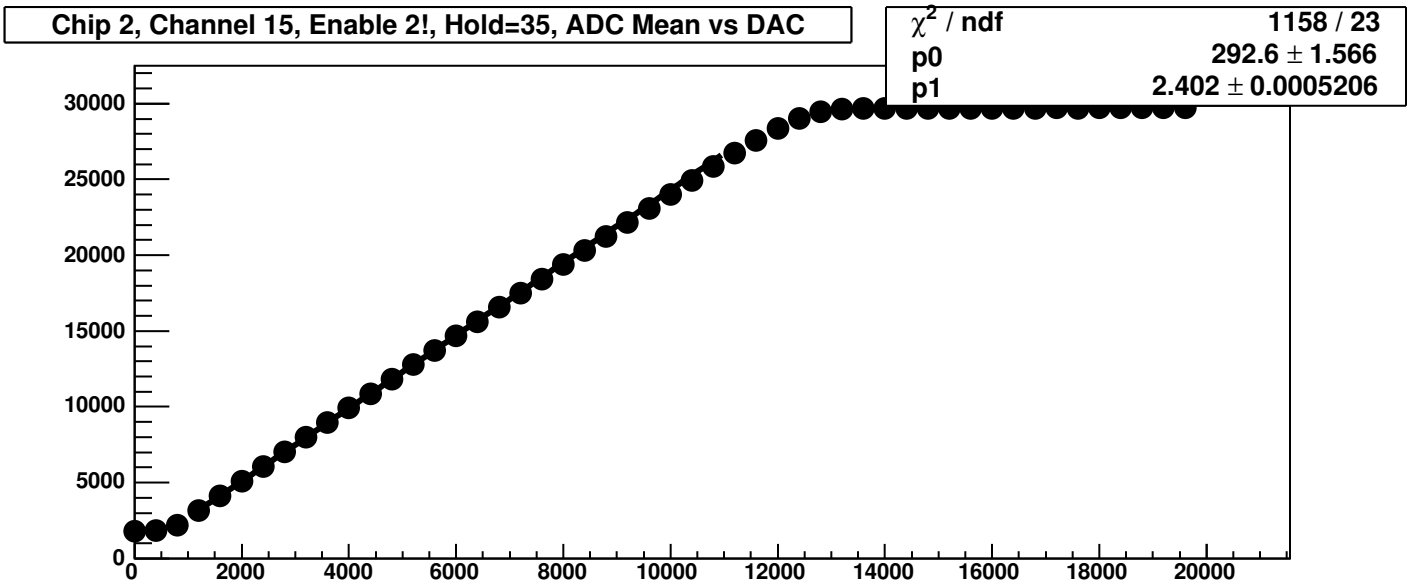


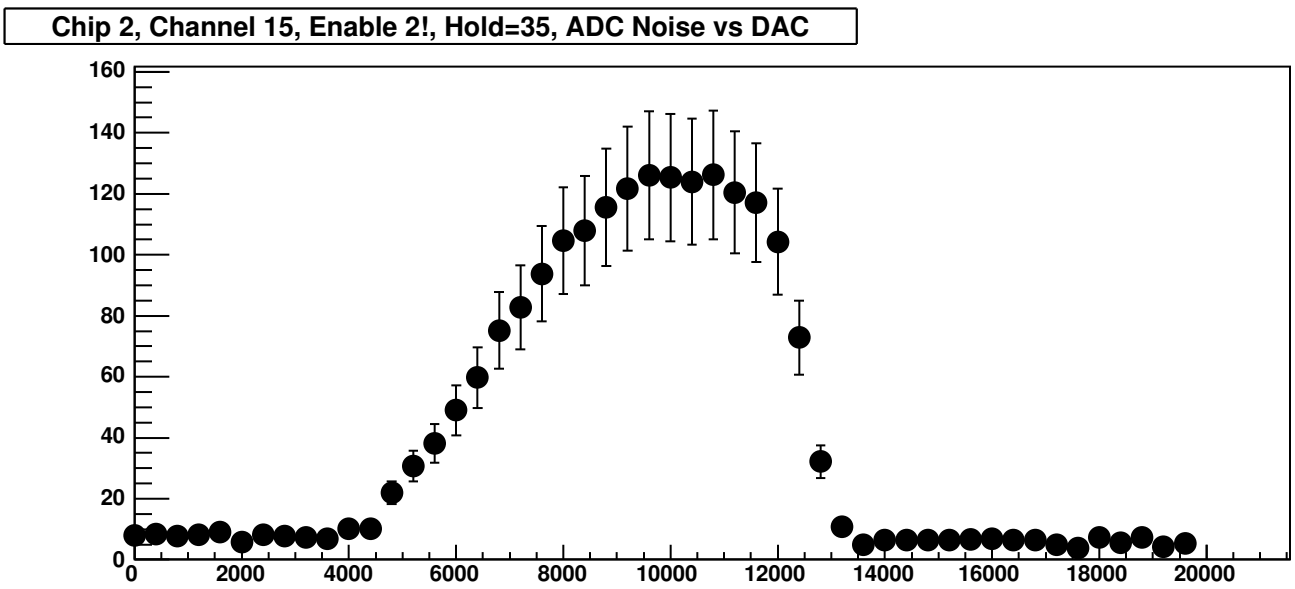
Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

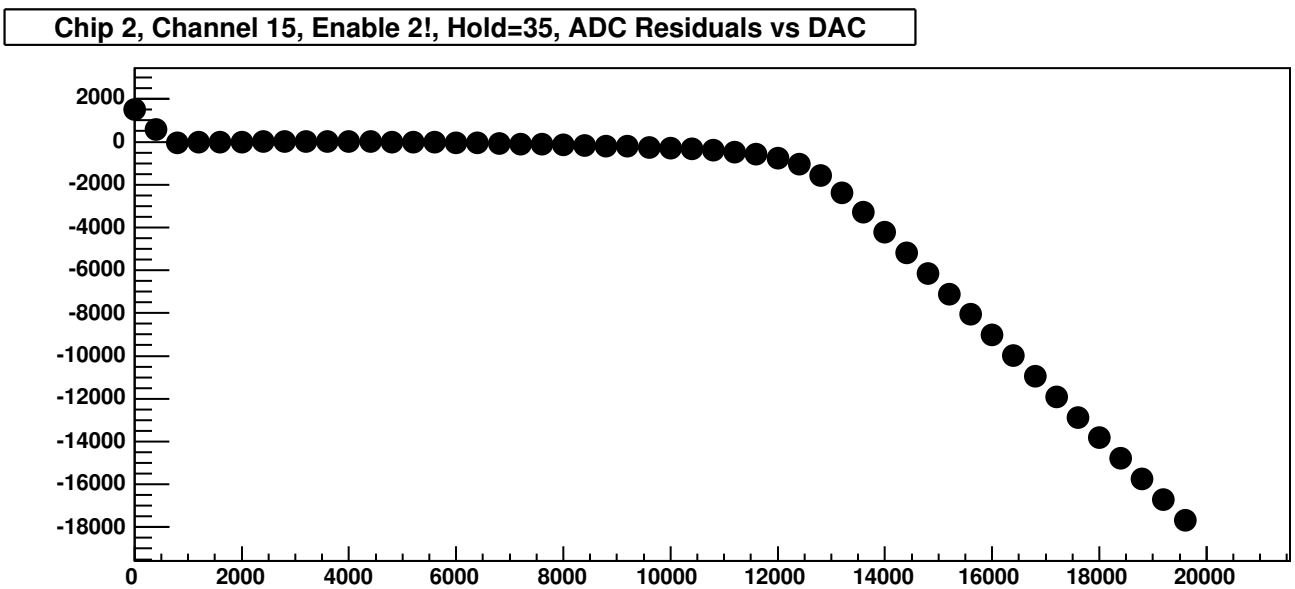


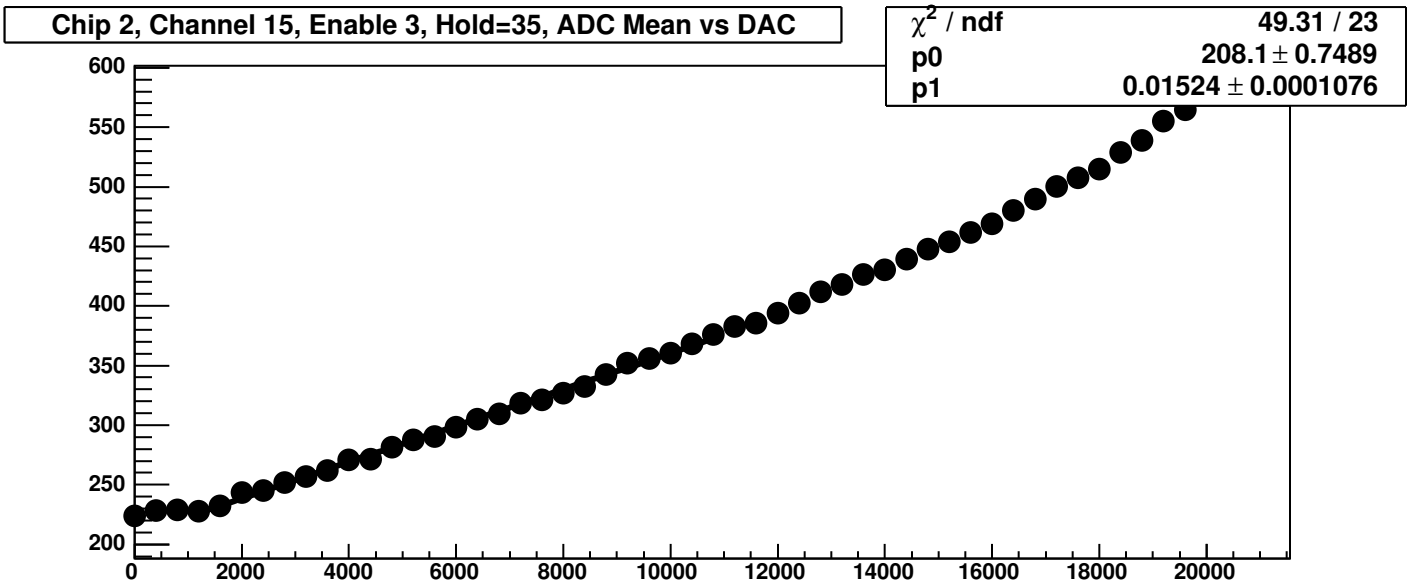
Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

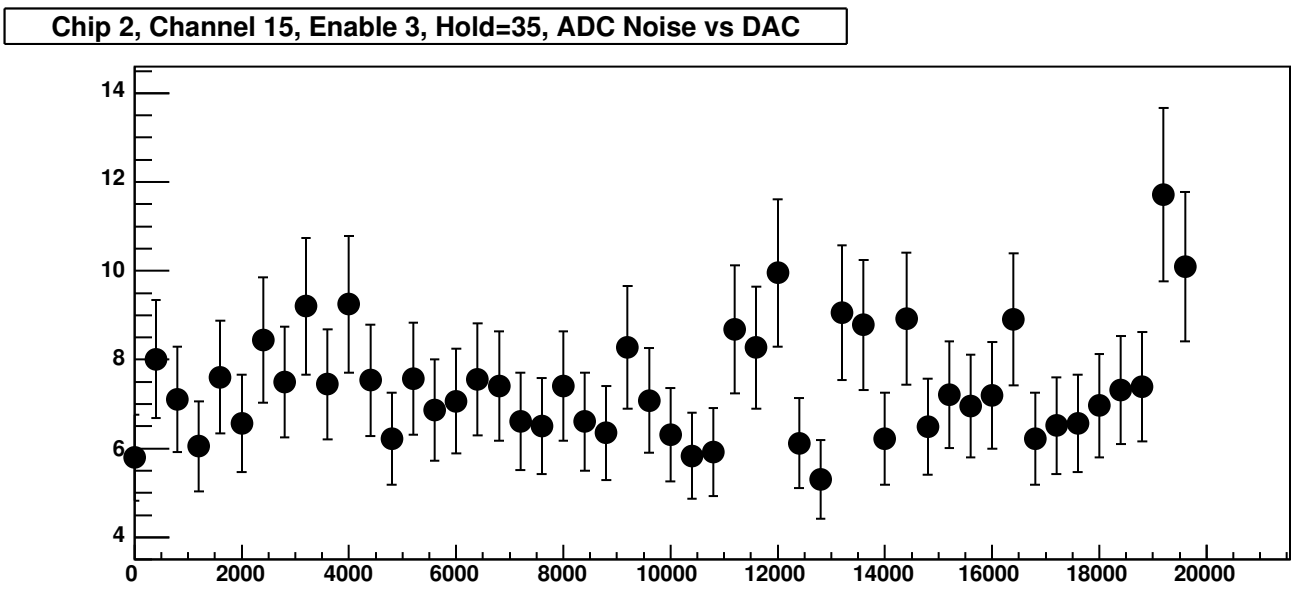


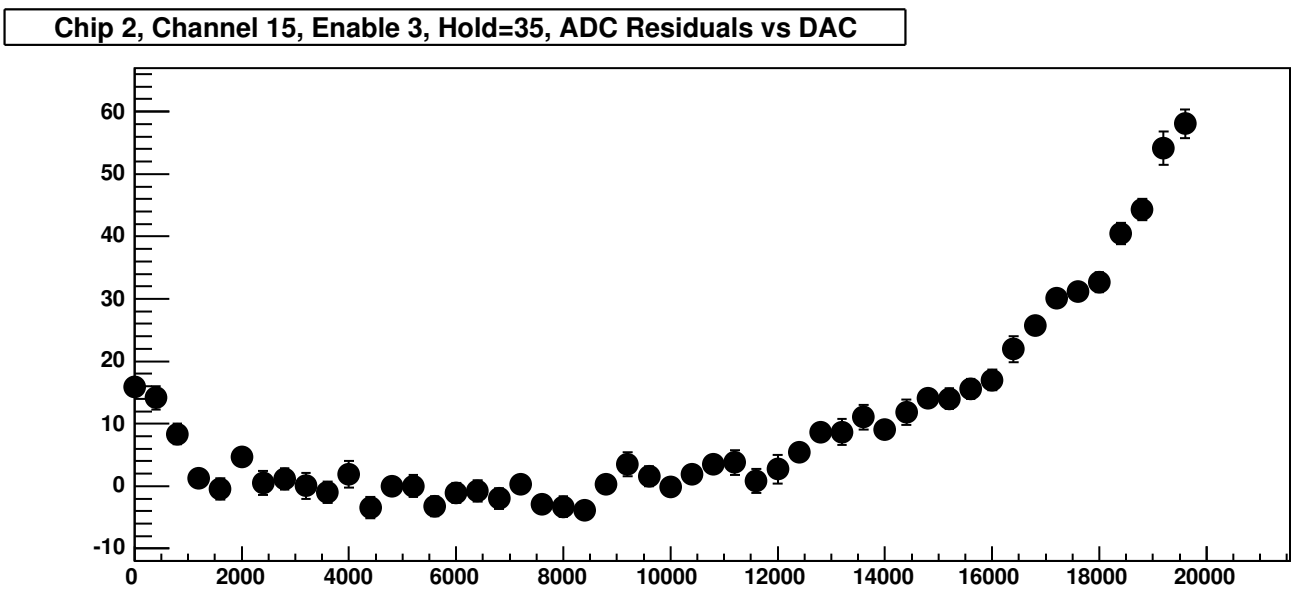

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1158 / 23
p0 292.6 ± 1.566
p1 2.402 ± 0.0005206
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

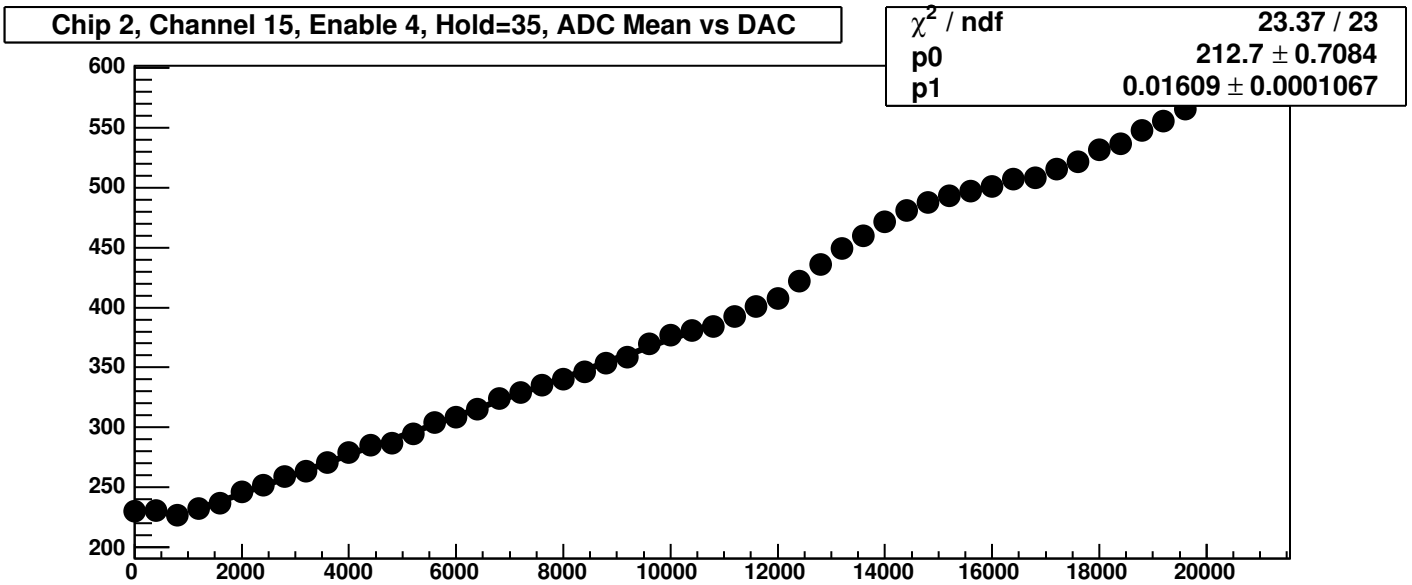

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

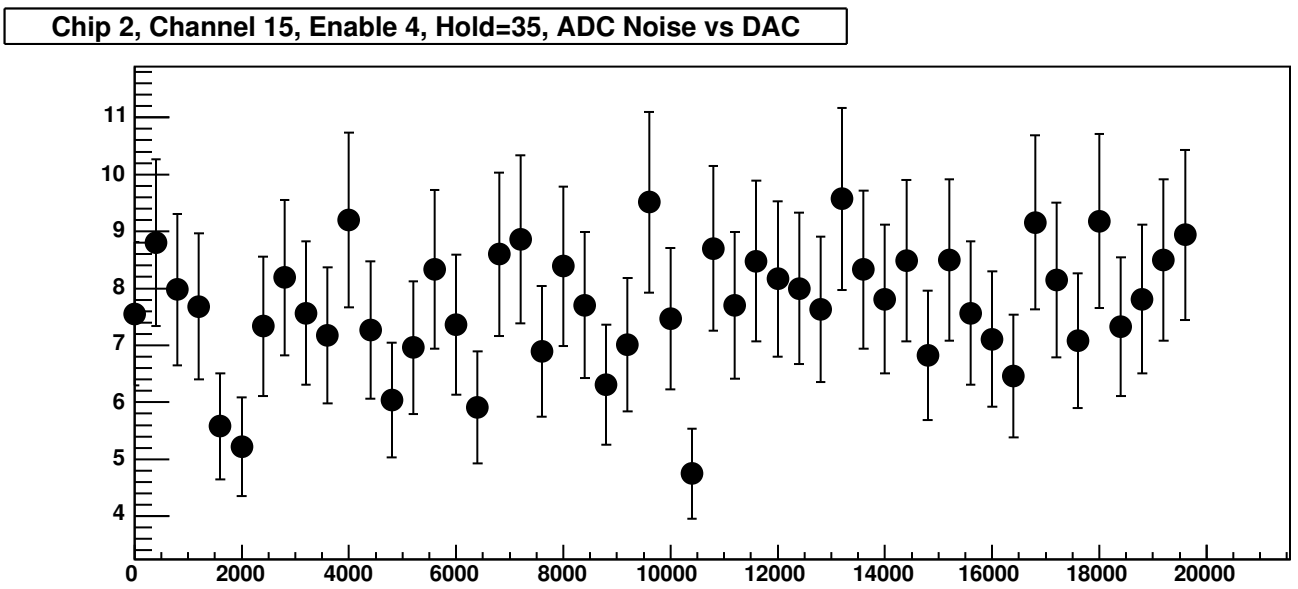

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
-18000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

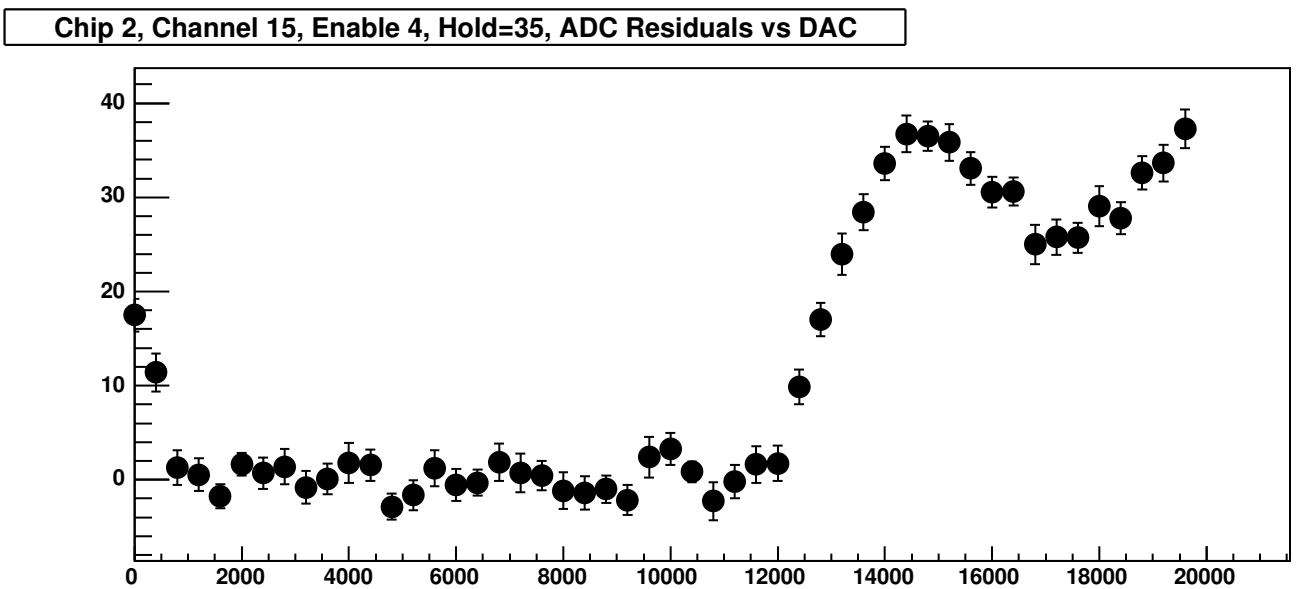

Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 49.31 / 23
p0 208.1 ± 0.7489
p1 0.01524 ± 0.0001076
600
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

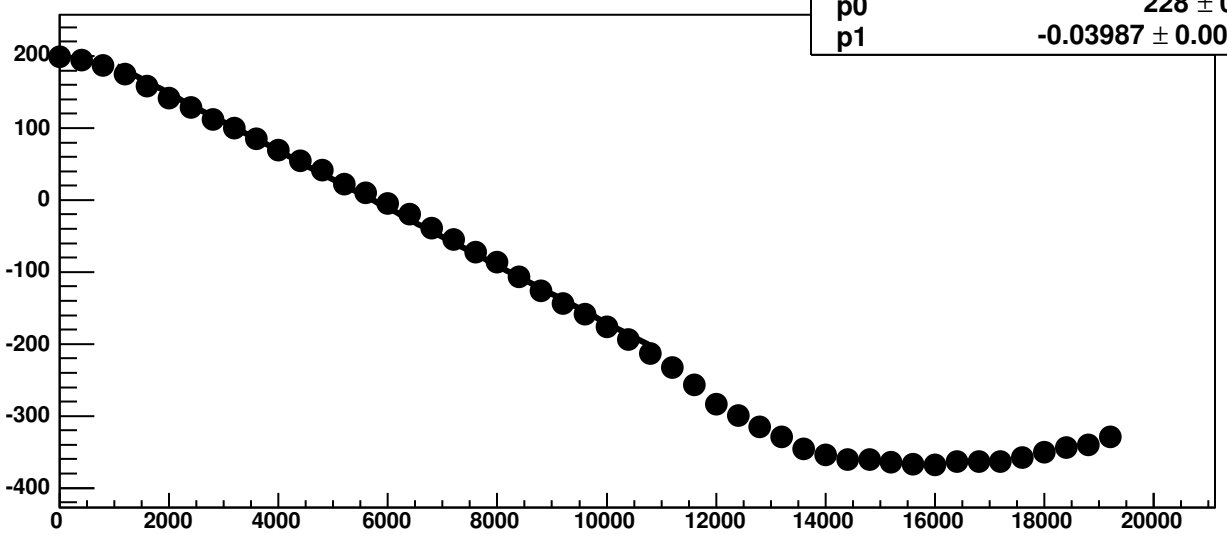

Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.37 / 23
p0 212.7 ± 0.7084
p1 0.01609 ± 0.0001067
600
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

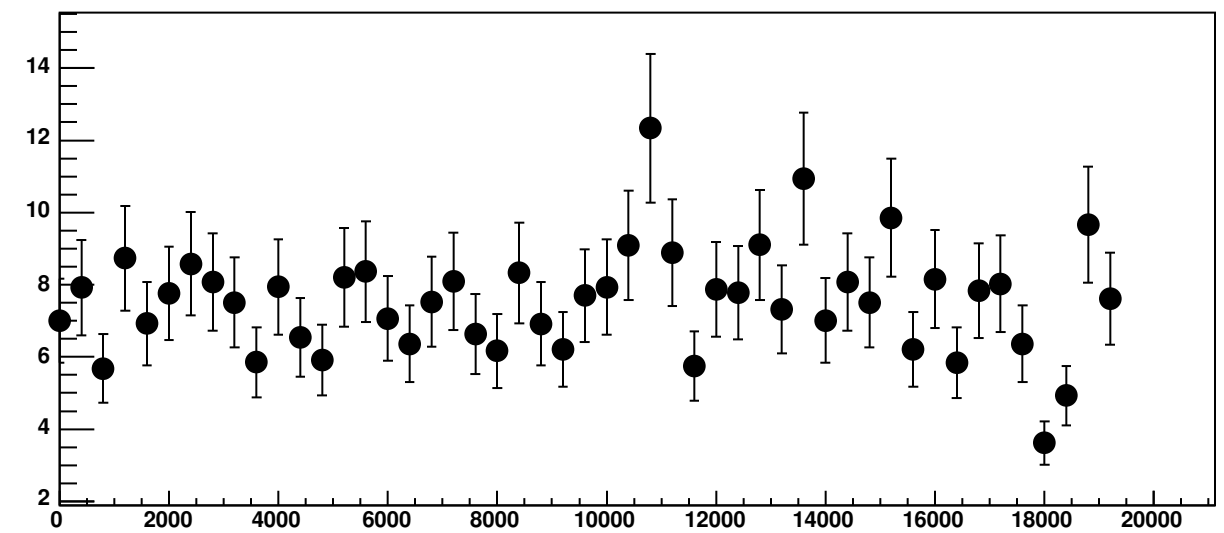

Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 2, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

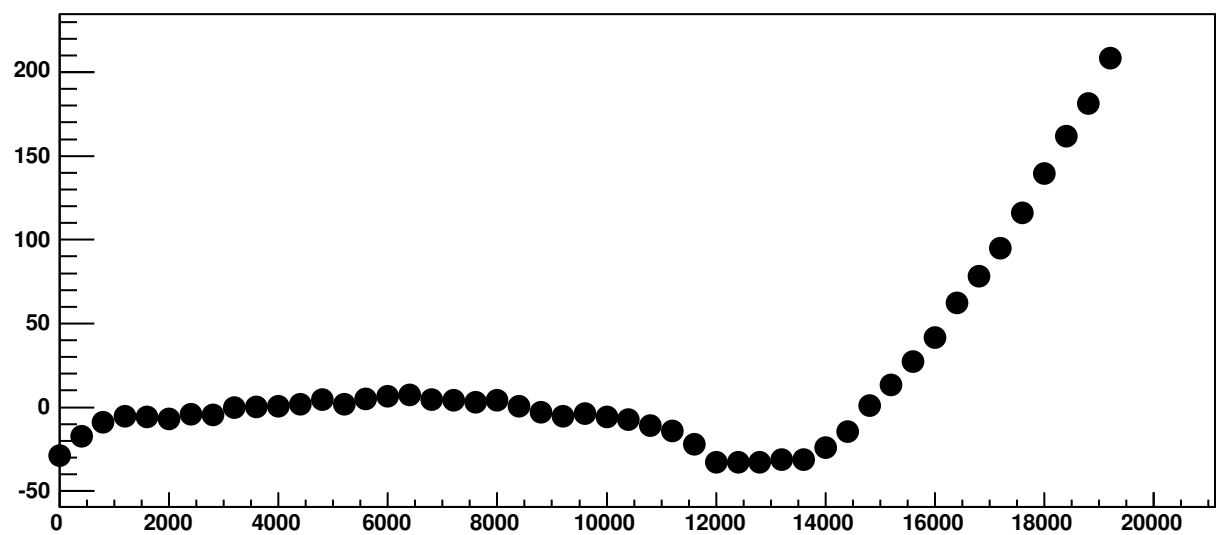
χ^2 / ndf	193.9 / 23
p0	228 ± 0.8129
p1	-0.03987 ± 0.0001252



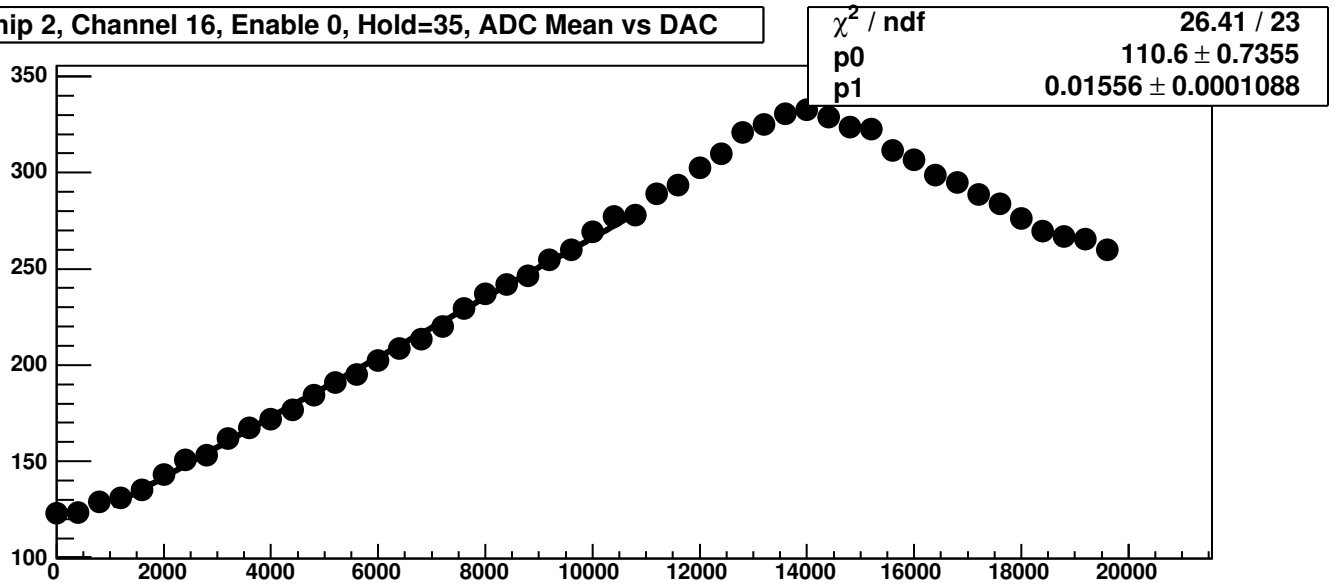
Chip 2, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



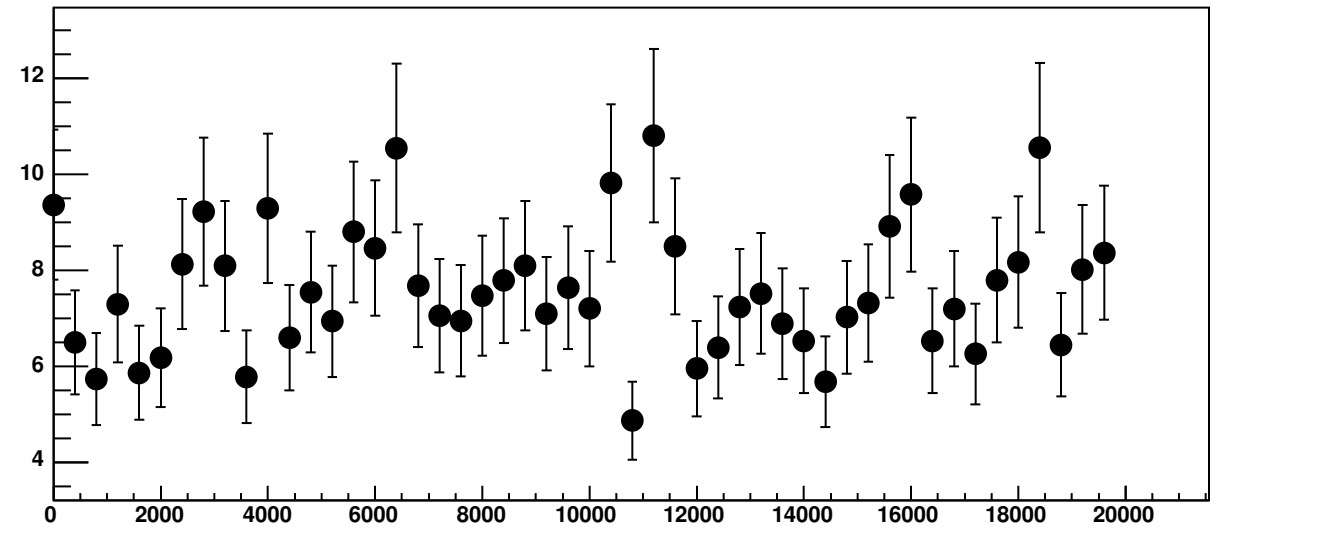
Chip 2, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



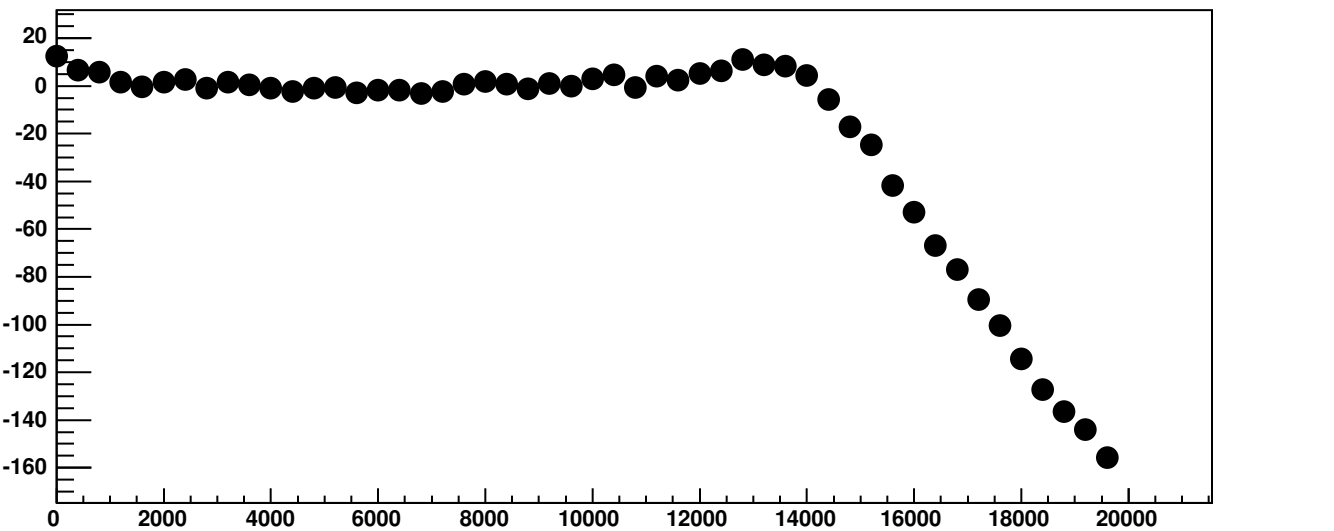
Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

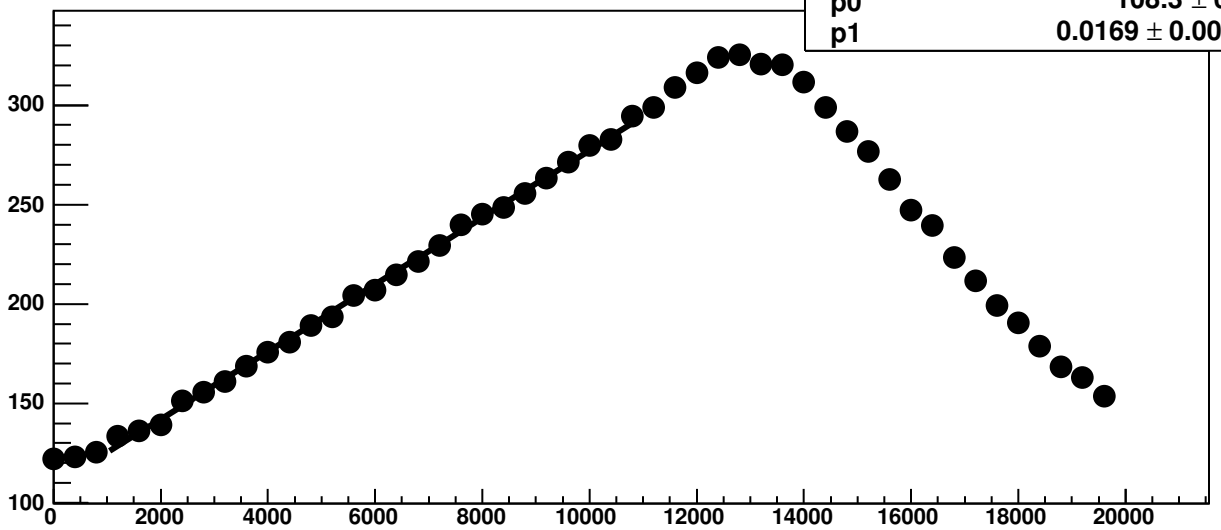


Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

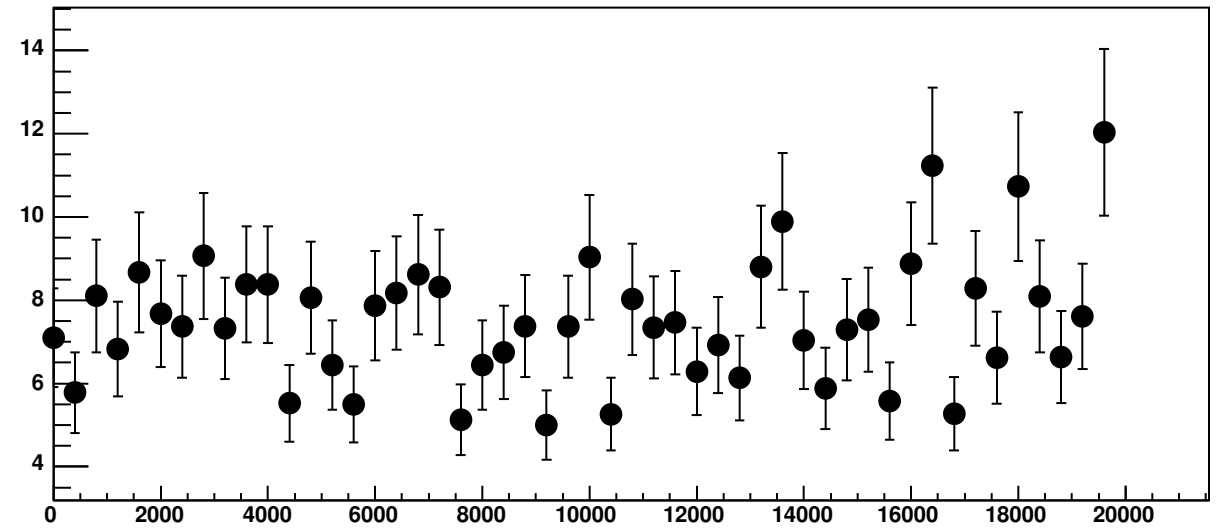


Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

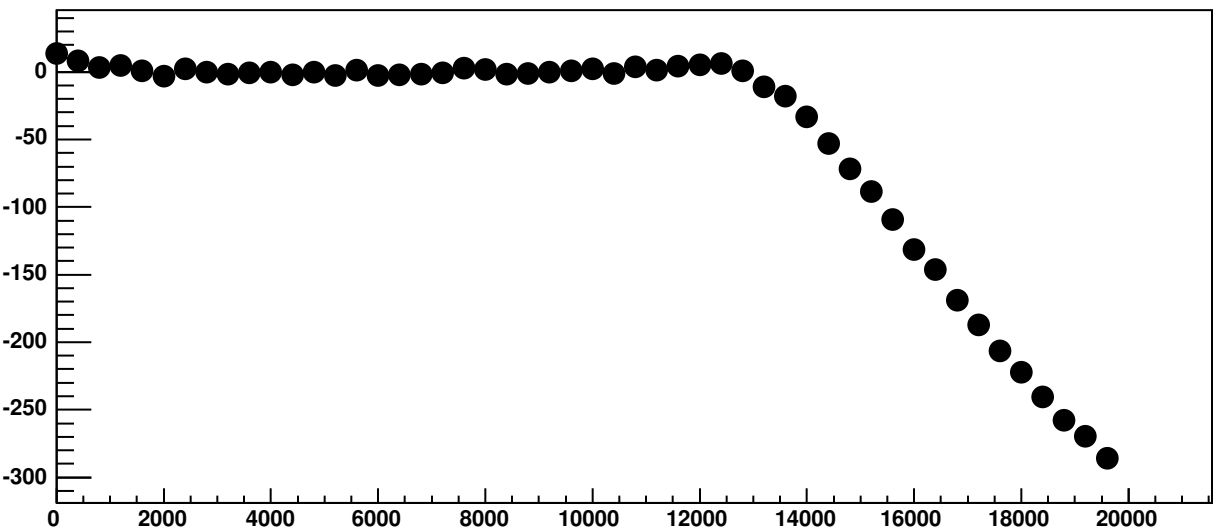
χ^2 / ndf	41.28 / 23
p0	108.3 ± 0.7817
p1	0.0169 ± 0.0001128



Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC

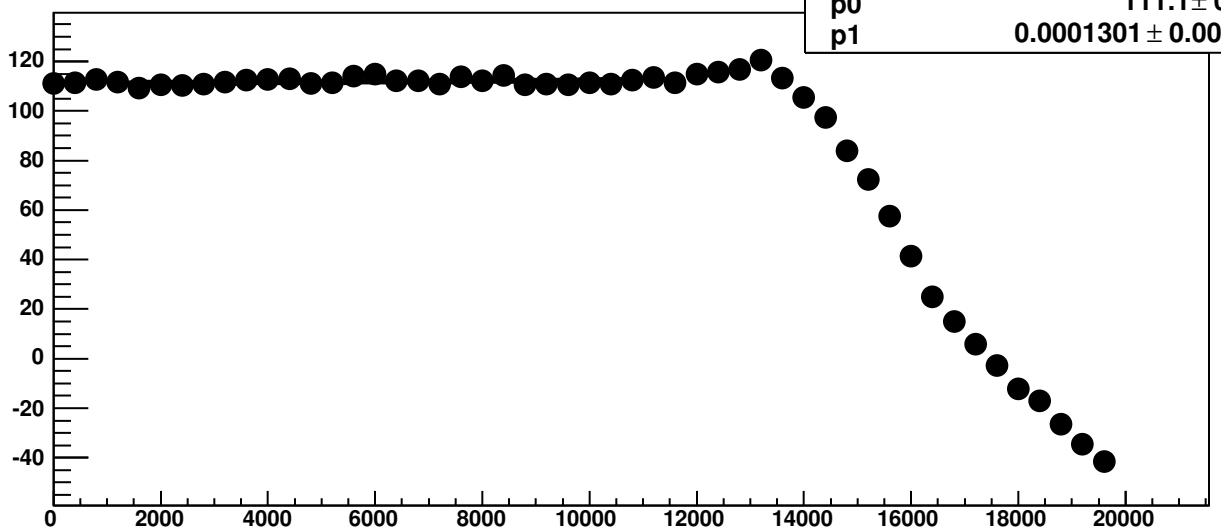


Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

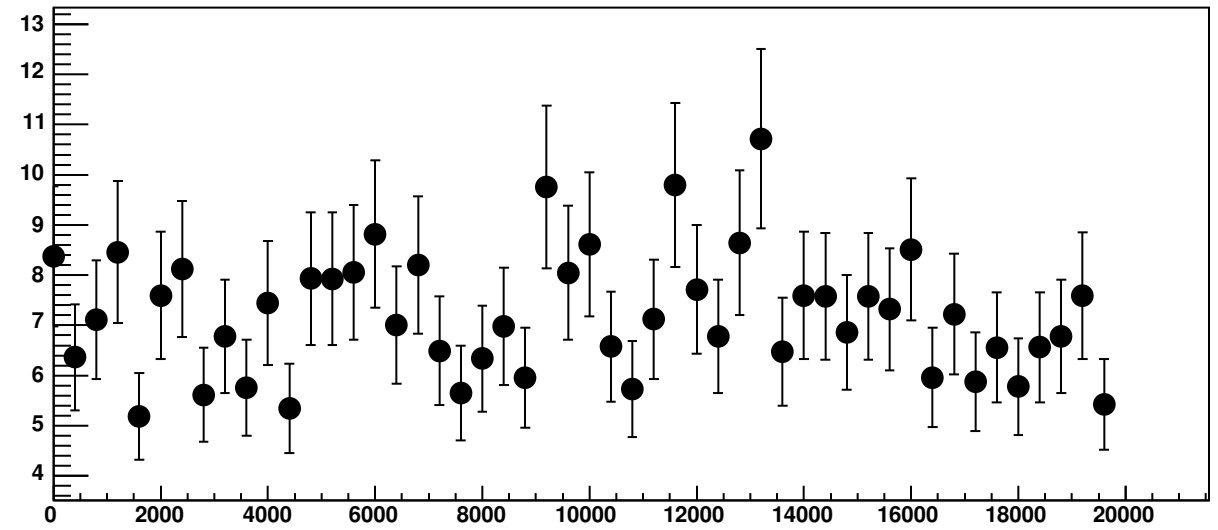


Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

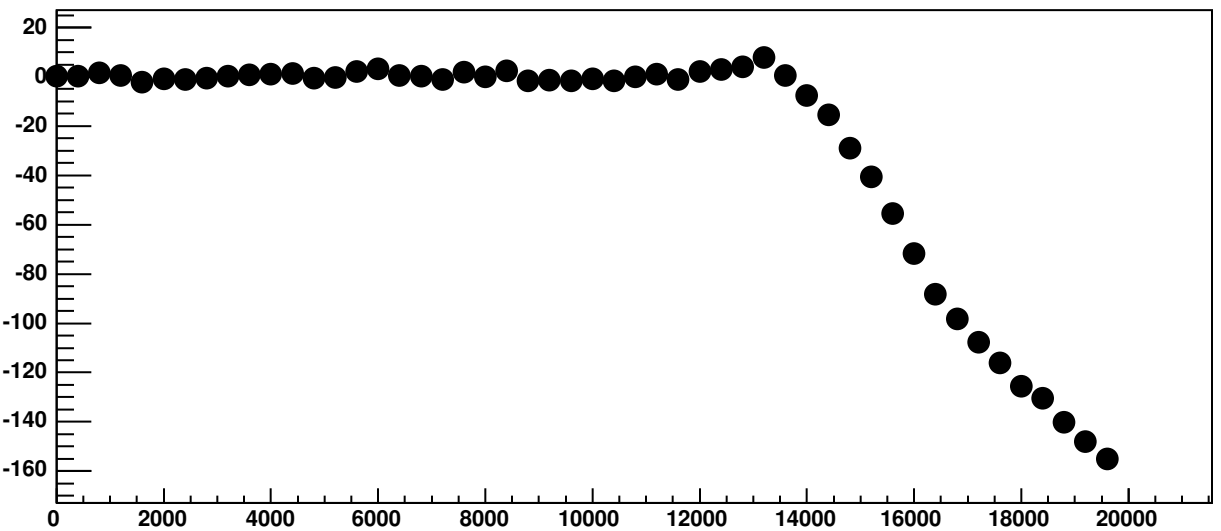
χ^2 / ndf	19.03 / 23
p0	111.1 ± 0.7039
p1	0.0001301 ± 0.0001068

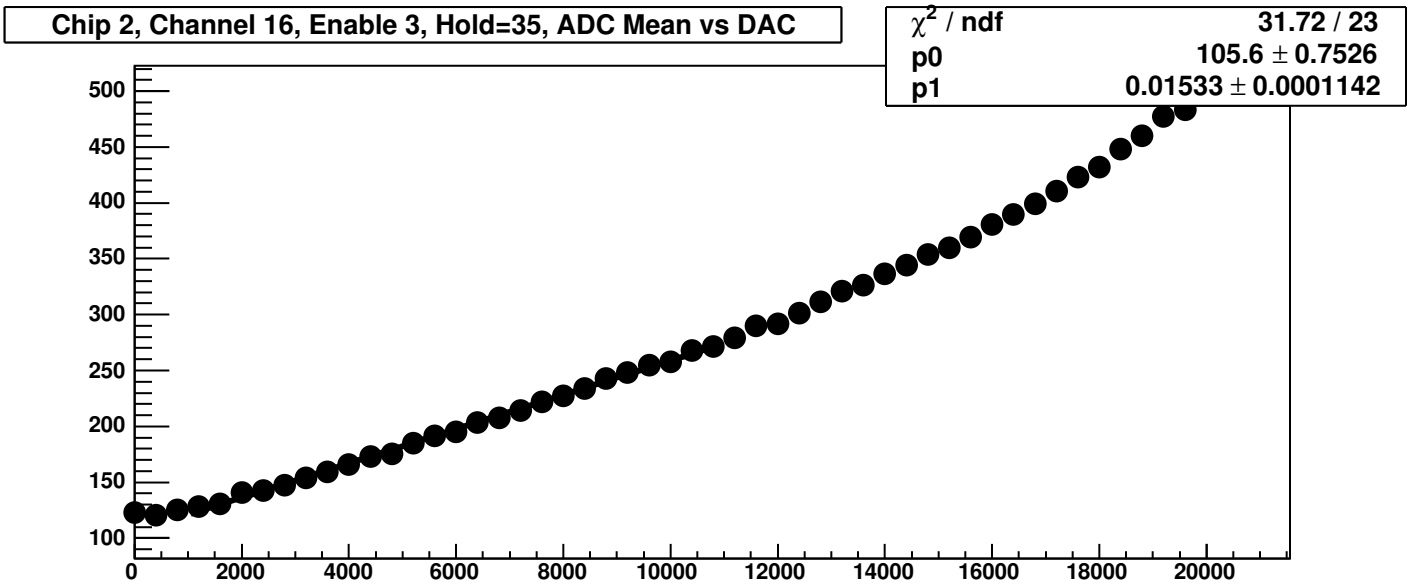


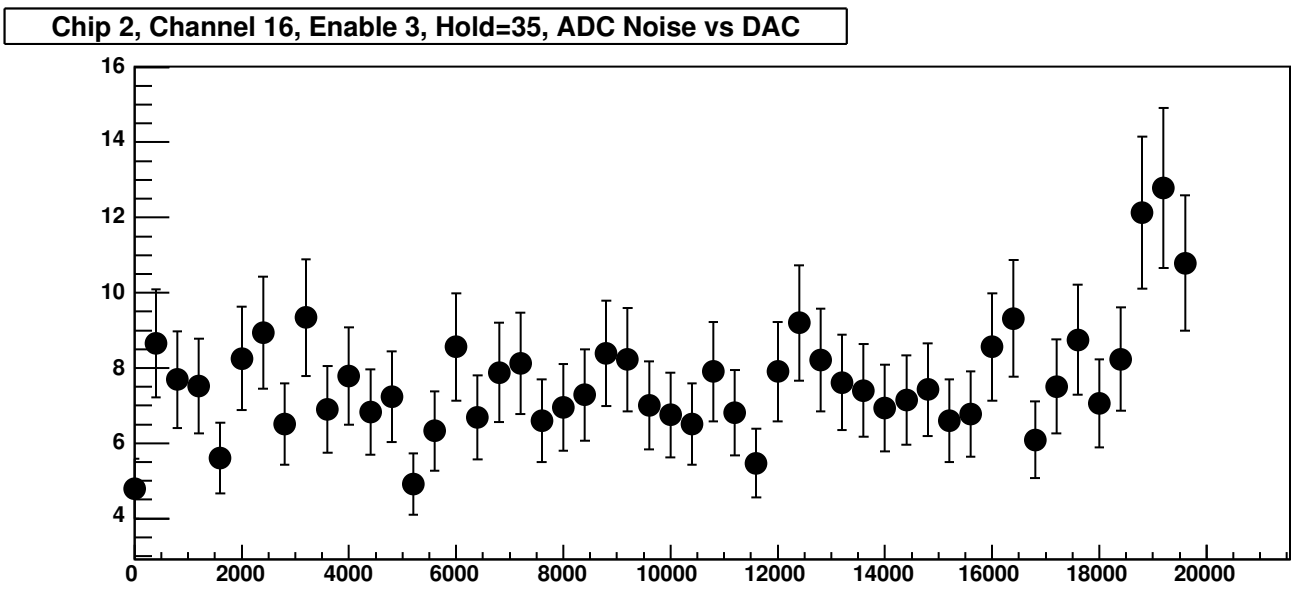
Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC

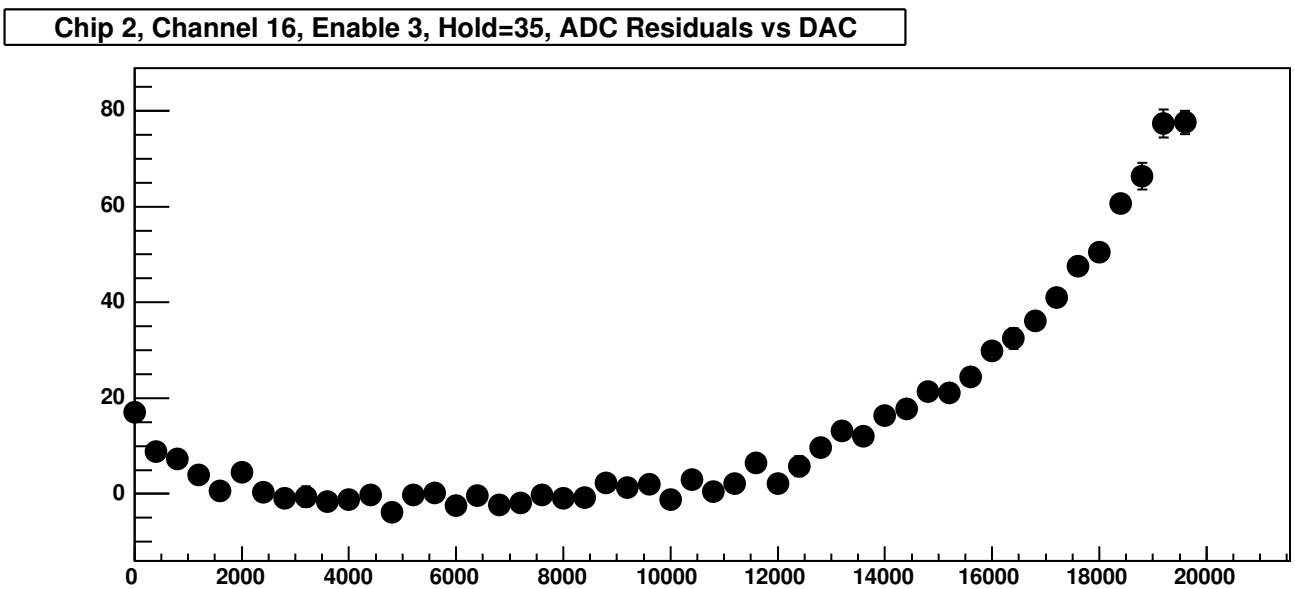


Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

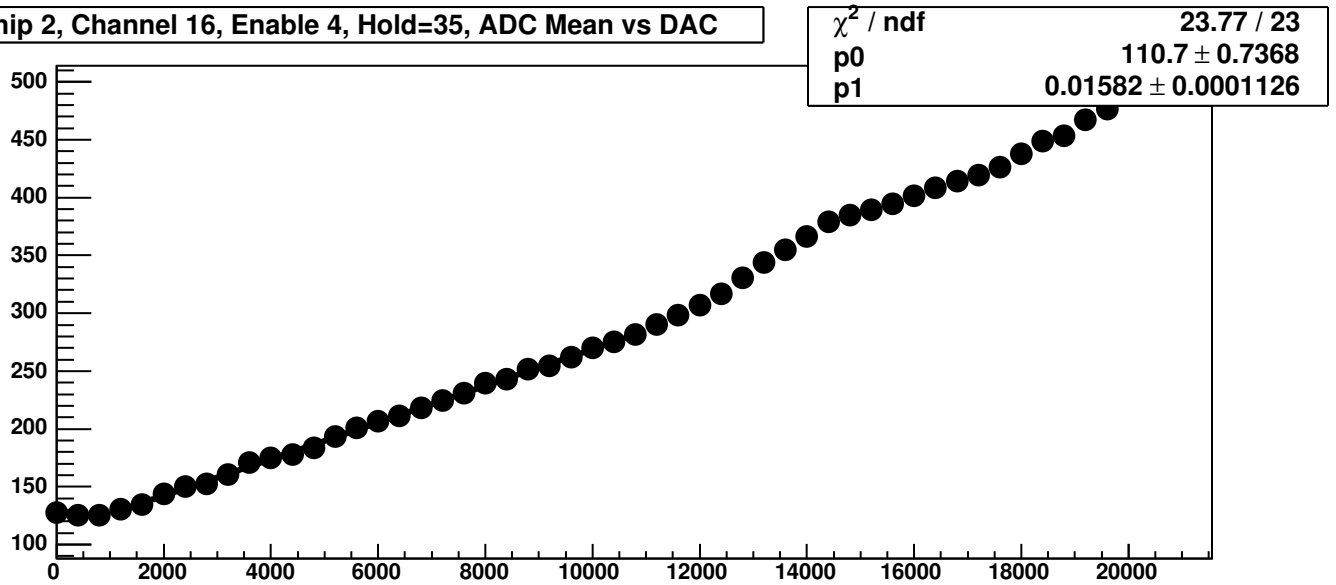



Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.72 / 23
p0 105.6 ± 0.7526
p1 0.01533 ± 0.0001142
500
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

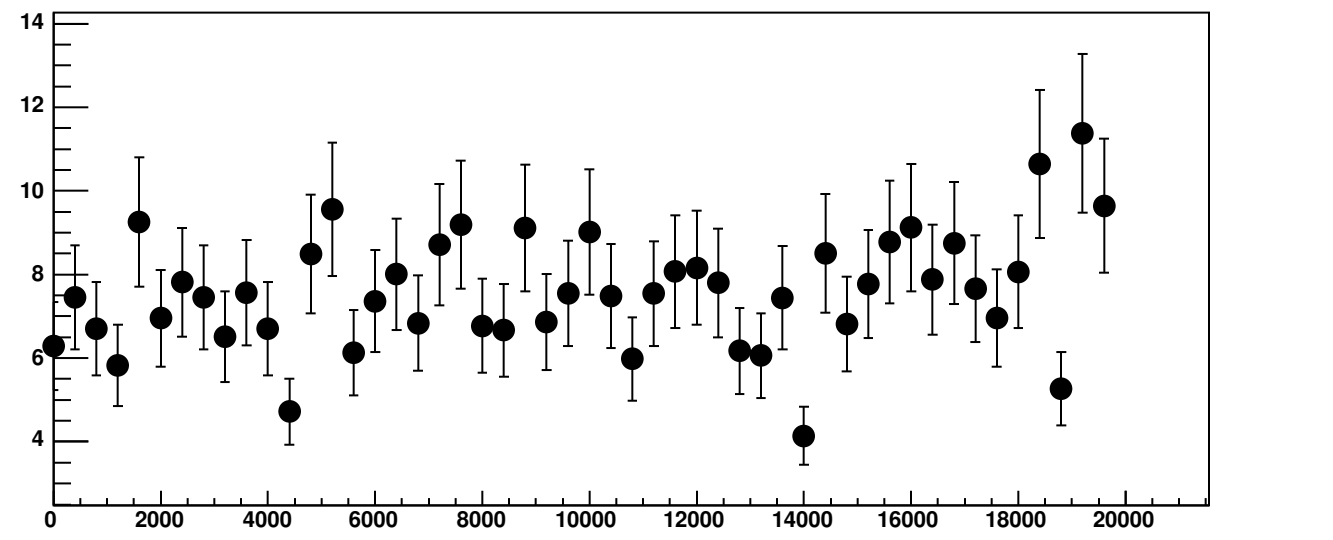

Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

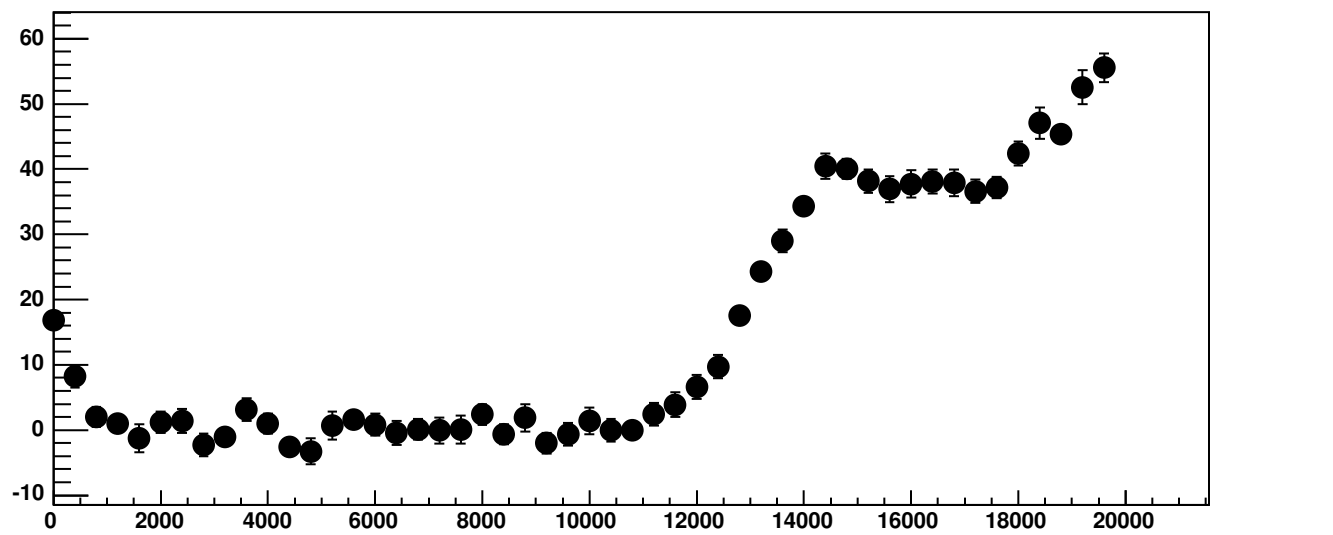
Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

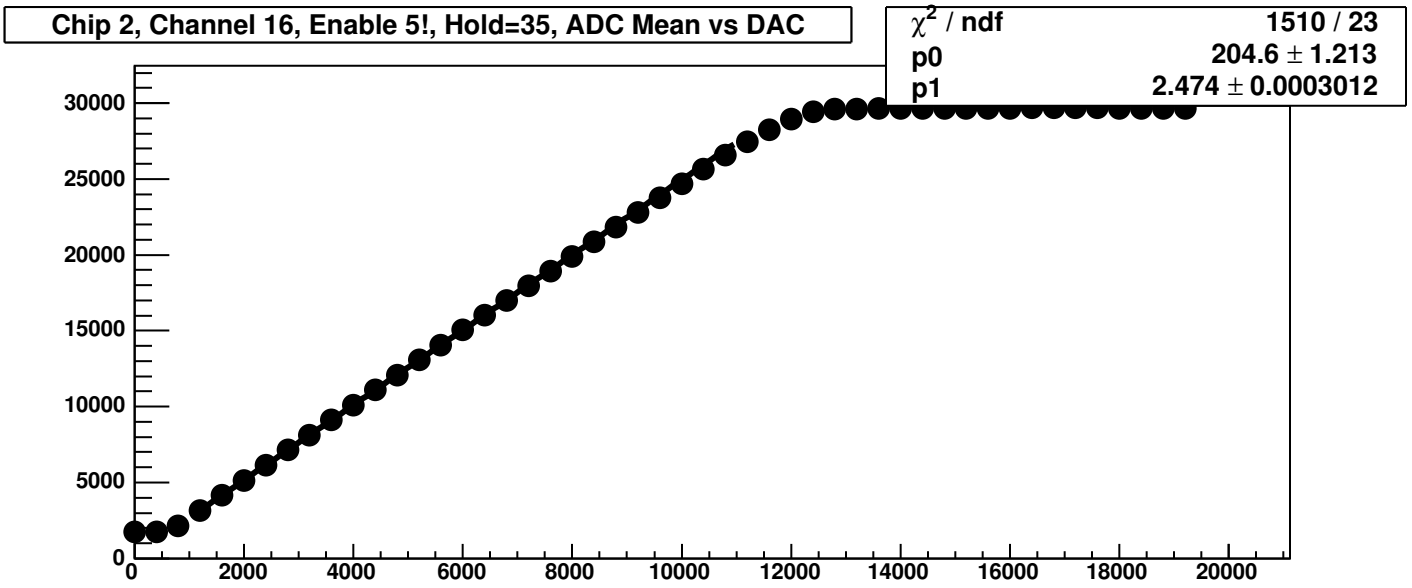


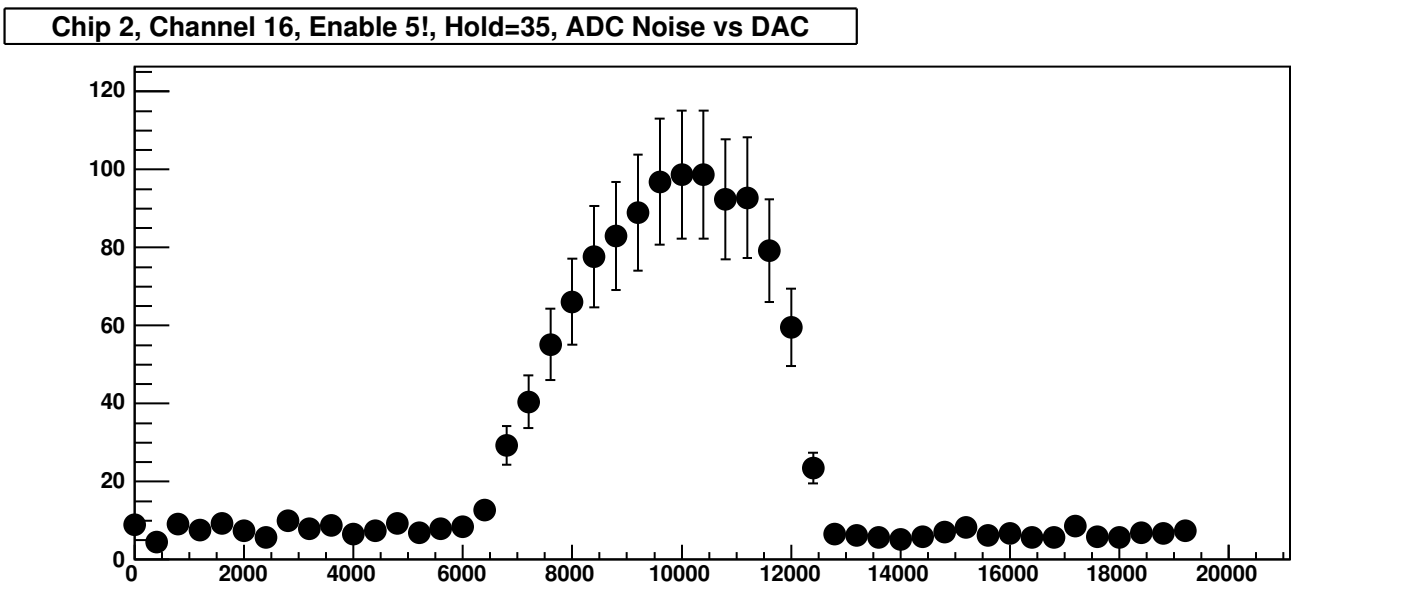
Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

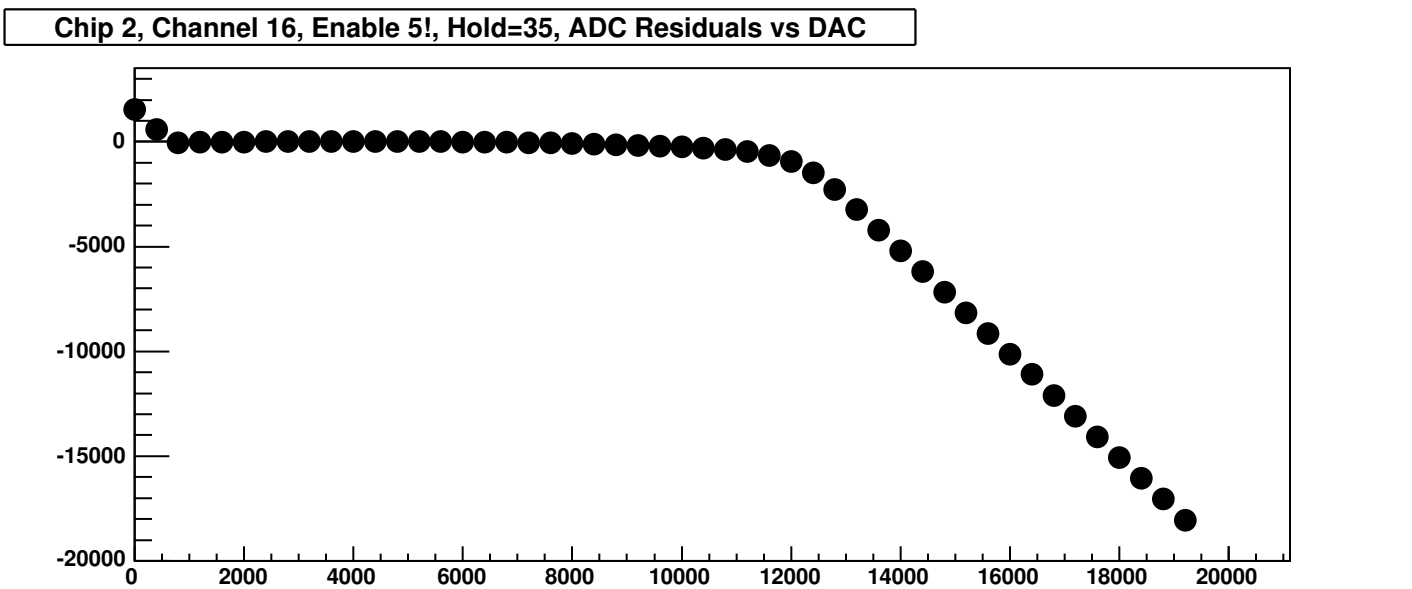


Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

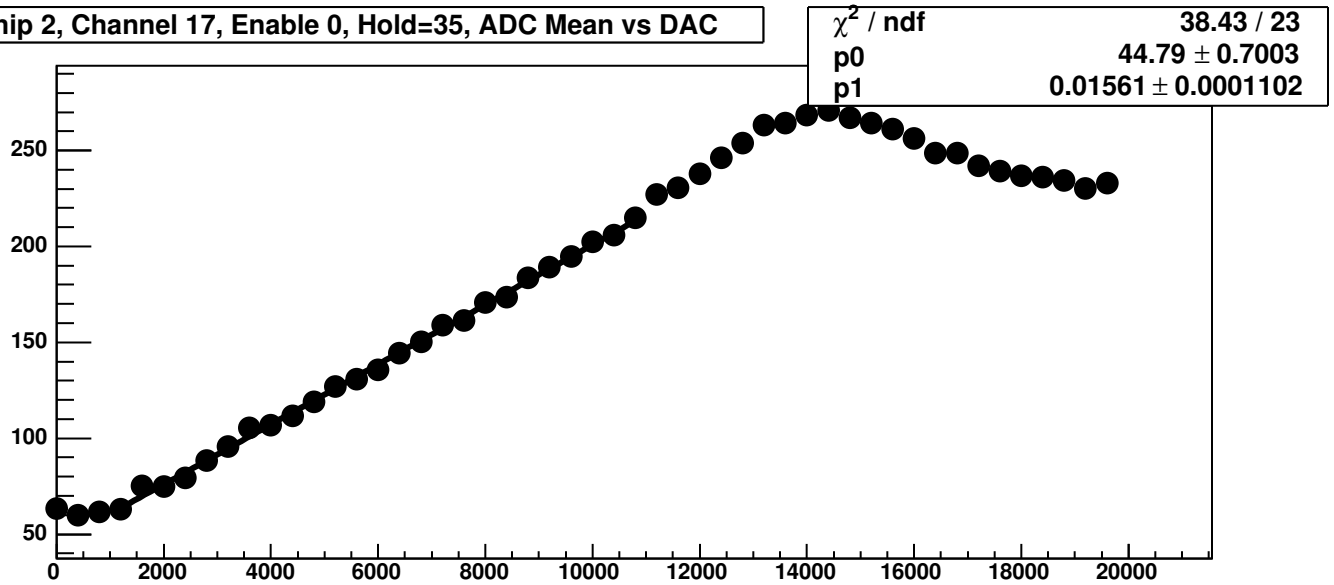



Chip 2, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1510 / 23
p0 204.6 ± 1.213
p1 2.474 ± 0.0003012
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

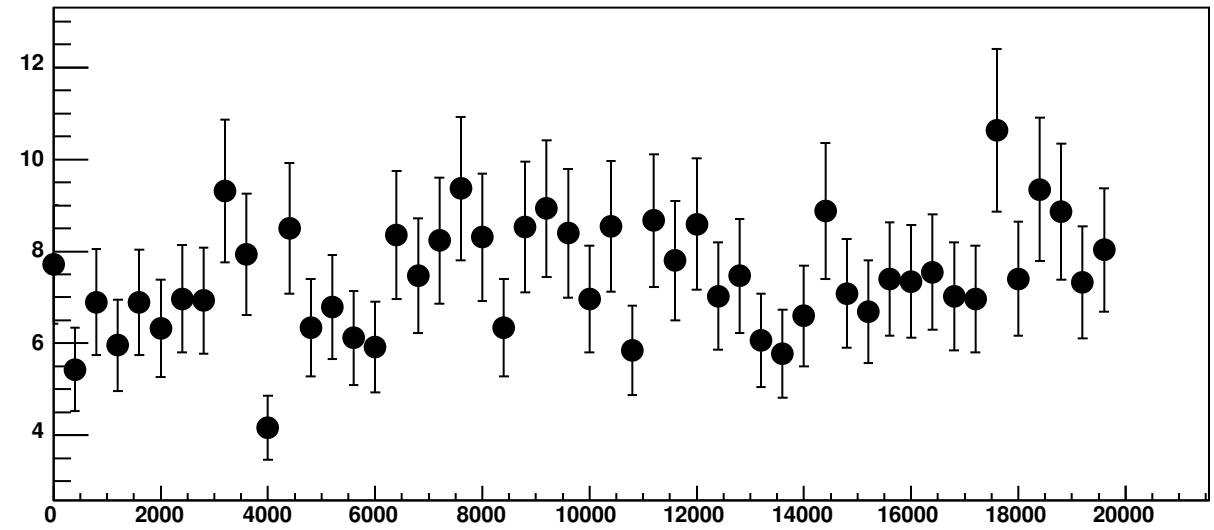

Chip 2, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

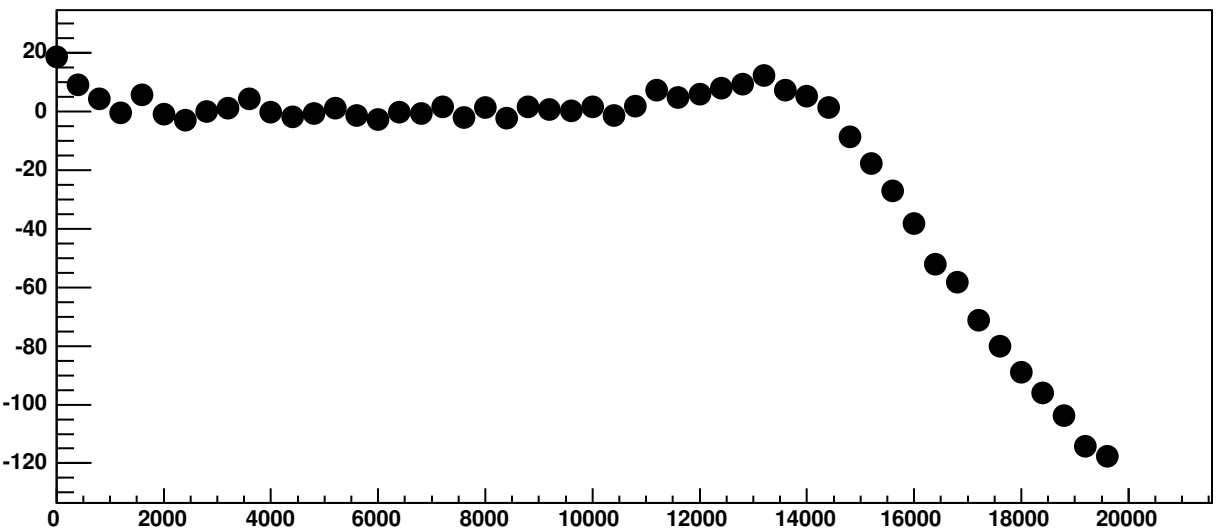
Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

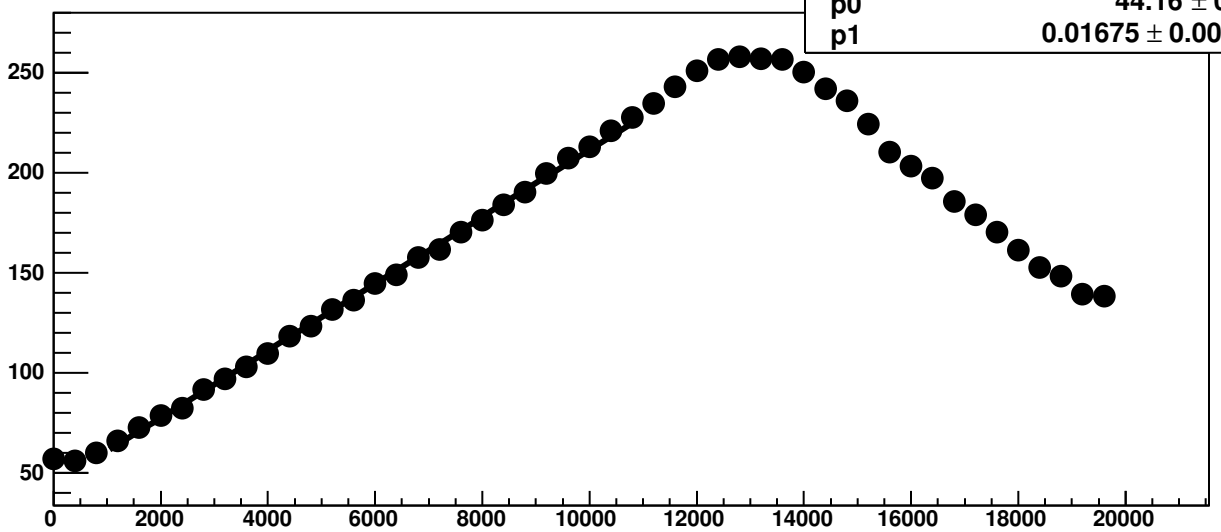


Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

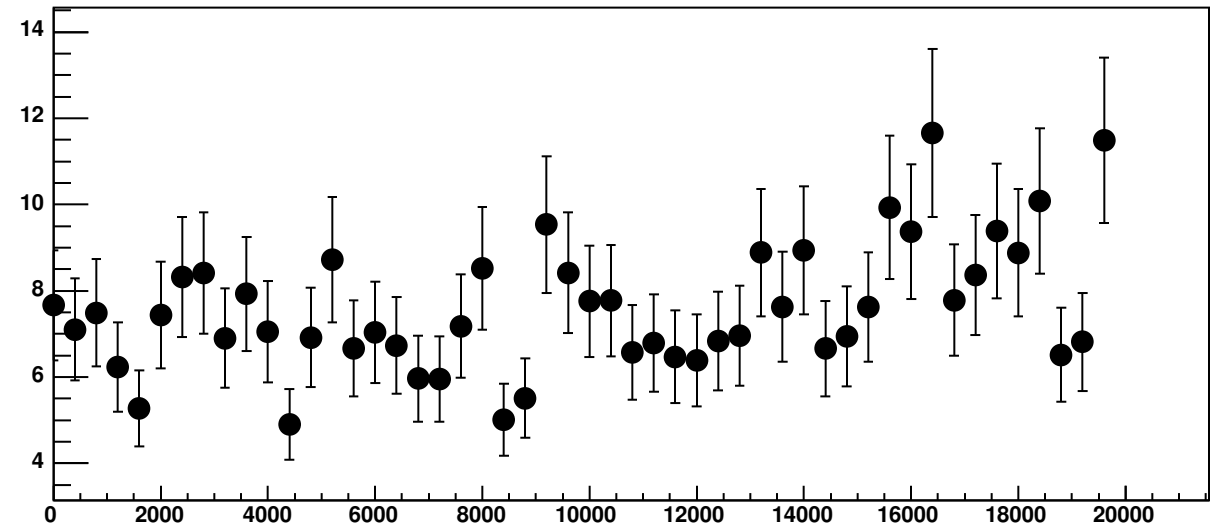


Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

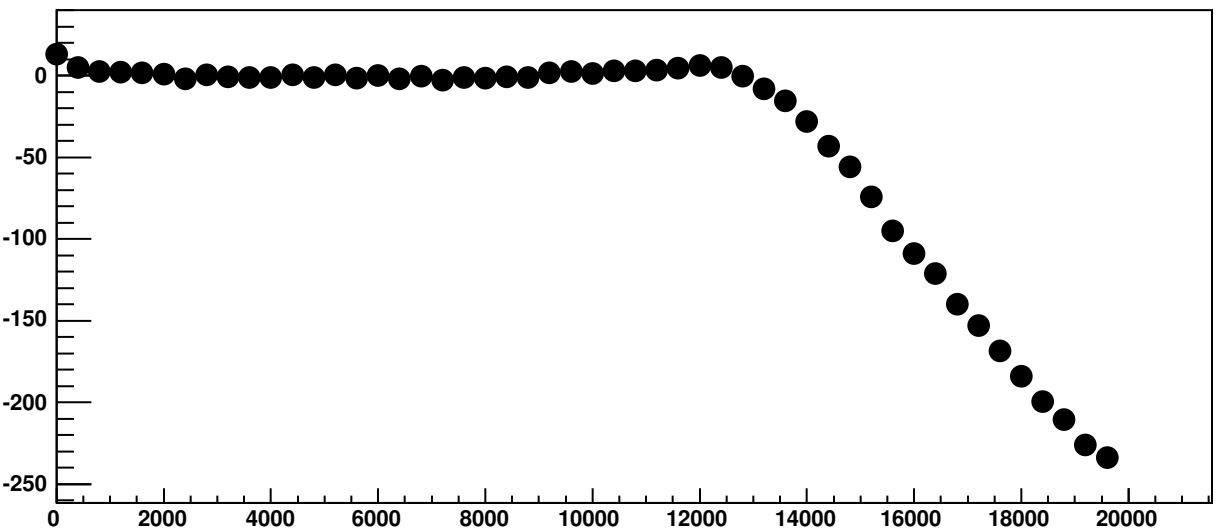
χ^2 / ndf	26.19 / 23
p0	44.16 ± 0.7123
p1	0.01675 ± 0.0001086

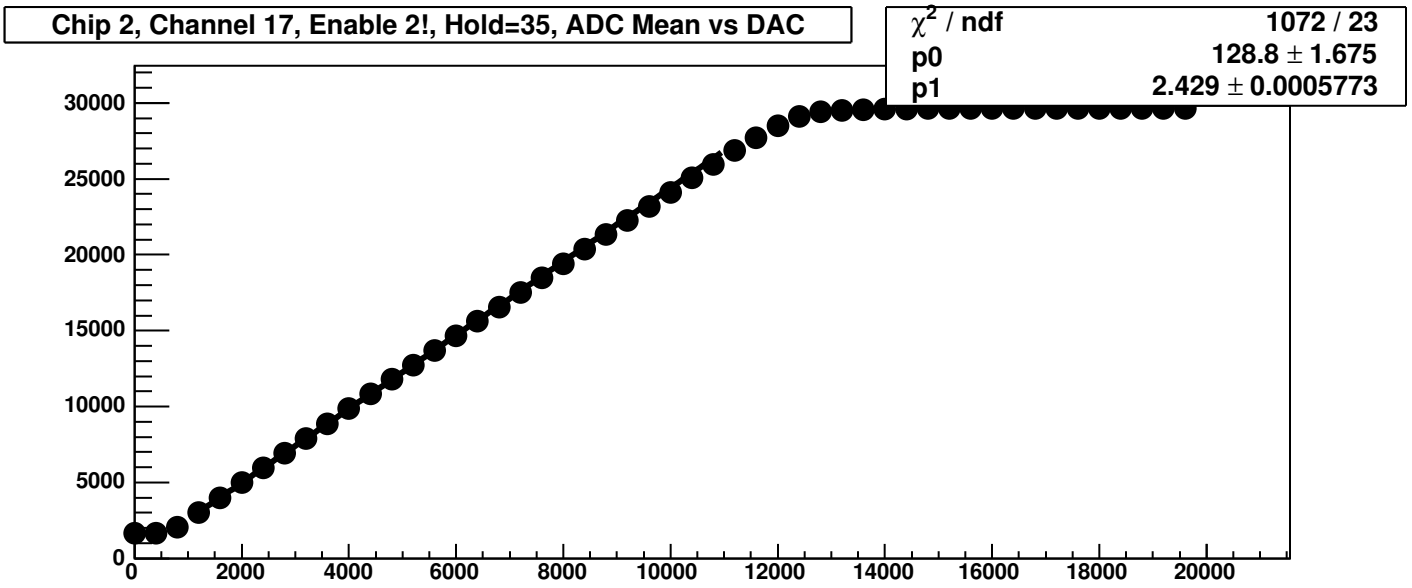


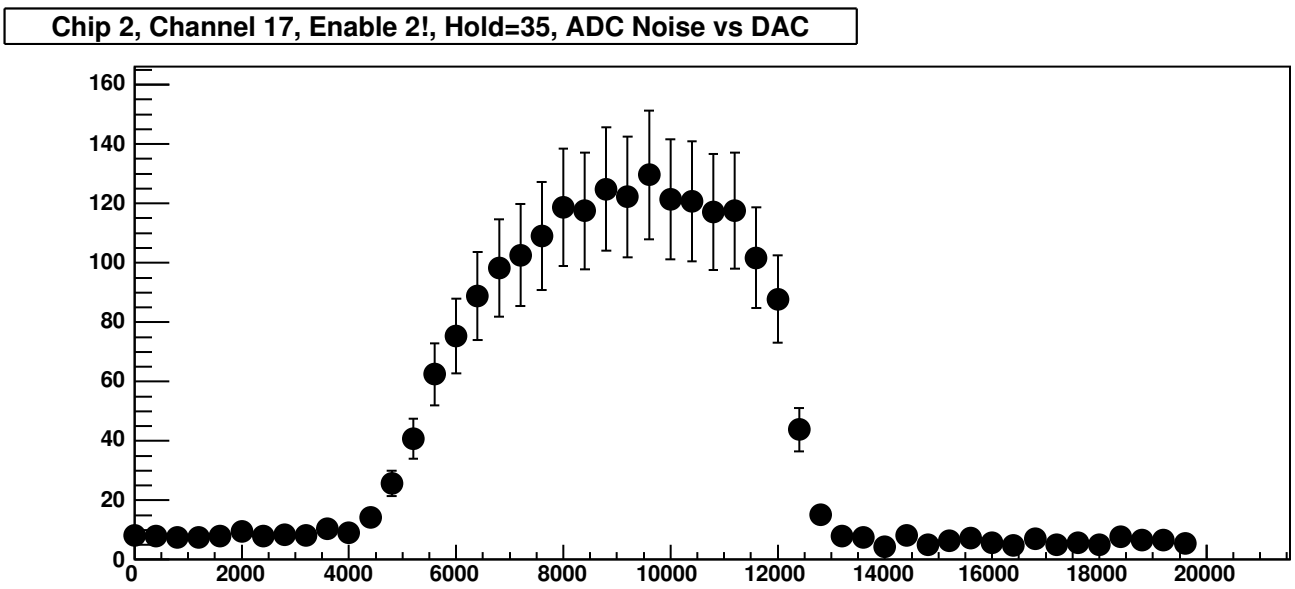
Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

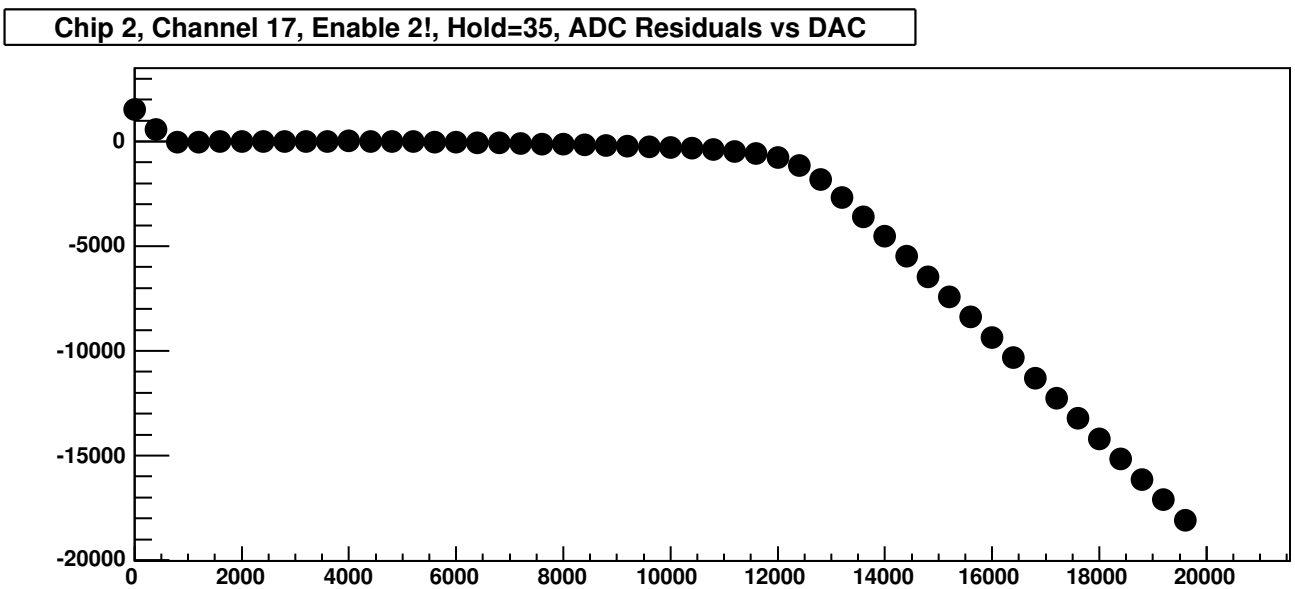


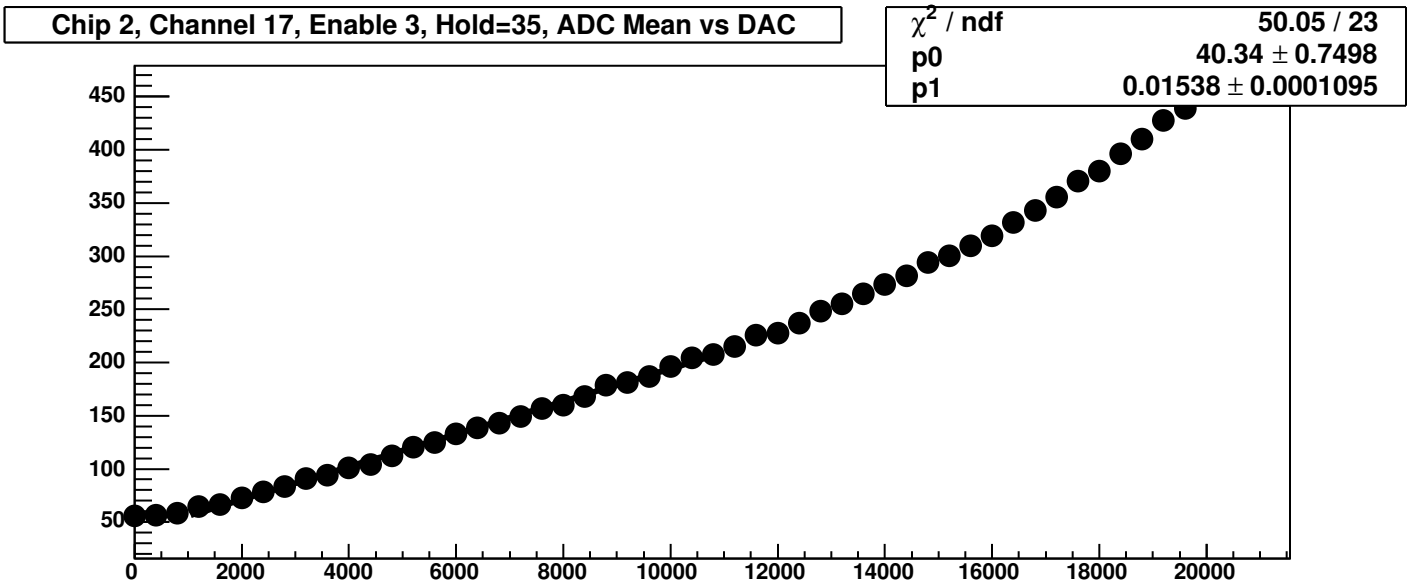
Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

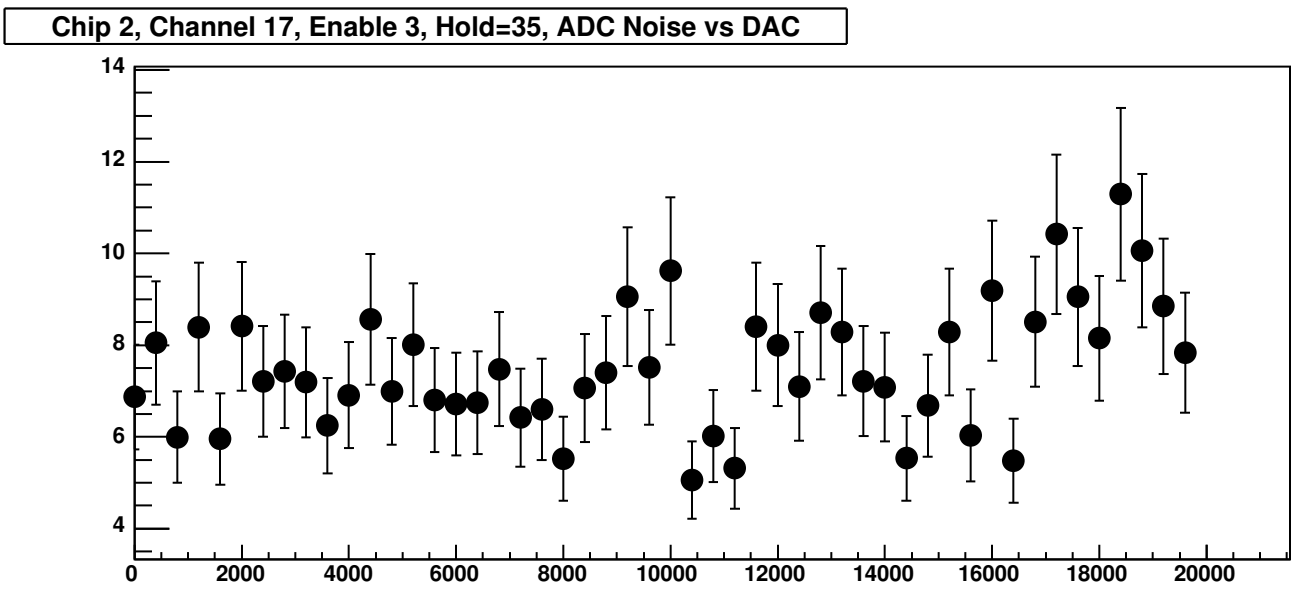


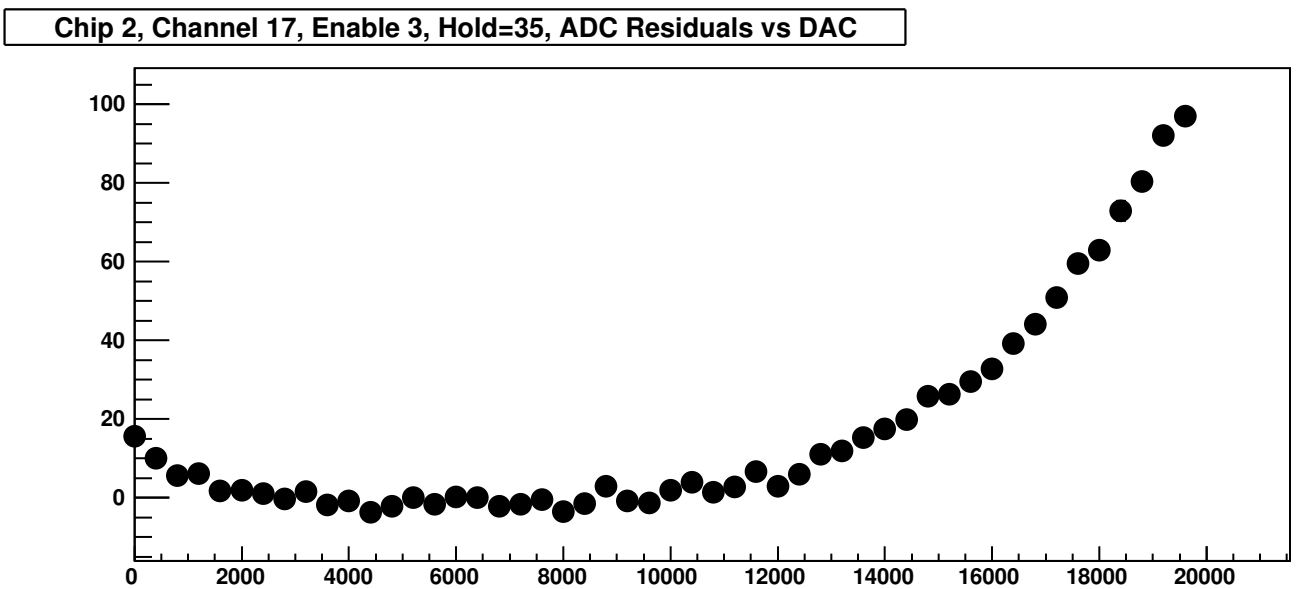

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1072 / 23
p0 128.8 ± 1.675
p1 2.429 ± 0.0005773
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

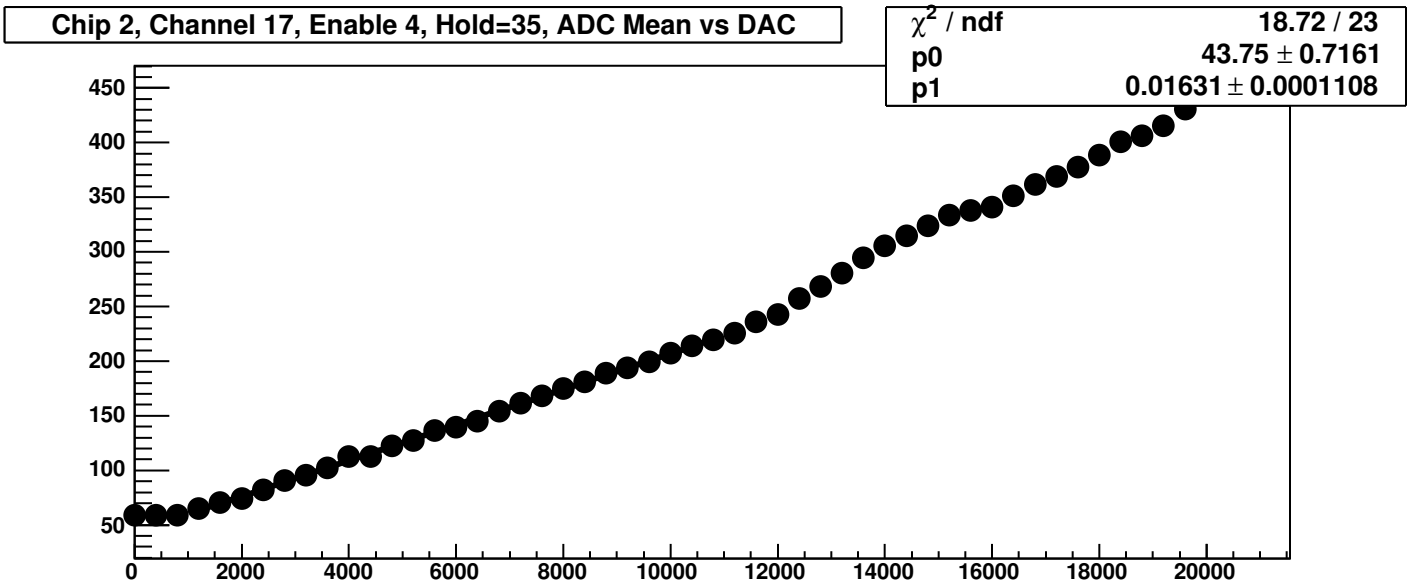

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

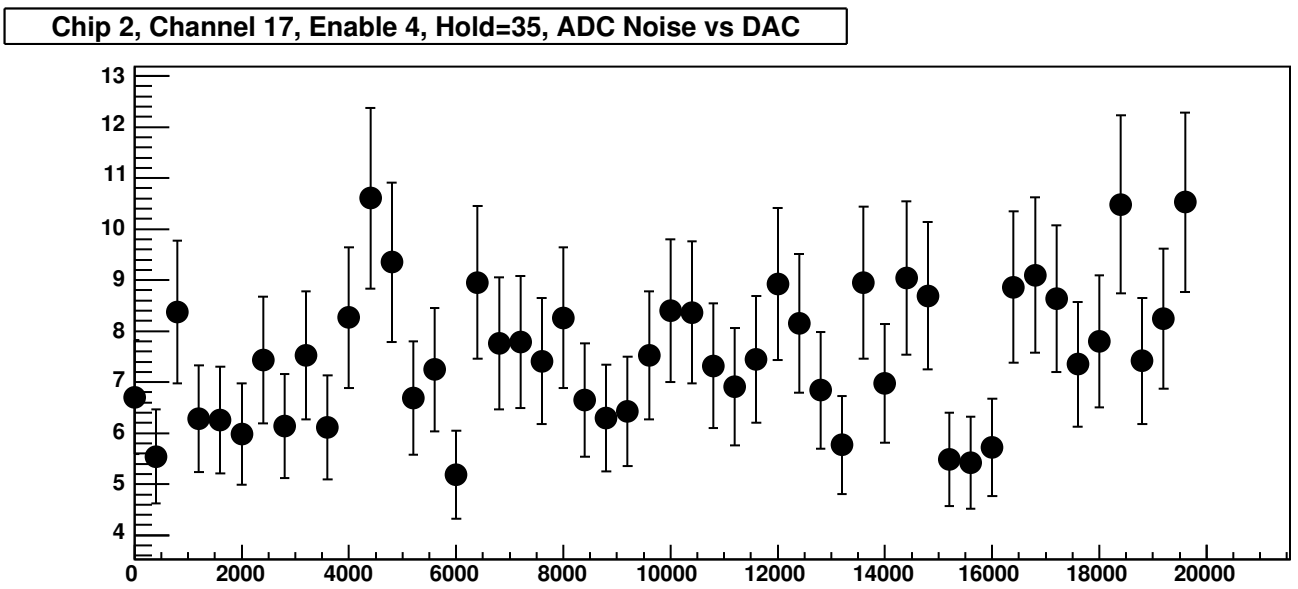

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

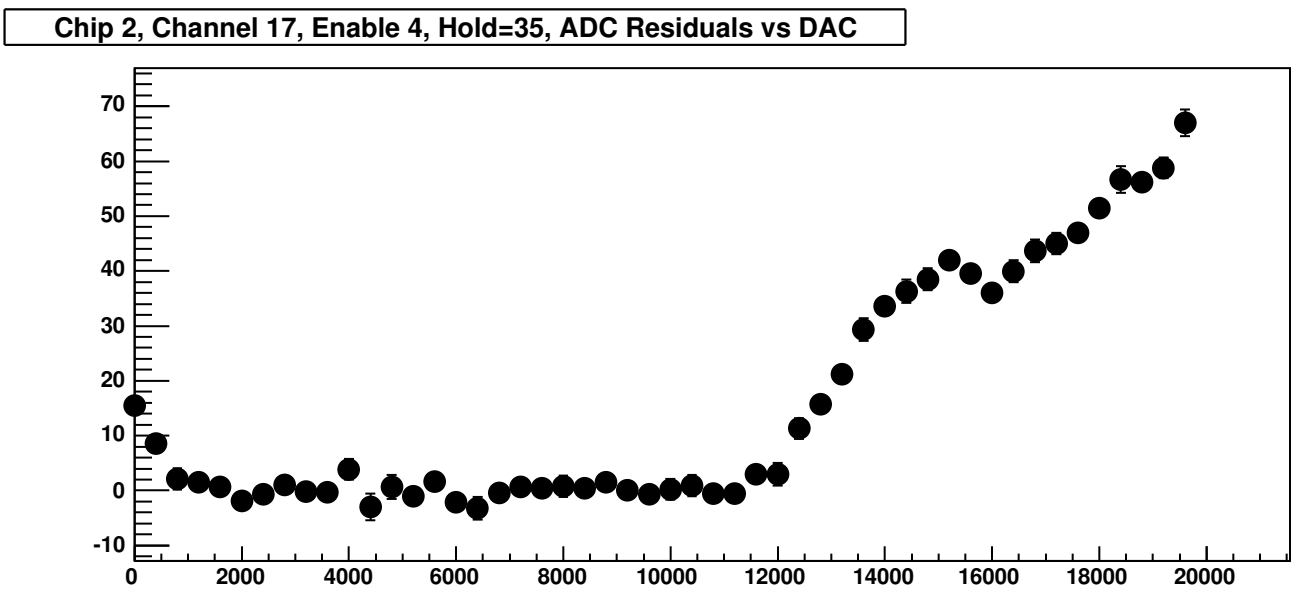

Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.05 / 23
p0 40.34 ± 0.7498
p1 0.01538 ± 0.0001095
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

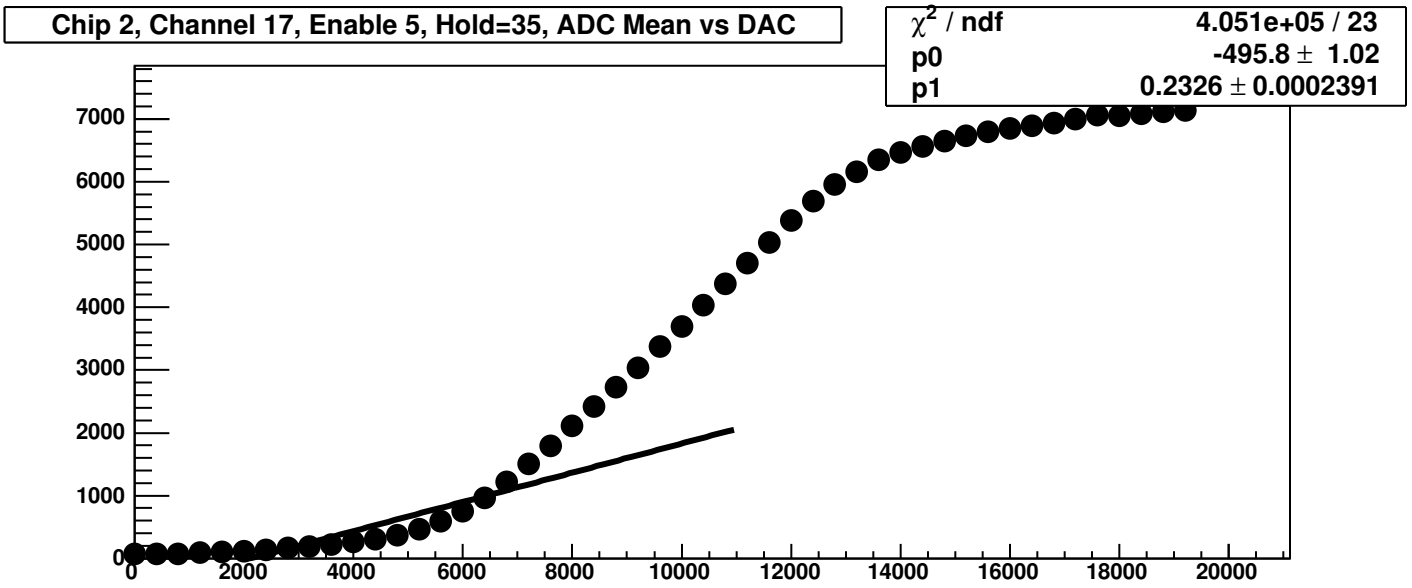

Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

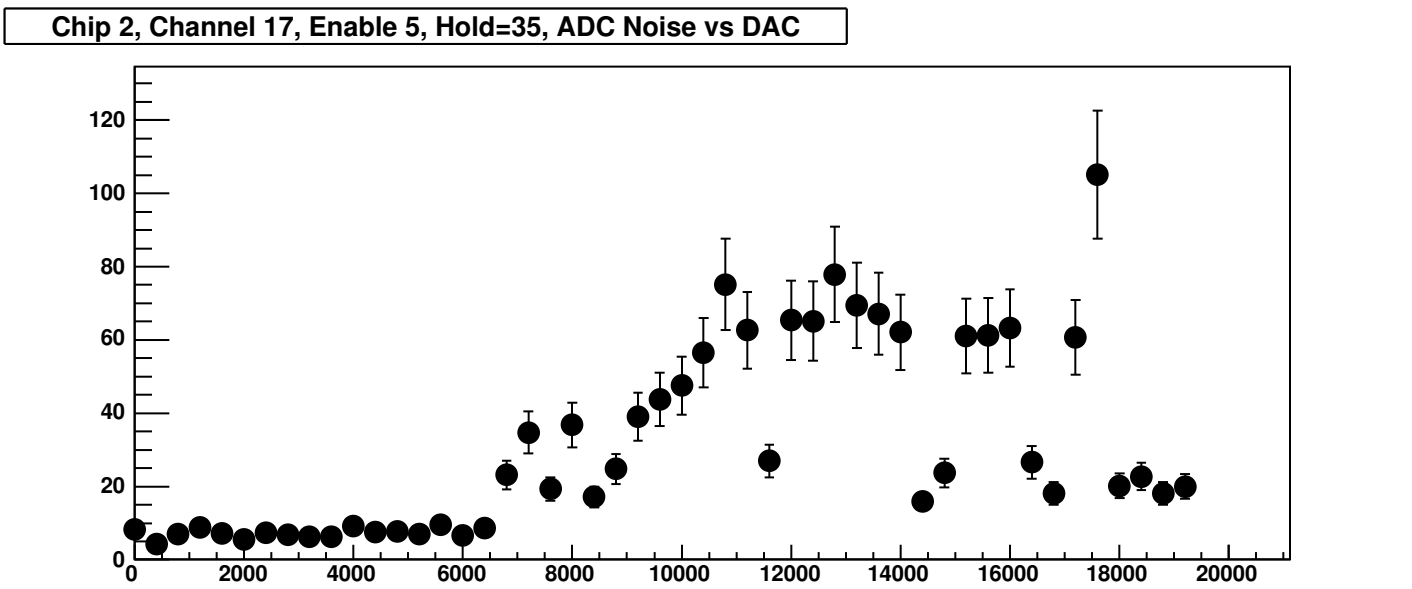

Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

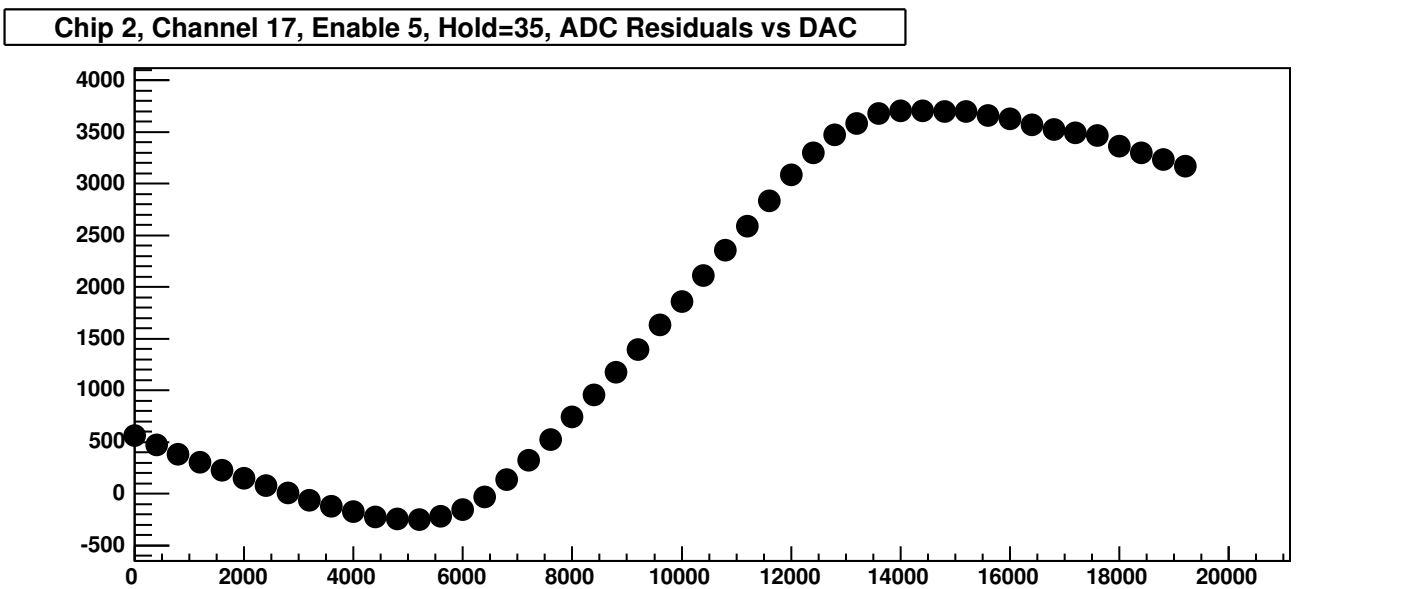

Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 18.72 / 23
p0 43.75 ± 0.7161
p1 0.01631 ± 0.0001108
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

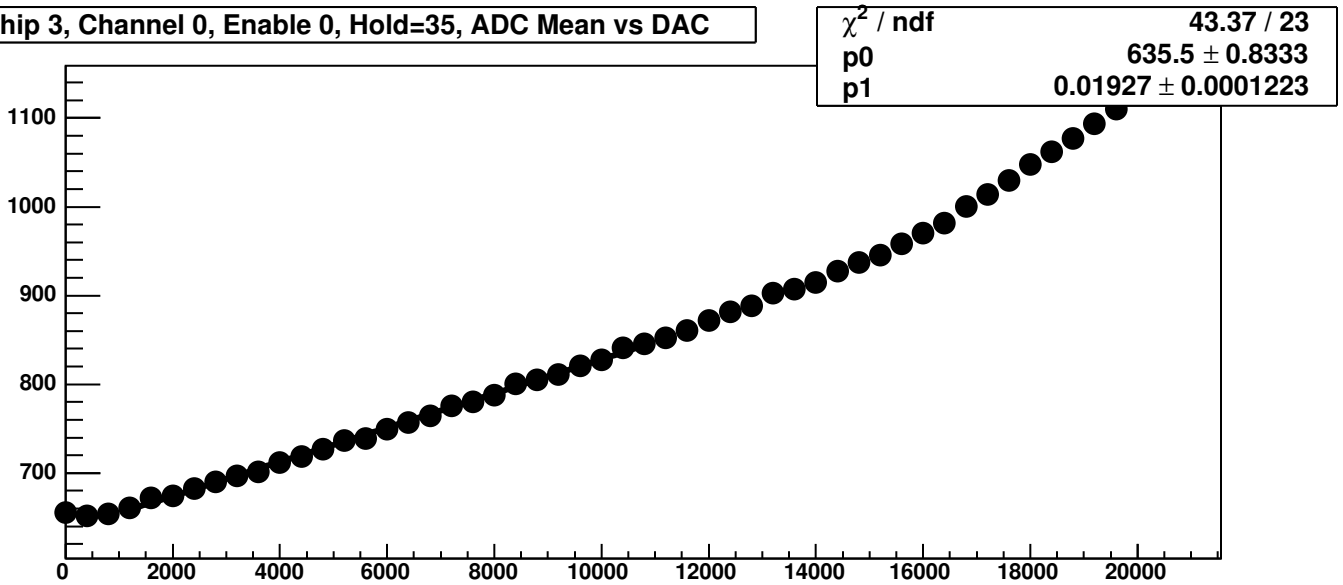

Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
70
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 2, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.051e+05 / 23
p0 -495.8 ± 1.02
p1 0.2326 ± 0.0002391
7000
6000
5000
4000
3000
2000
1000
0
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

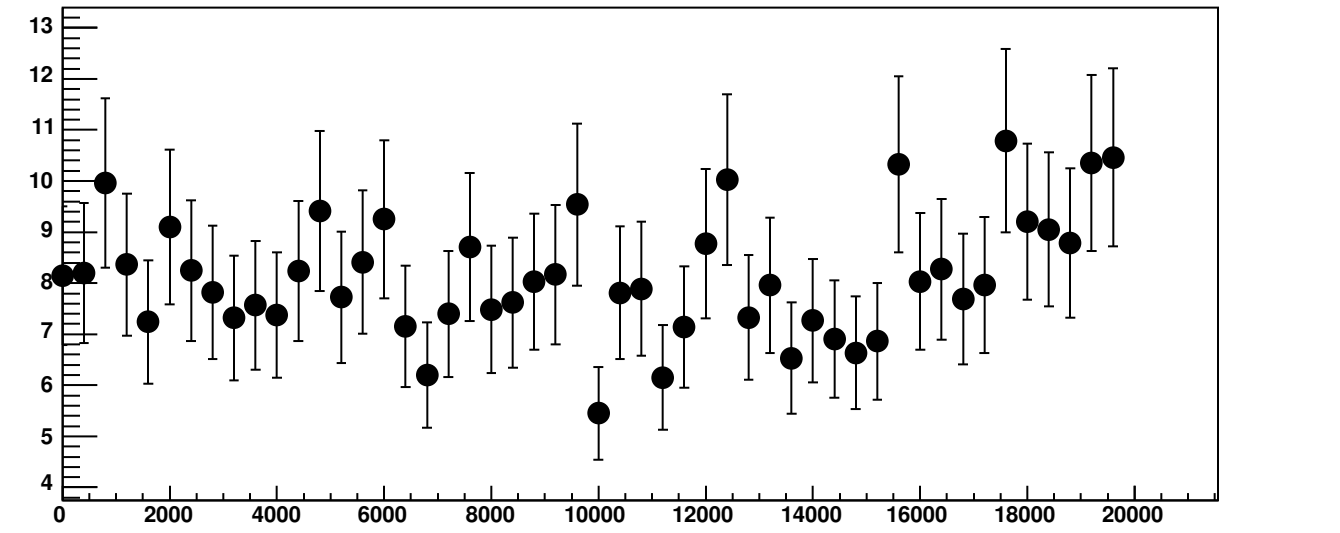

Chip 2, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000


Chip 2, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

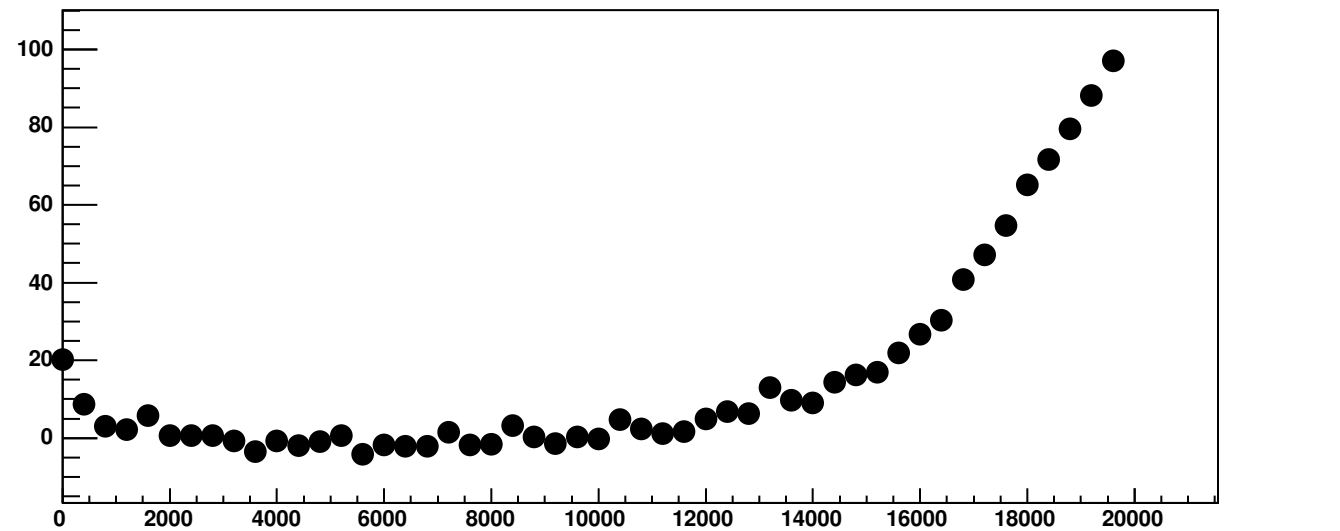
Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

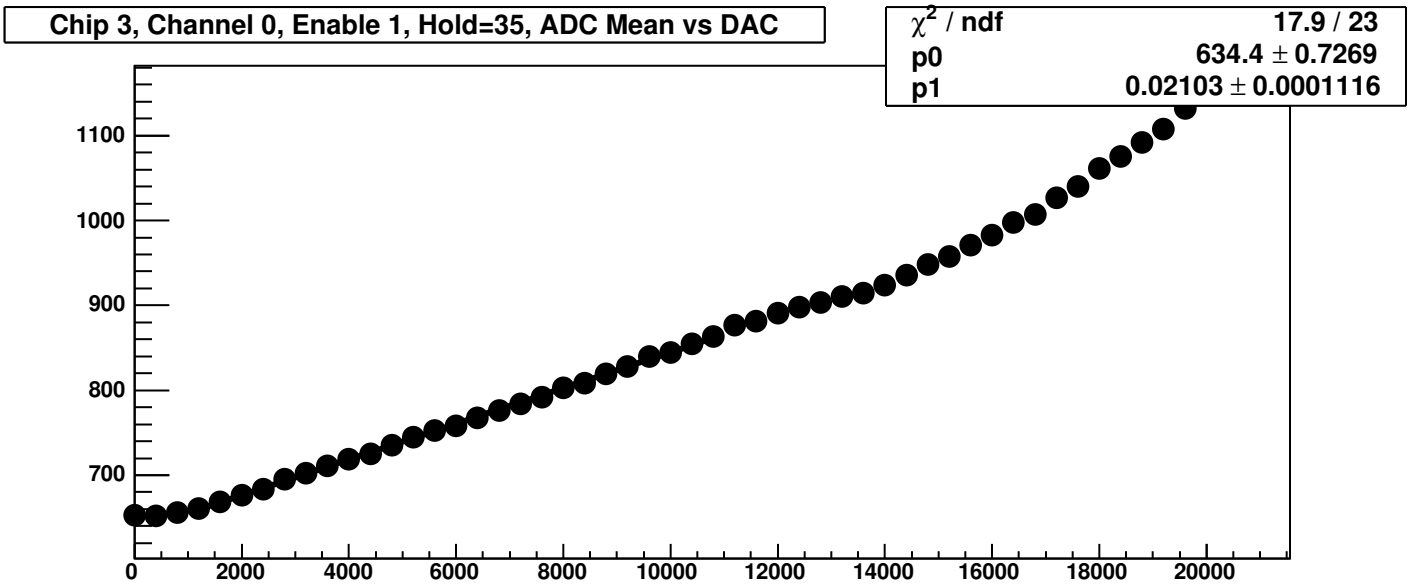


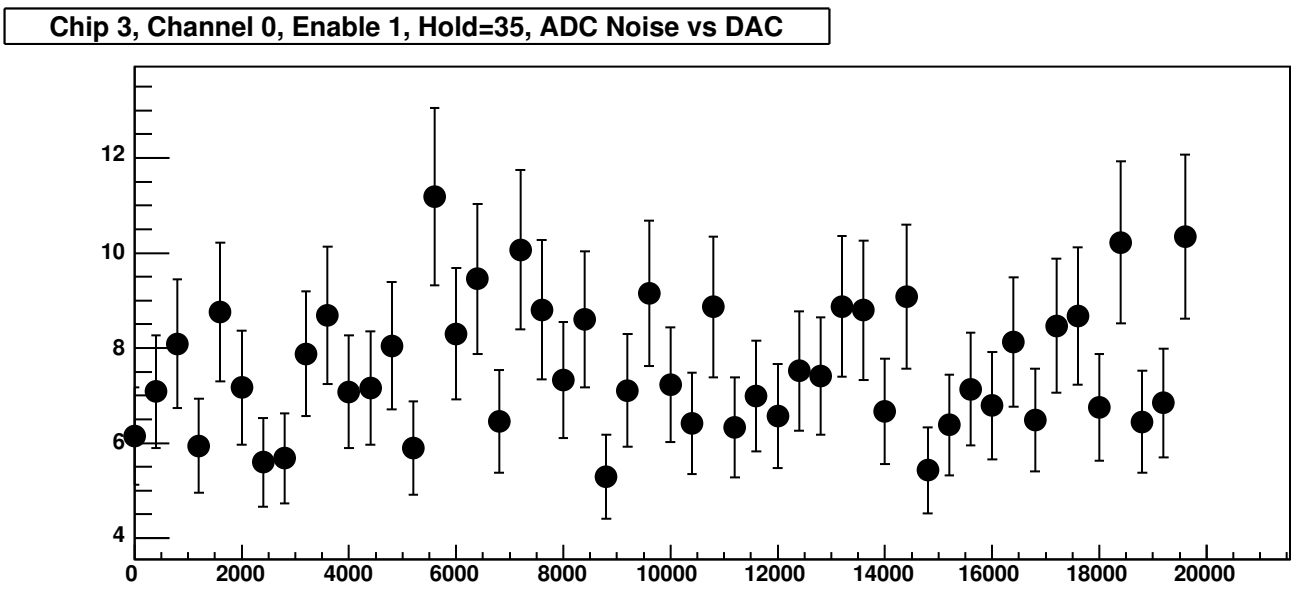
Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC

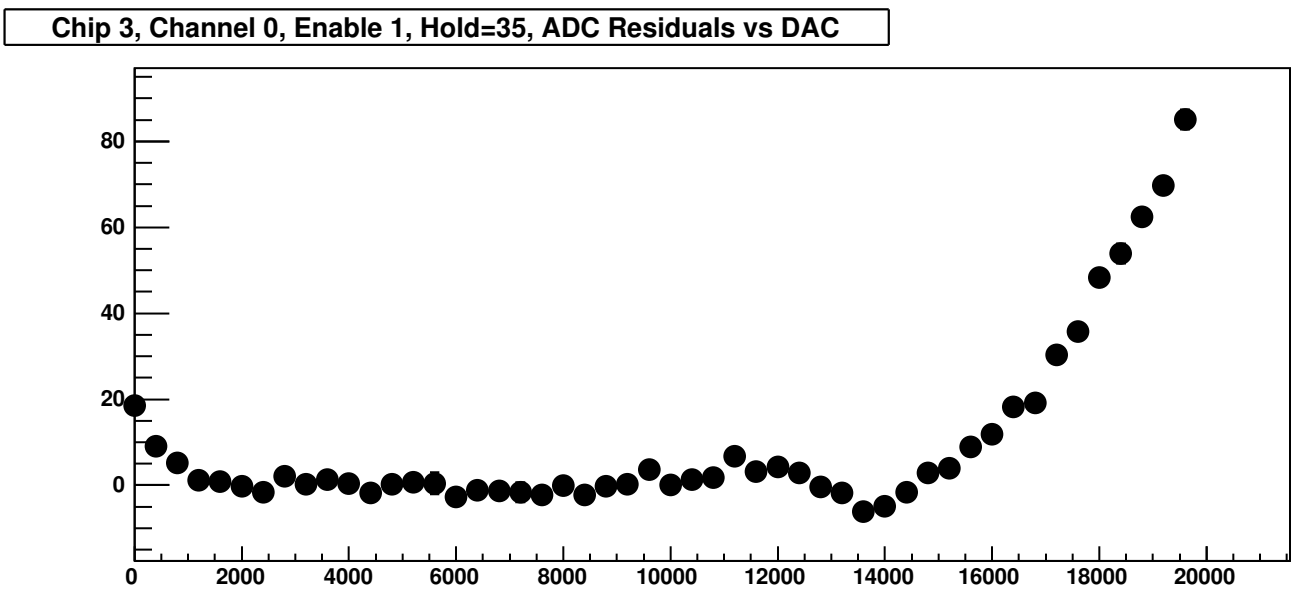


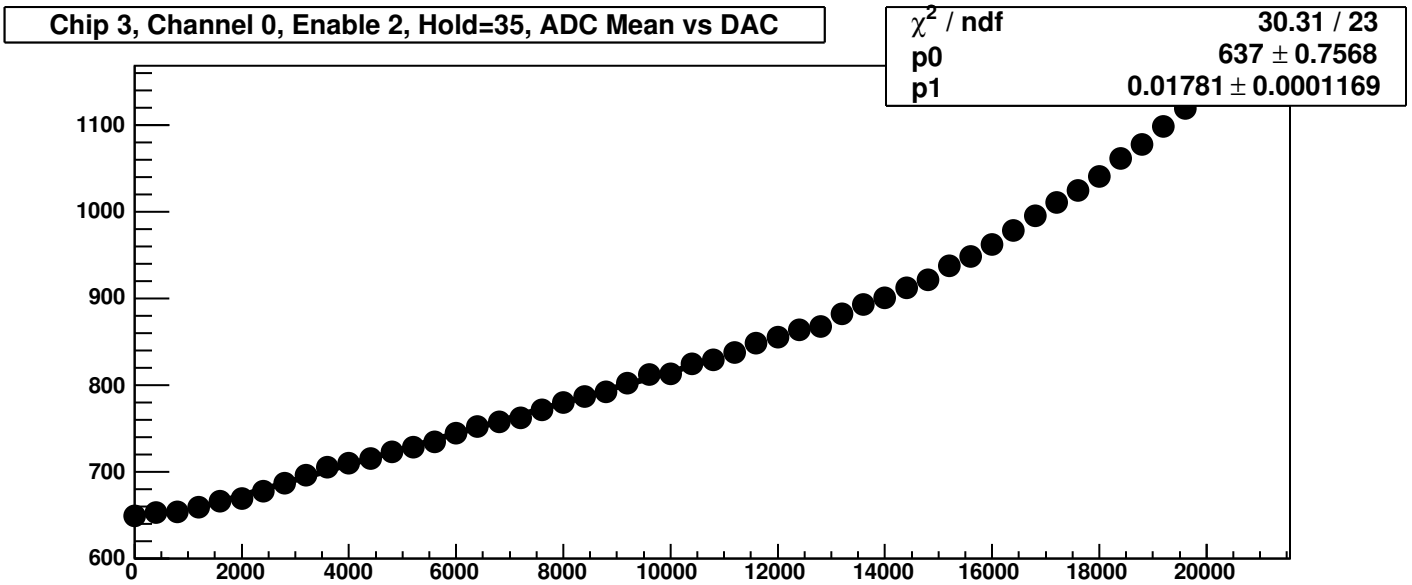
Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

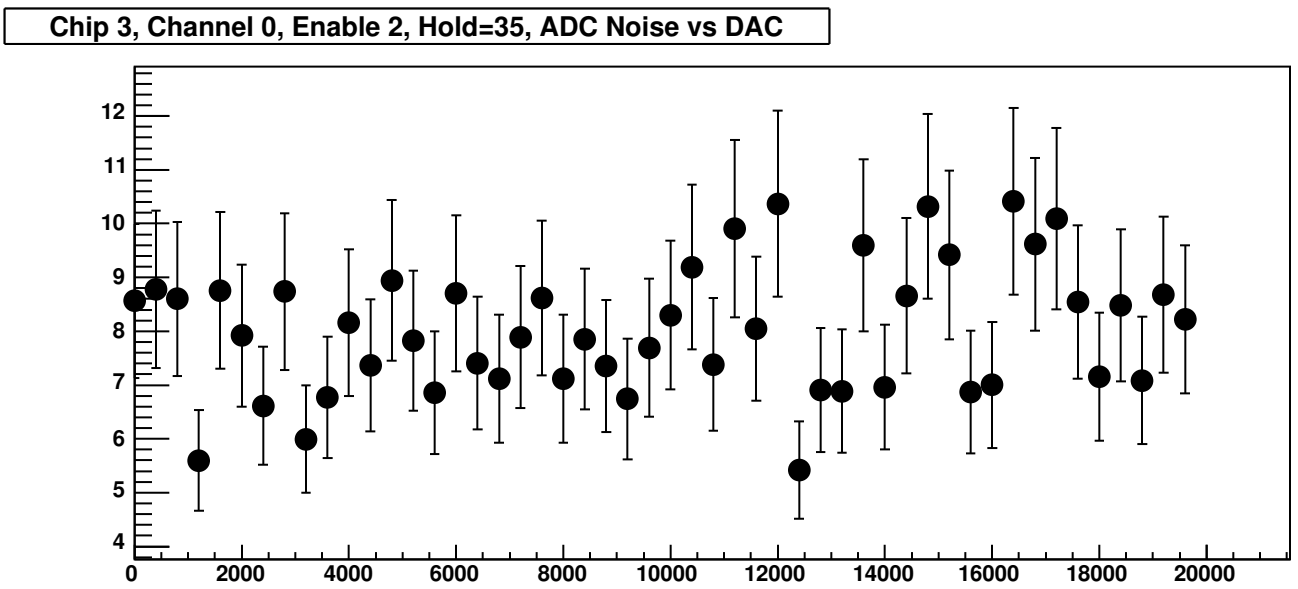


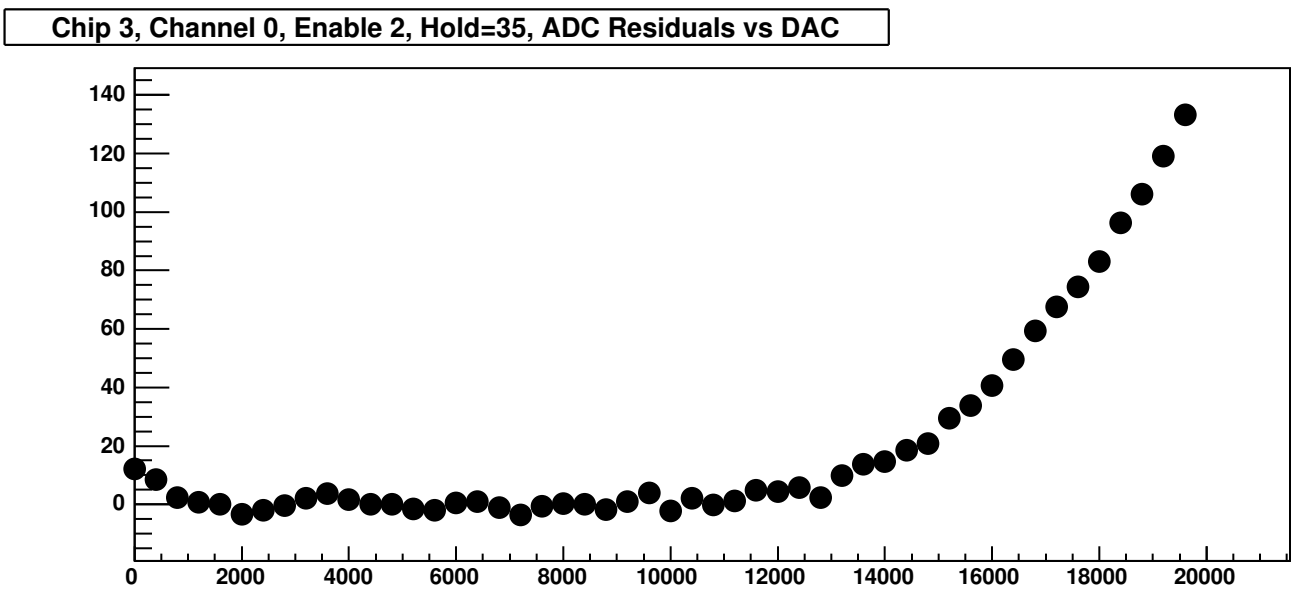

Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 17.9 / 23
p0 634.4 ± 0.7269
p1 0.02103 ± 0.0001116
1100
1000
900
800
700
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

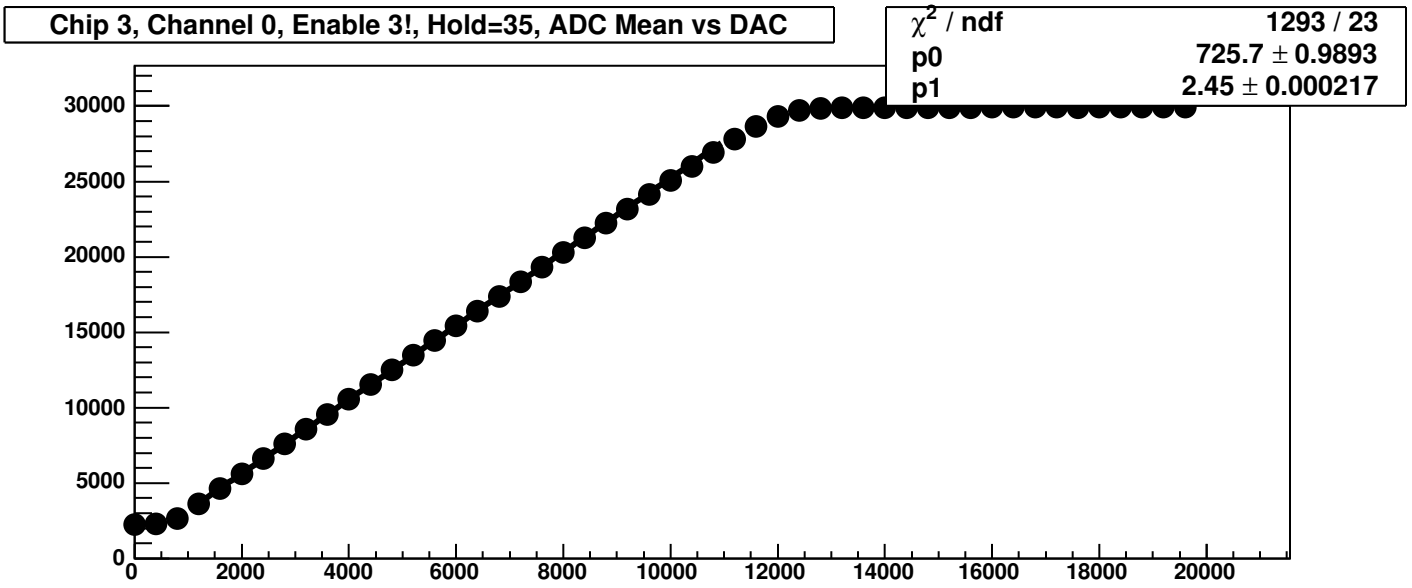

Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

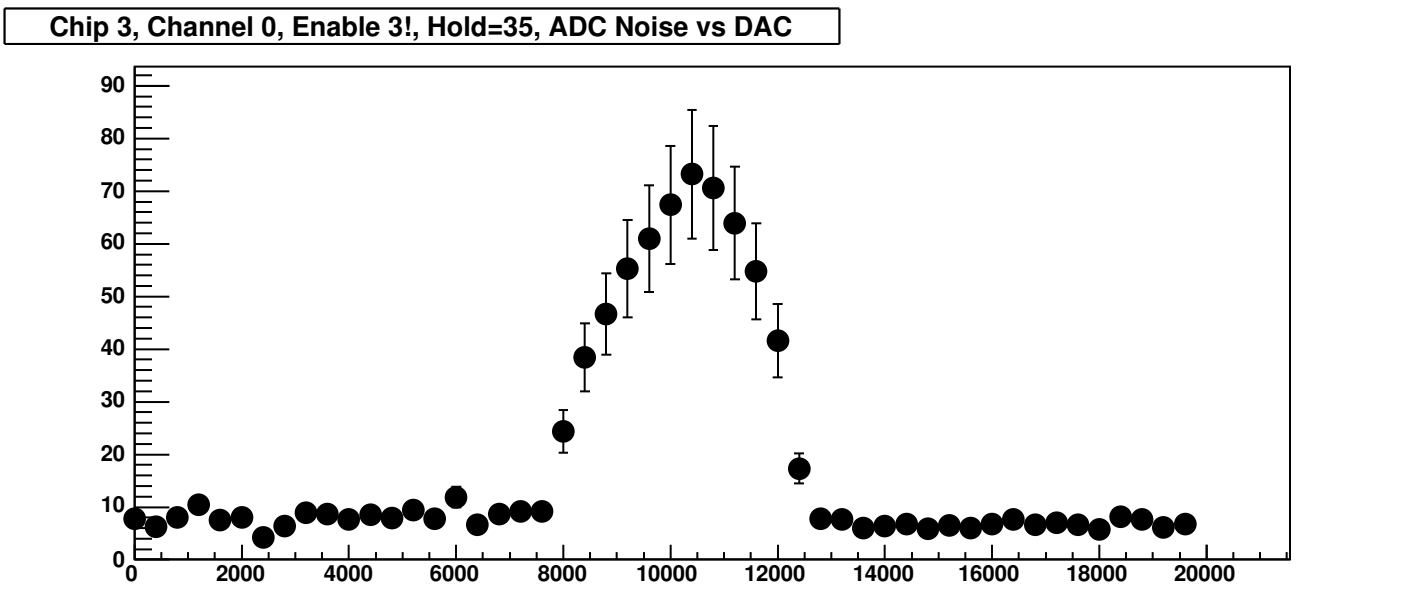

Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

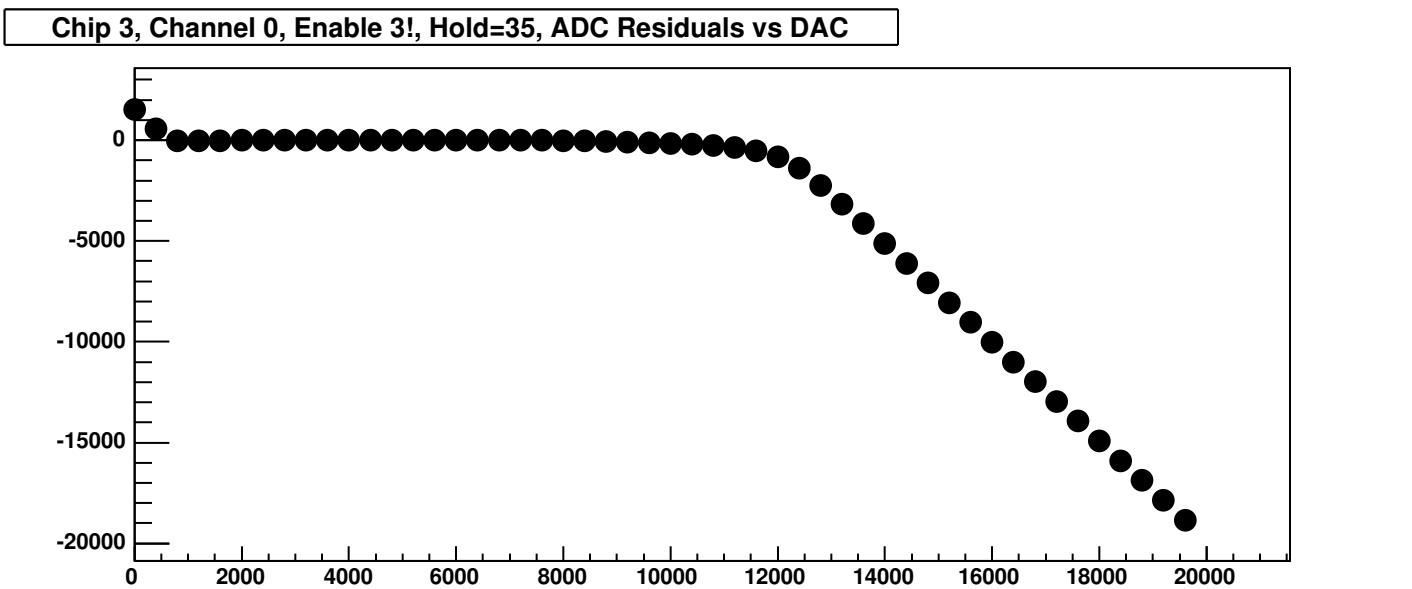

Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.31 / 23
p0 637 ± 0.7568
p1 0.01781 ± 0.0001169
1100
1000
900
800
700
600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

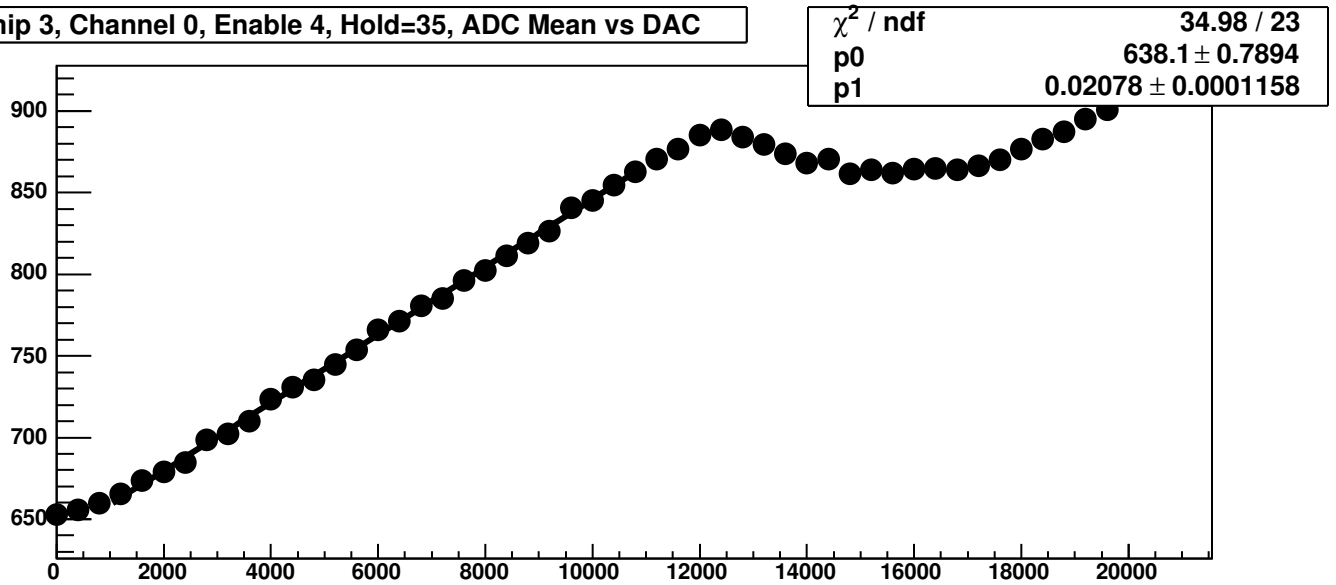

Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ^2 / ndf 1293 / 23
p0 725.7 ± 0.9893
p1 2.45 ± 0.000217
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

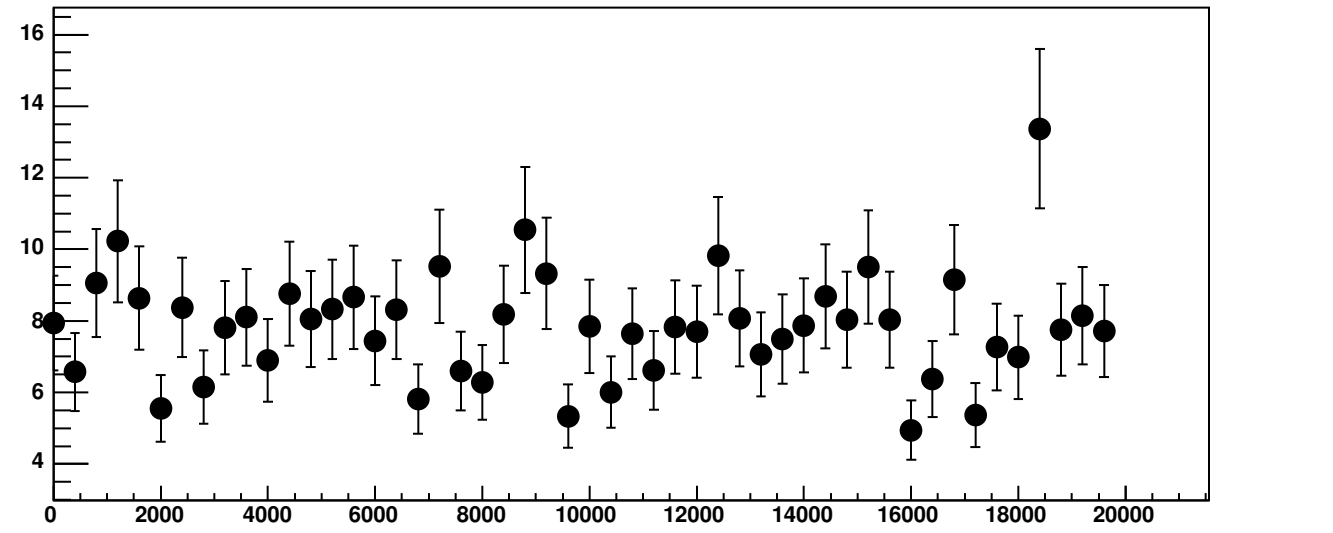

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

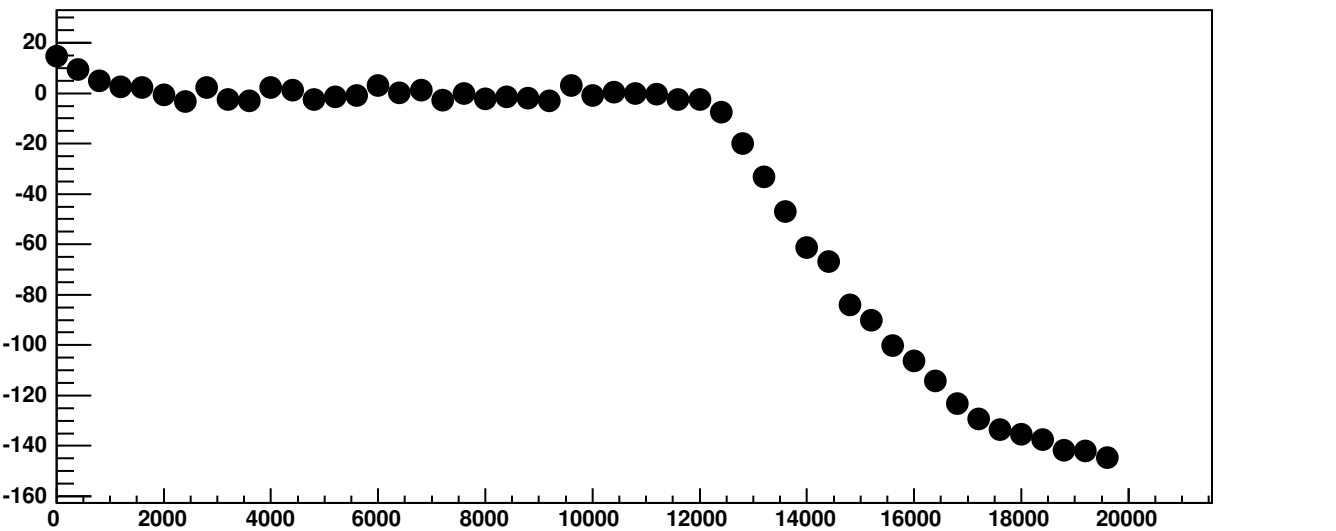
Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

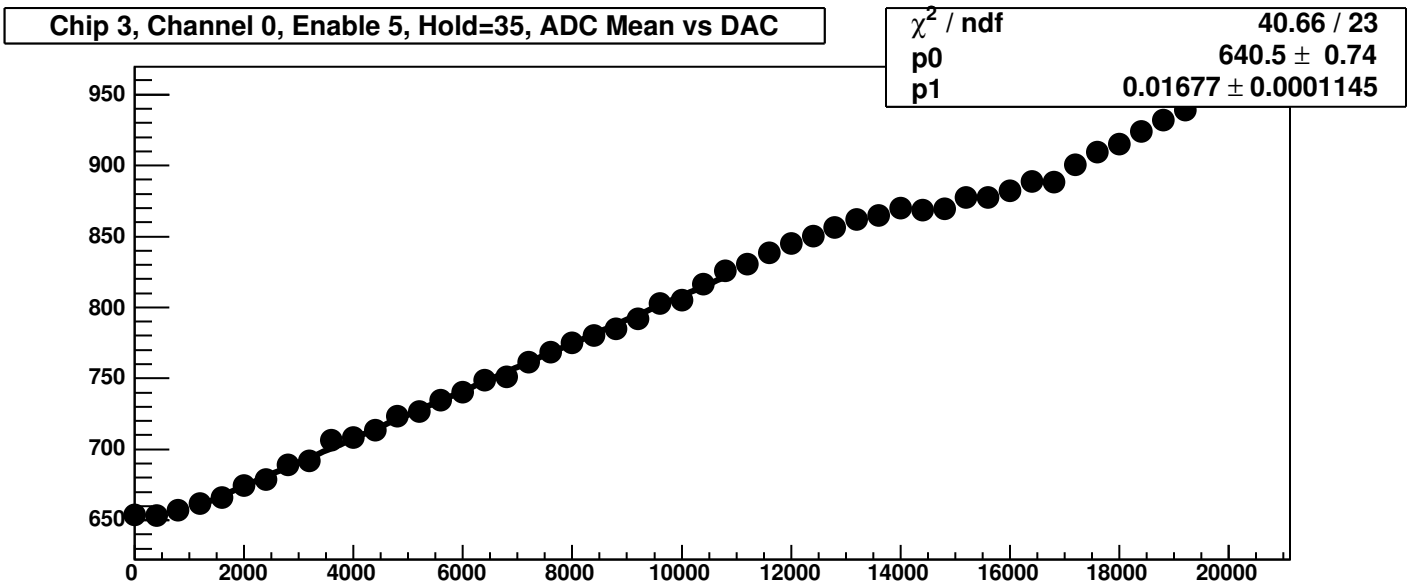


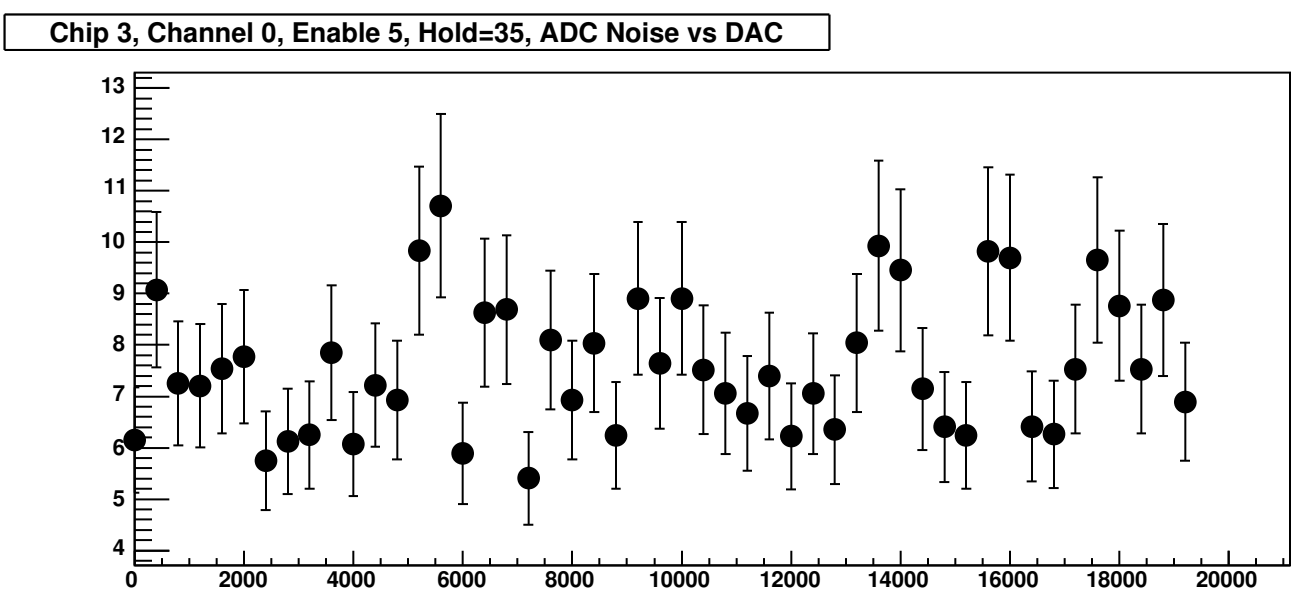
Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

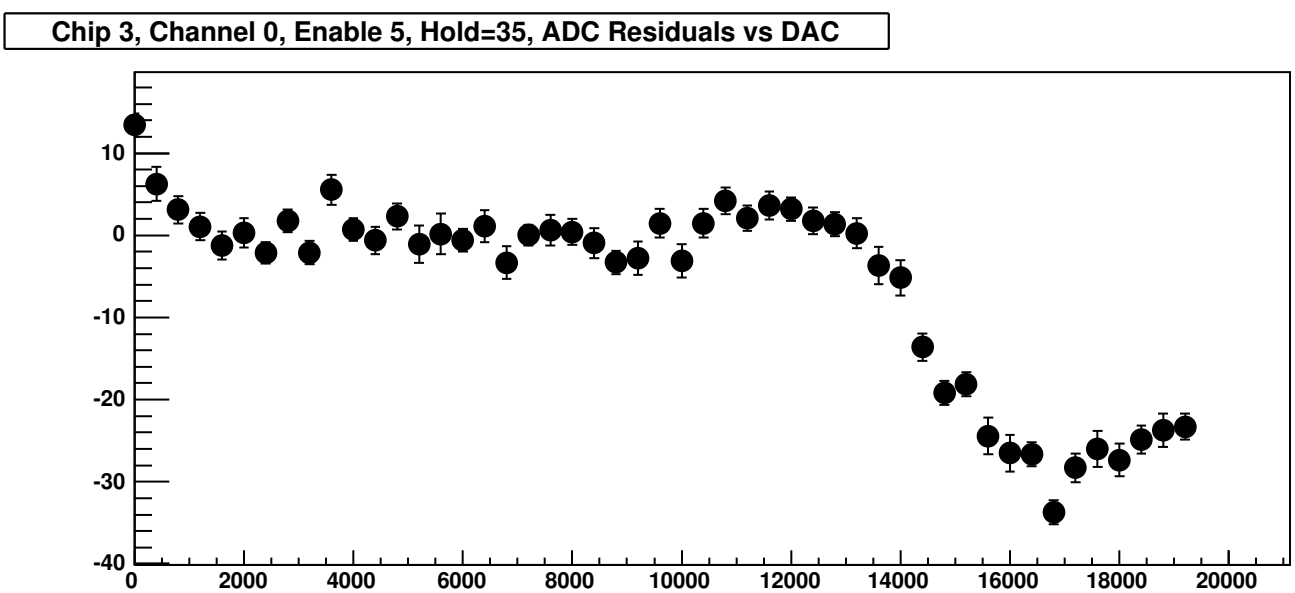


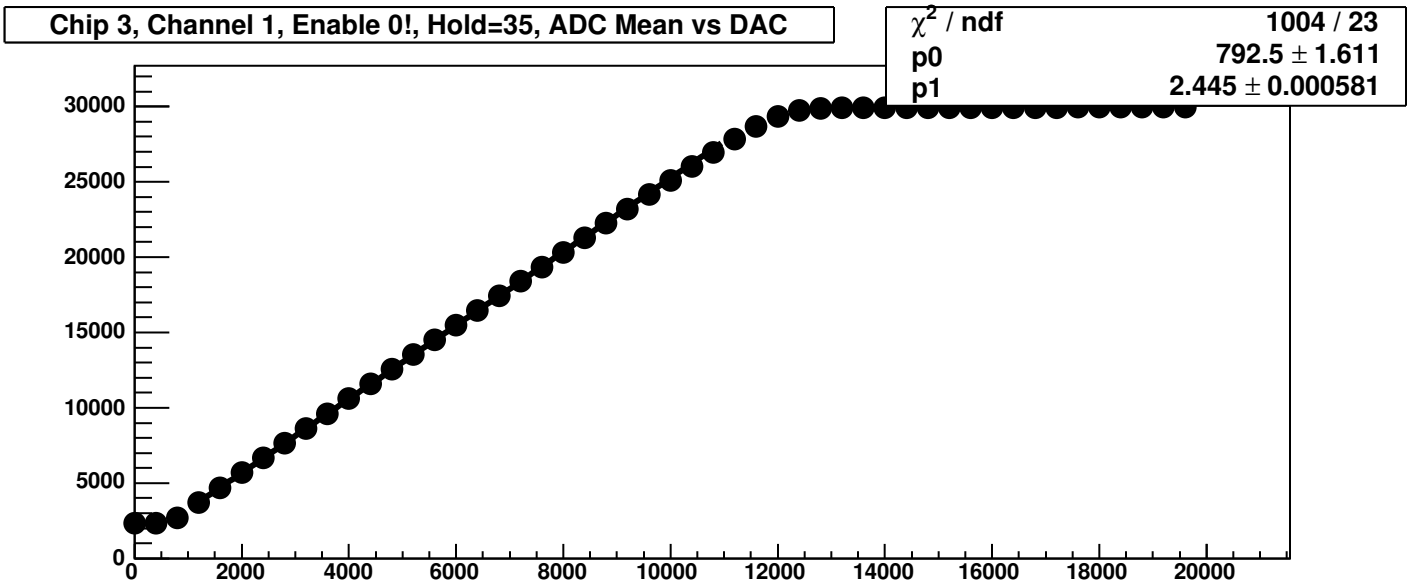
Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

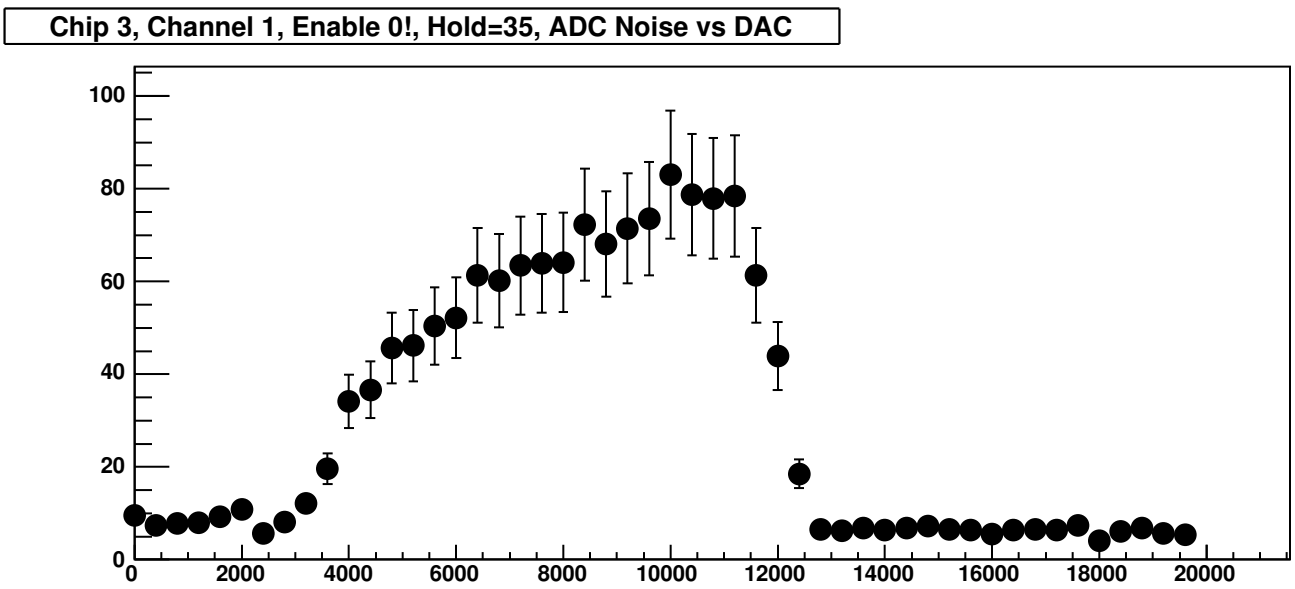


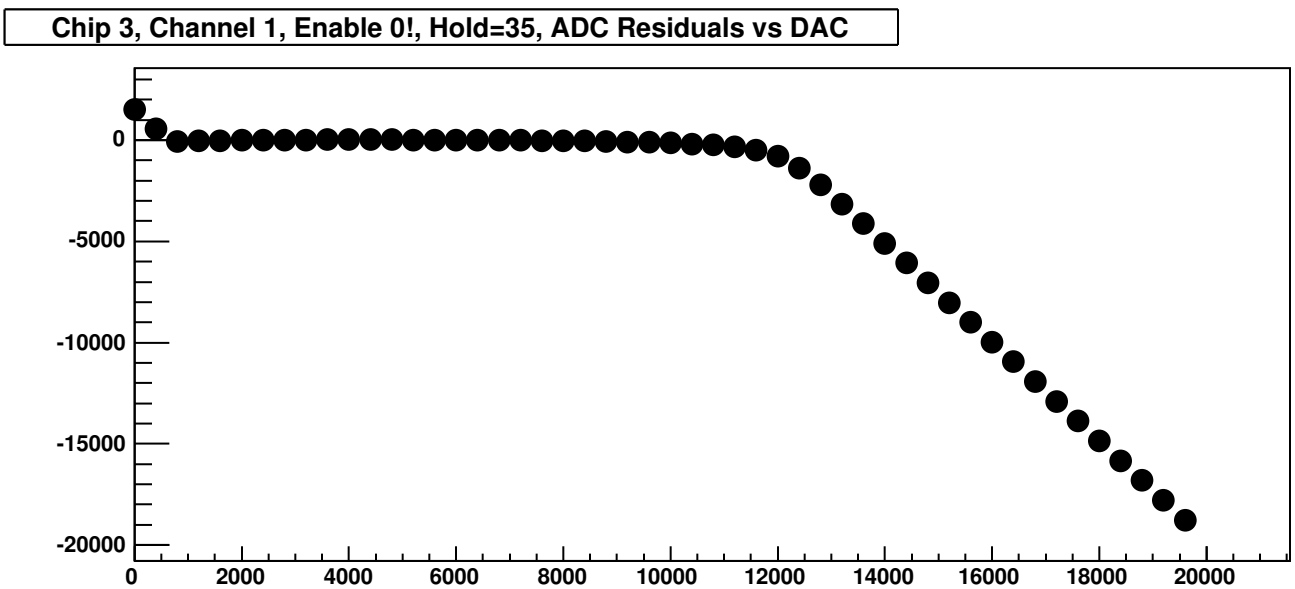

Chip 3, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 40.66 / 23
p0 640.5 ± 0.74
p1 0.01677 ± 0.0001145
950
900
850
800
750
700
650
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

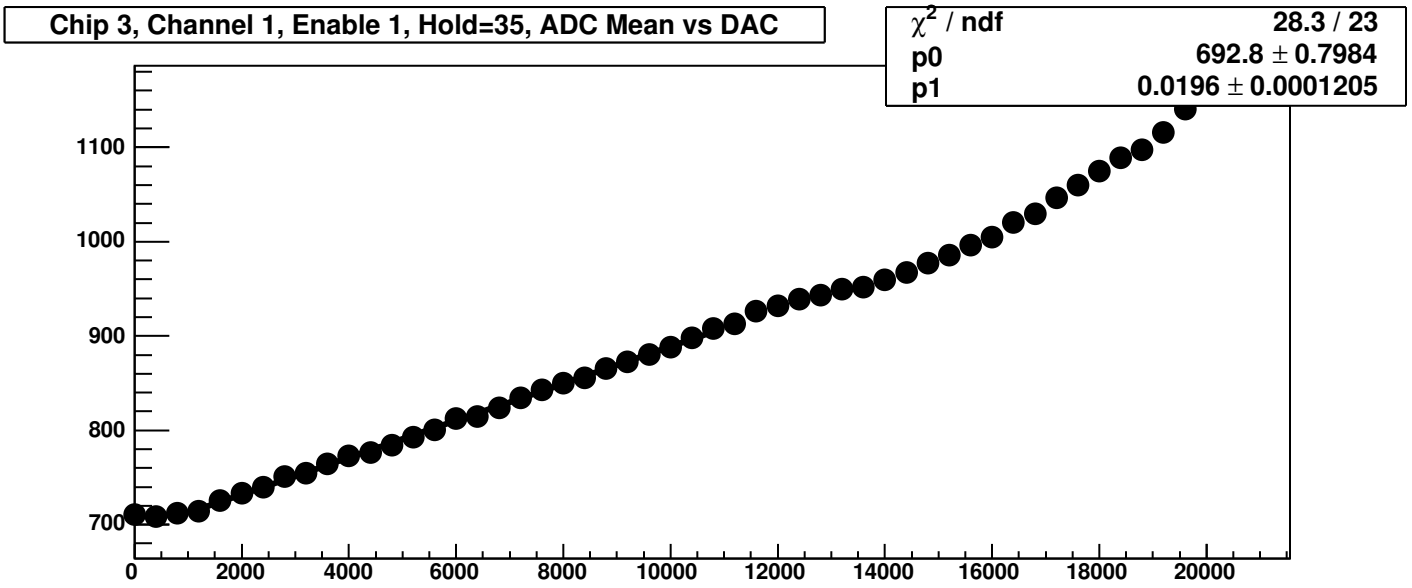

Chip 3, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

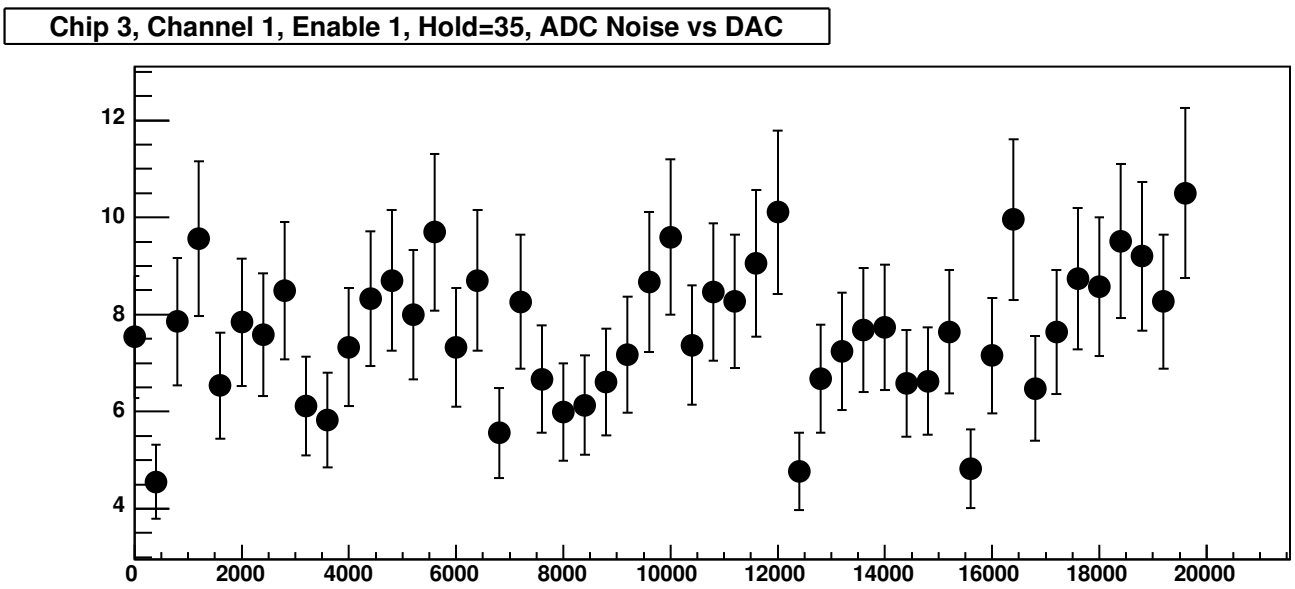

Chip 3, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

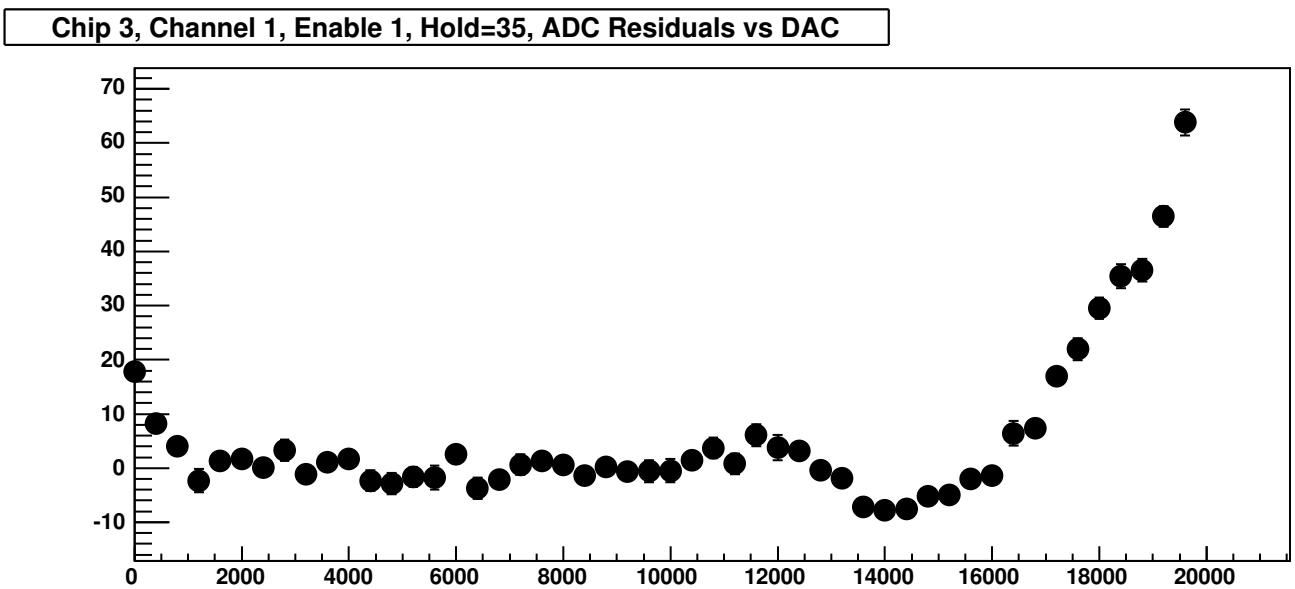

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1004 / 23
p0 792.5 ± 1.611
p1 2.445 ± 0.000581
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

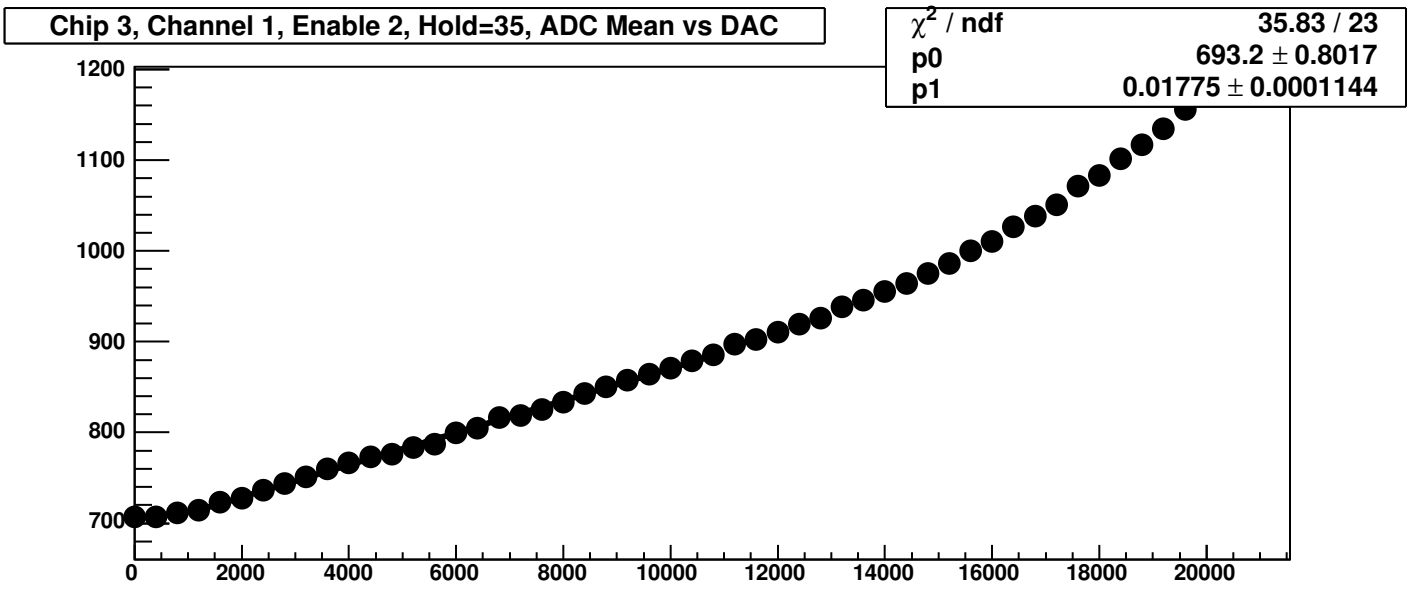

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

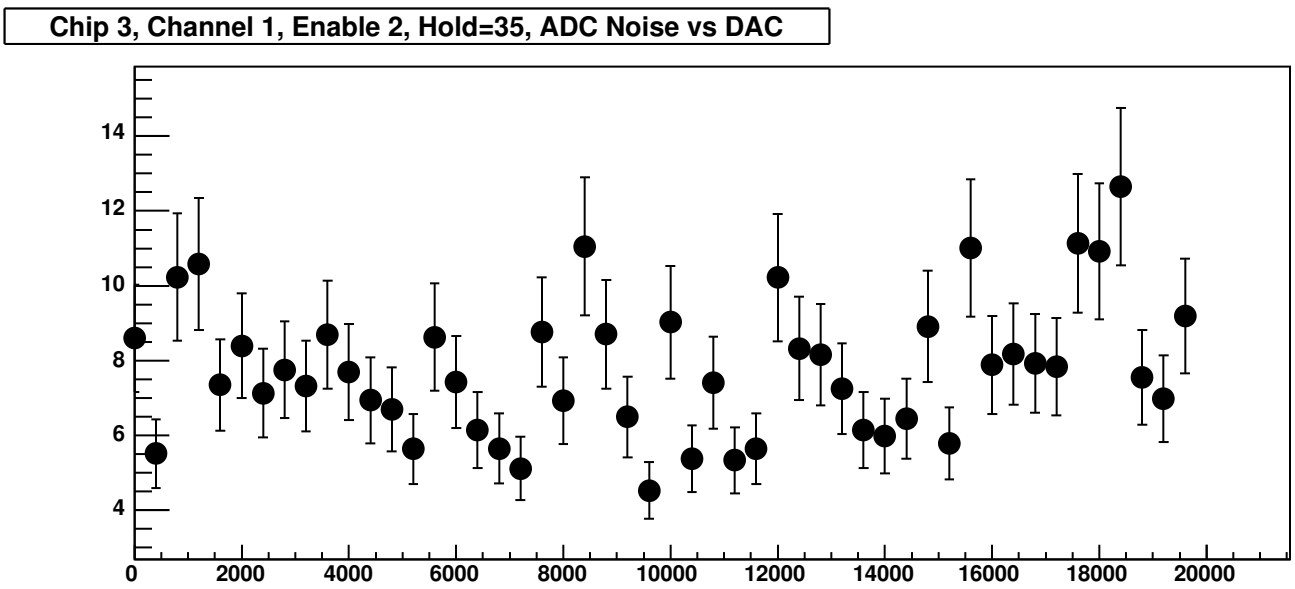

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

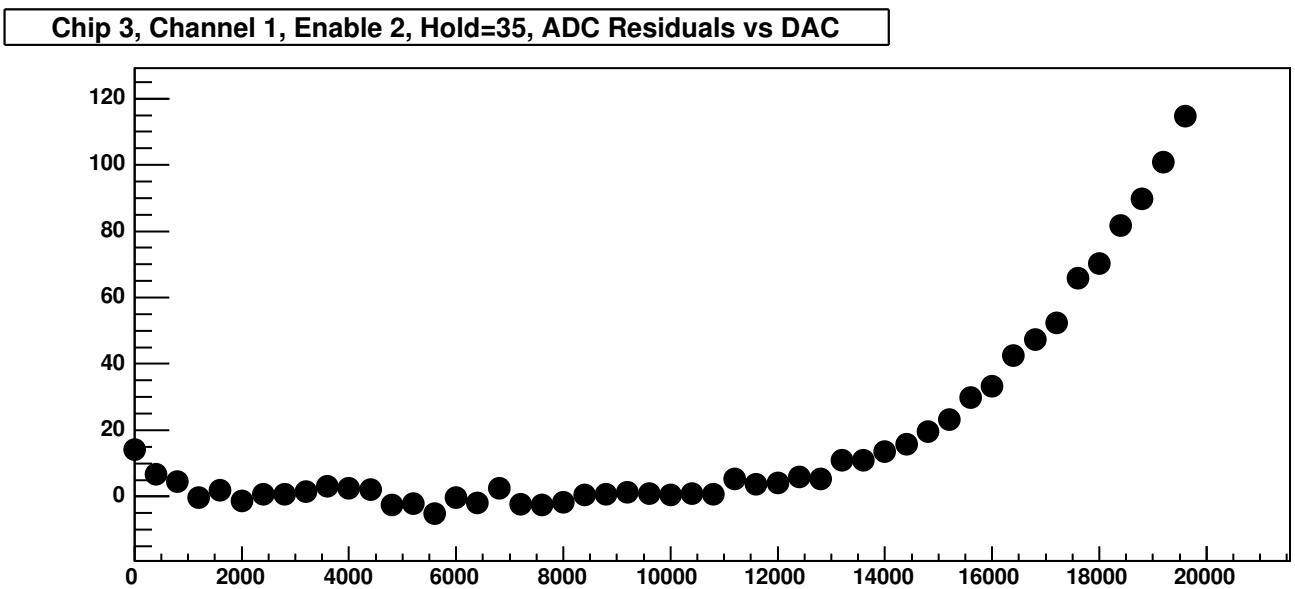

Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.3 / 23
p0 692.8 ± 0.7984
p1 0.0196 ± 0.0001205
1100
1000
900
800
700
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
70
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

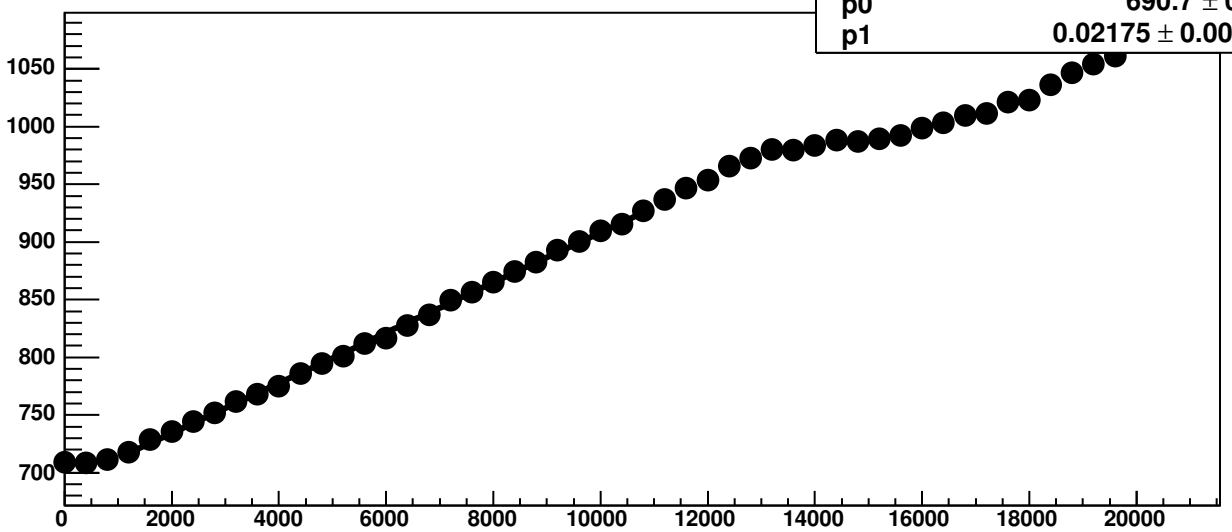

Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.83 / 23
p0 693.2 ± 0.8017
p1 0.01775 ± 0.0001144
1200
1100
1000
900
800
700
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

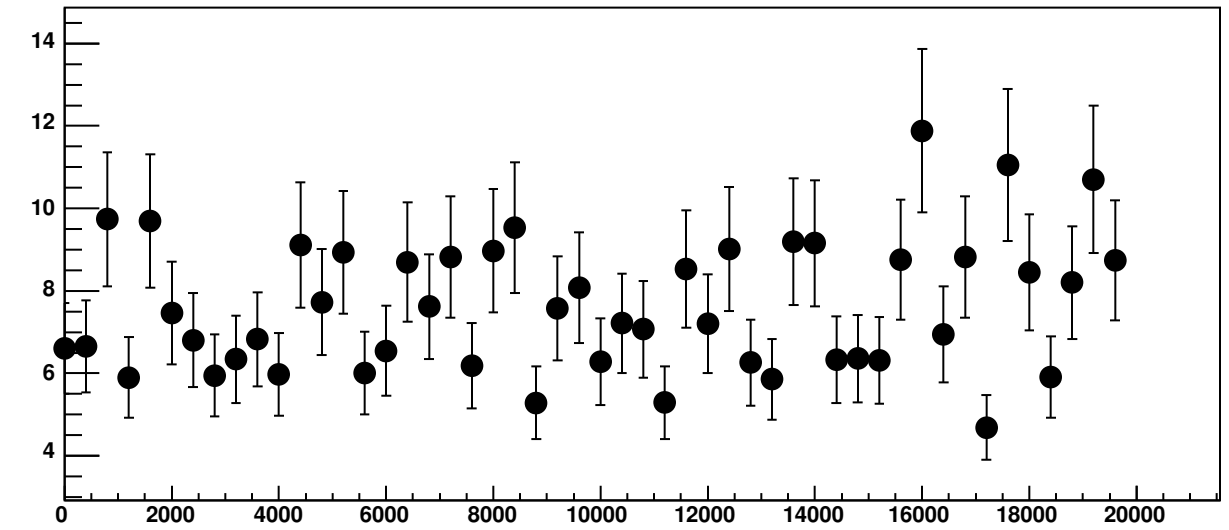

Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

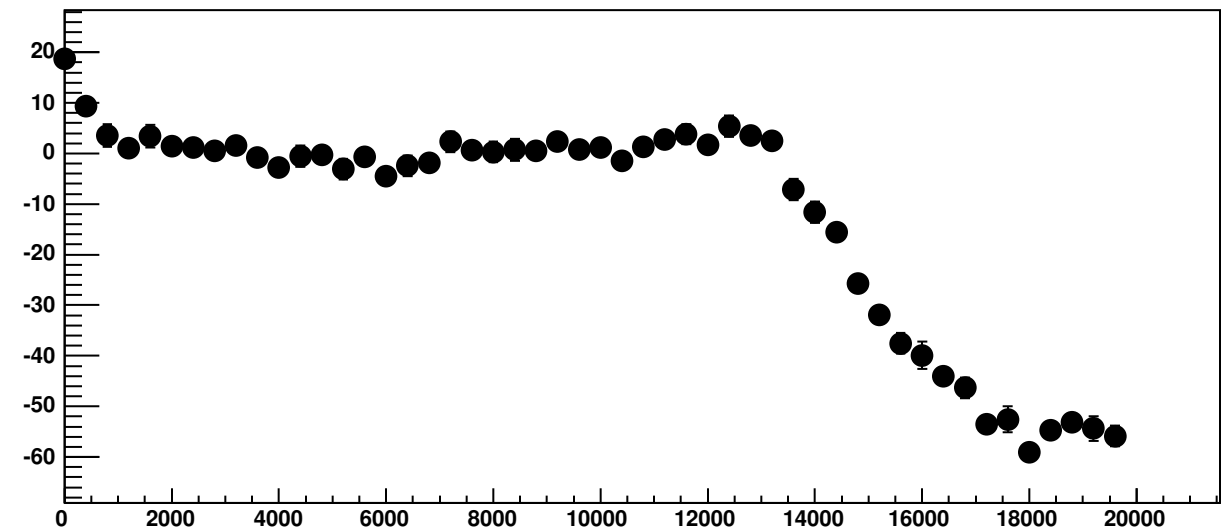
χ^2 / ndf	29.97 / 23
p0	690.7 ± 0.7288
p1	0.02175 ± 0.0001103



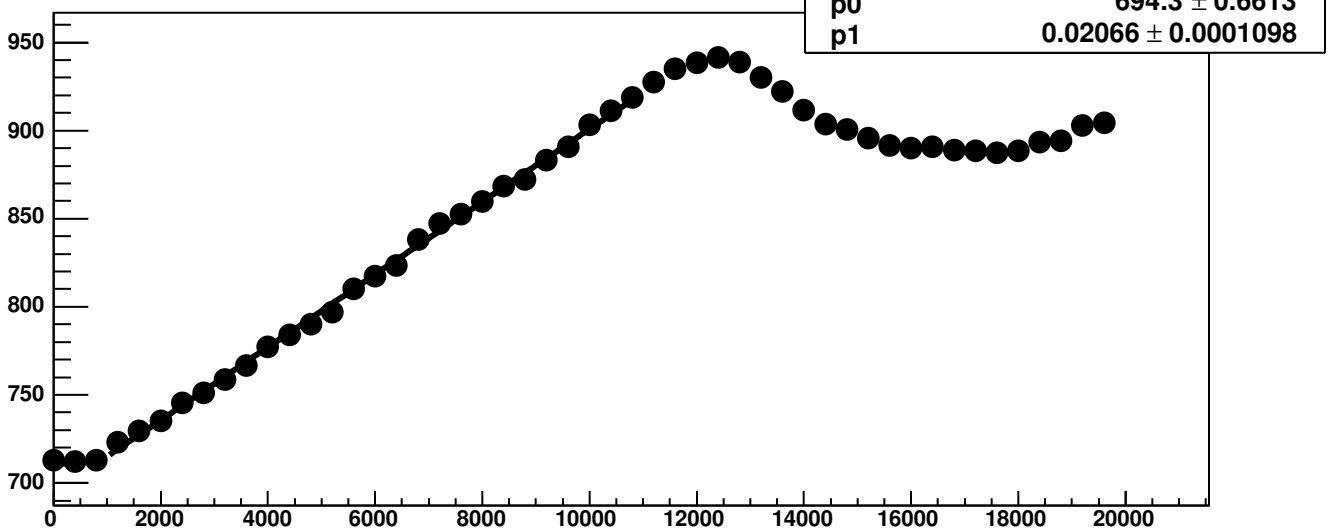
Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



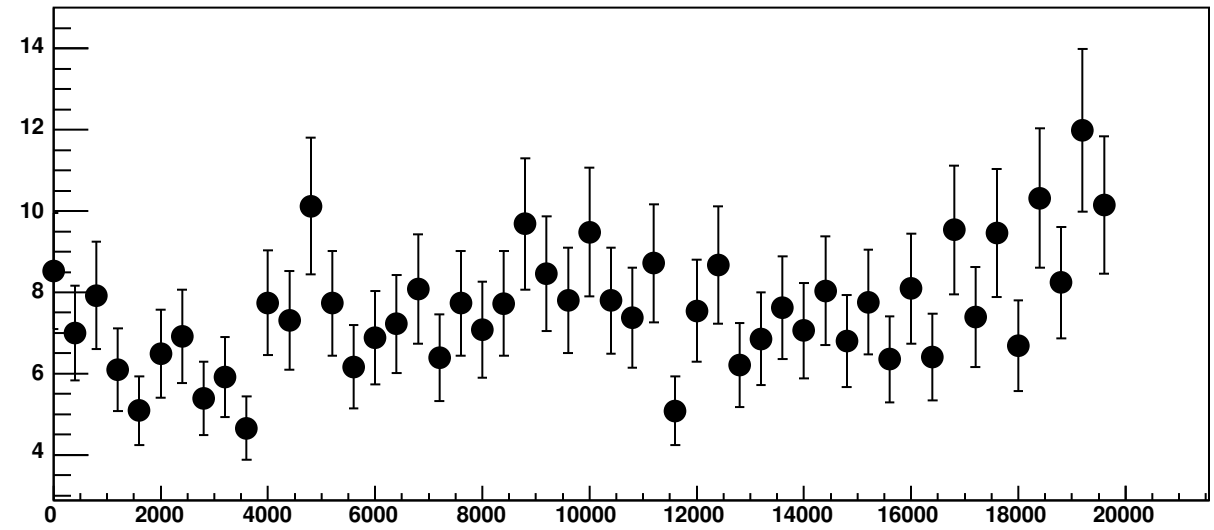
Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



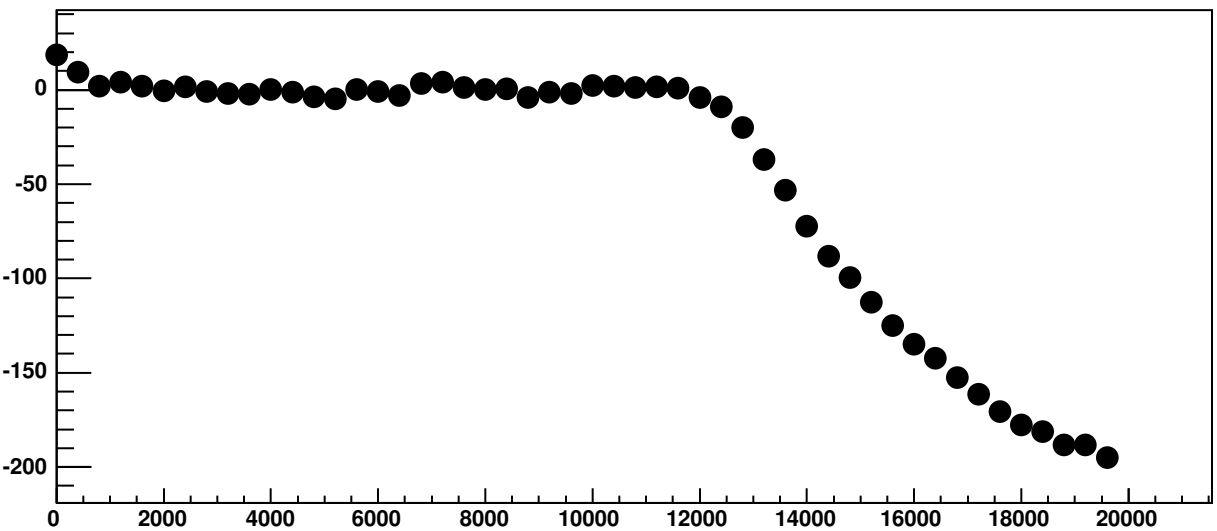
Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



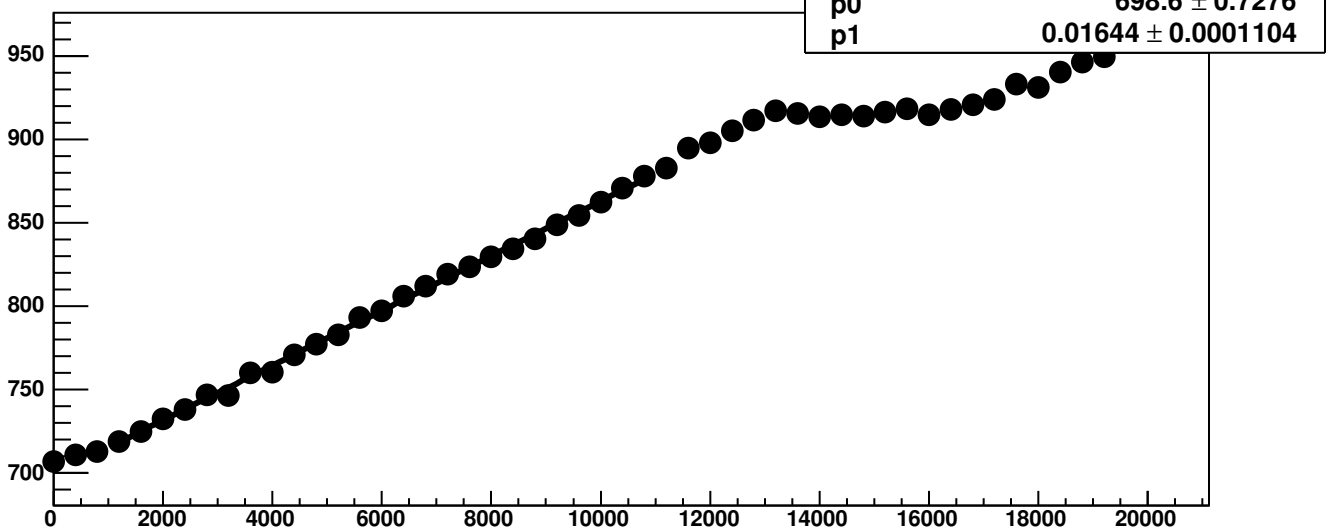
Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



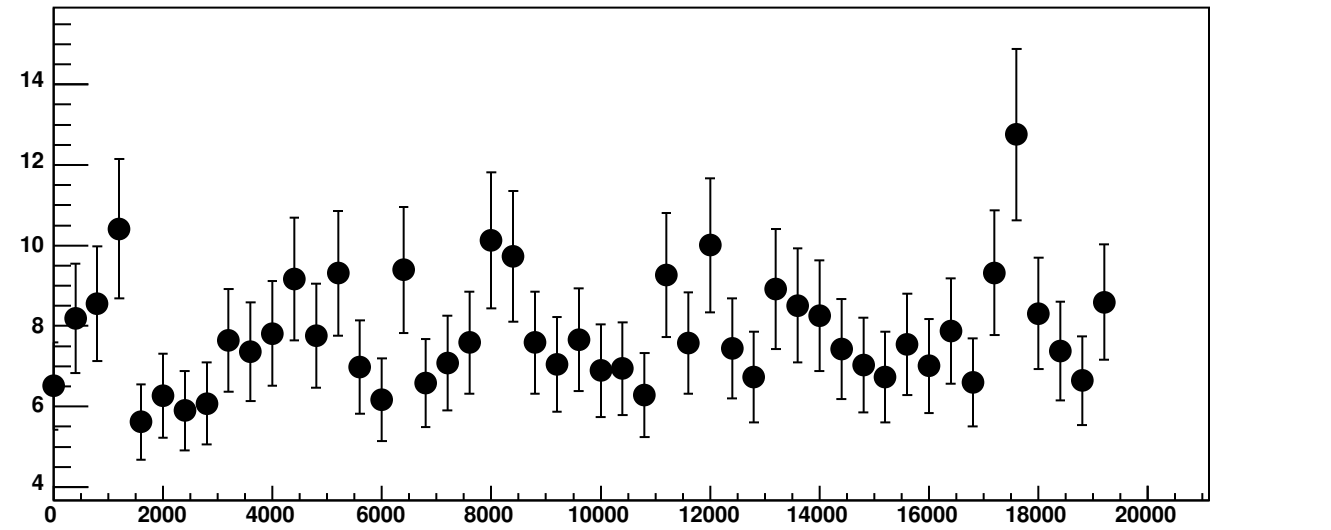
Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



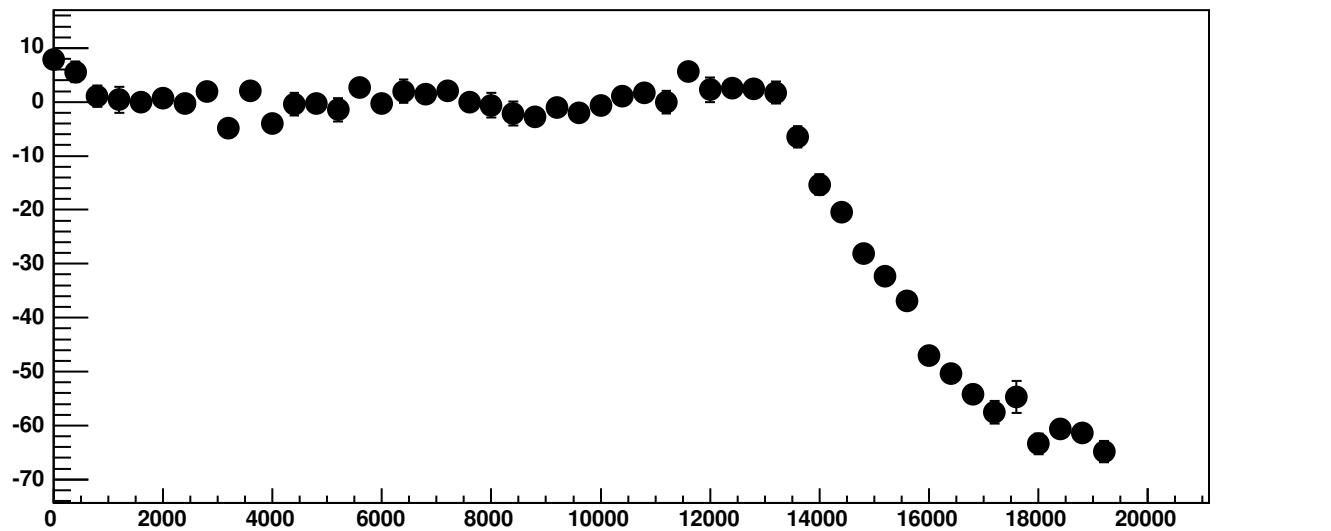
Chip 3, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

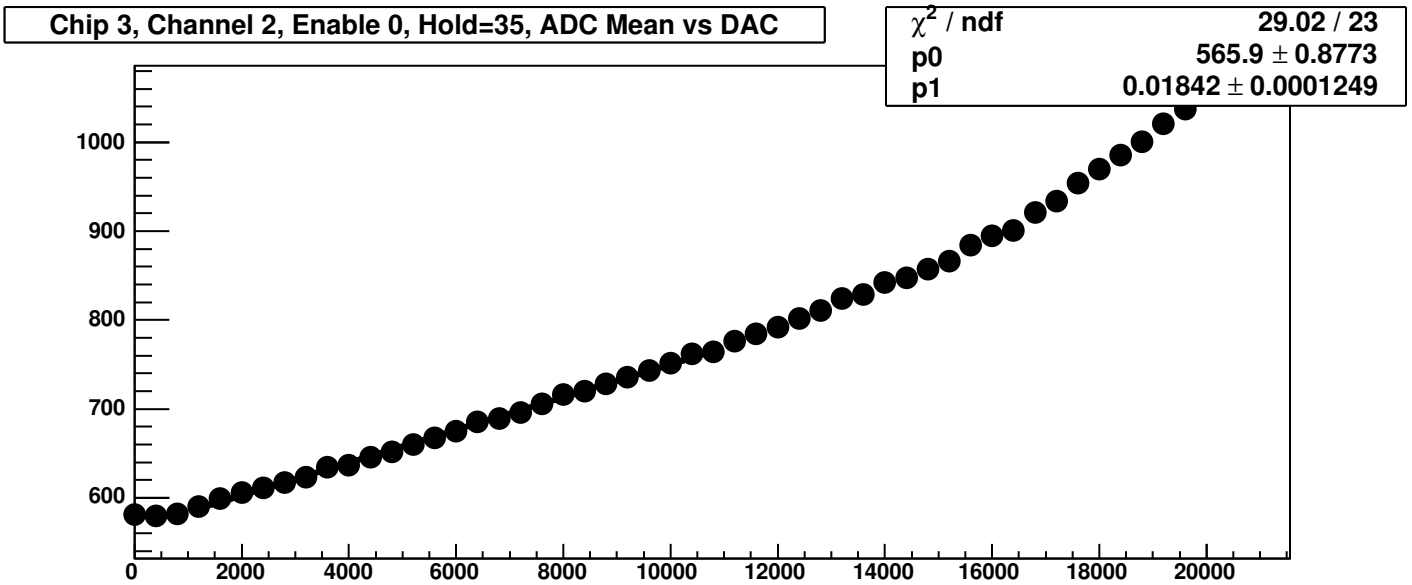


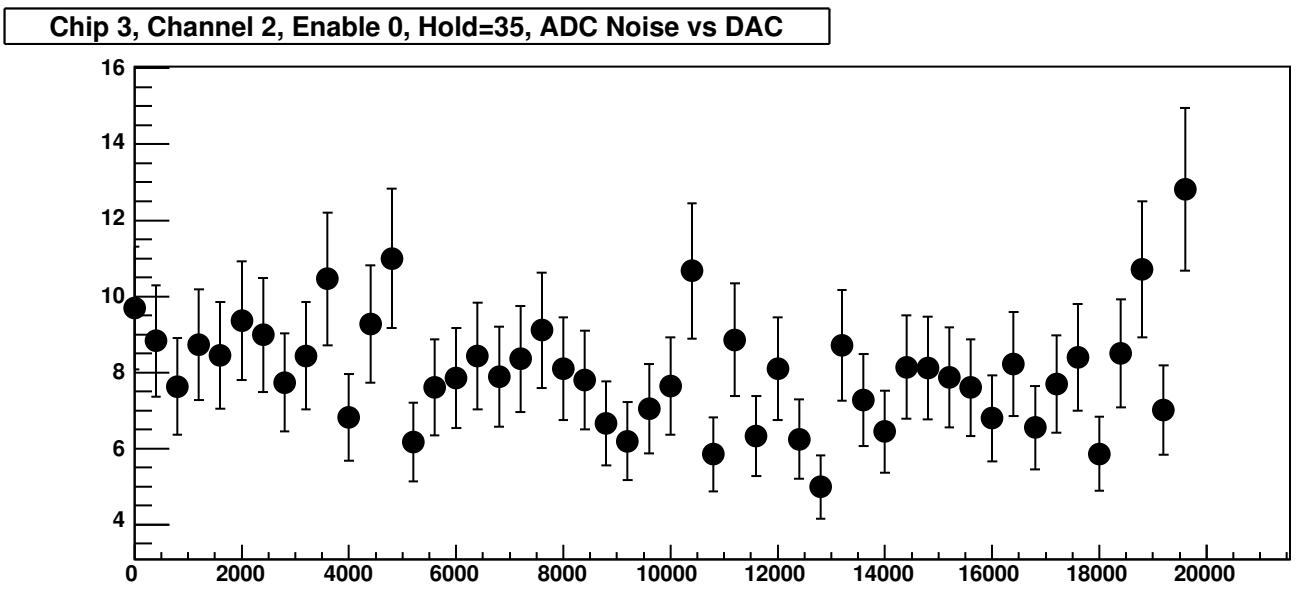
Chip 3, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

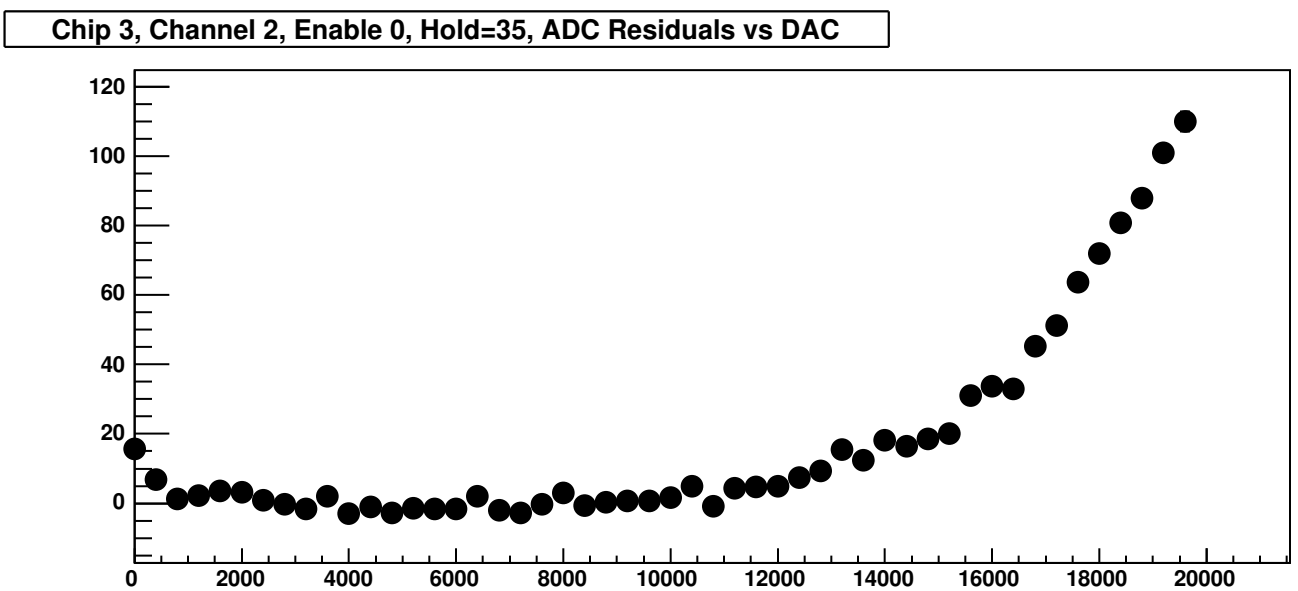


Chip 3, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



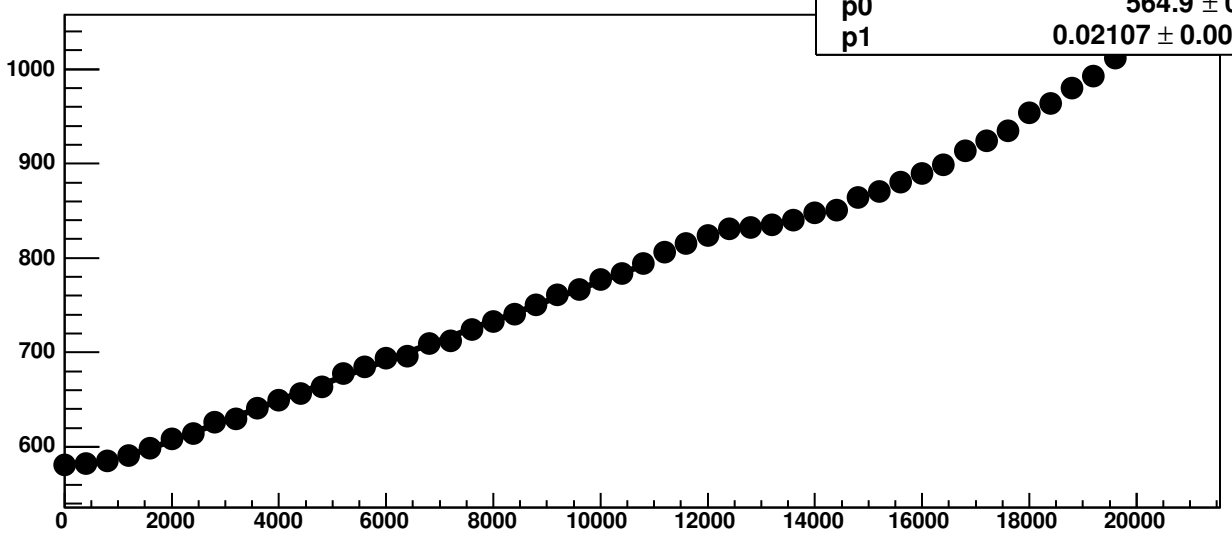

Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 29.02 / 23
p0 565.9 ± 0.8773
p1 0.01842 ± 0.0001249
1000
900
800
700
600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

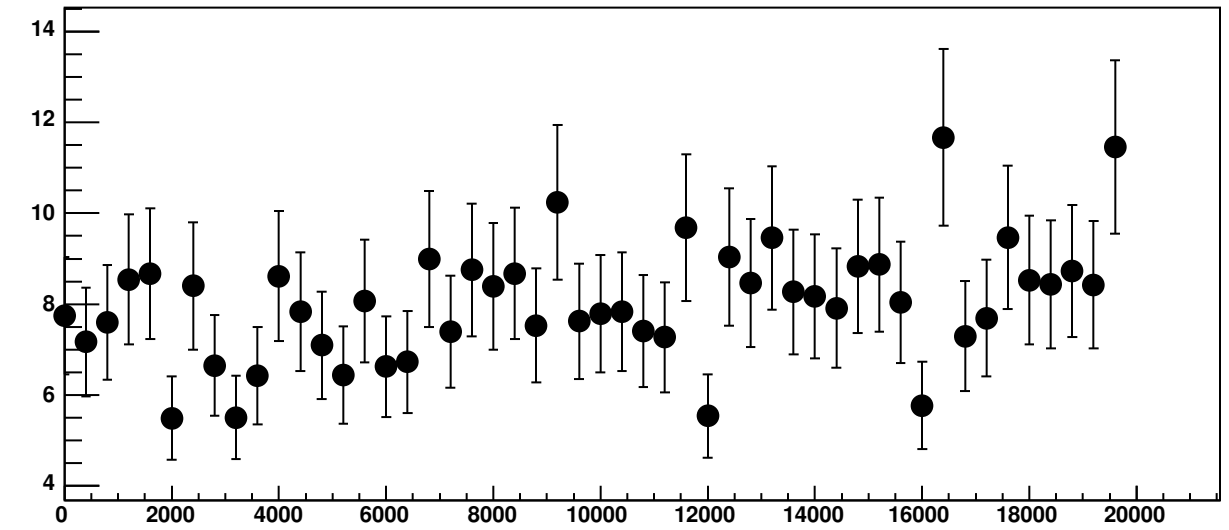

Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

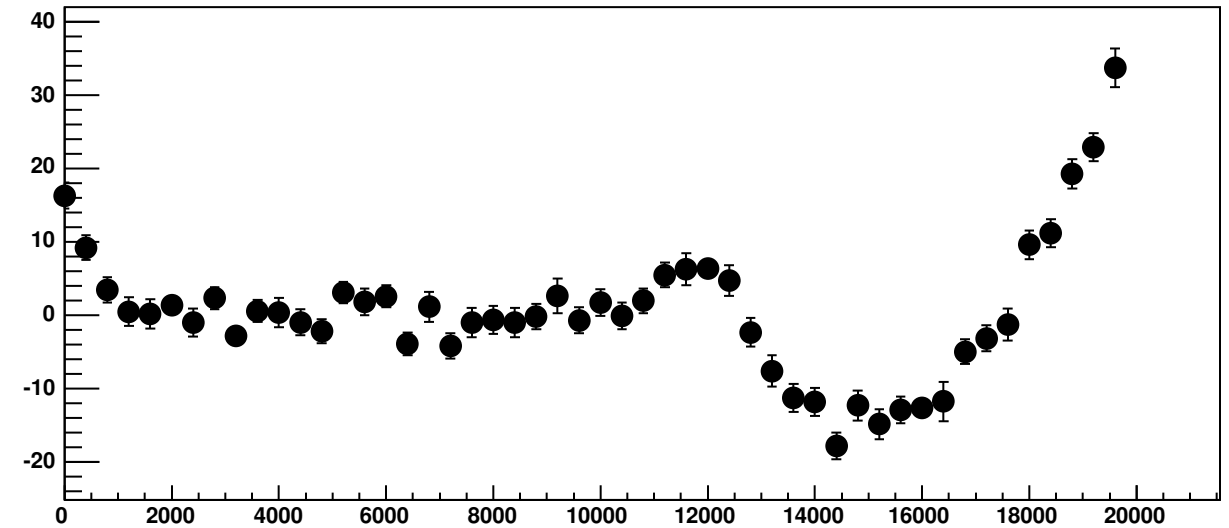
χ^2 / ndf	36.45 / 23
p0	564.9 ± 0.7603
p1	0.02107 ± 0.0001196

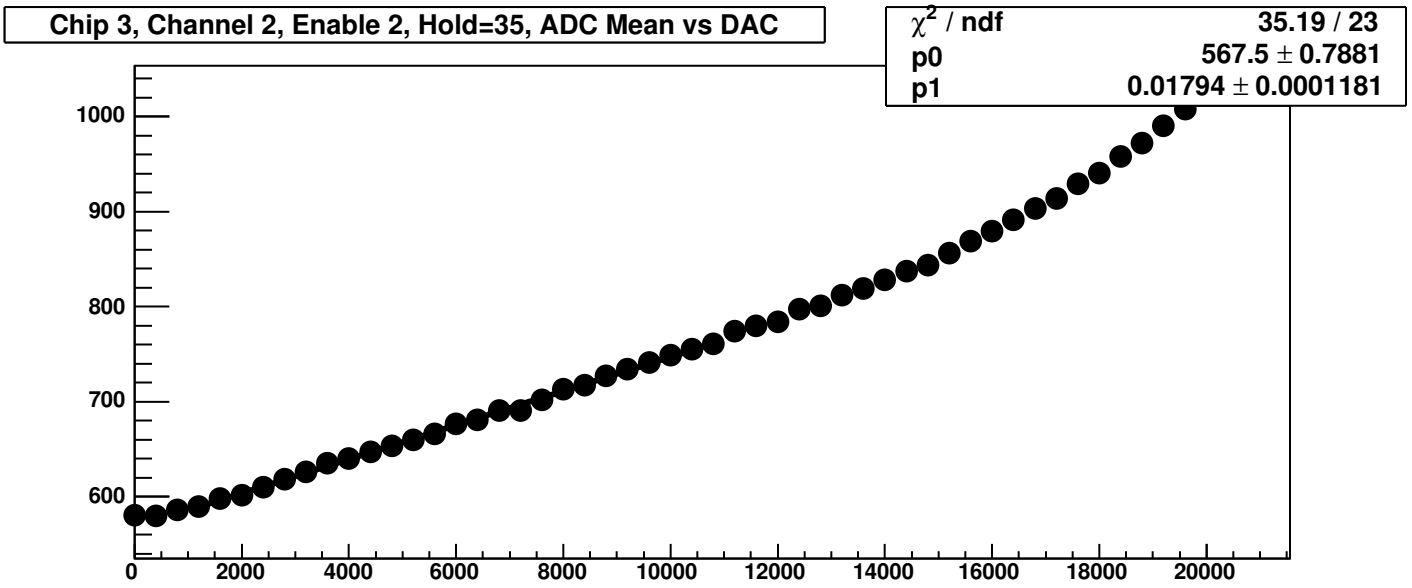


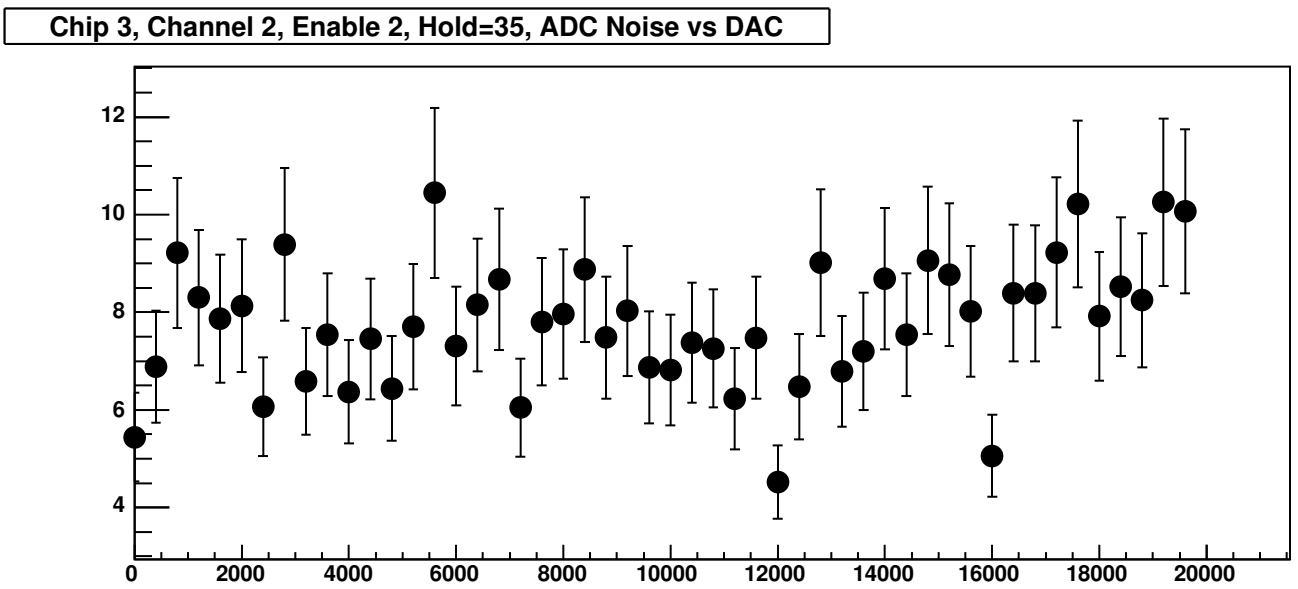
Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

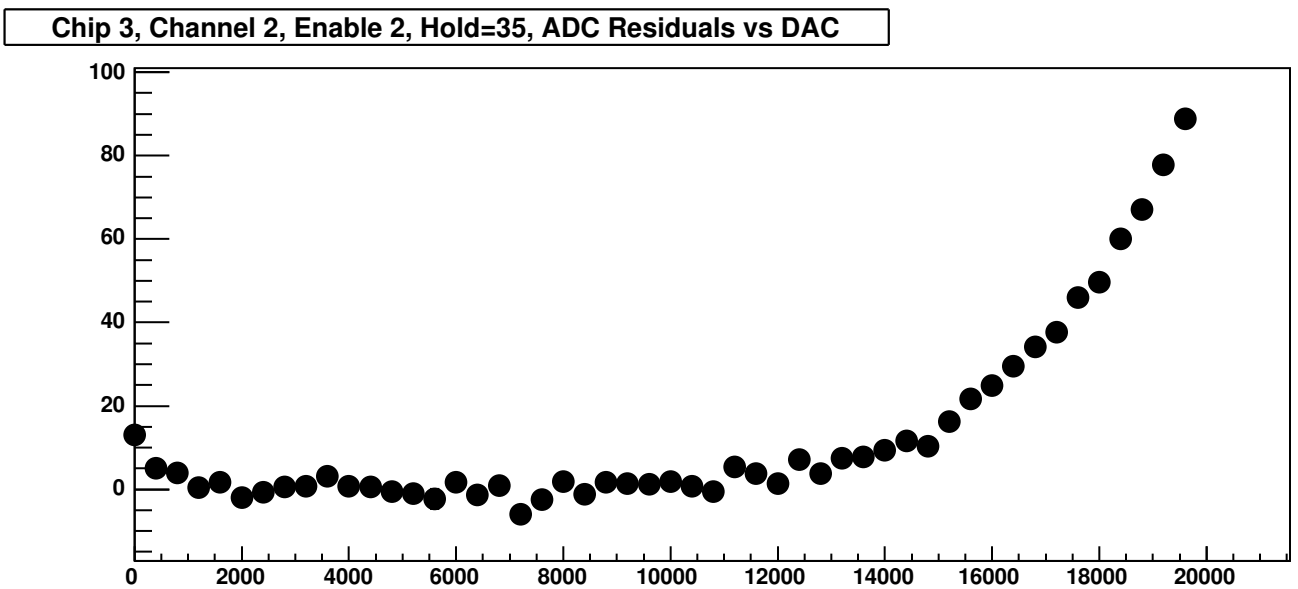


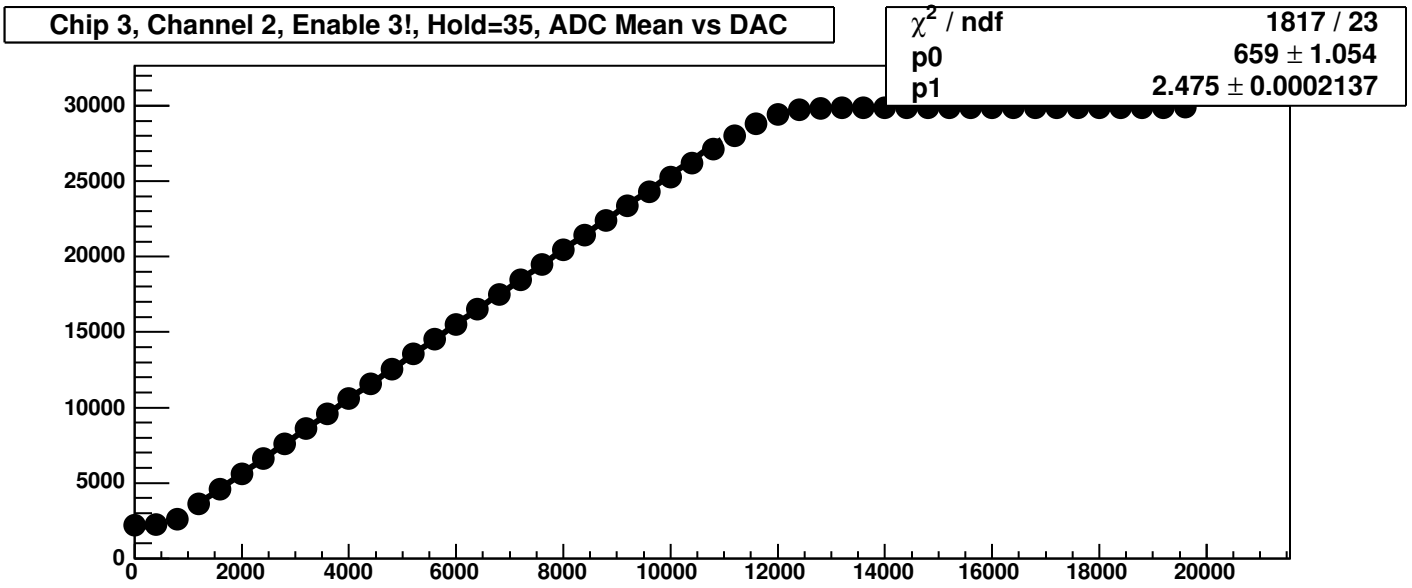
Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

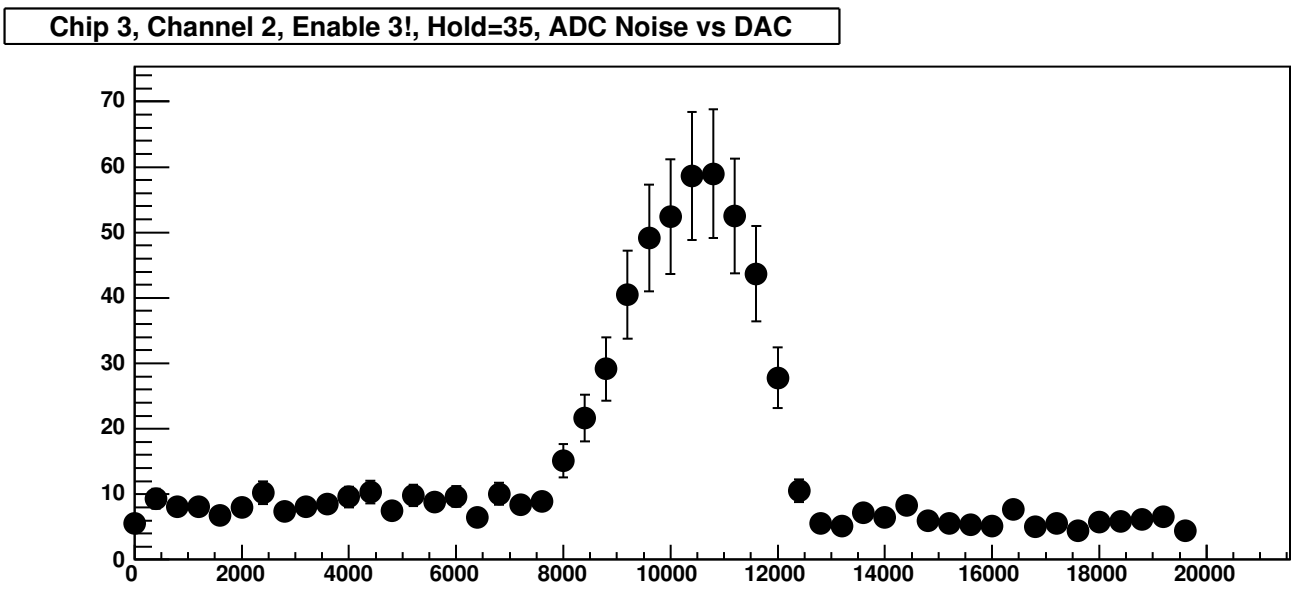


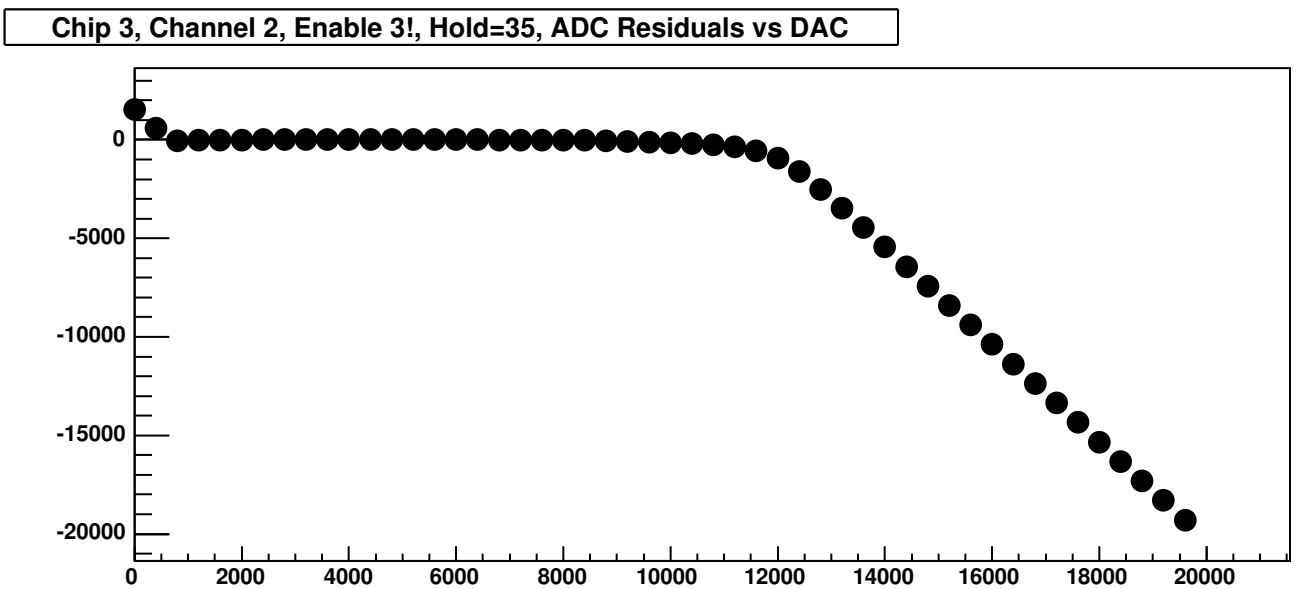

Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.19 / 23
p0 567.5 ± 0.7881
p1 0.01794 ± 0.0001181
1000
900
800
700
600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

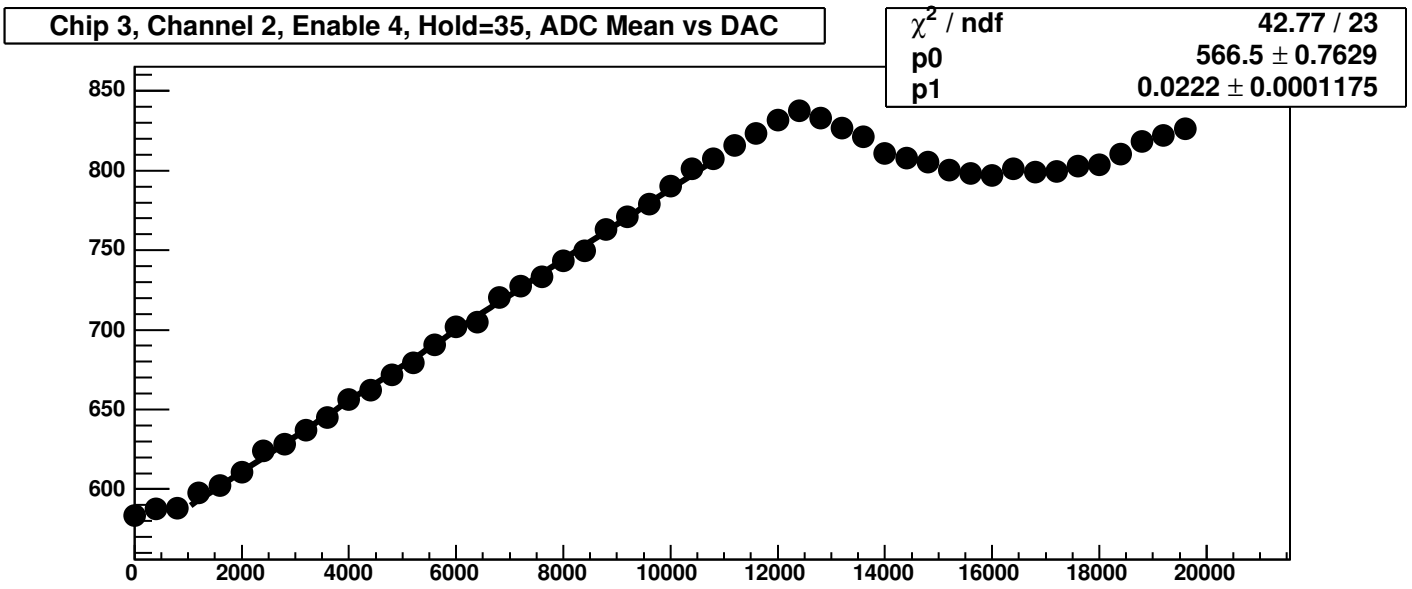

Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

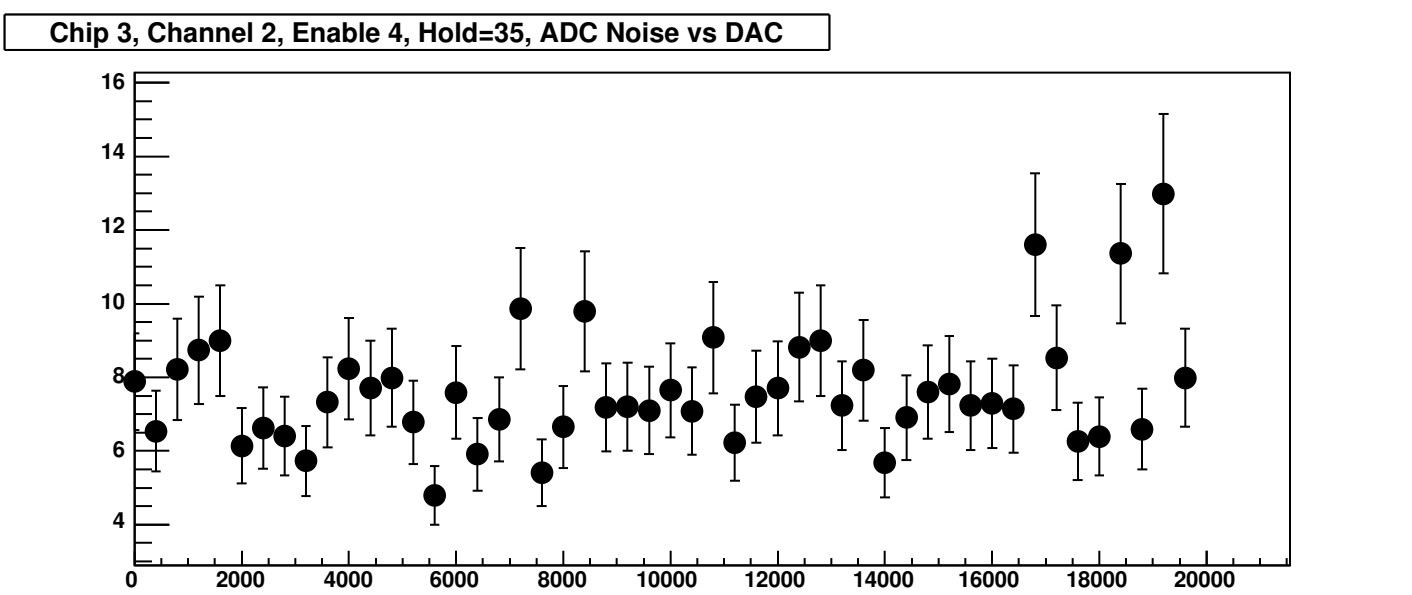

Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

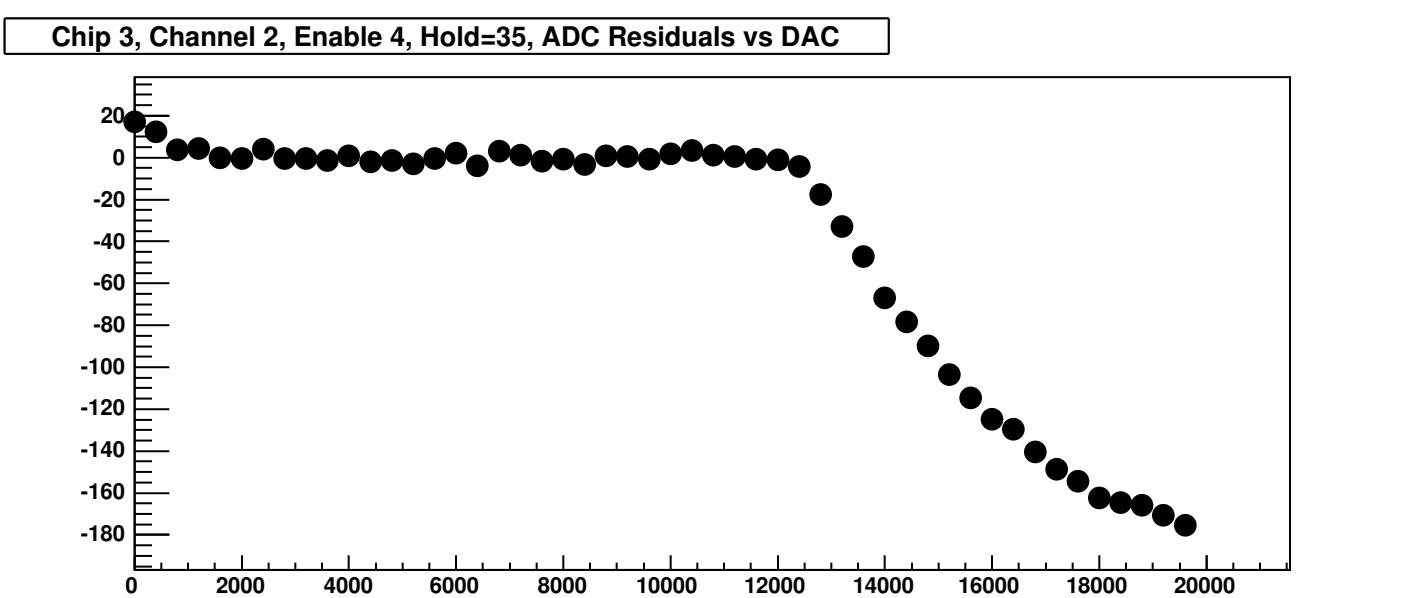

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1817 / 23
p0 659 ± 1.054
p1 2.475 ± 0.0002137
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

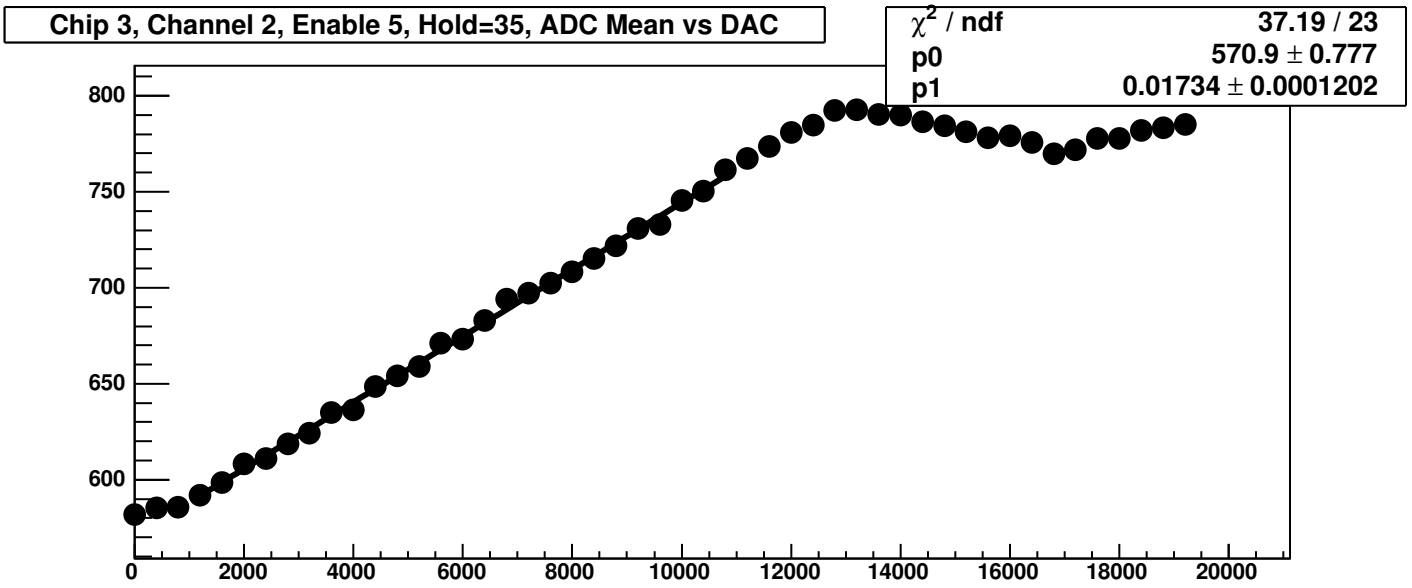

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

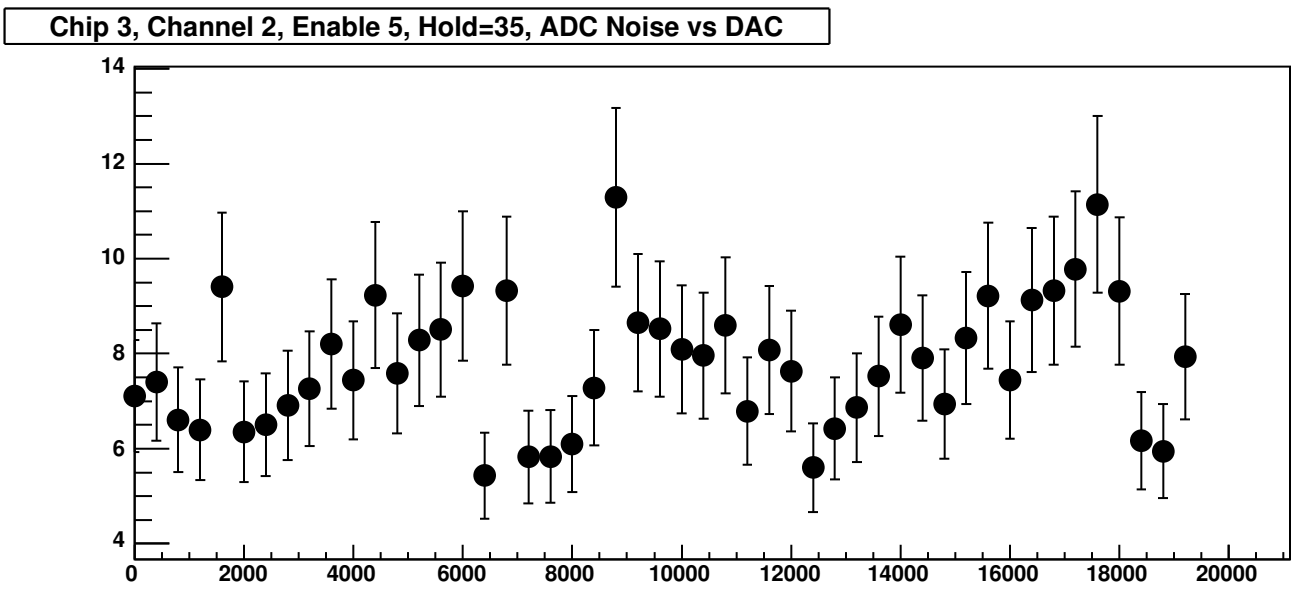

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

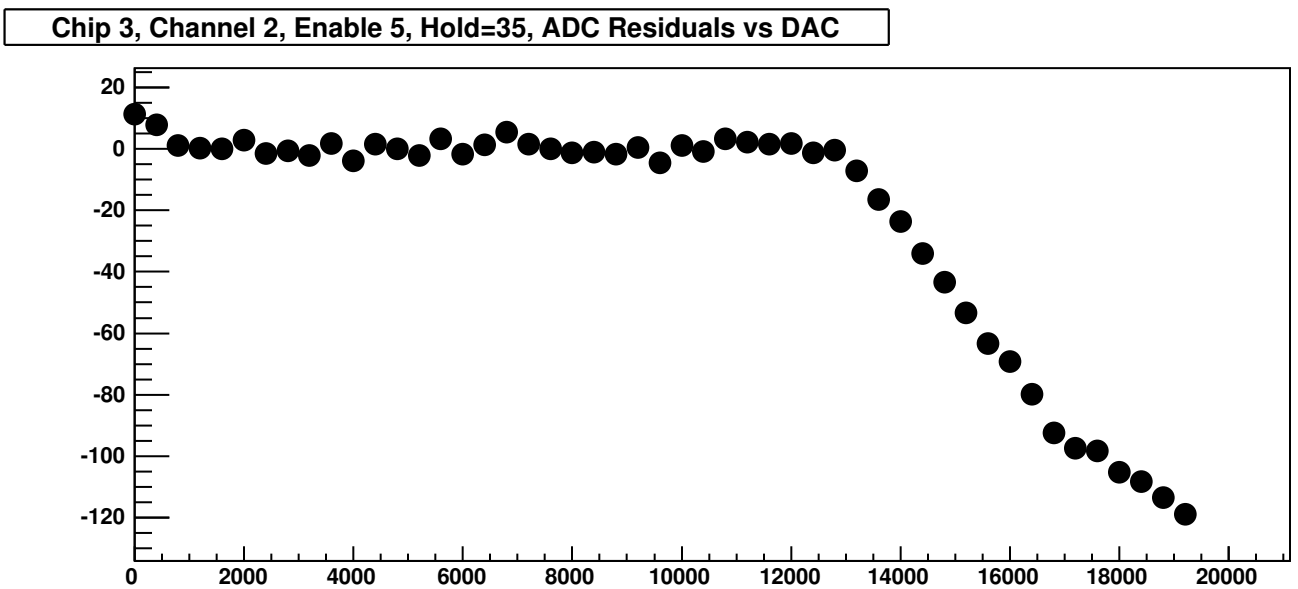

Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 42.77 / 23
p0 566.5 ± 0.7629
p1 0.0222 ± 0.0001175
850
800
750
700
650
600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

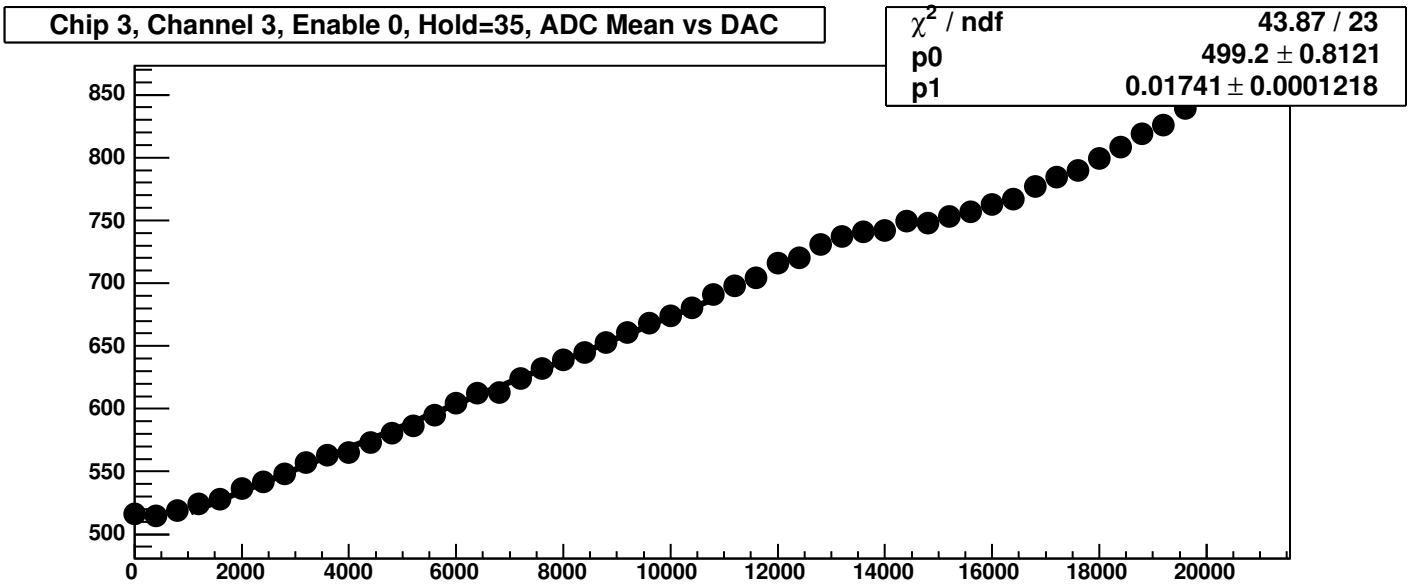

Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

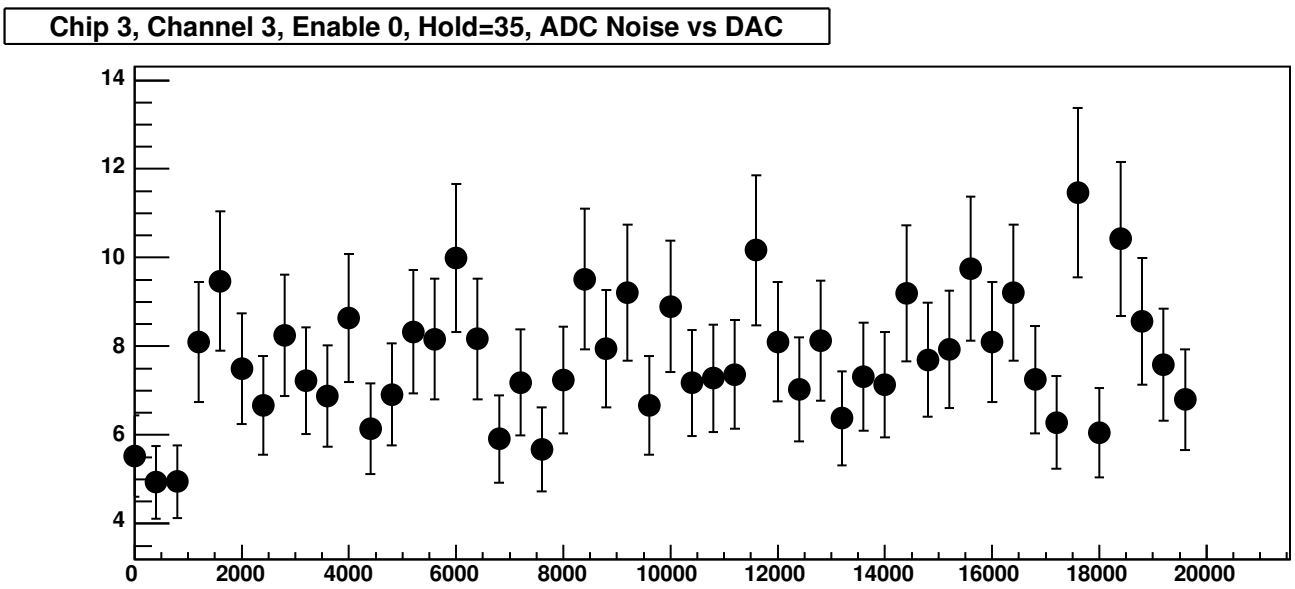

Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

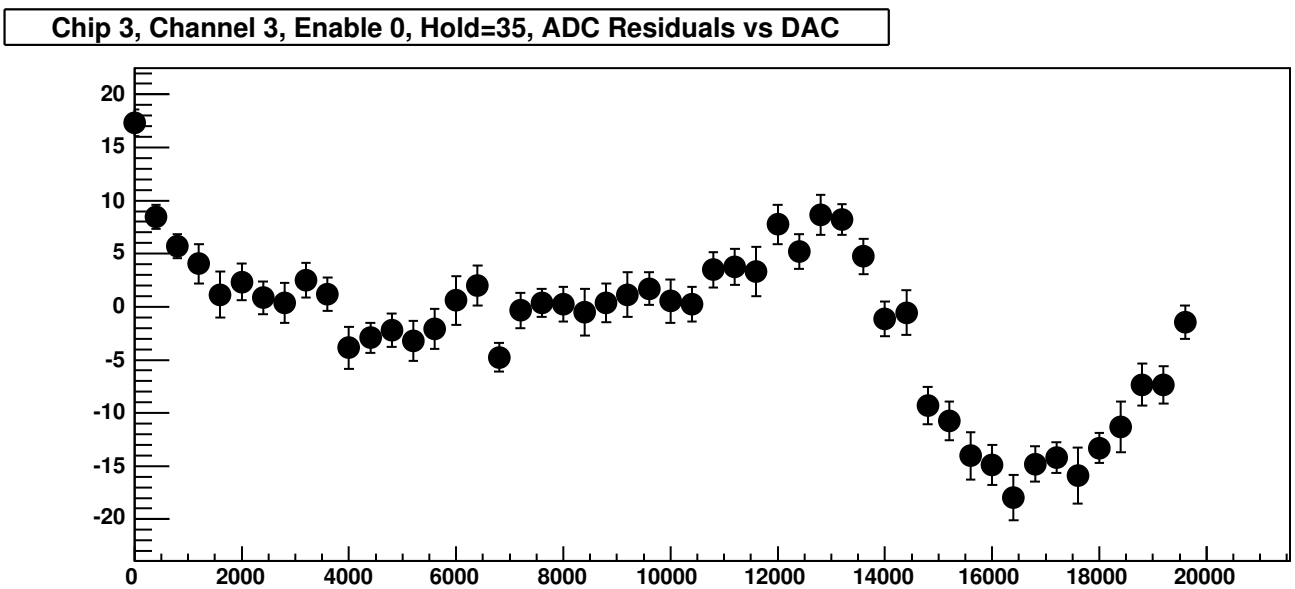

Chip 3, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 37.19 / 23
p0 570.9 ± 0.777
p1 0.01734 ± 0.0001202
800
750
700
650
600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

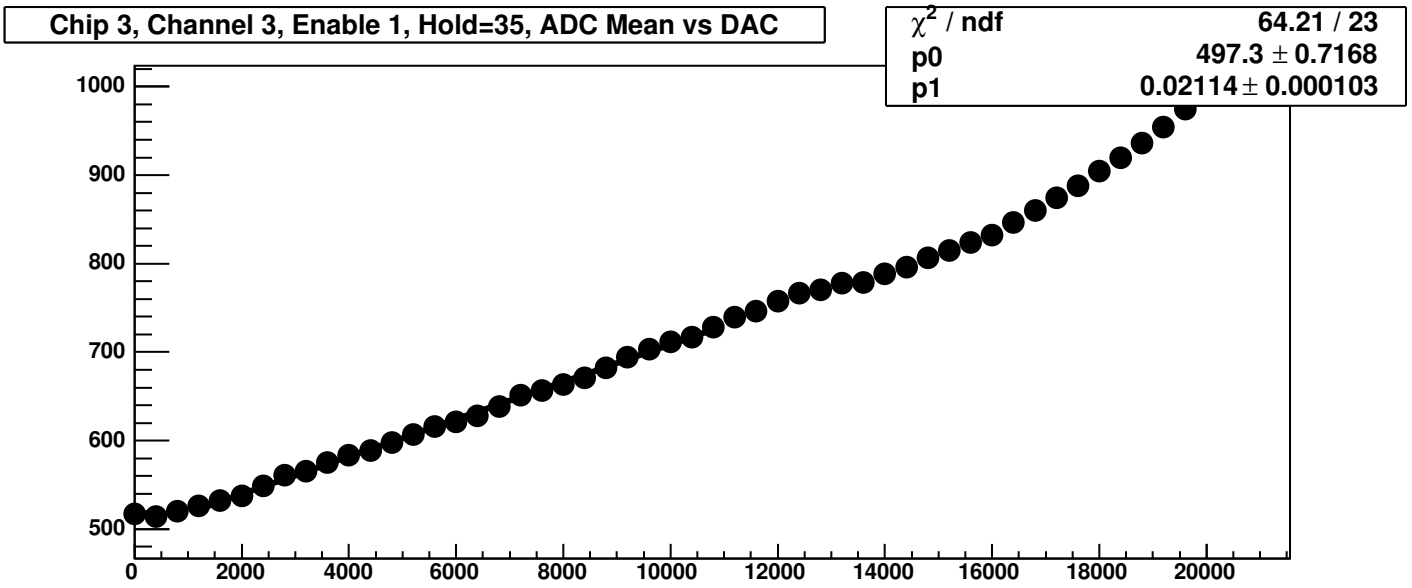

Chip 3, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

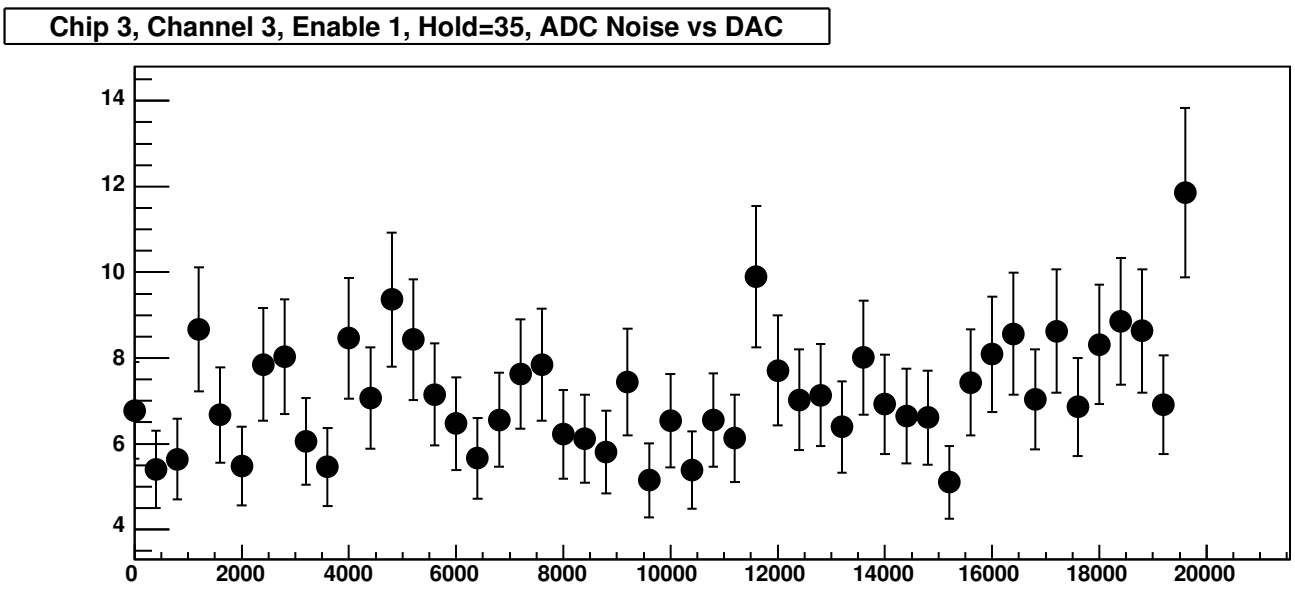

Chip 3, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

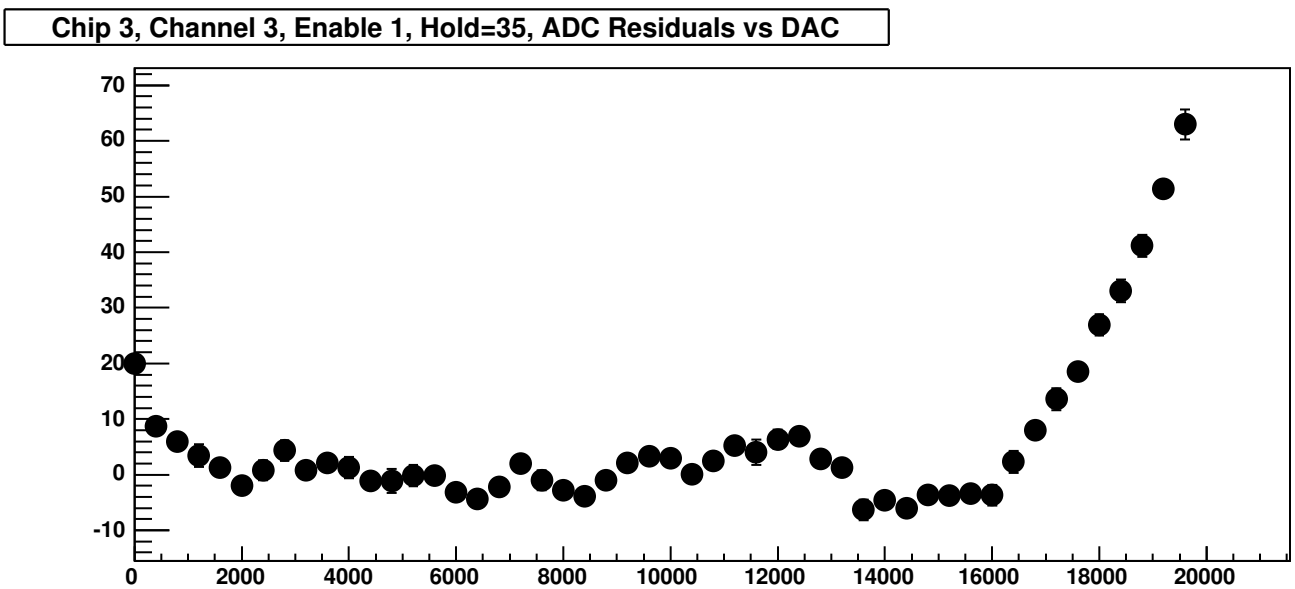

Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 43.87 / 23
p0 499.2 ± 0.8121
p1 0.01741 ± 0.0001218
850
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

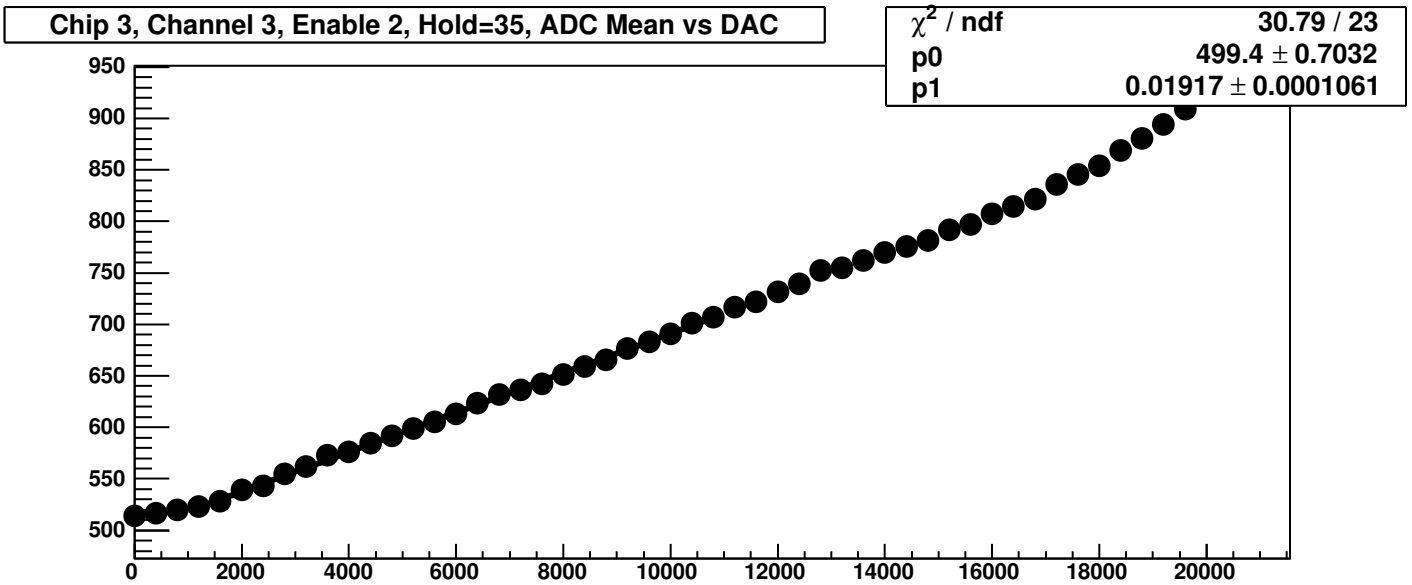

Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

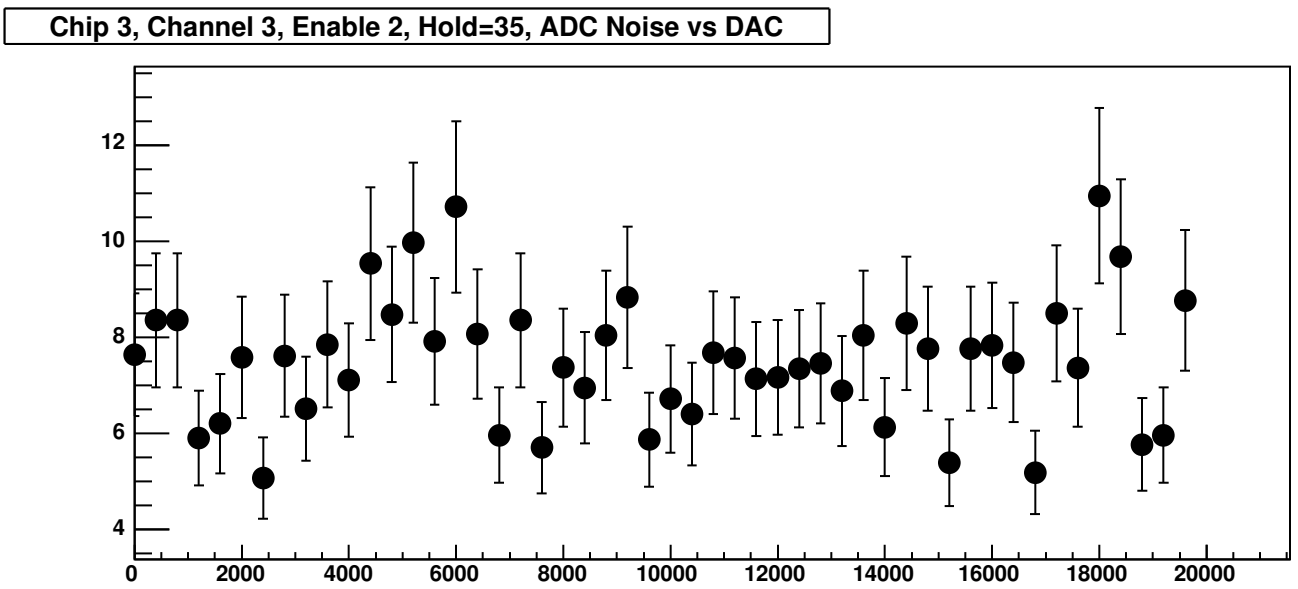

Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

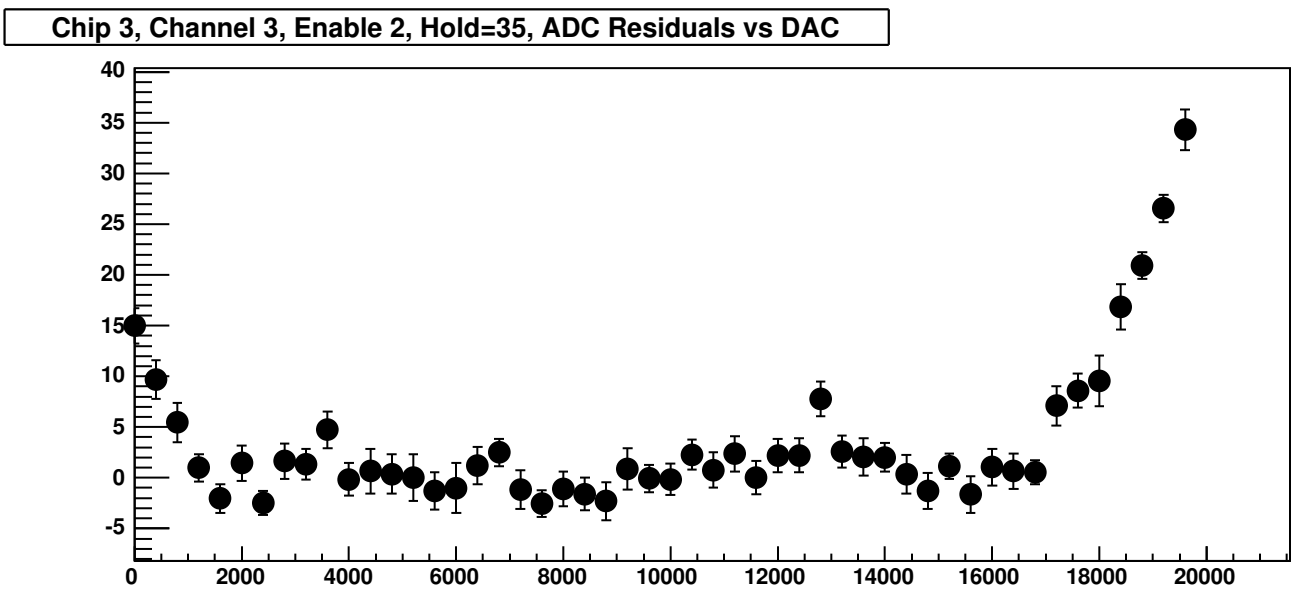

Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 64.21 / 23
p0 497.3 ± 0.7168
p1 0.02114 ± 0.000103
1000
900
800
700
600
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

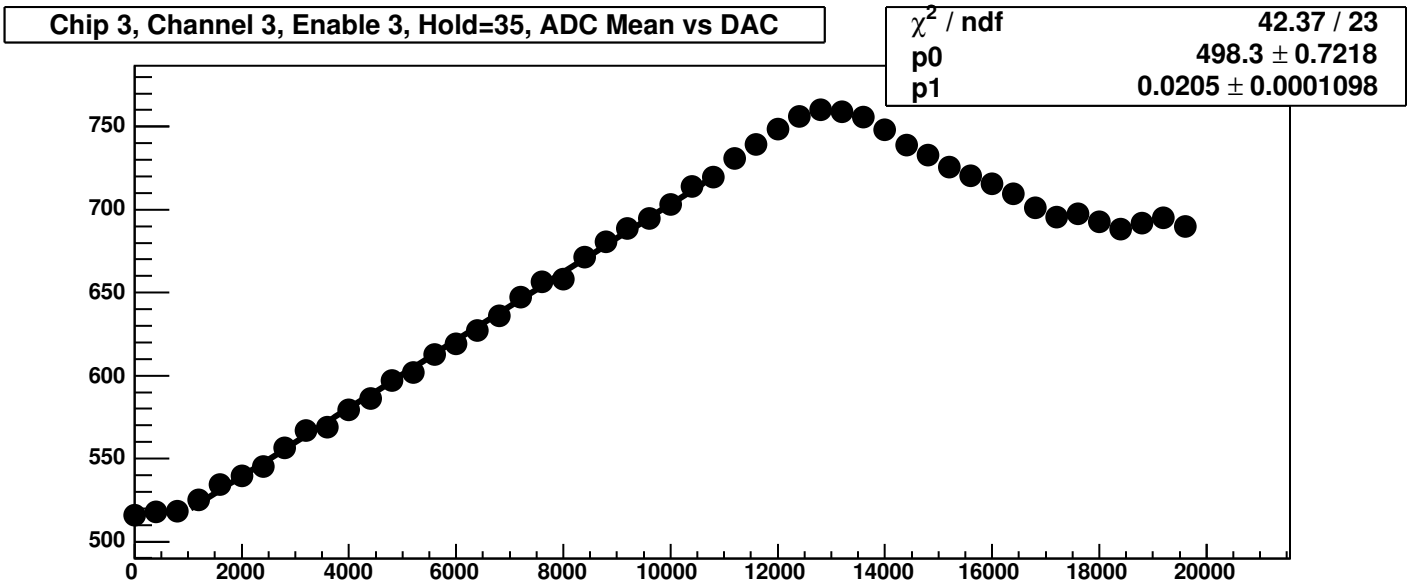

Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

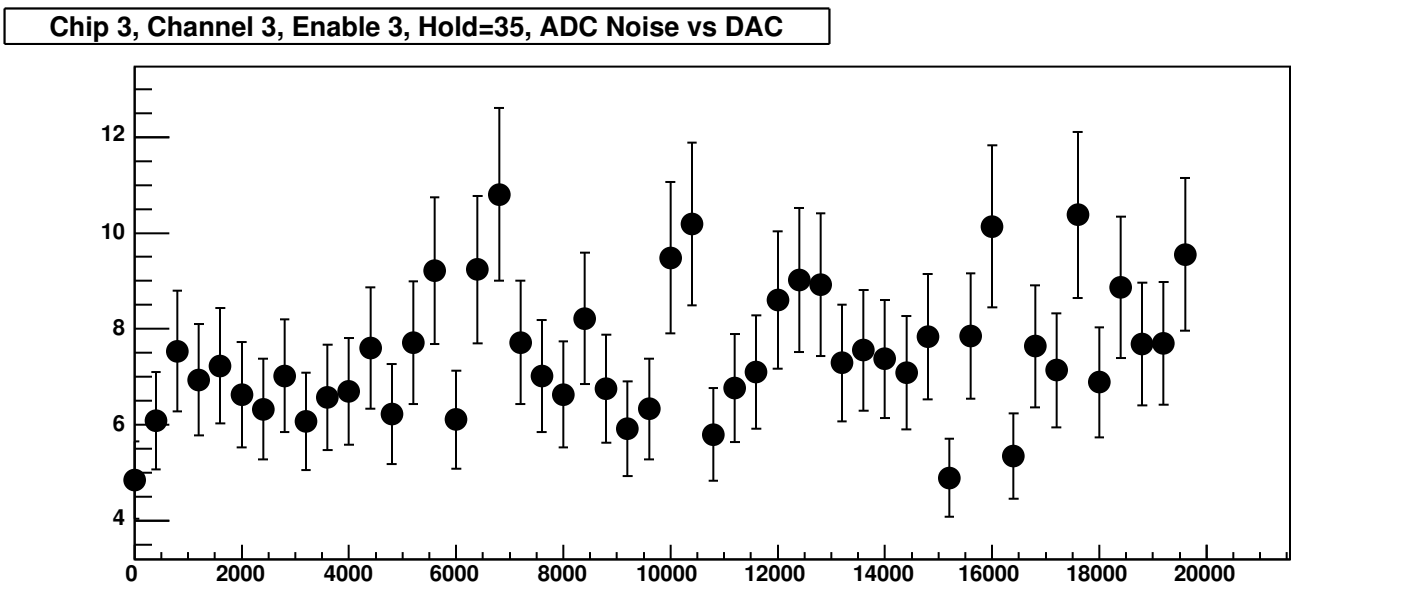

Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
70
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

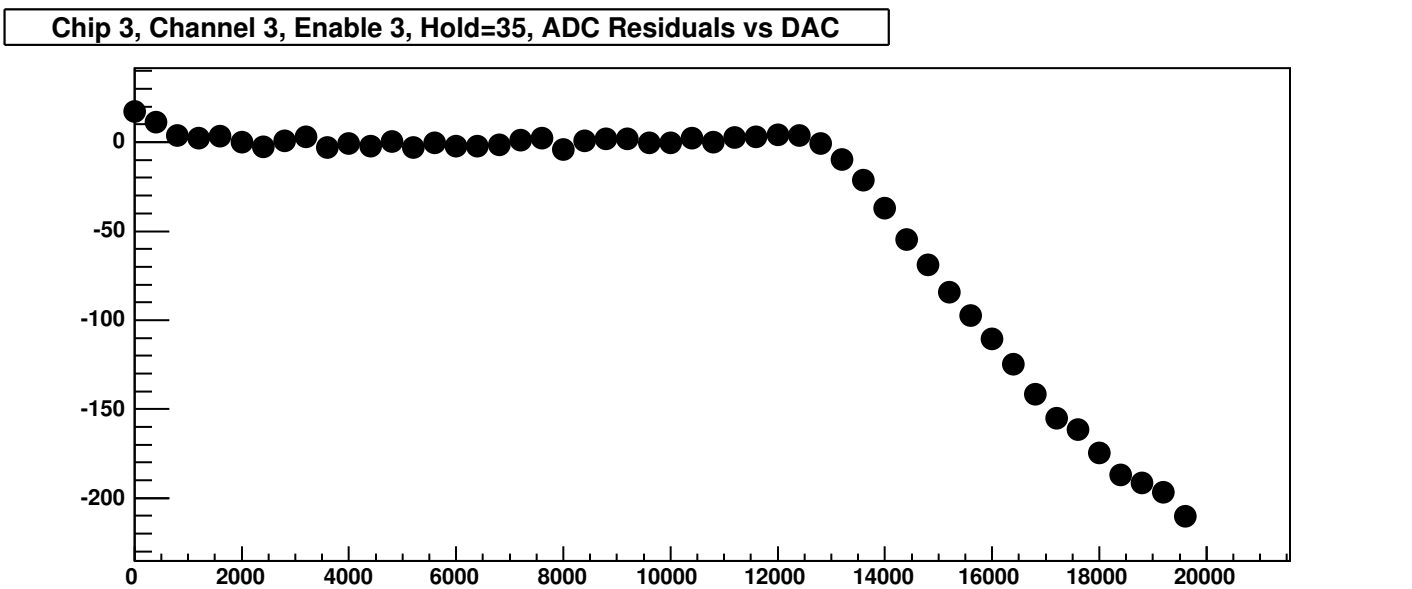

Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.79 / 23
p0 499.4 ± 0.7032
p1 0.01917 ± 0.0001061
950
900
850
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

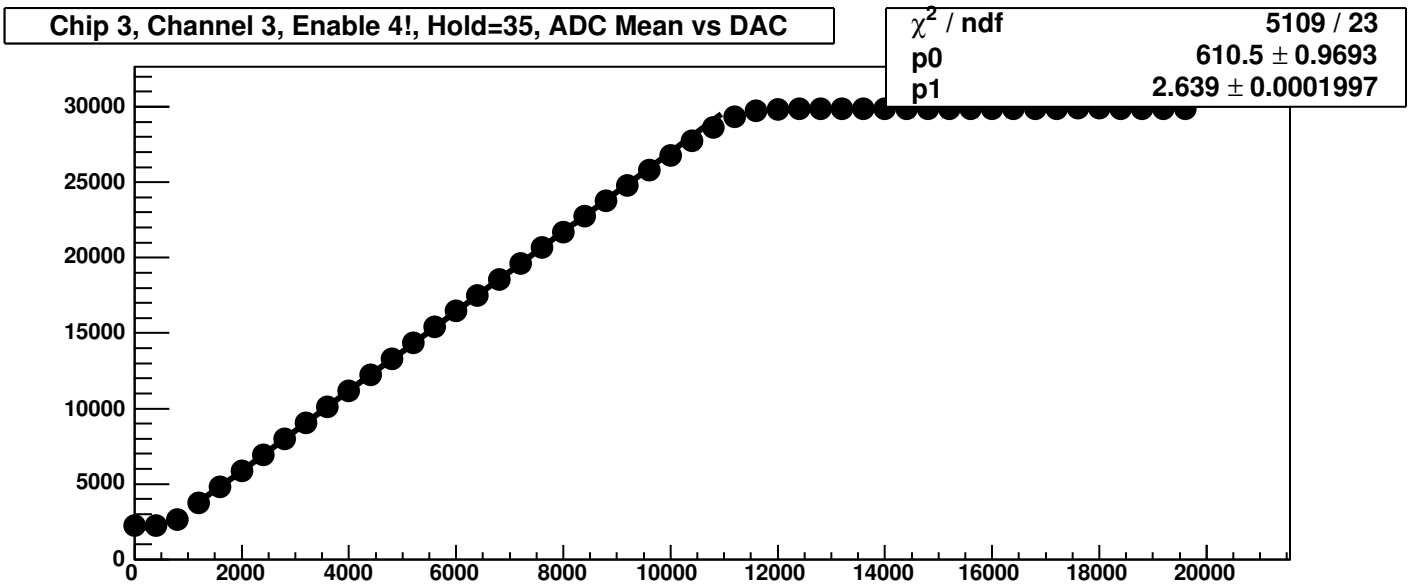

Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

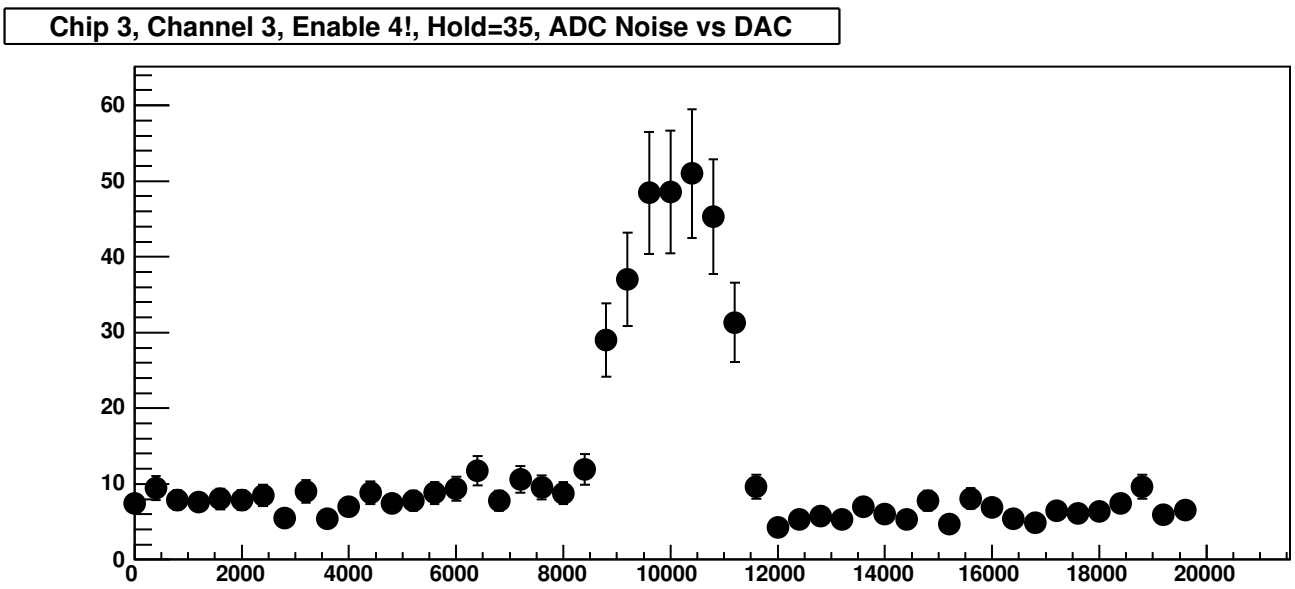

Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
40
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

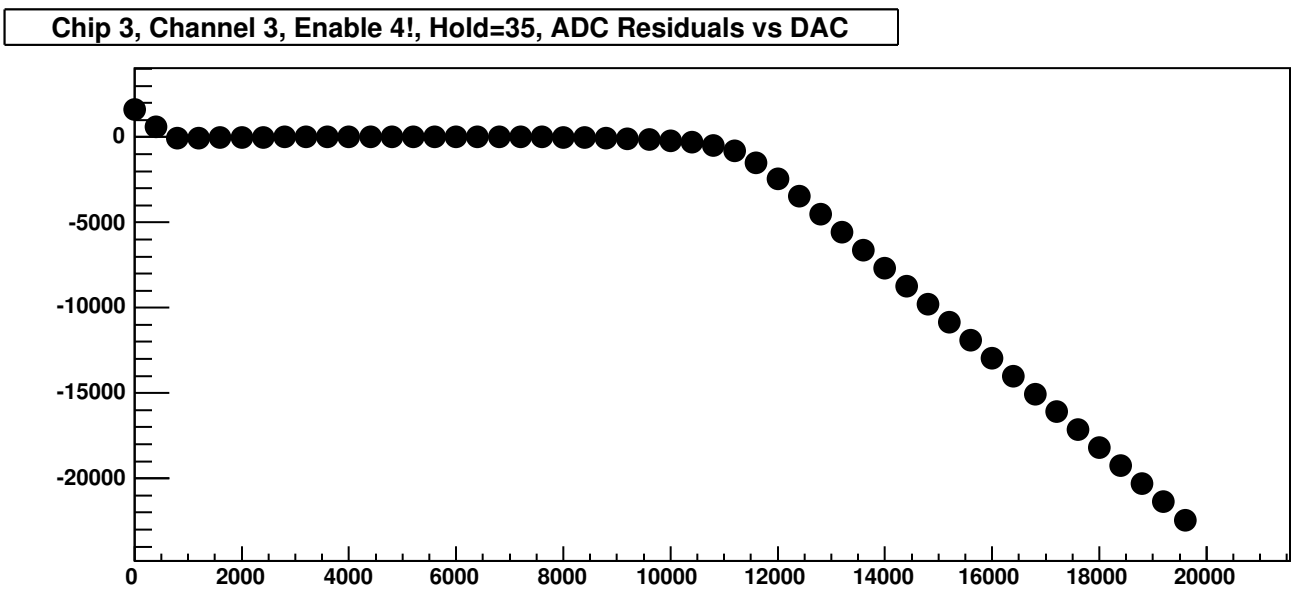

Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.37 / 23
p0 498.3 ± 0.7218
p1 0.0205 ± 0.0001098
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

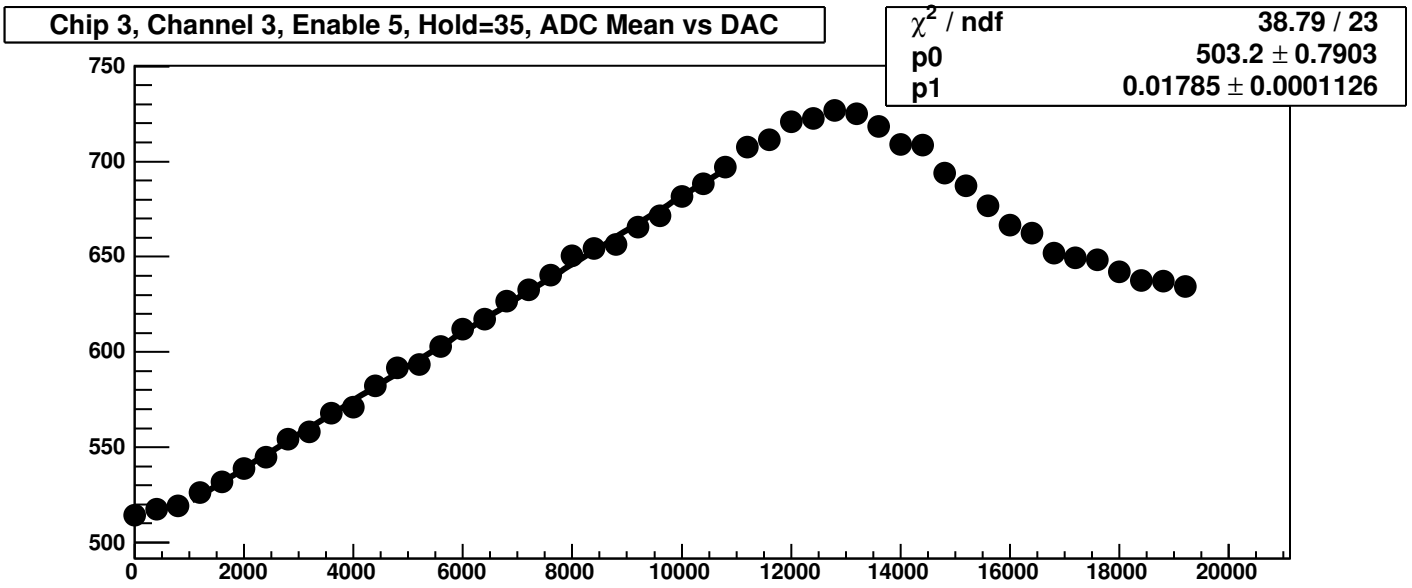

Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

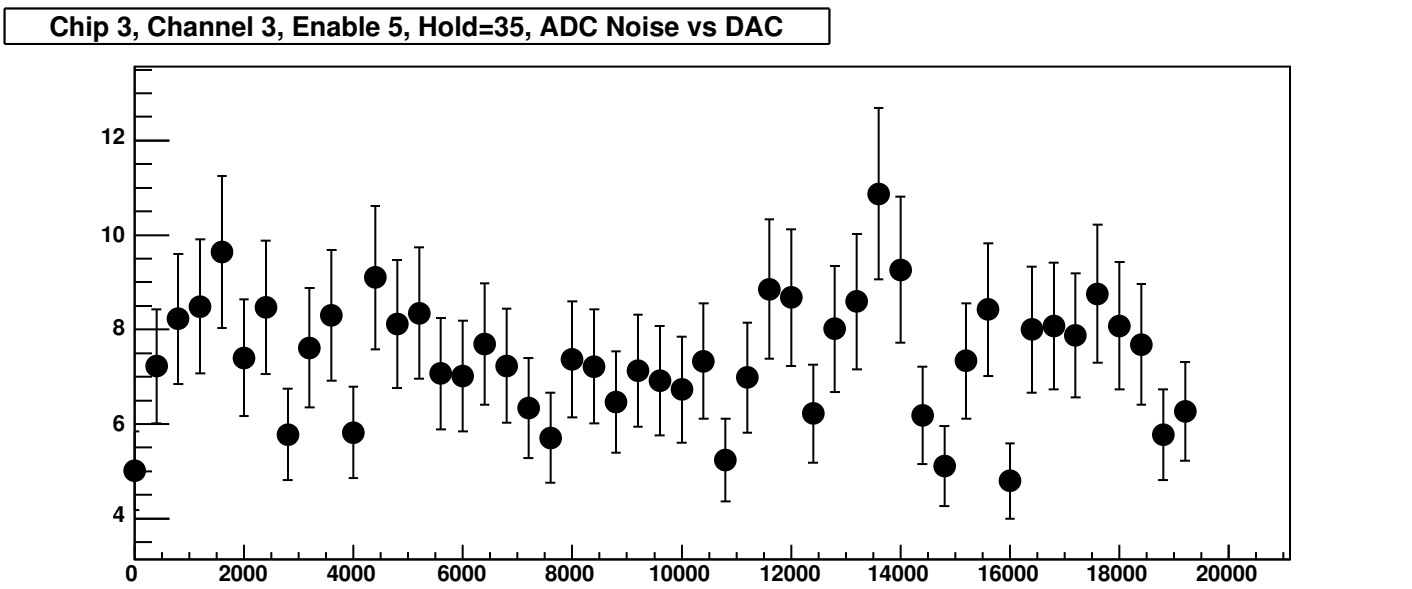

Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

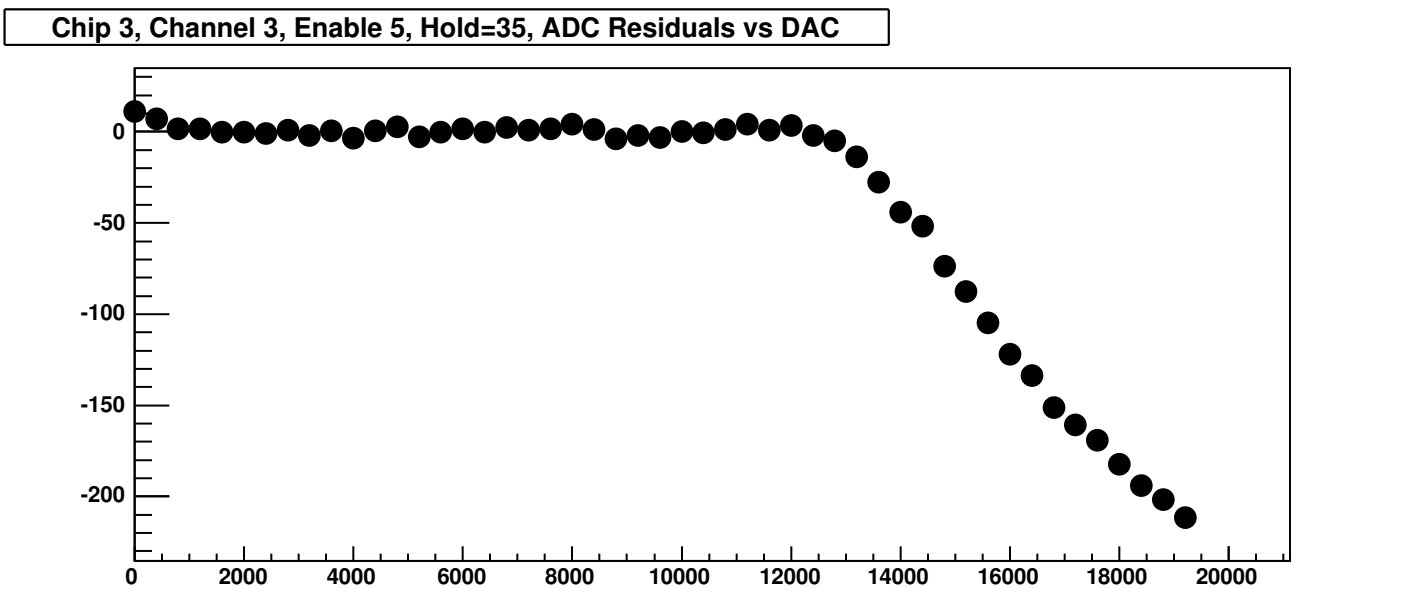

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 5109 / 23
p0 610.5 ± 0.9693
p1 2.639 ± 0.0001997
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

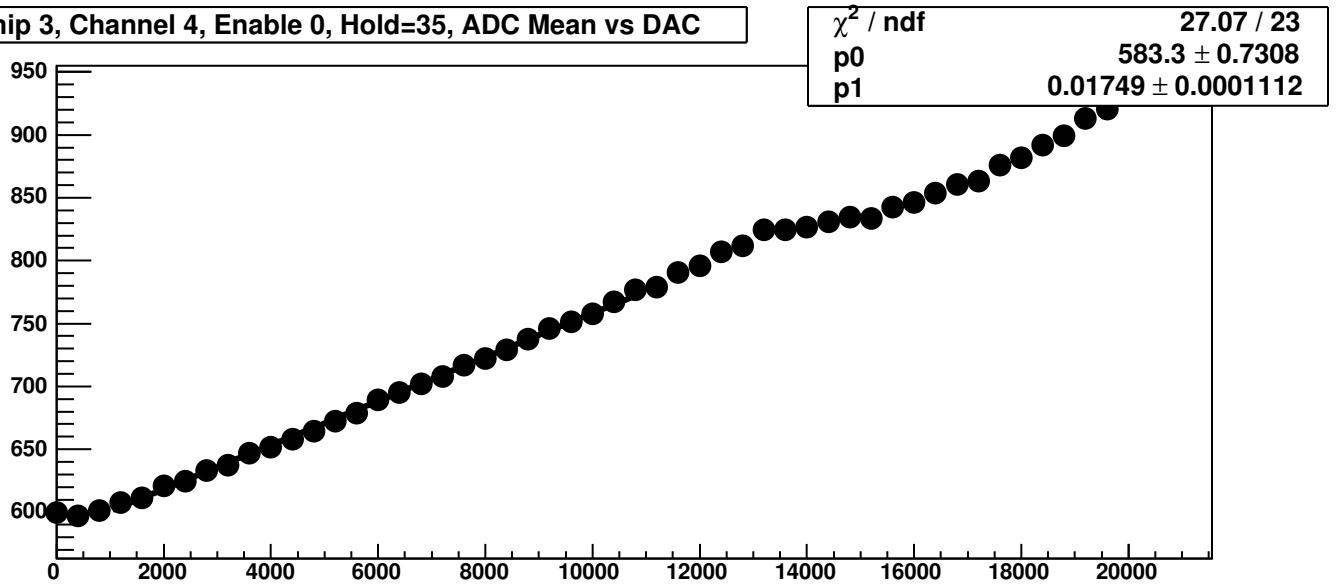

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 38.79 / 23
p0 503.2 ± 0.7903
p1 0.01785 ± 0.0001126
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

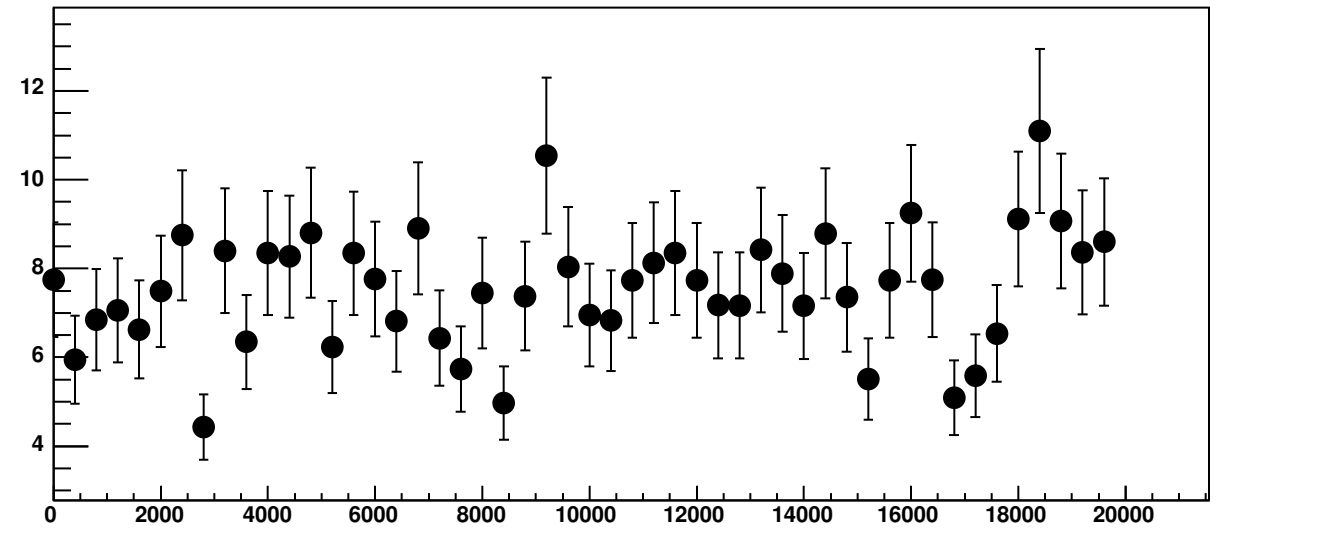

Chip 3, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

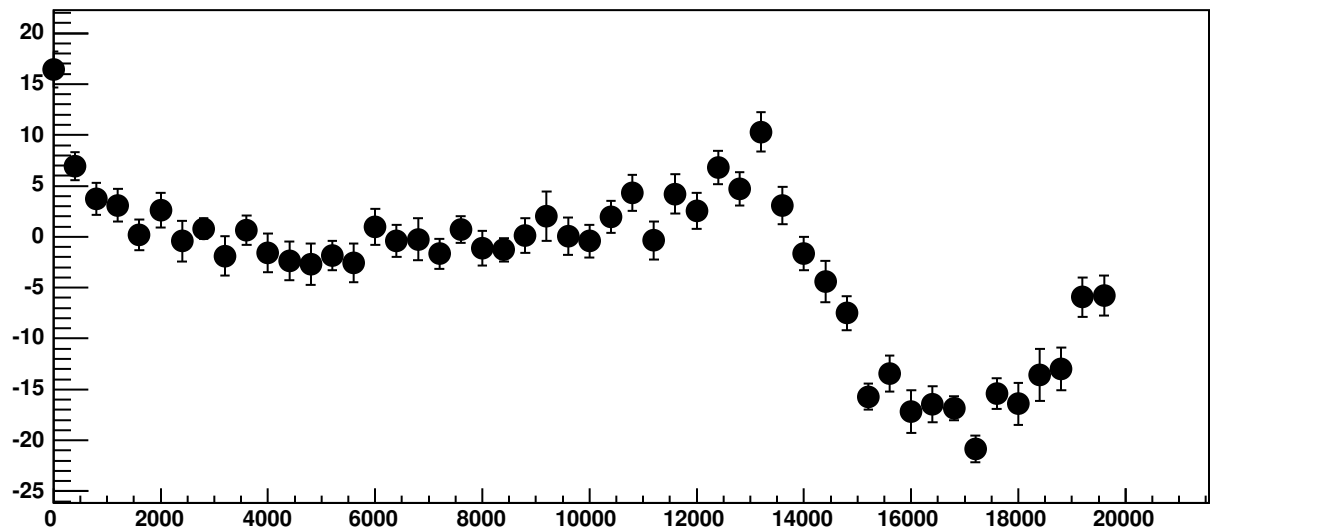
Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

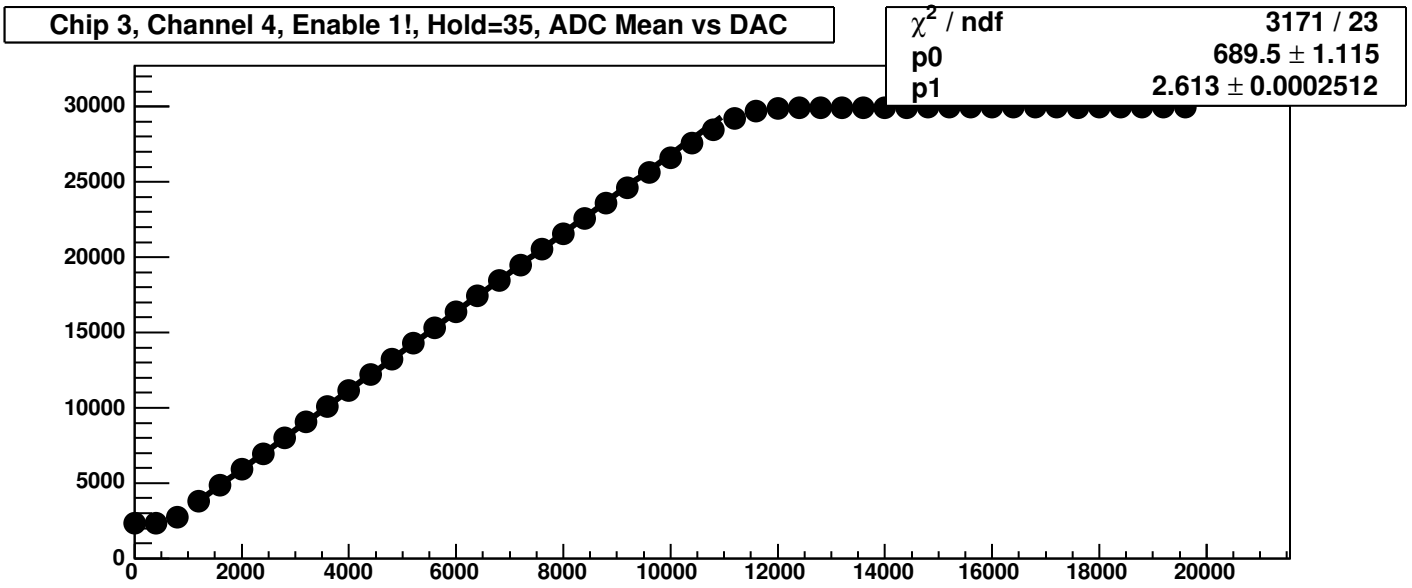


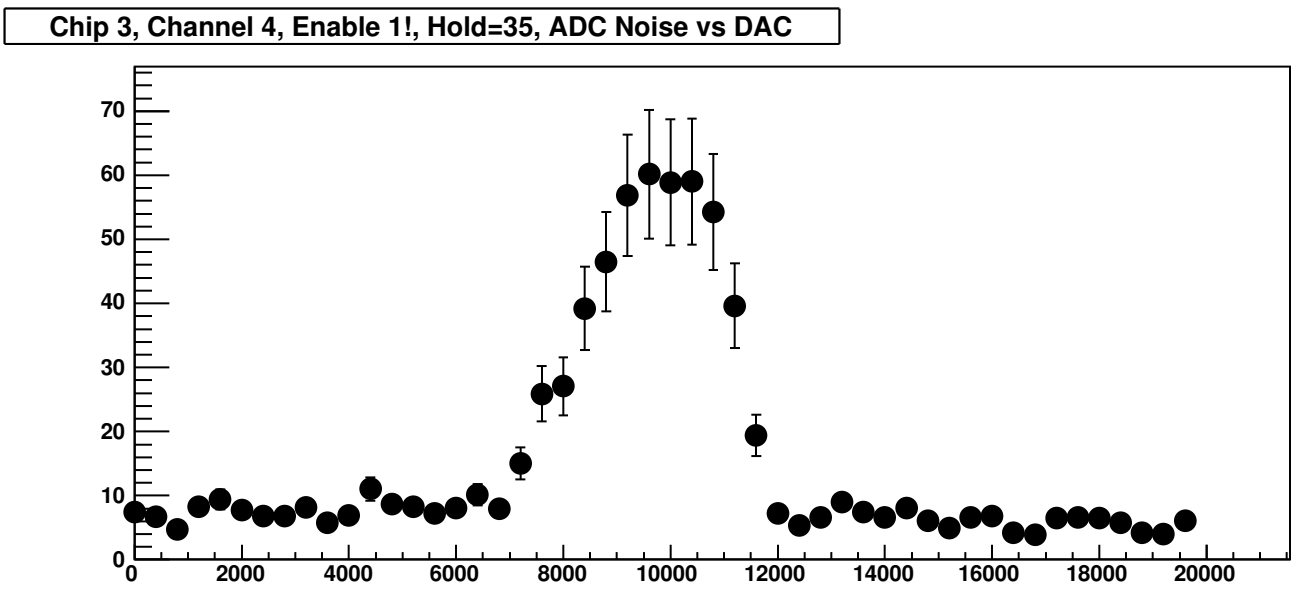
Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

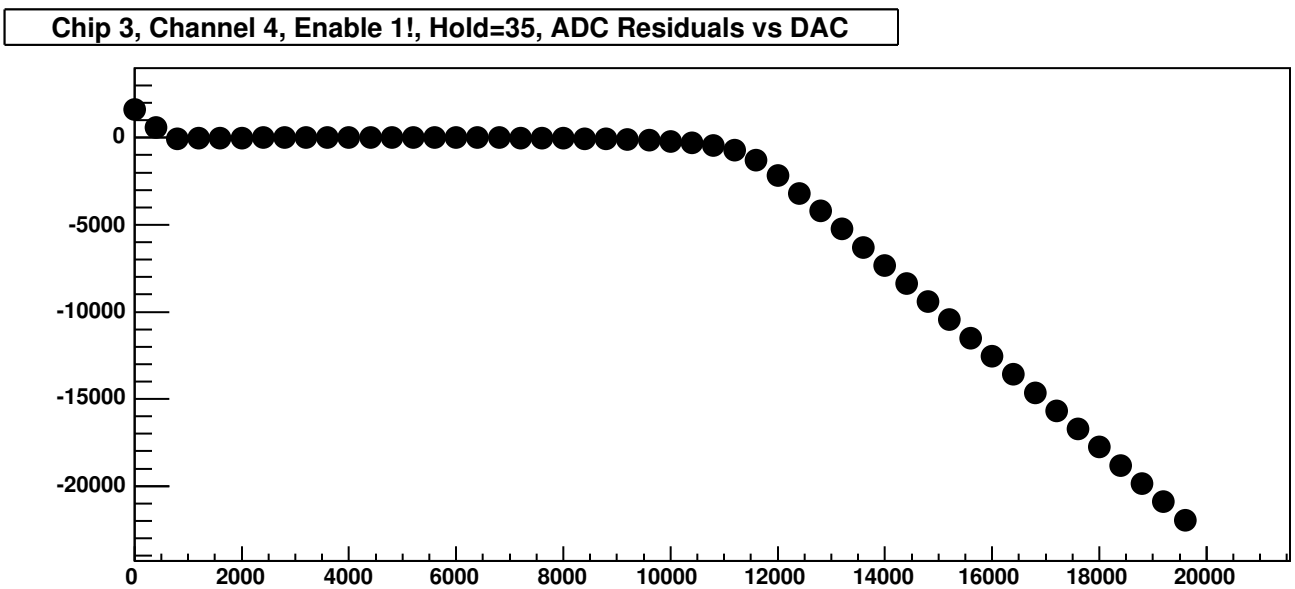


Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

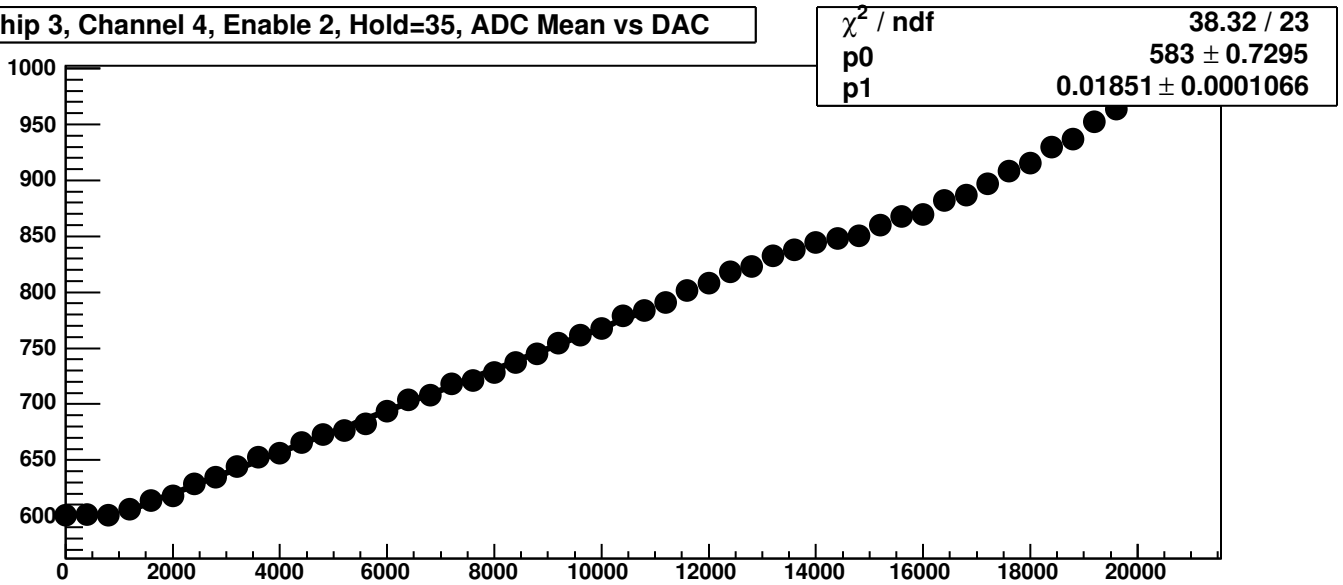



Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 3171 / 23
p0 689.5 ± 1.115
p1 2.613 ± 0.0002512
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

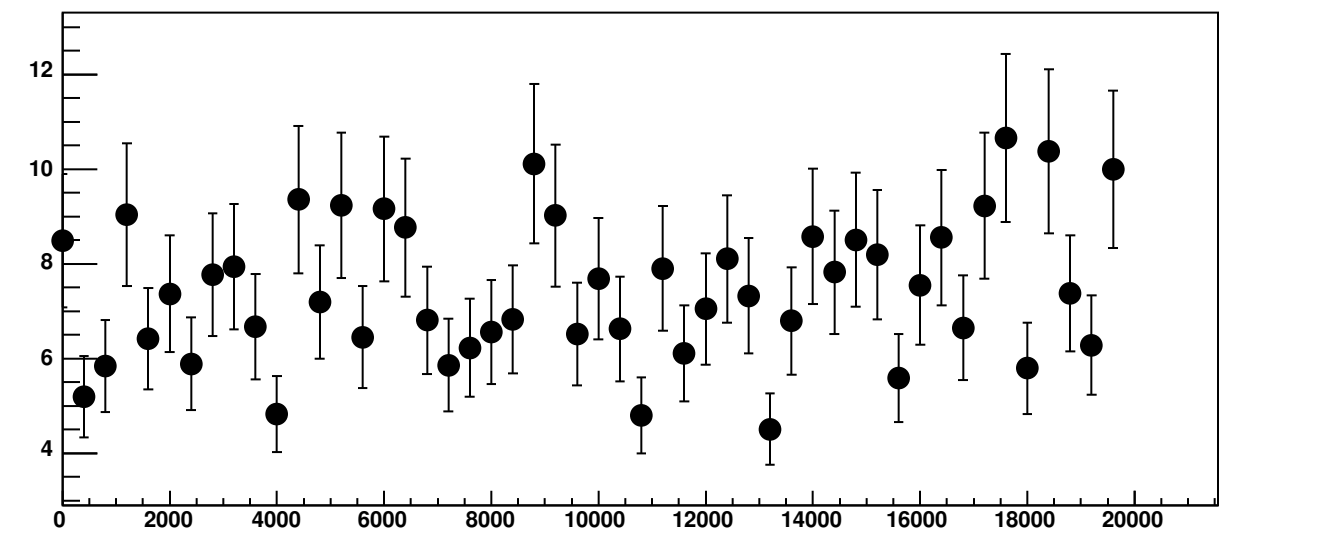

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

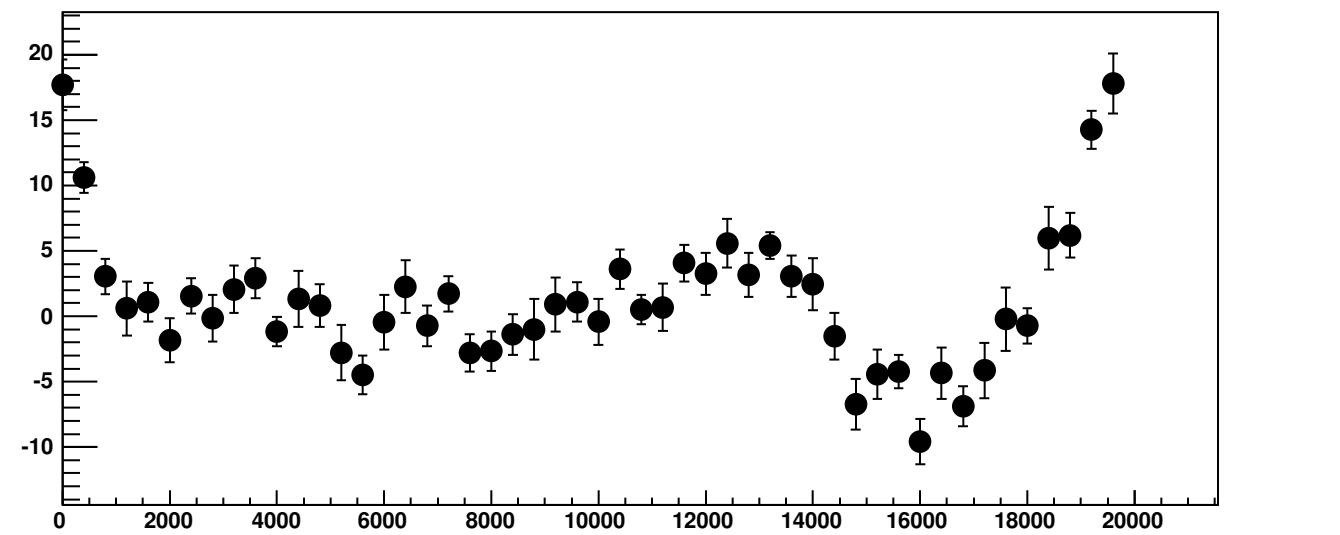
Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

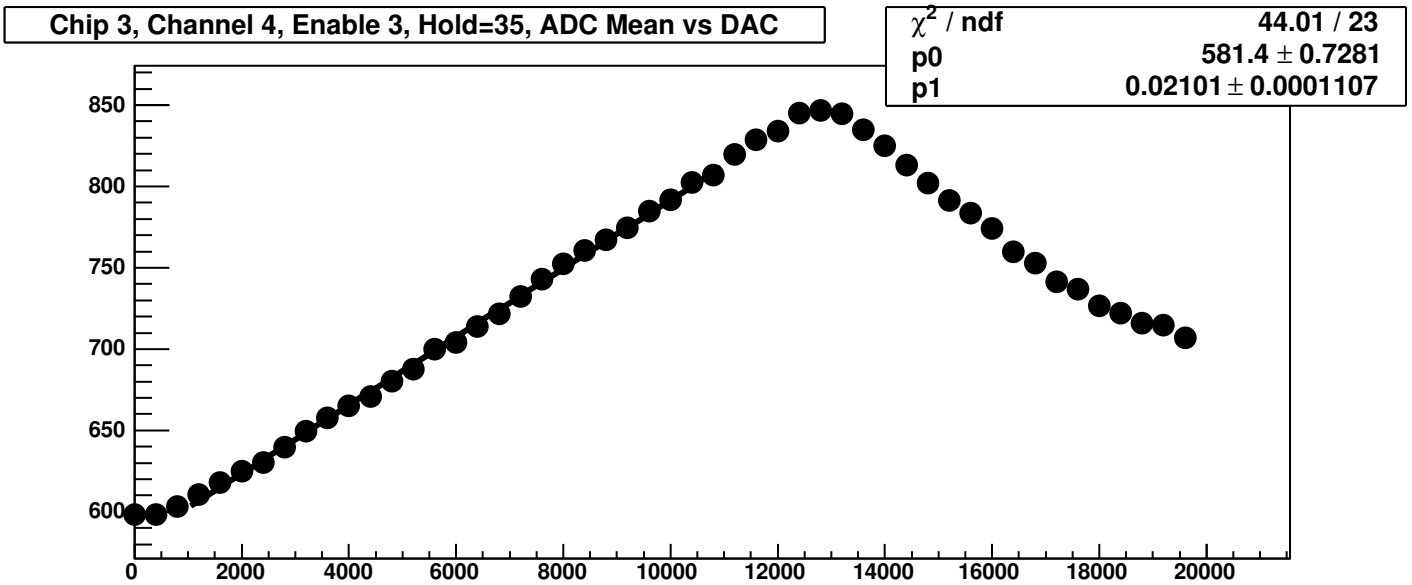


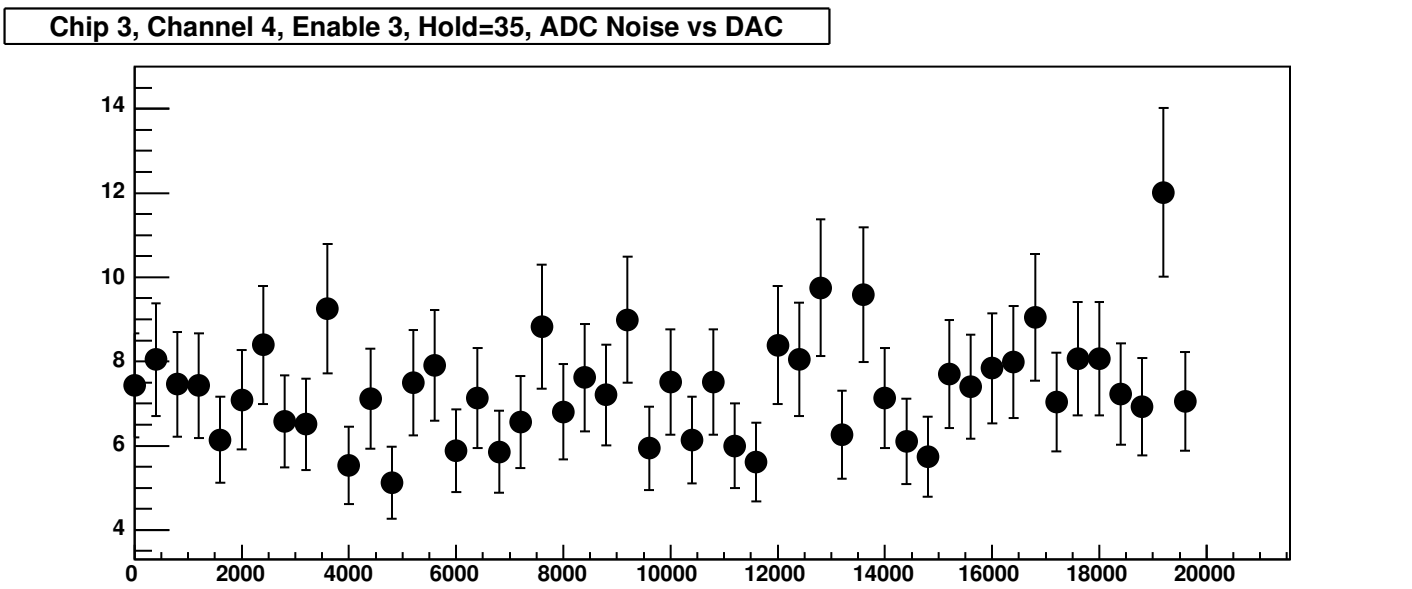
Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

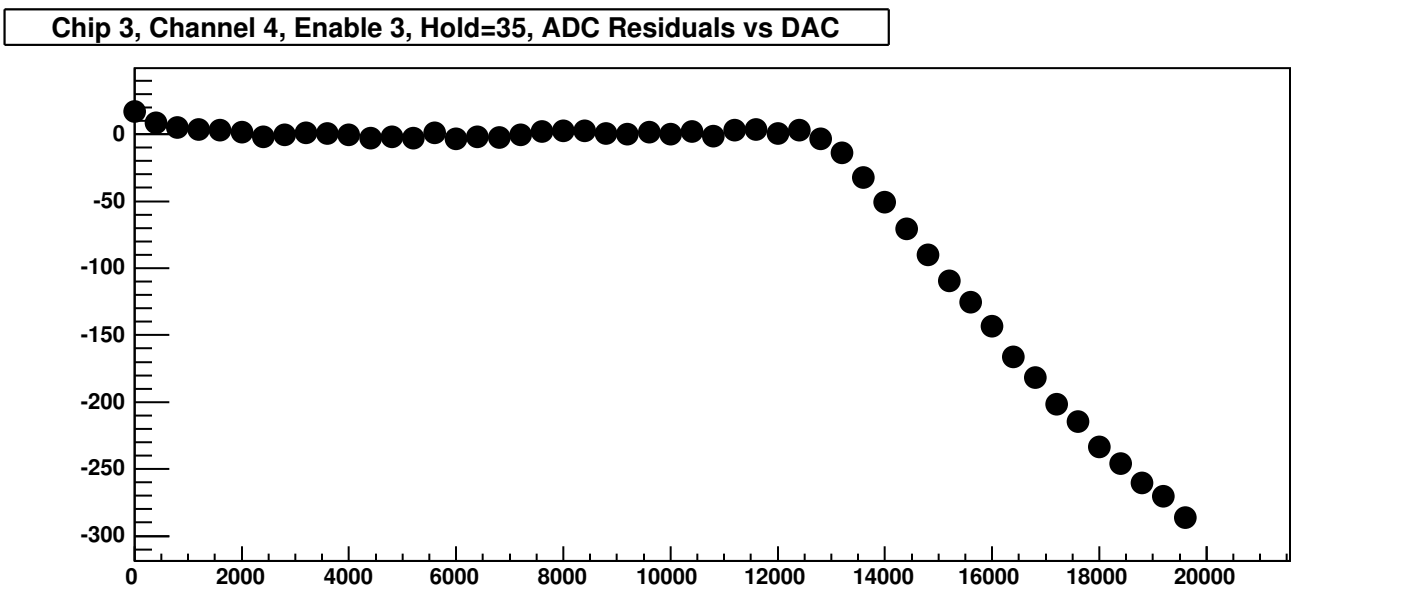


Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

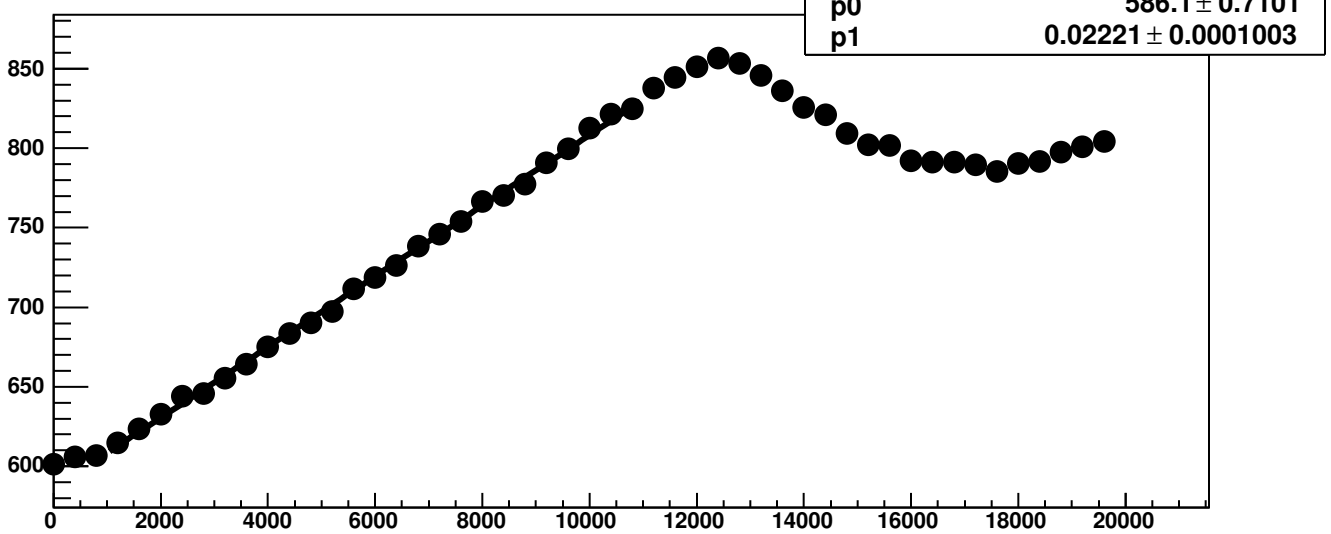



Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.01 / 23
p0 581.4 ± 0.7281
p1 0.02101 ± 0.0001107
850
800
750
700
650
600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

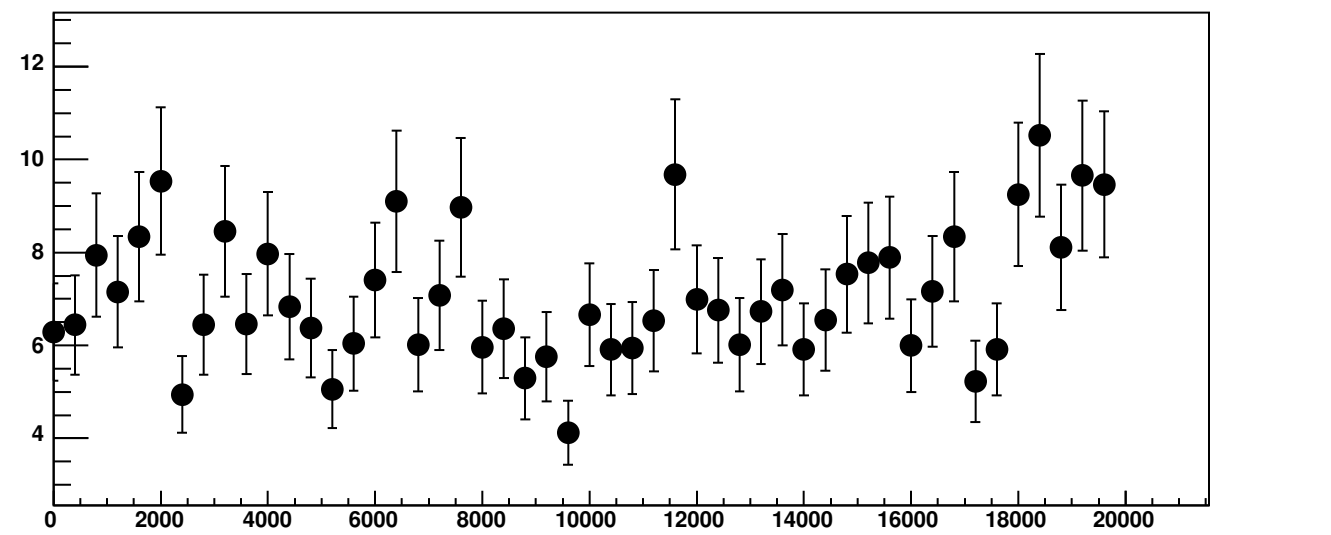

Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

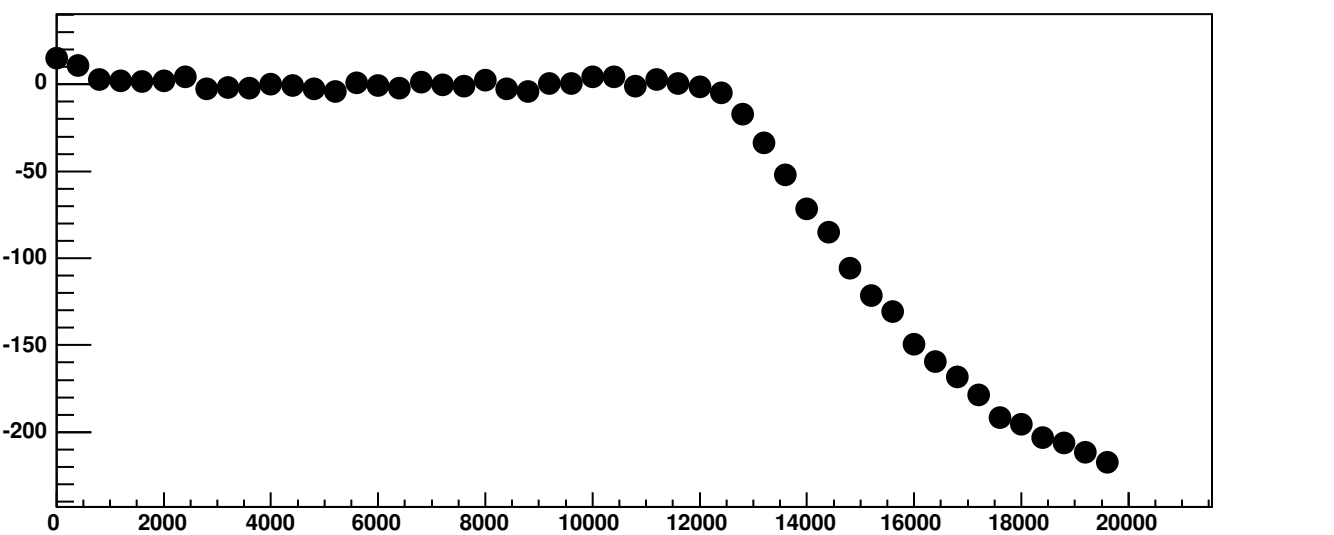
Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



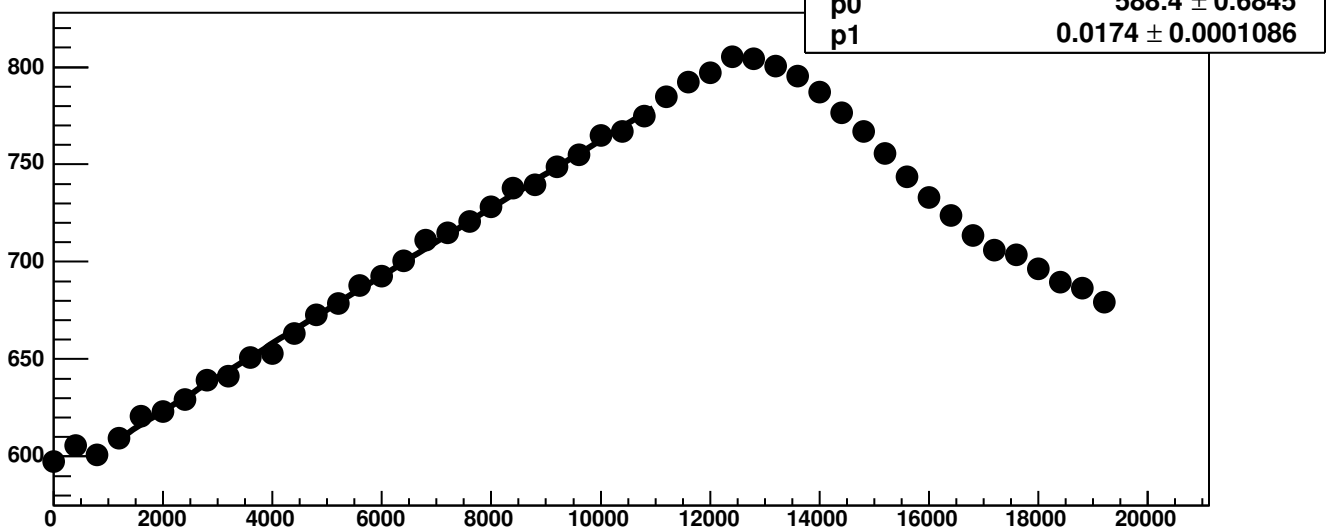
Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



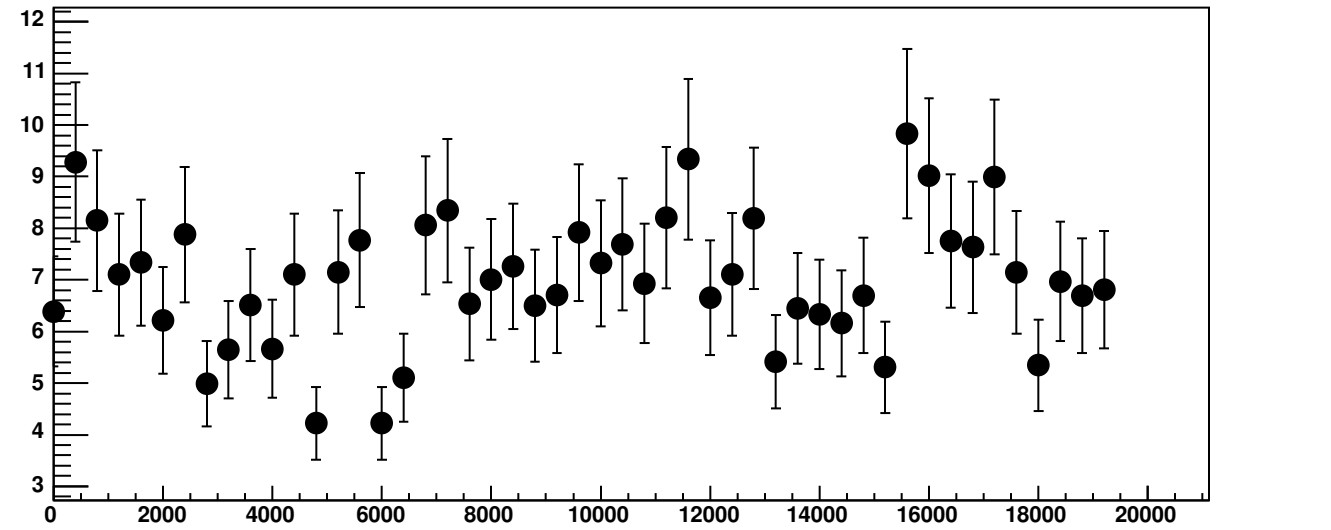
Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



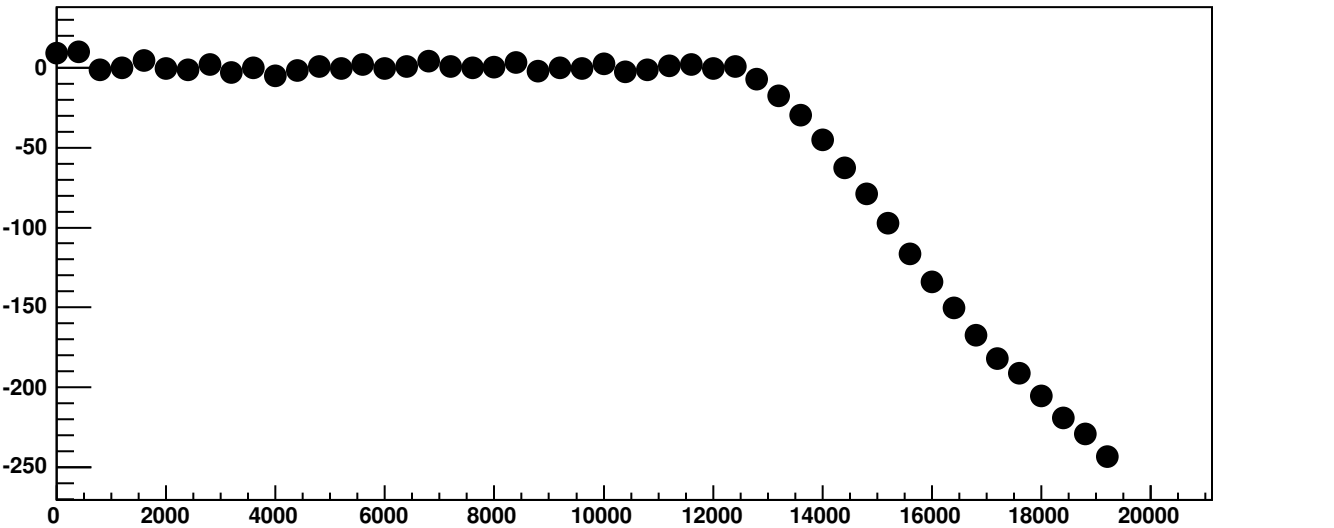
Chip 3, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

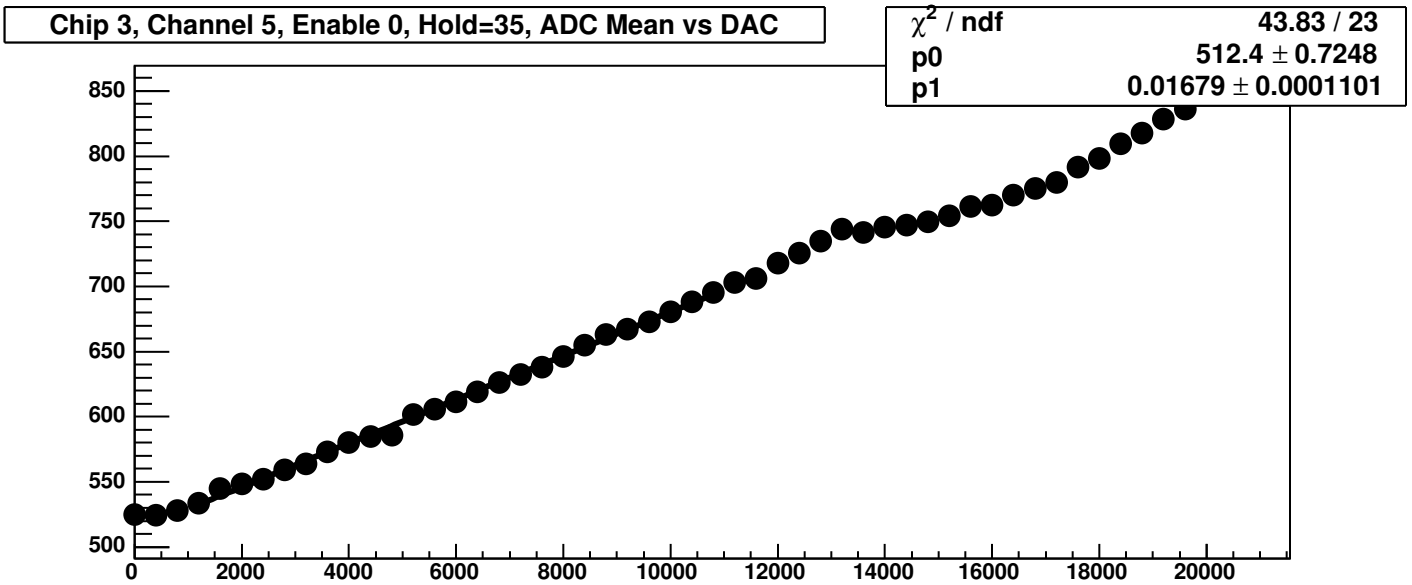


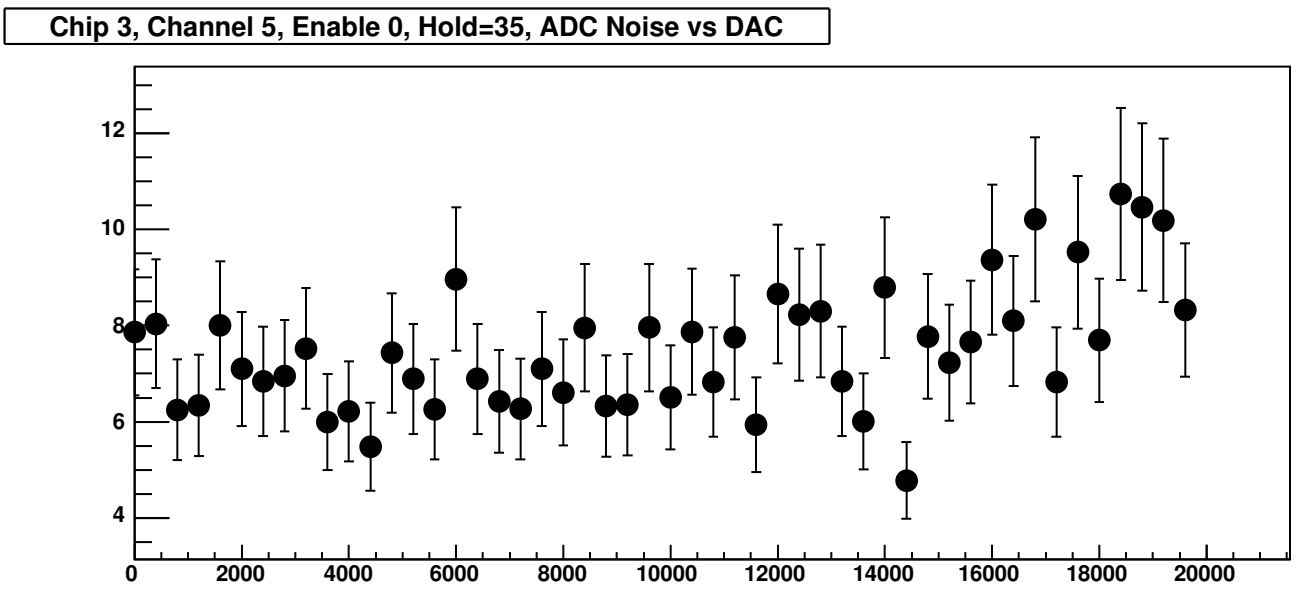
Chip 3, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

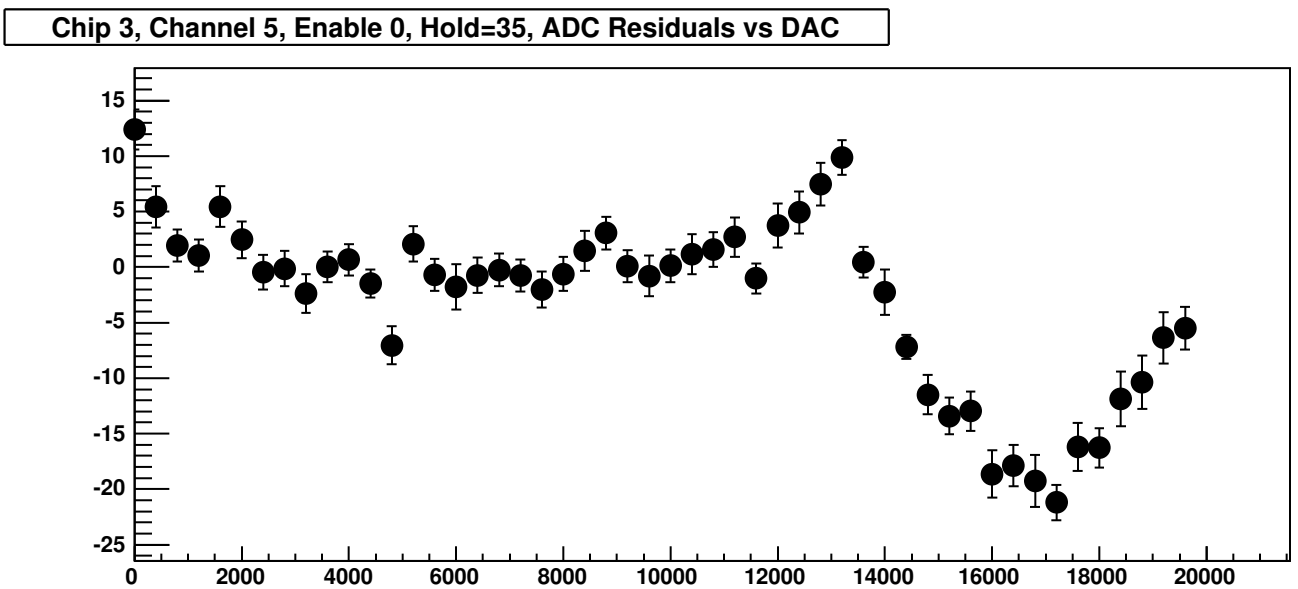


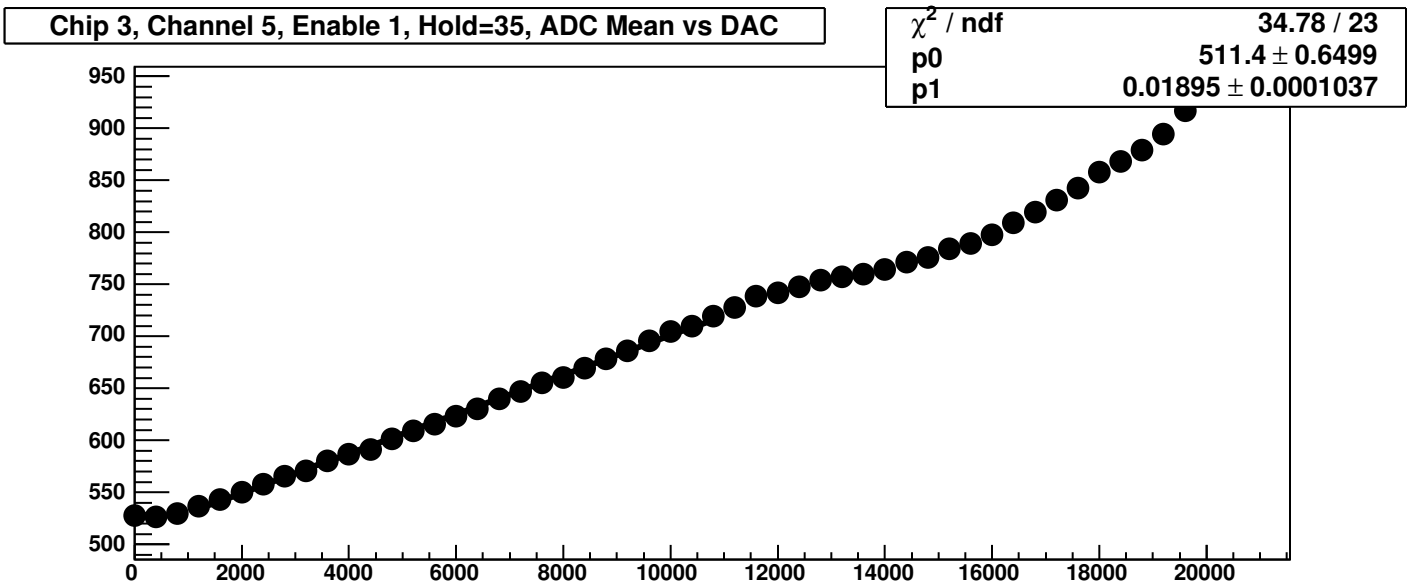
Chip 3, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

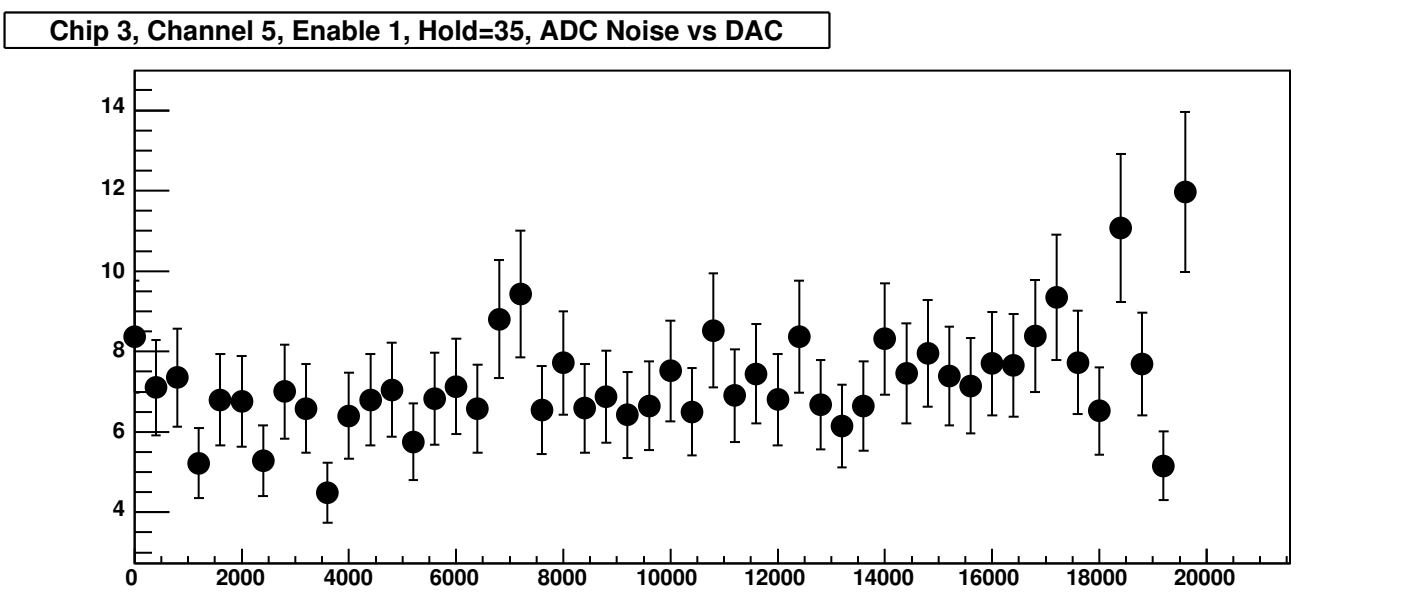


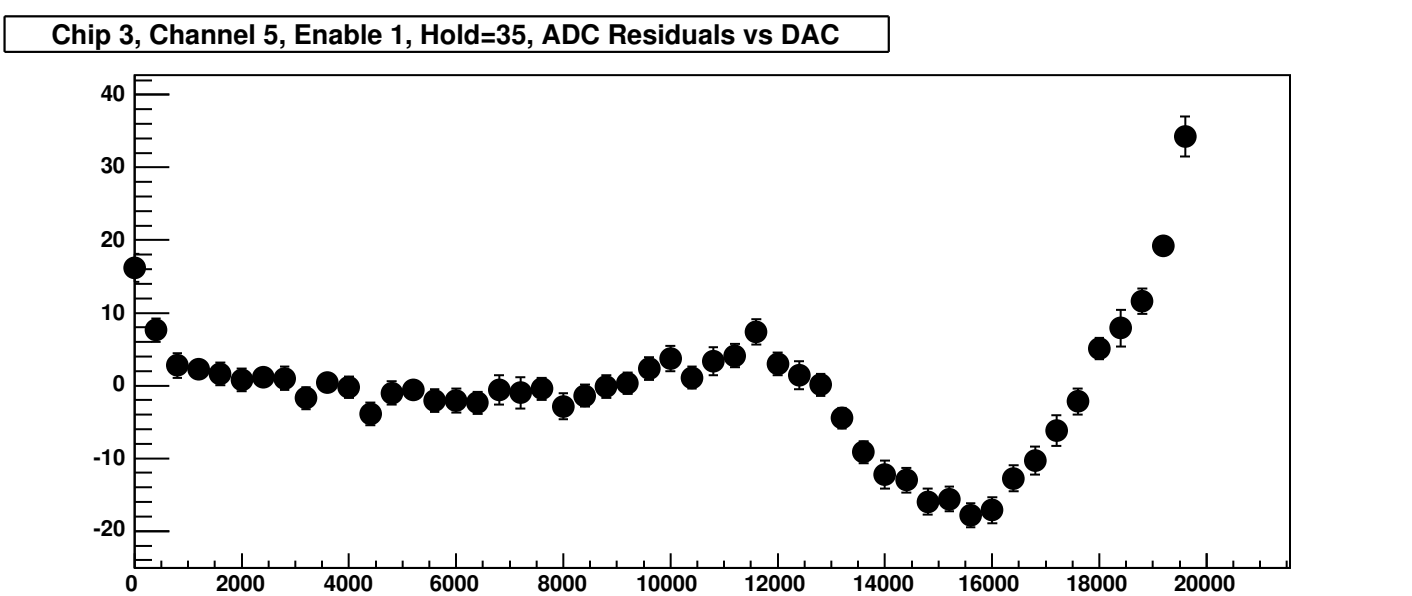

Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 43.83 / 23
p0 512.4 ± 0.7248
p1 0.01679 ± 0.0001101
850
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

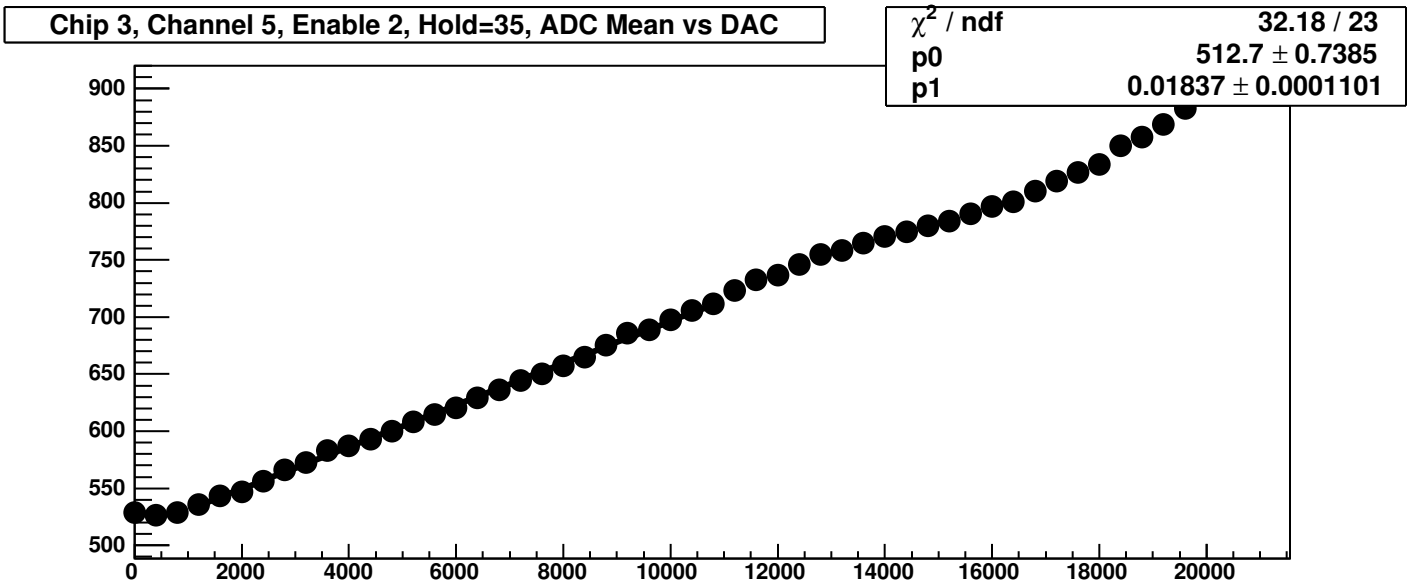

Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

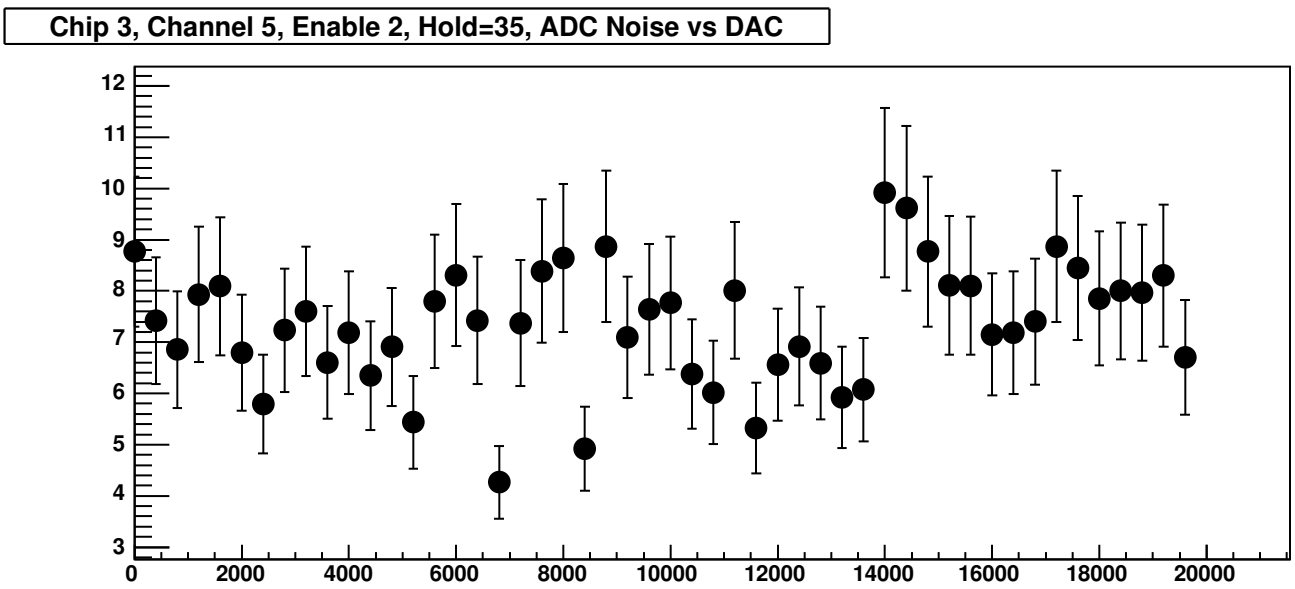

Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

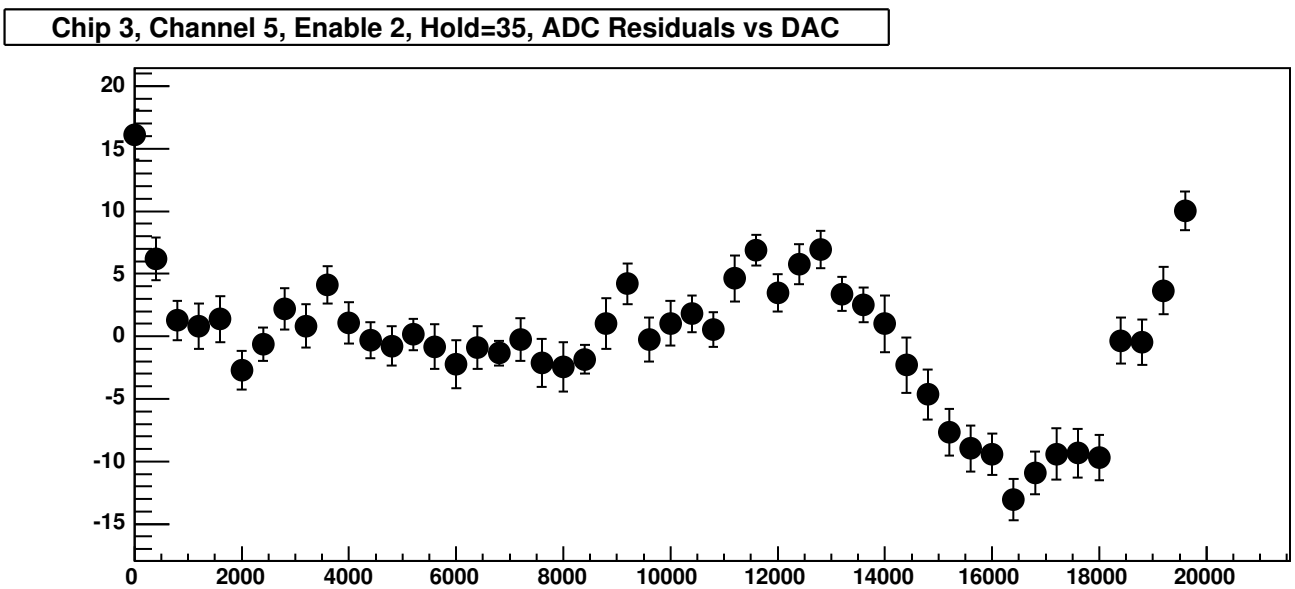

Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.78 / 23
p0 511.4 ± 0.6499
p1 0.01895 ± 0.0001037
950
900
850
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

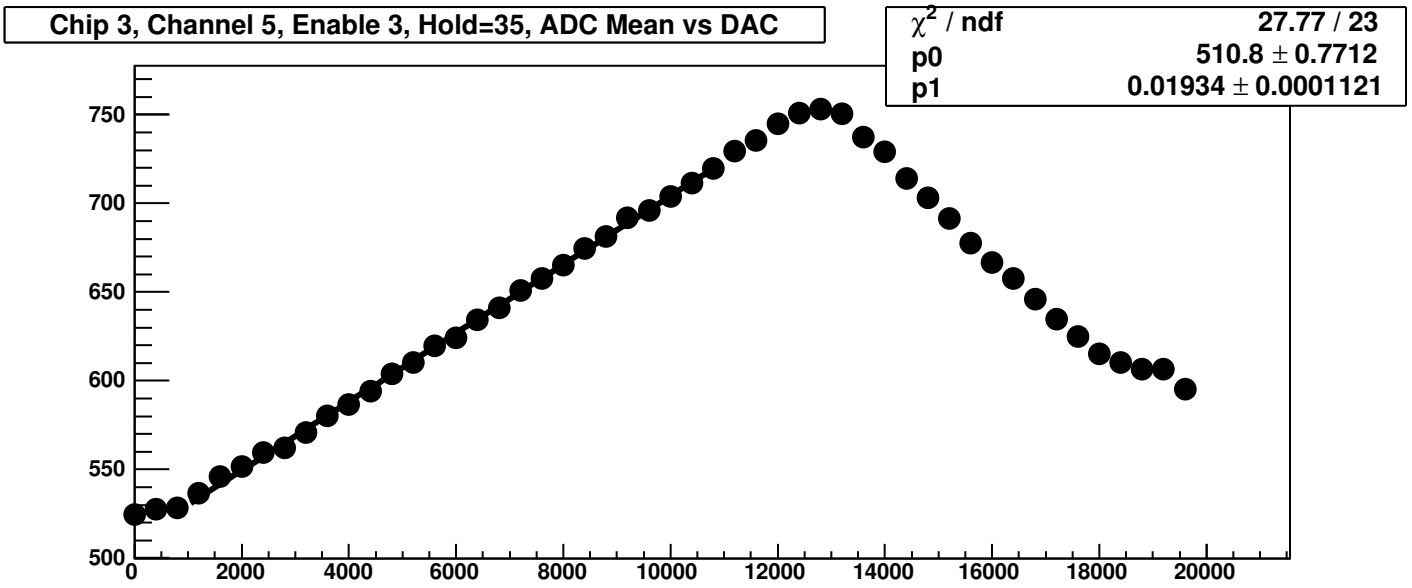

Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

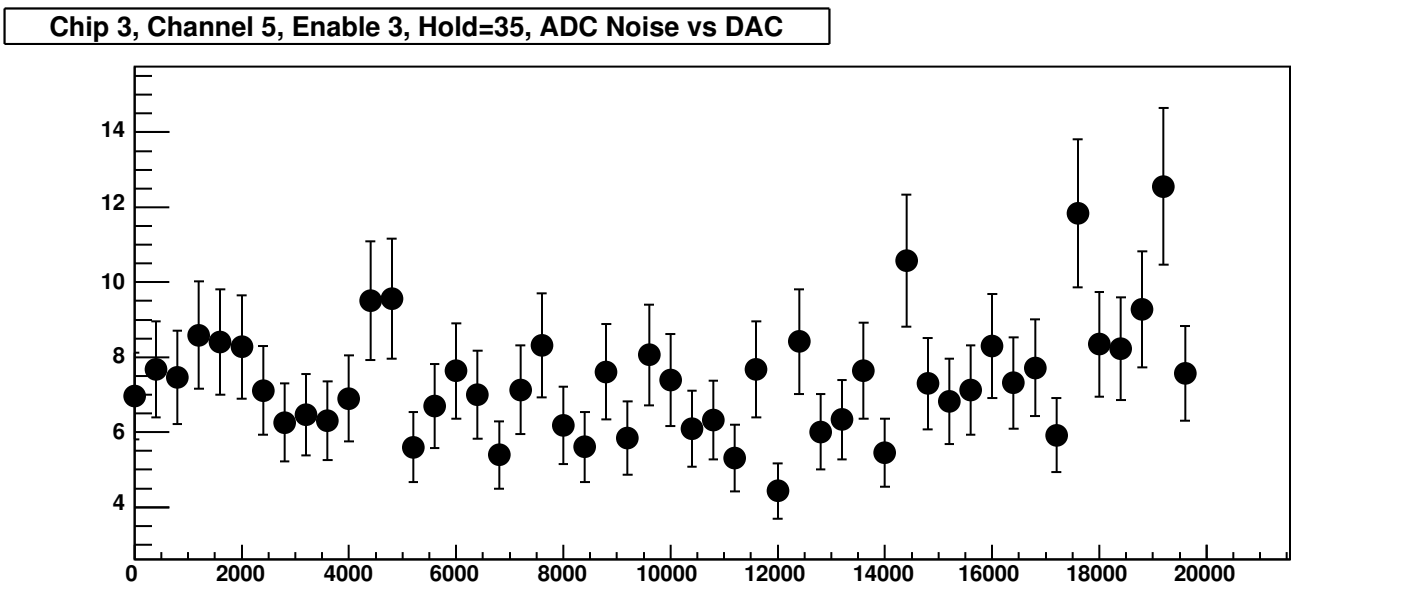

Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

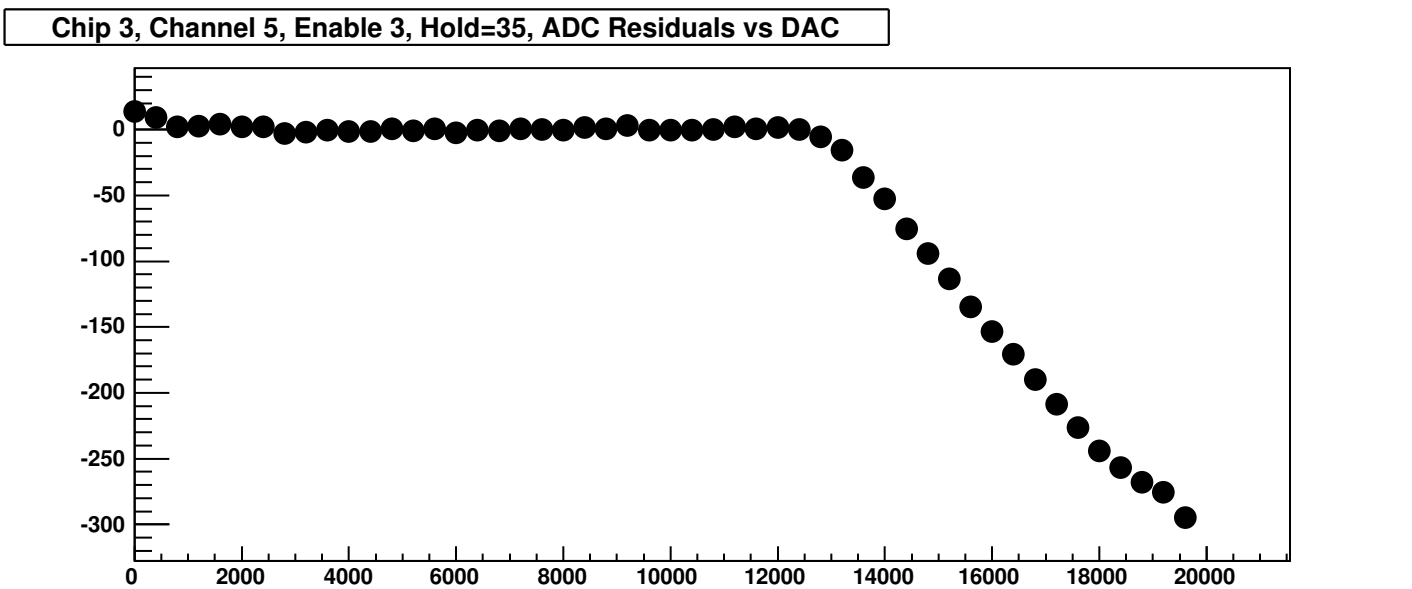

Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.18 / 23
p0 512.7 ± 0.7385
p1 0.01837 ± 0.0001101
900
850
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

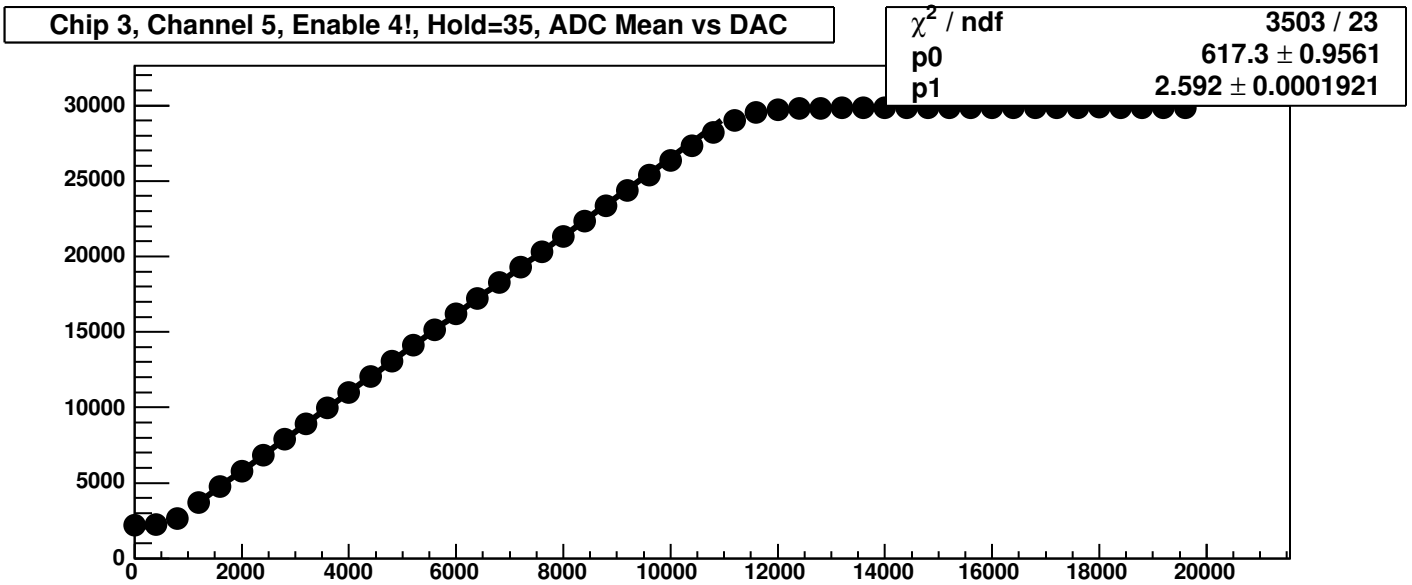

Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

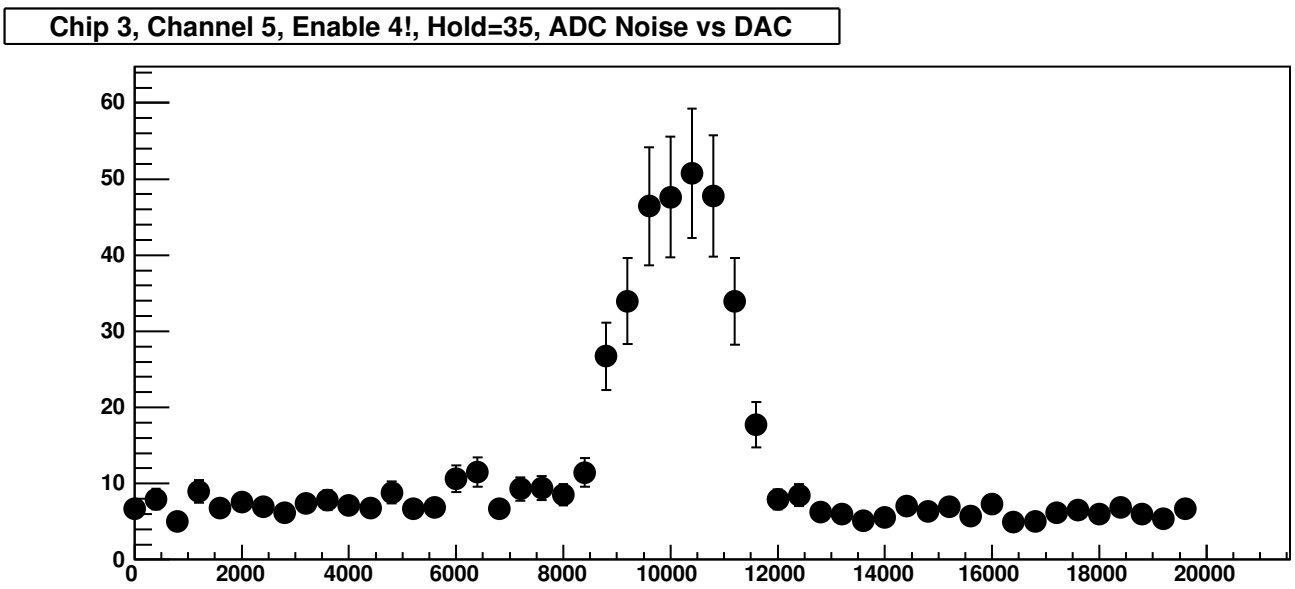

Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

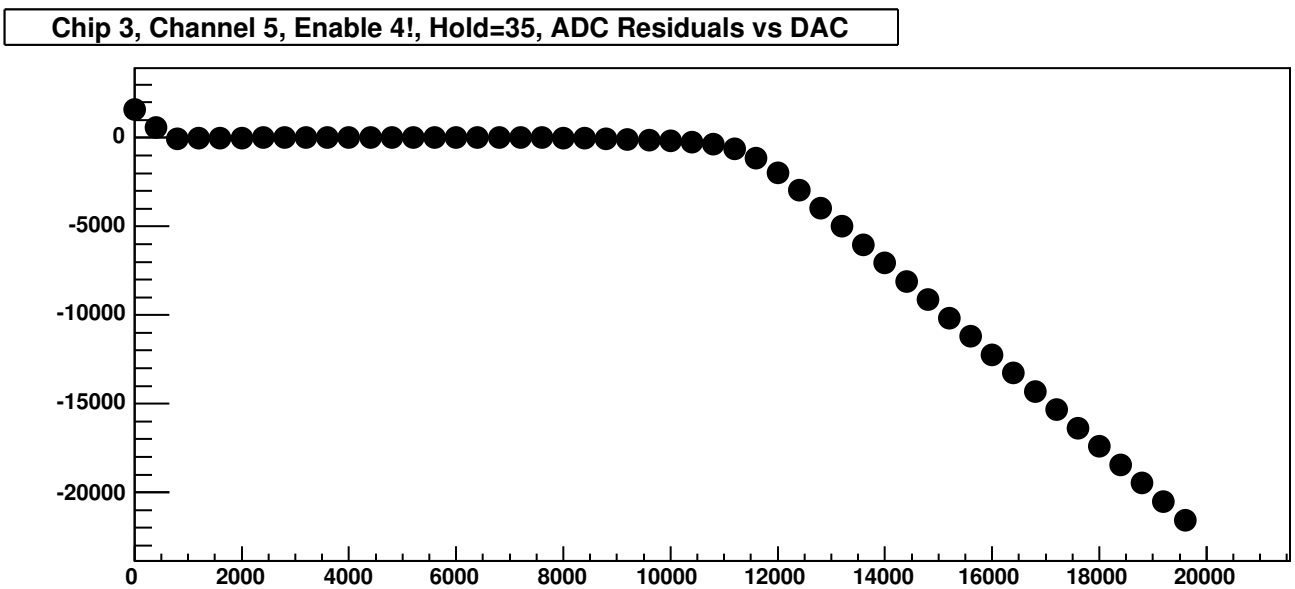

Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.77 / 23
p0 510.8 ± 0.7712
p1 0.01934 ± 0.0001121
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

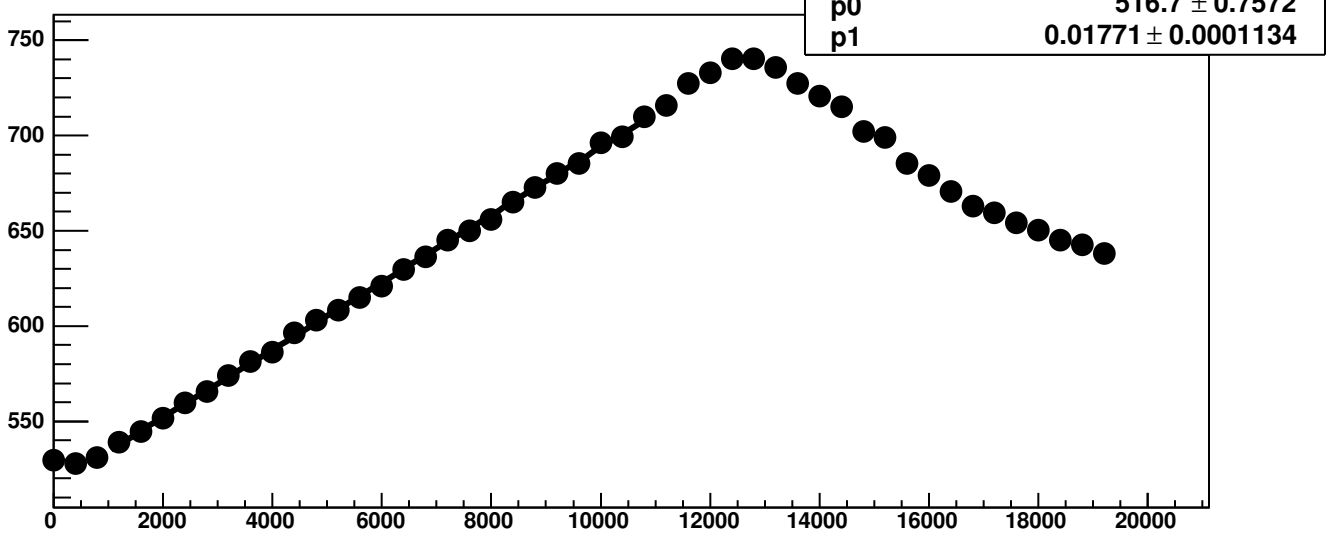

Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 3503 / 23
p0 617.3 ± 0.9561
p1 2.592 ± 0.0001921
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

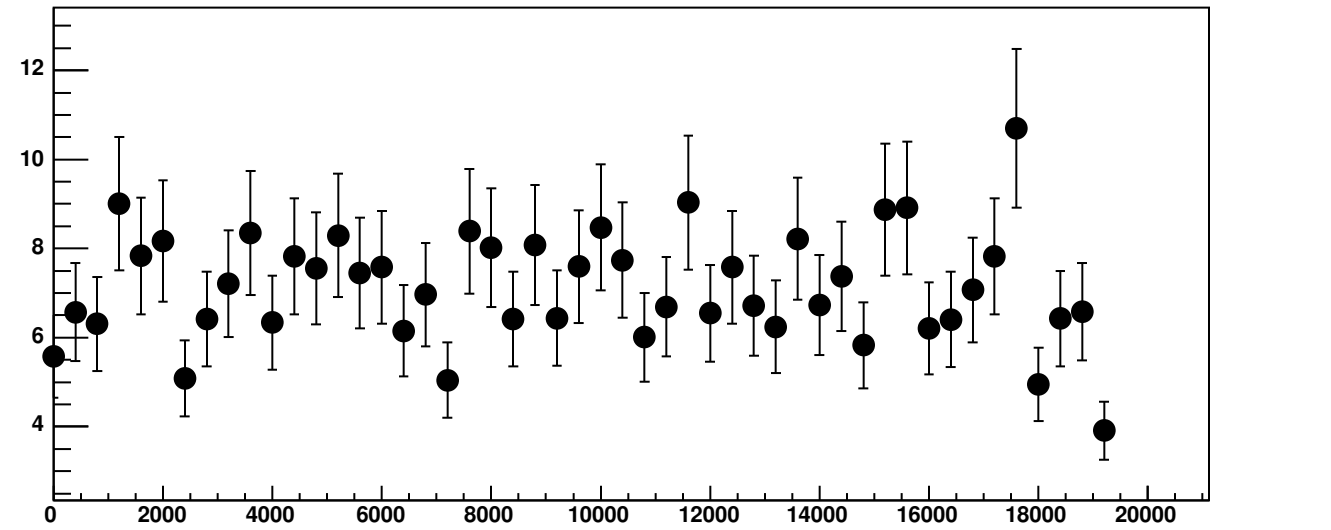

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

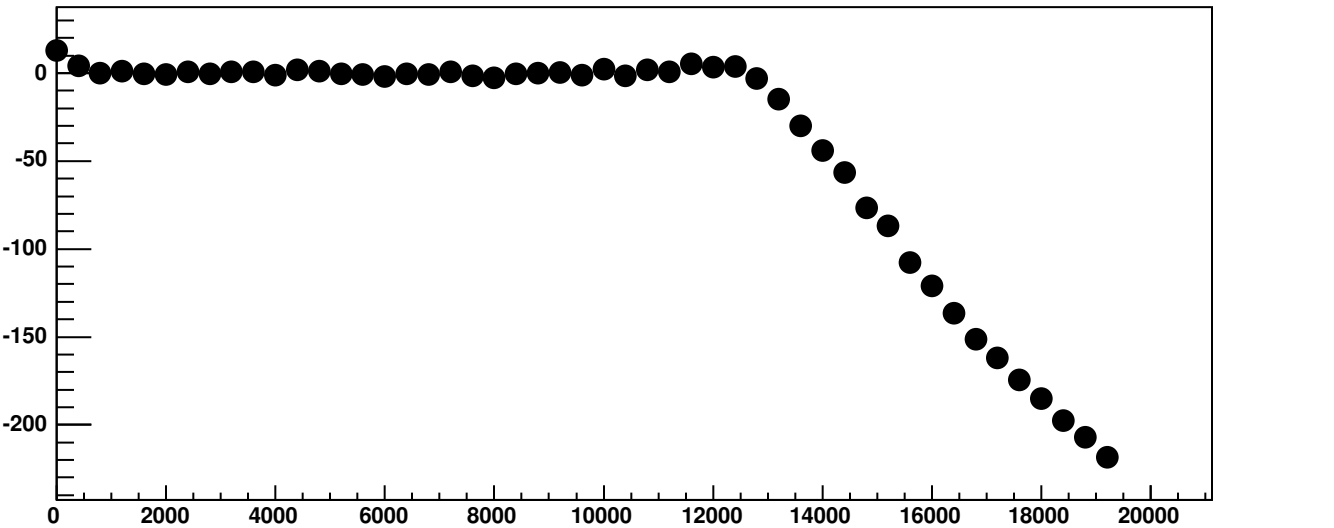
Chip 3, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

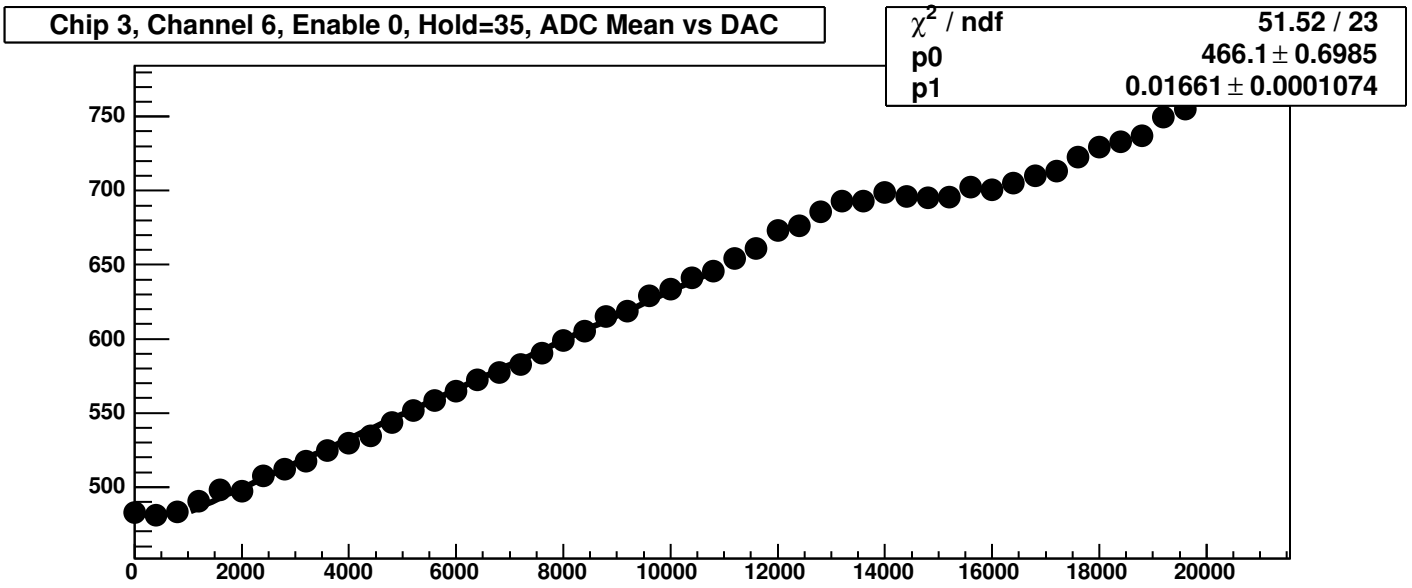


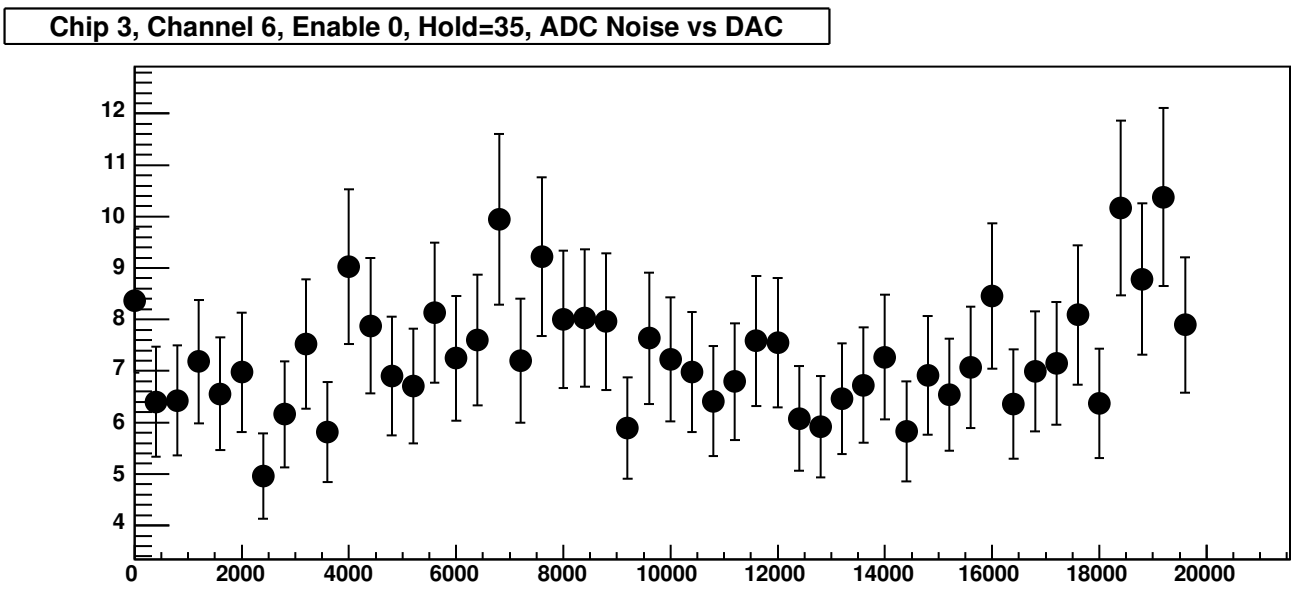
Chip 3, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

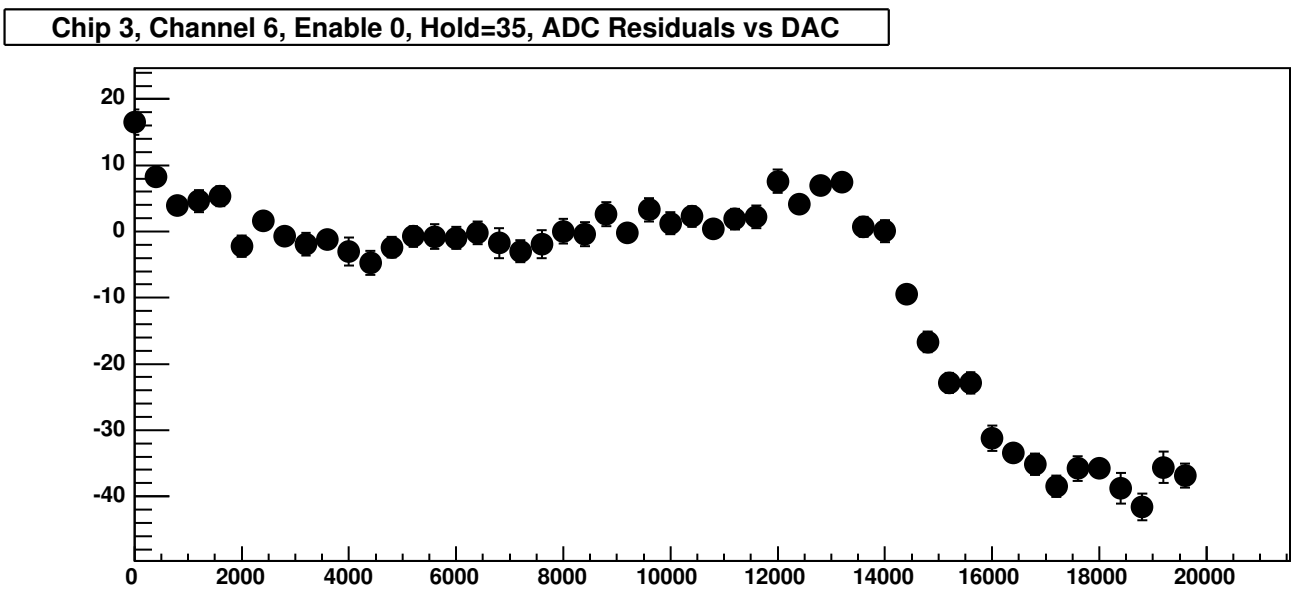


Chip 3, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

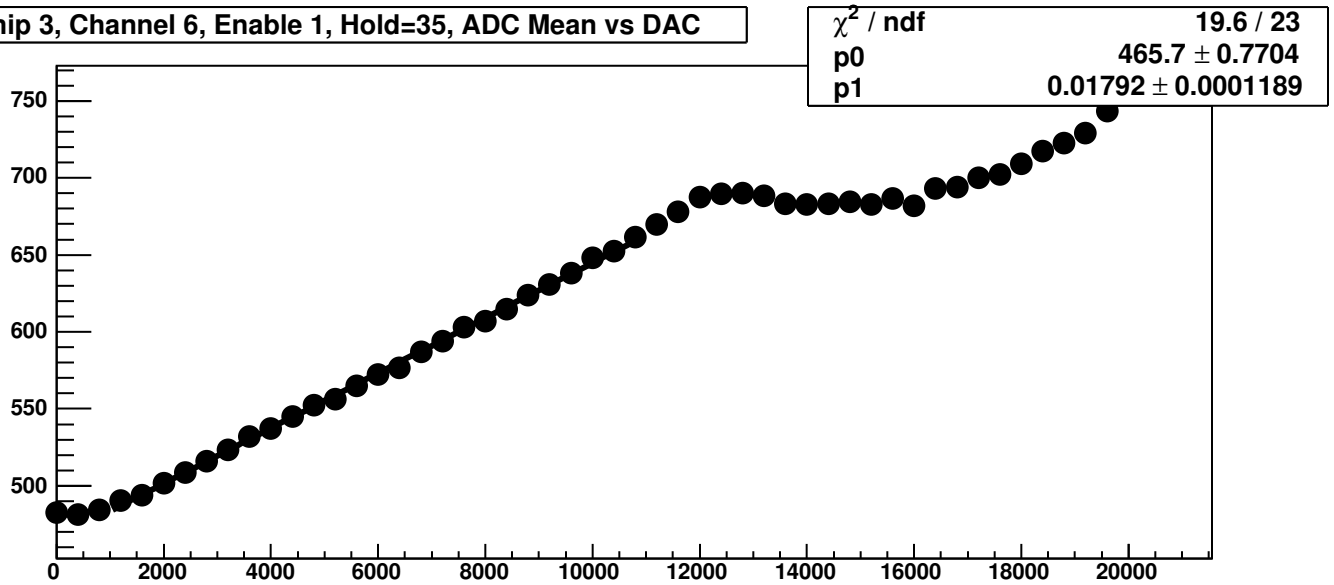



Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 51.52 / 23
p0 466.1 ± 0.6985
p1 0.01661 ± 0.0001074
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

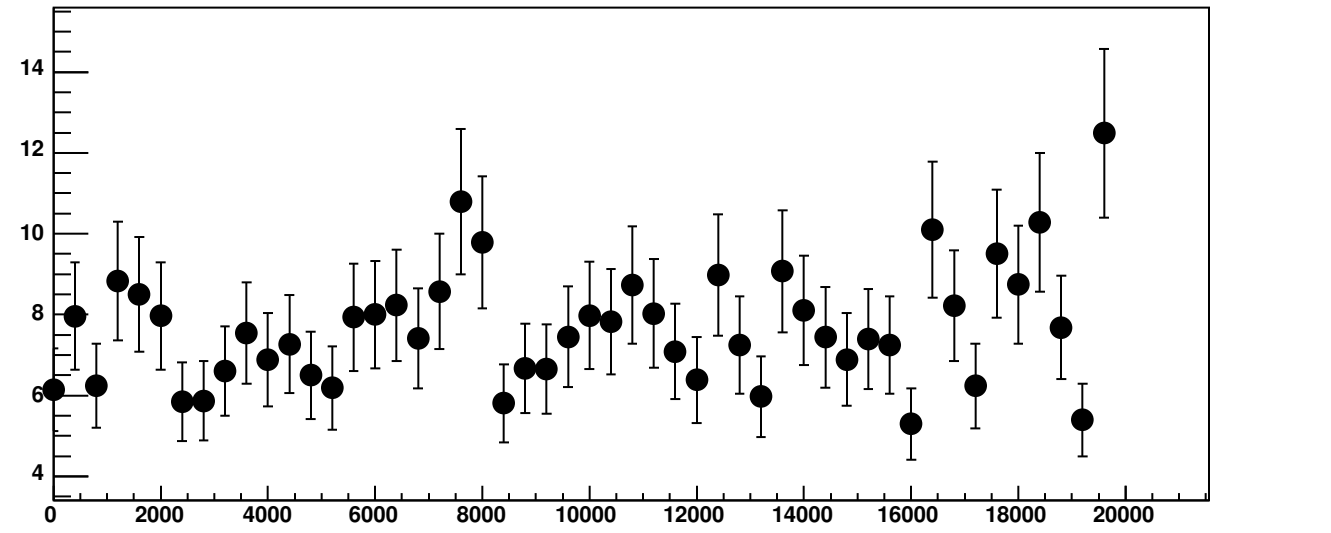

Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

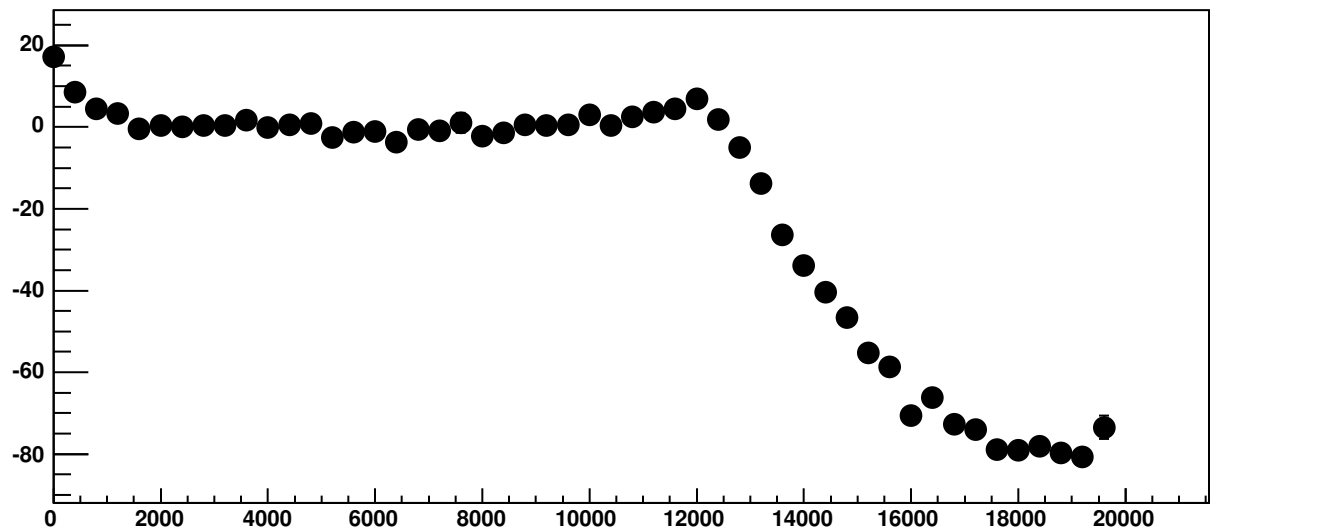
Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

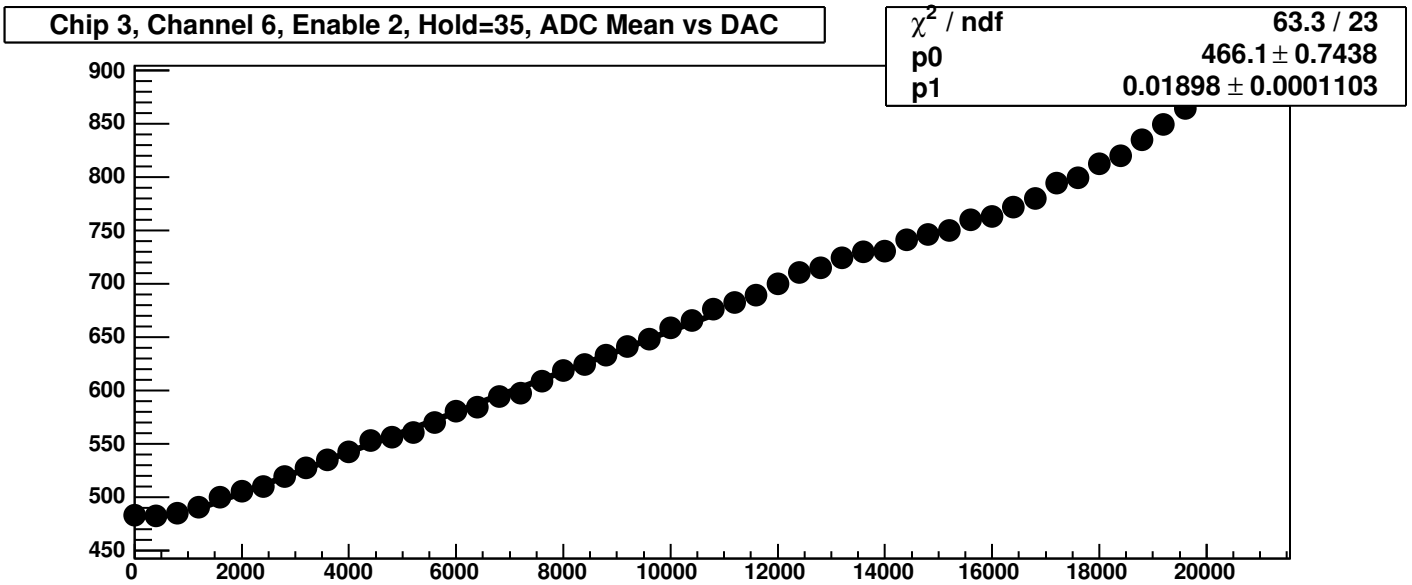


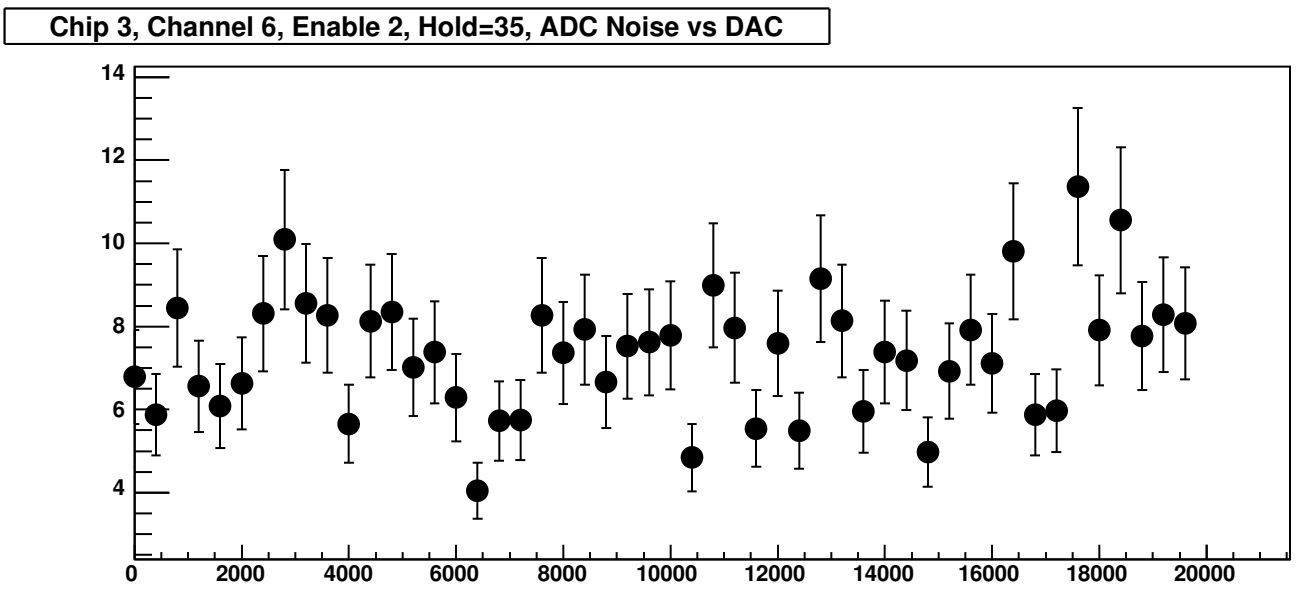
Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

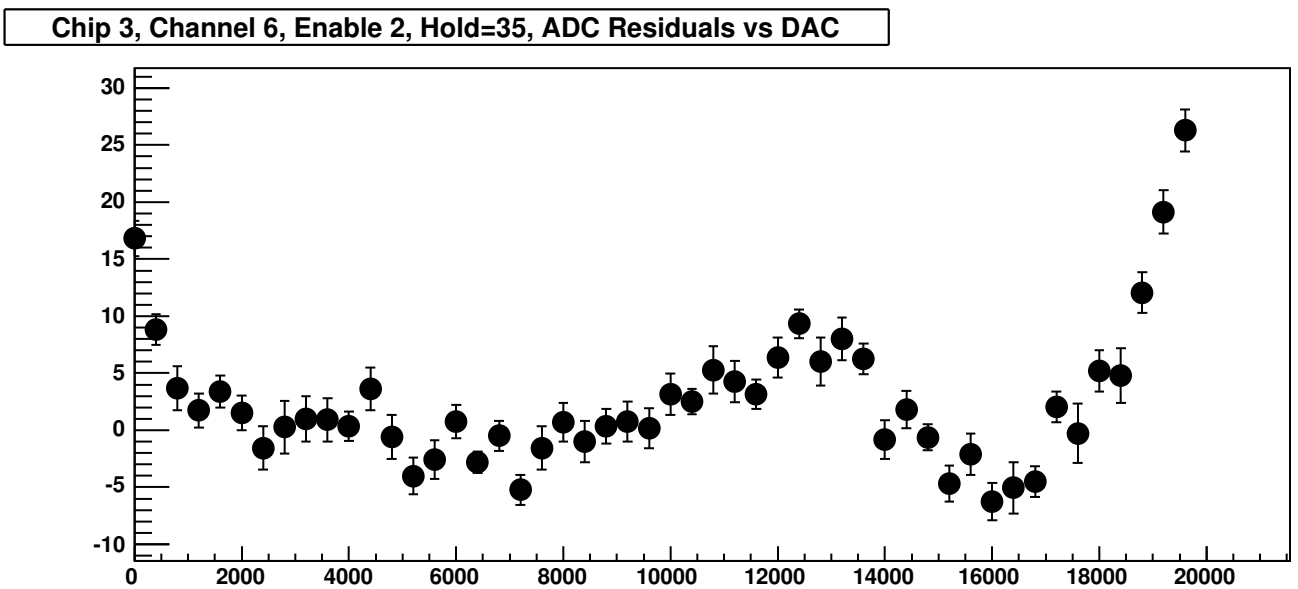


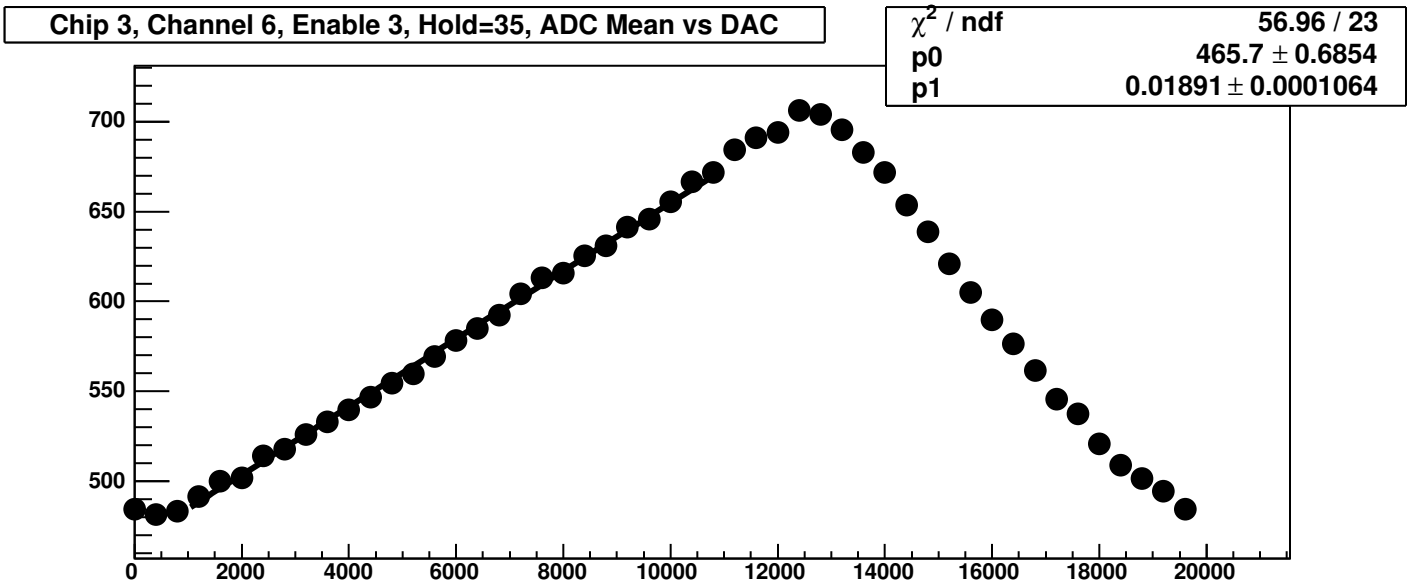
Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

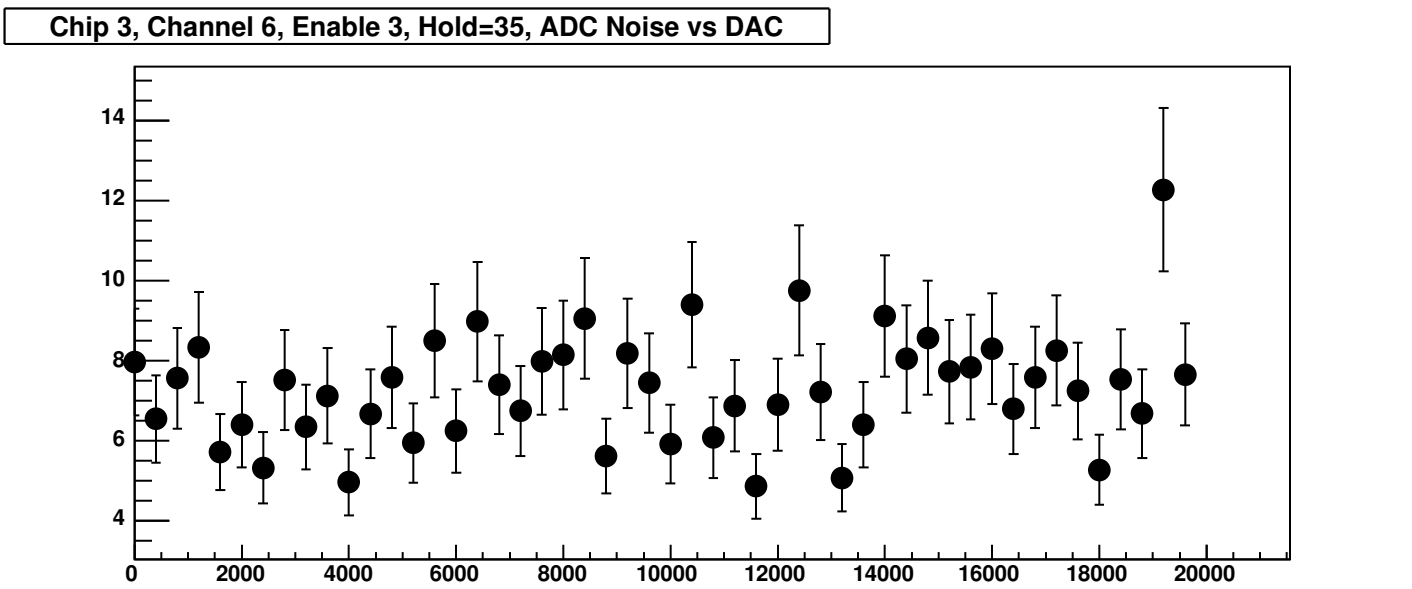


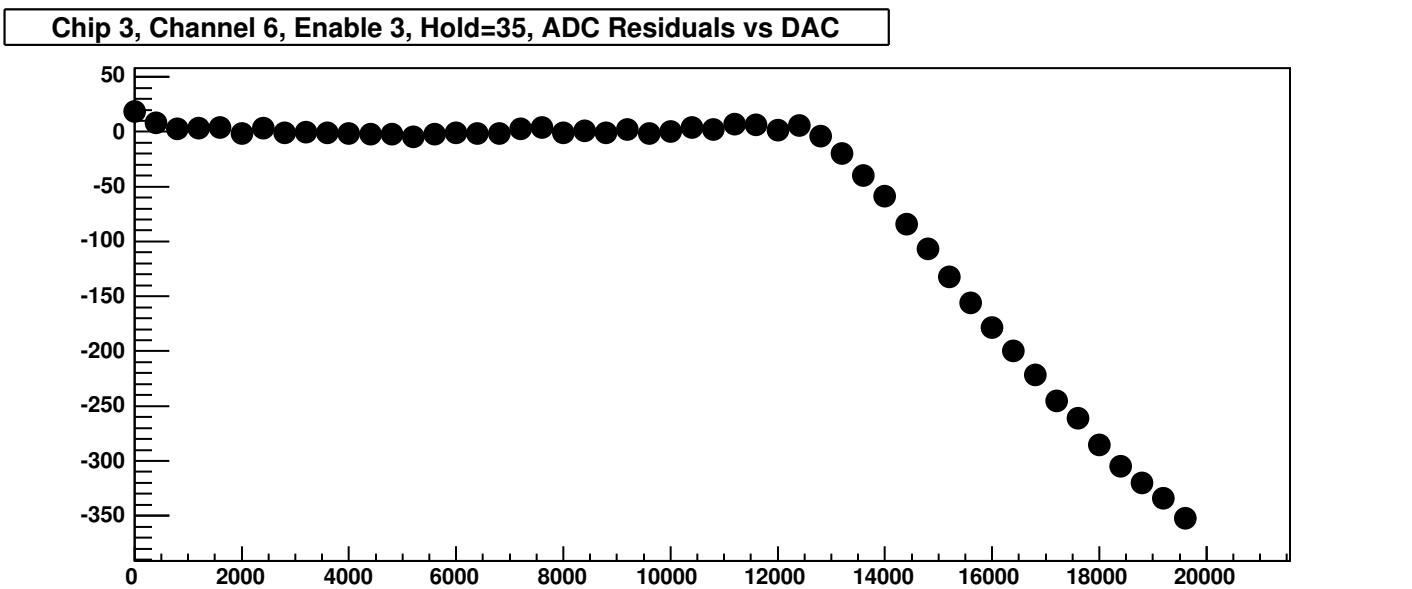

Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 63.3 / 23
p0 466.1 ± 0.7438
p1 0.01898 ± 0.0001103
900
850
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

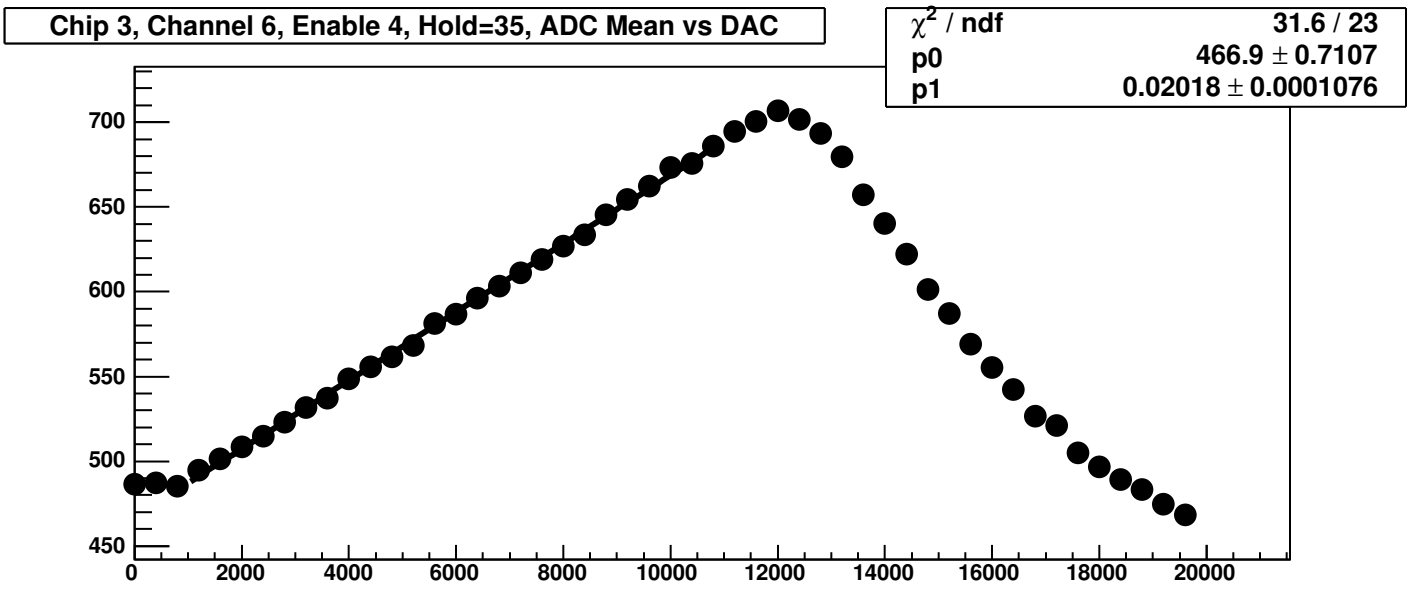

Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

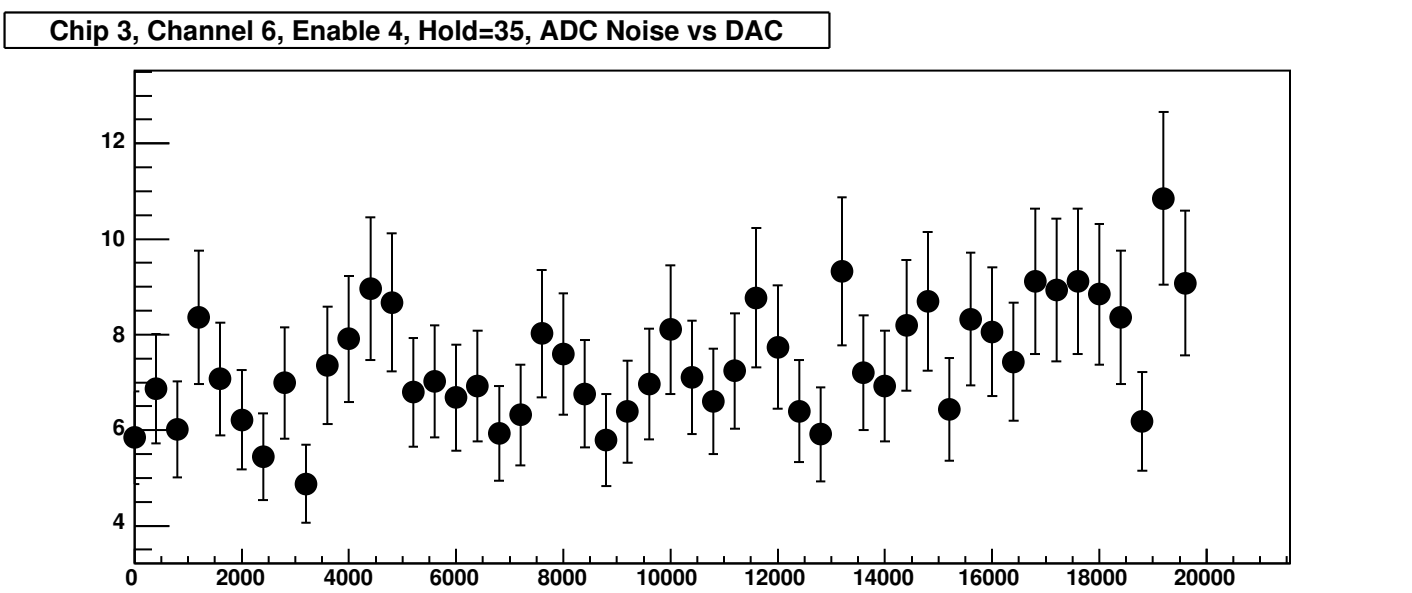

Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

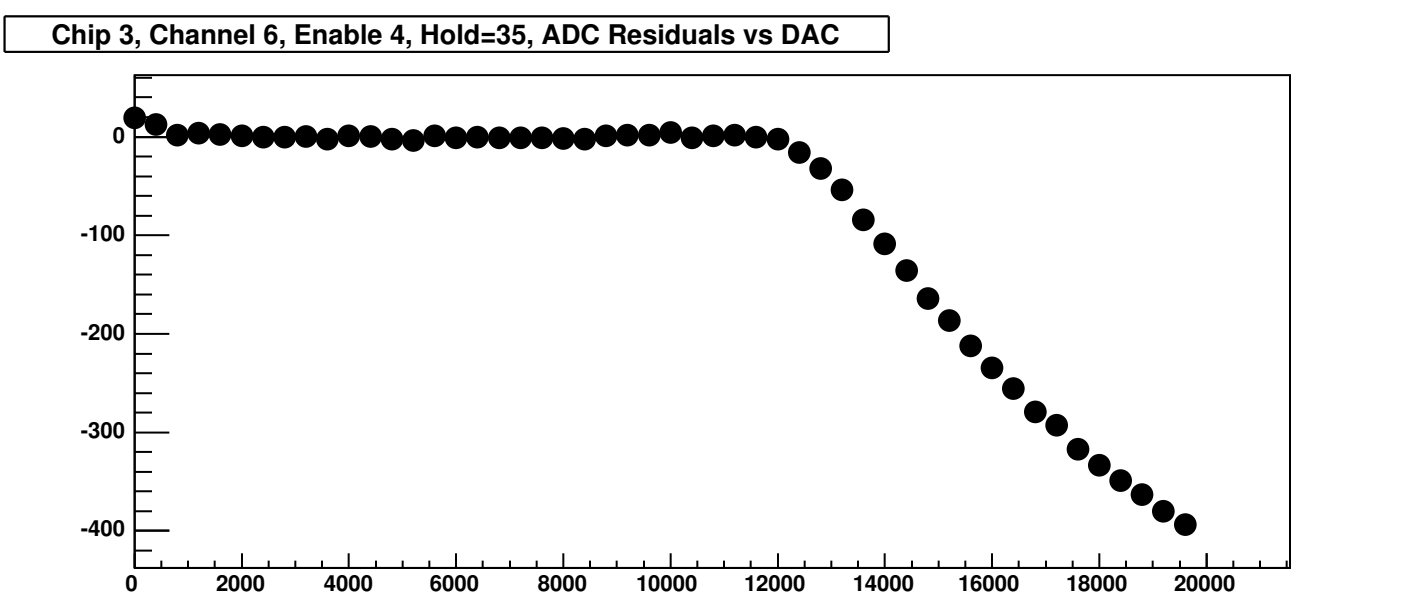

Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 56.96 / 23
p0 465.7 ± 0.6854
p1 0.01891 ± 0.0001064
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

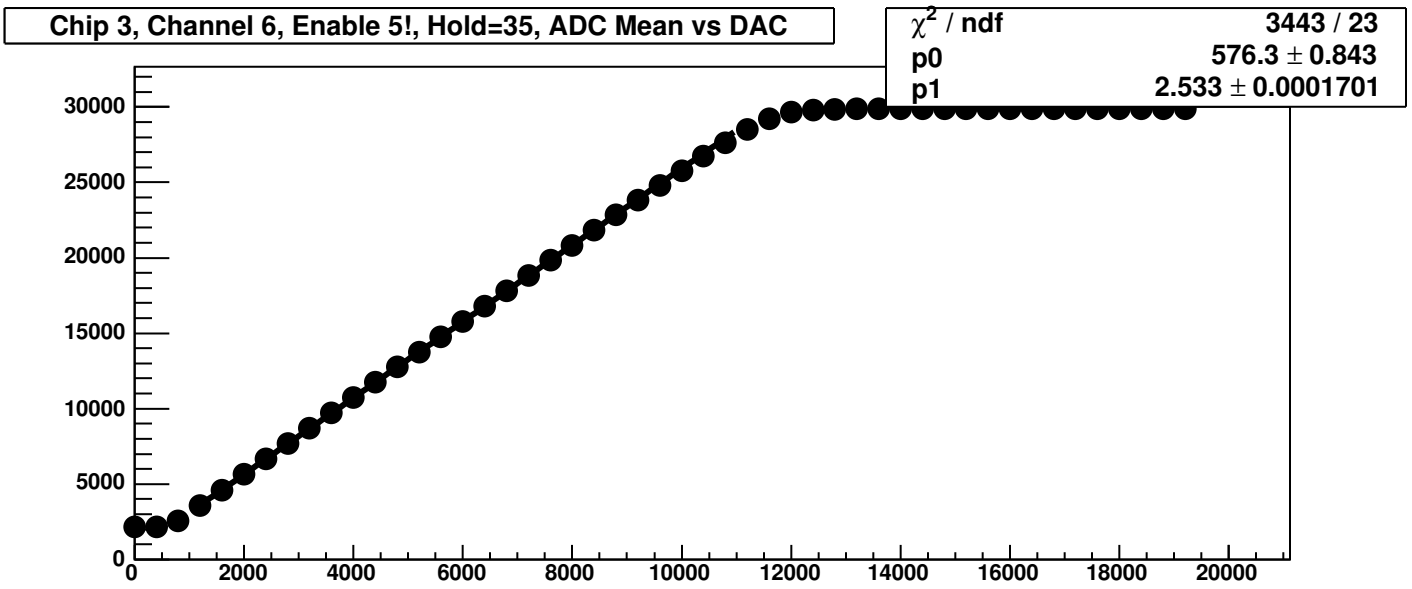

Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

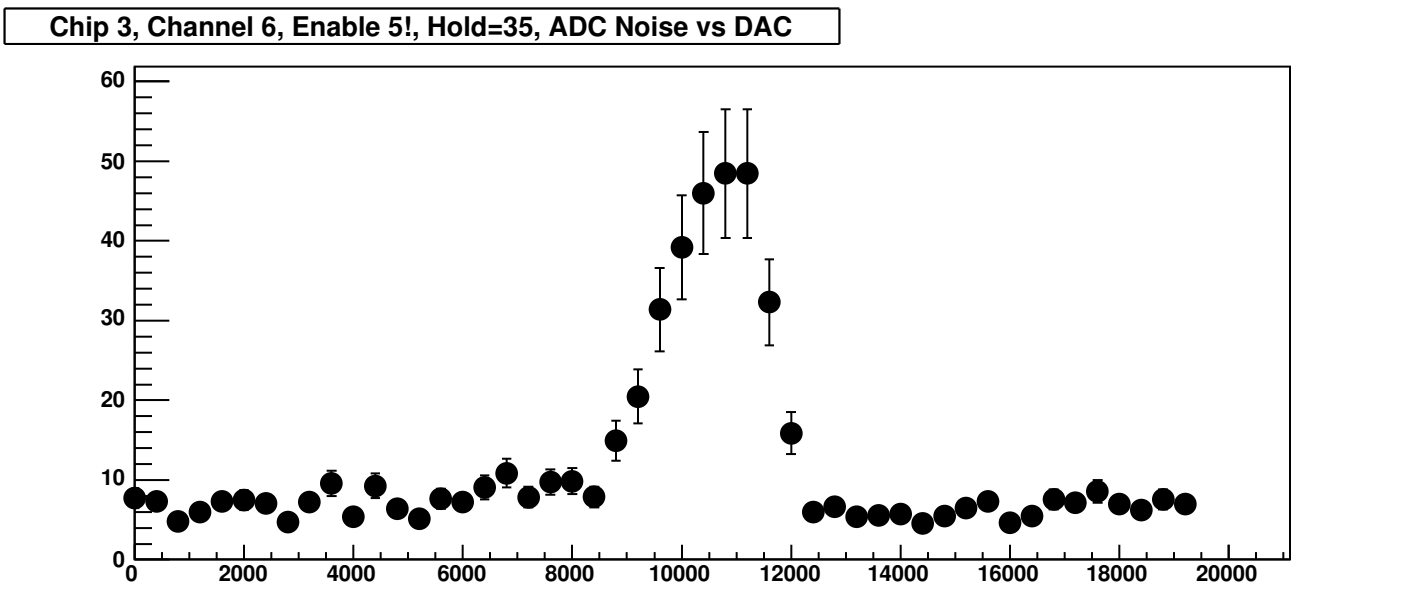

Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

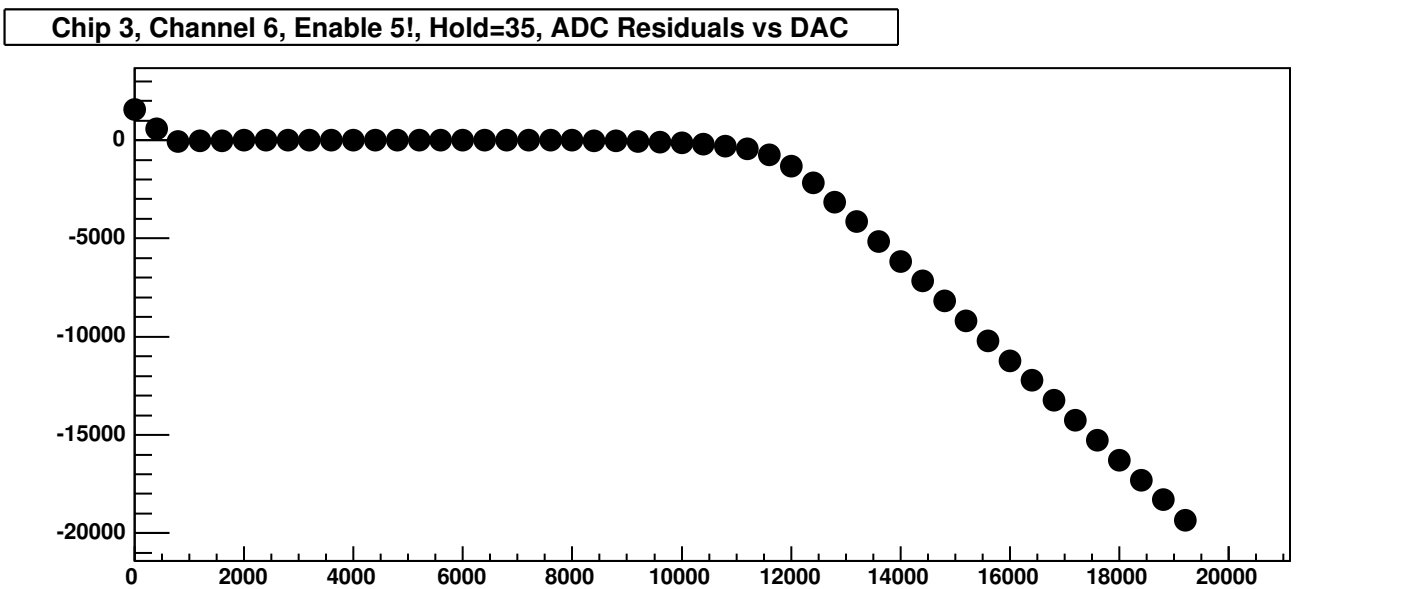

Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.6 / 23
p0 466.9 ± 0.7107
p1 0.02018 ± 0.0001076
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

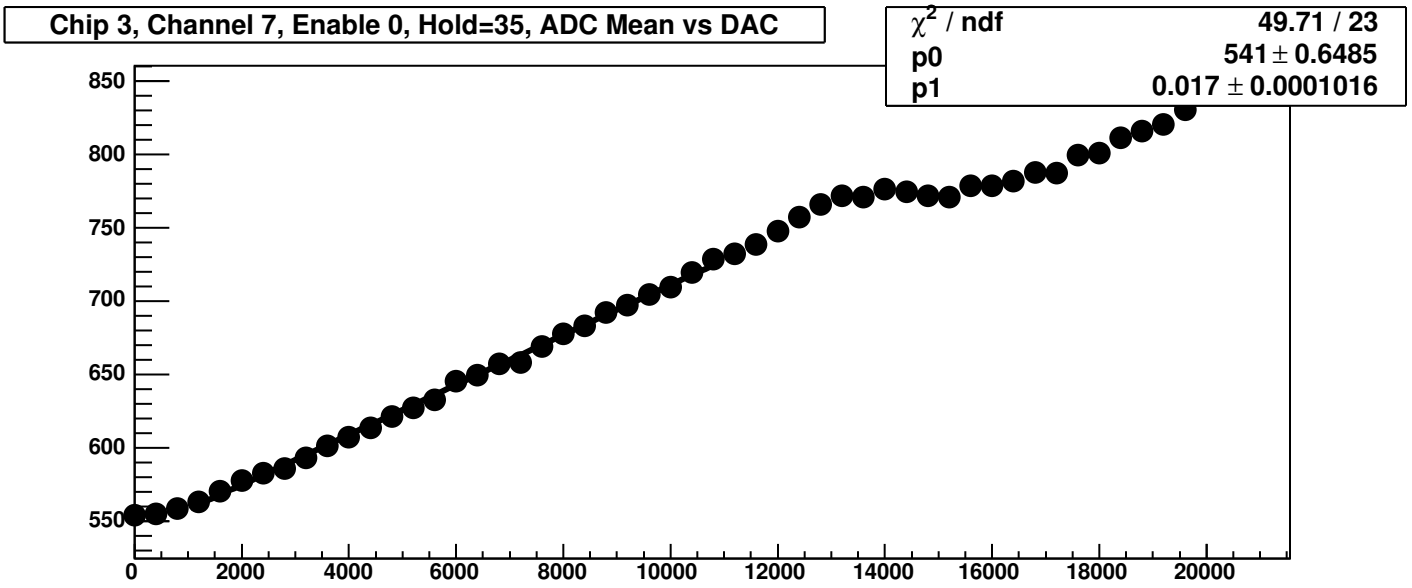

Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

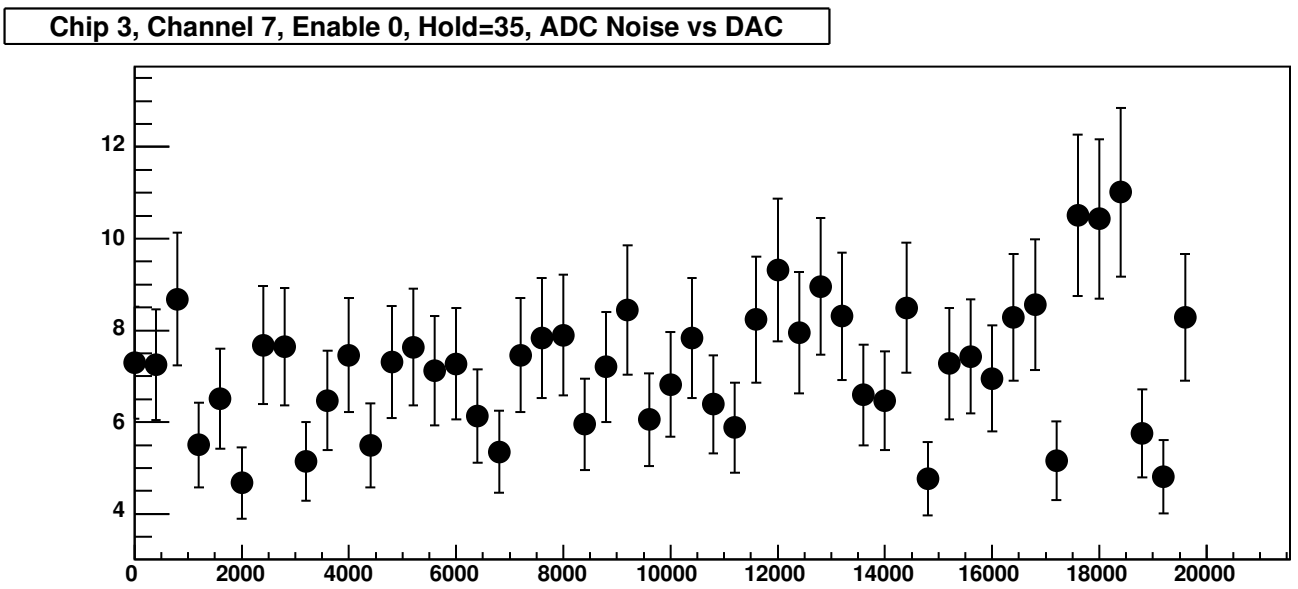

Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

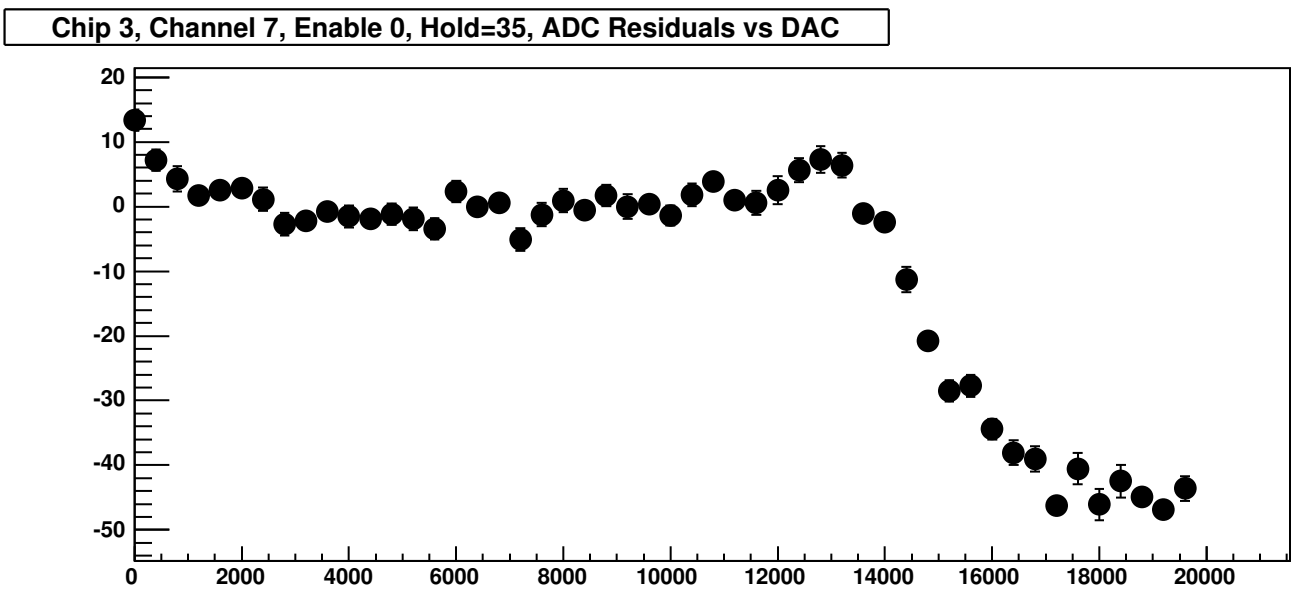

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3443 / 23
p0 576.3 ± 0.843
p1 2.533 ± 0.0001701
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

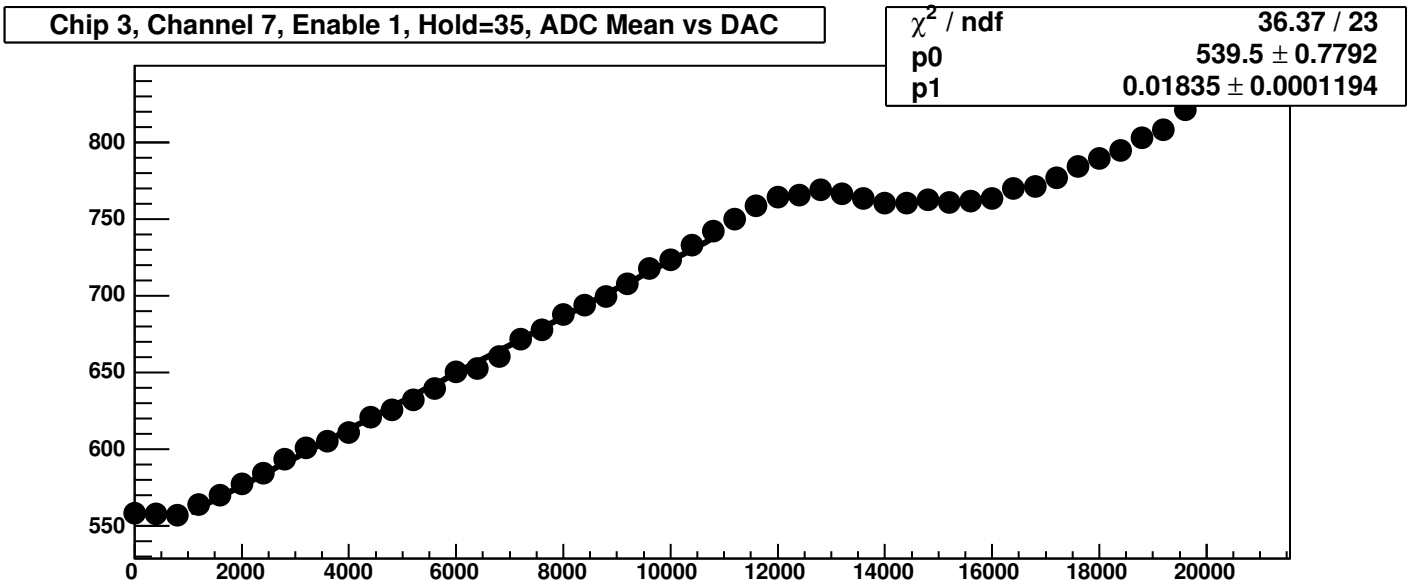

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

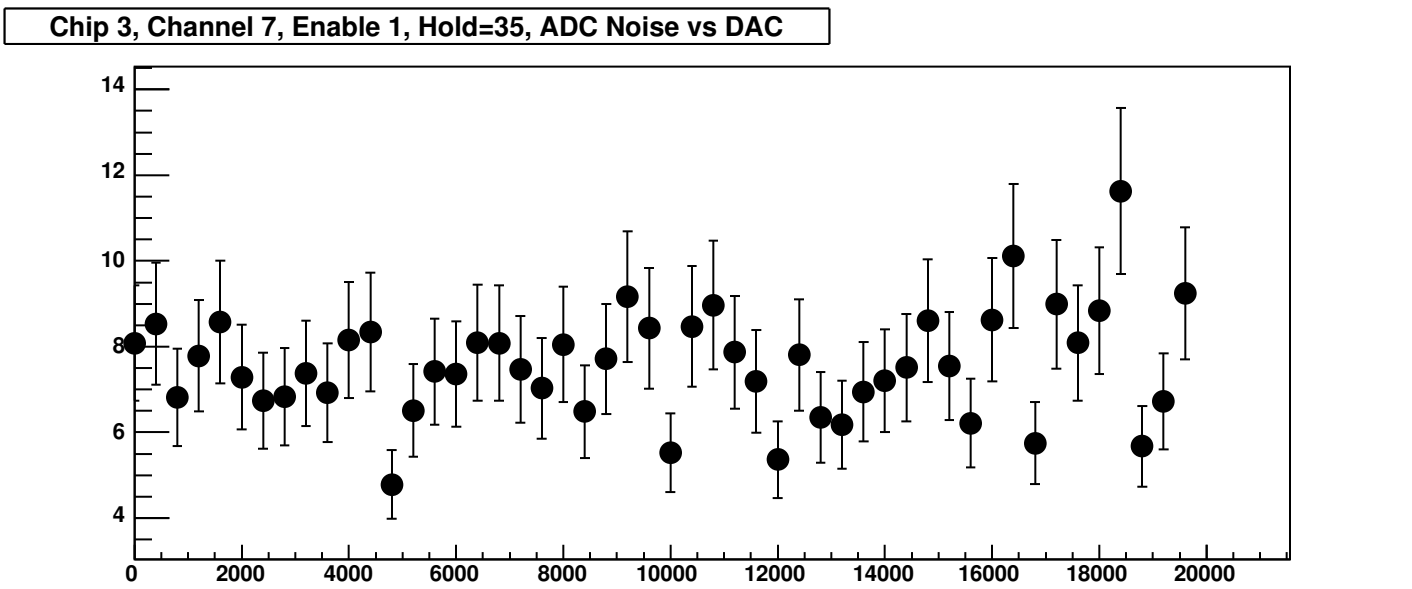

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

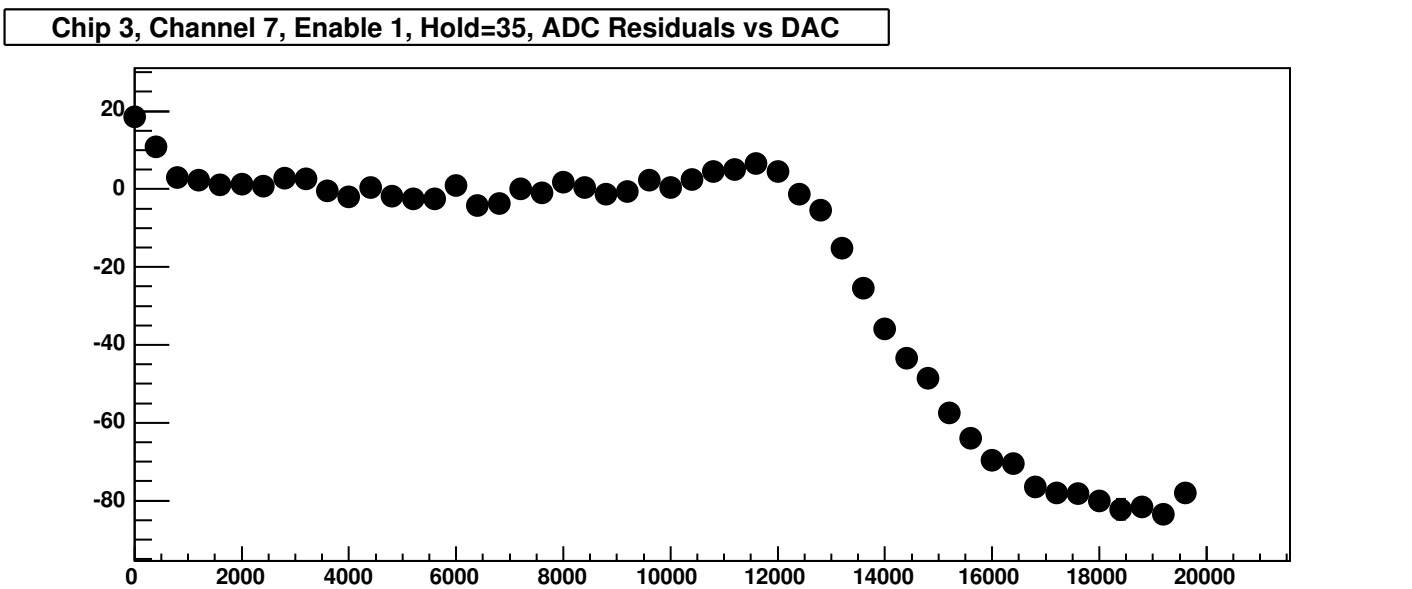

Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 49.71 / 23
p0 541 ± 0.6485
p1 0.017 ± 0.0001016
850
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

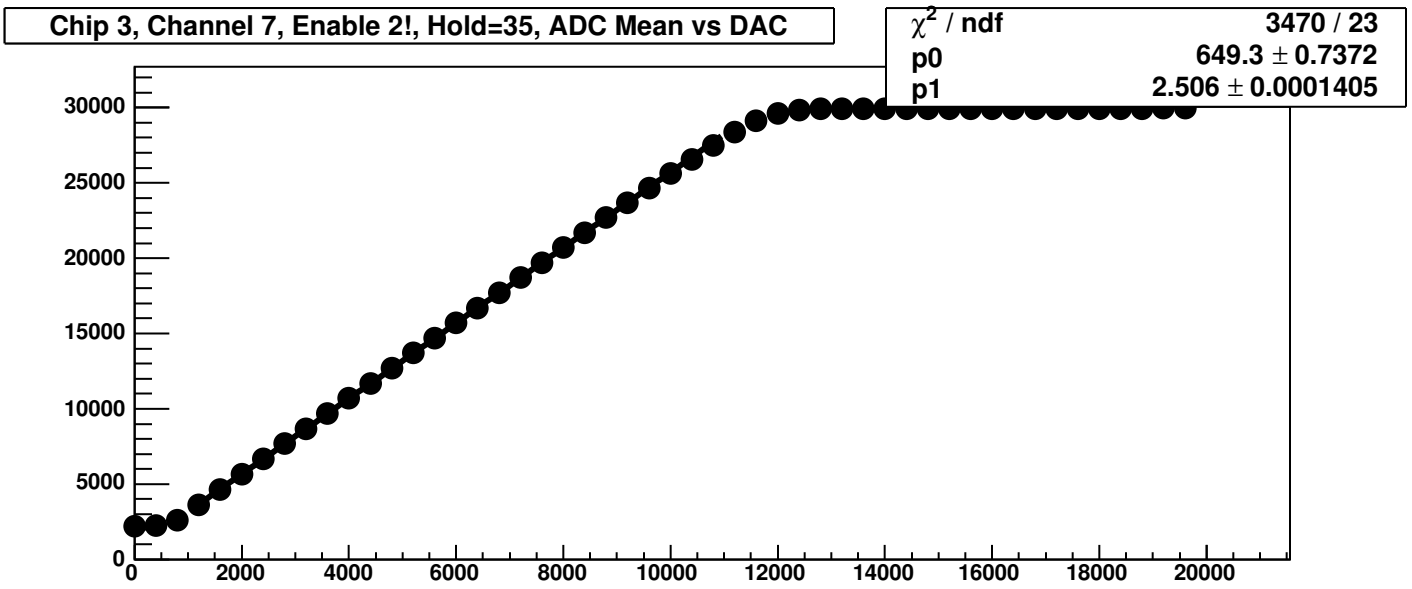

Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

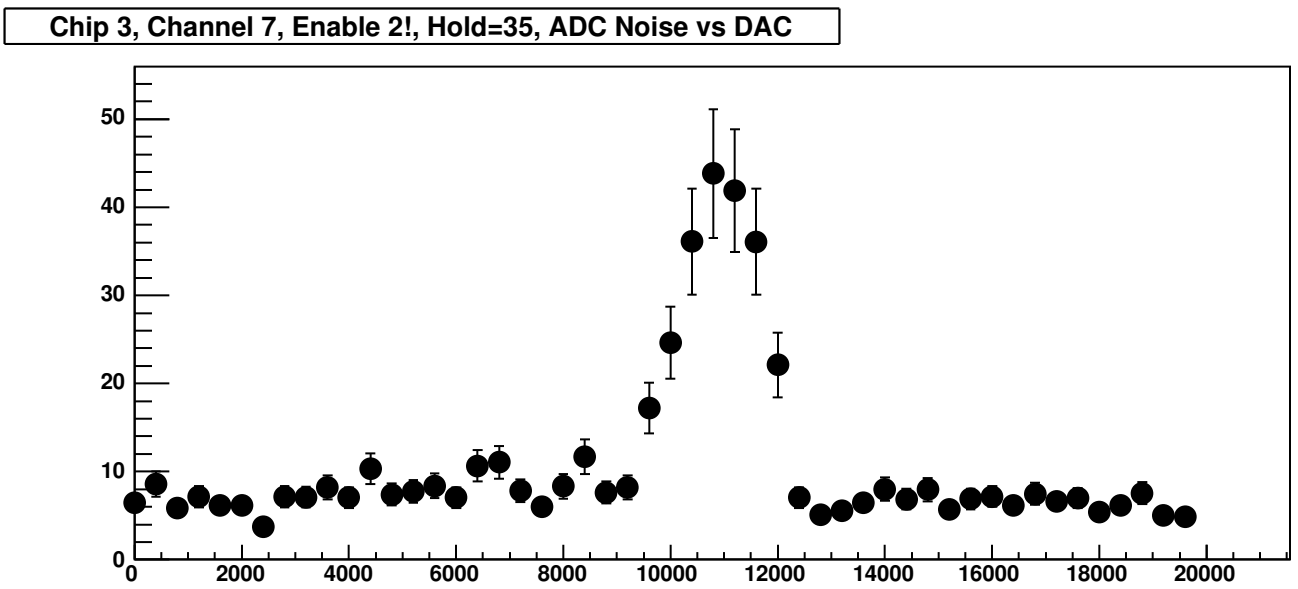

Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

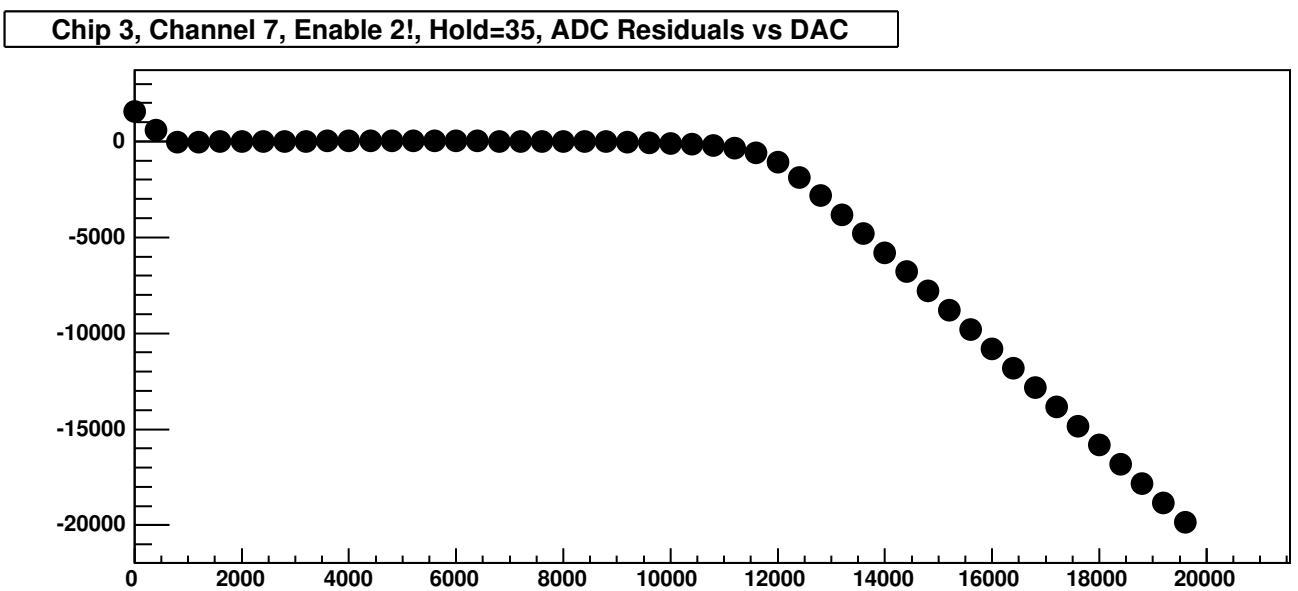

Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.37 / 23
p0 539.5 ± 0.7792
p1 0.01835 ± 0.0001194
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

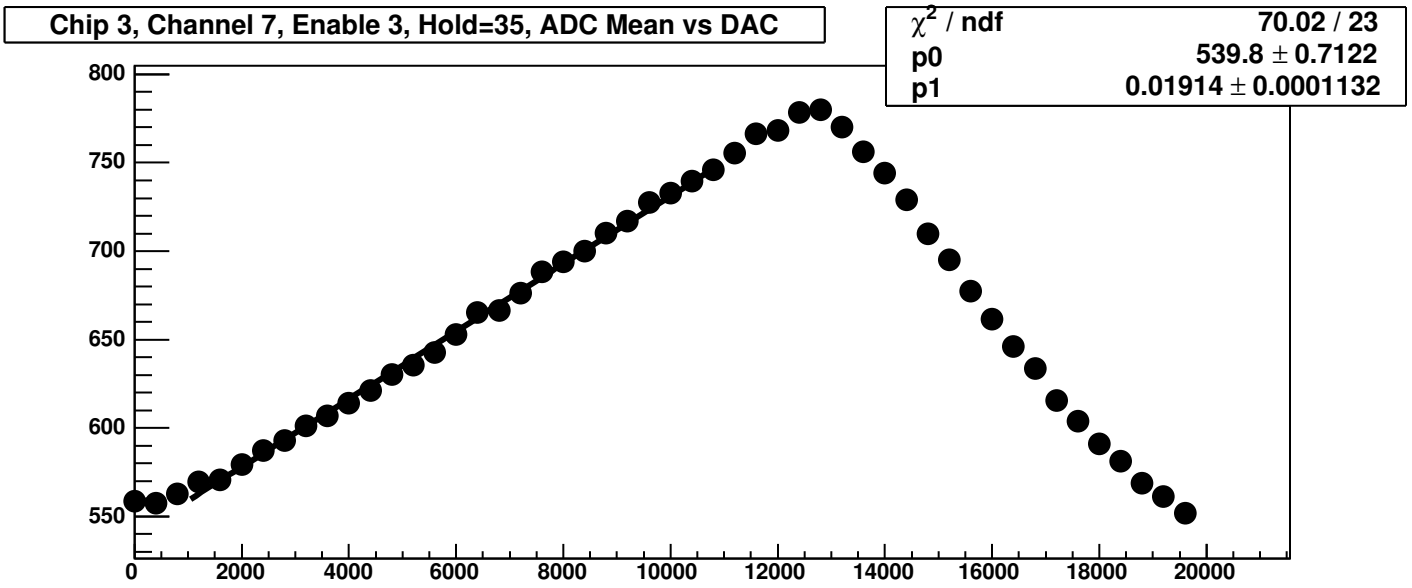

Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

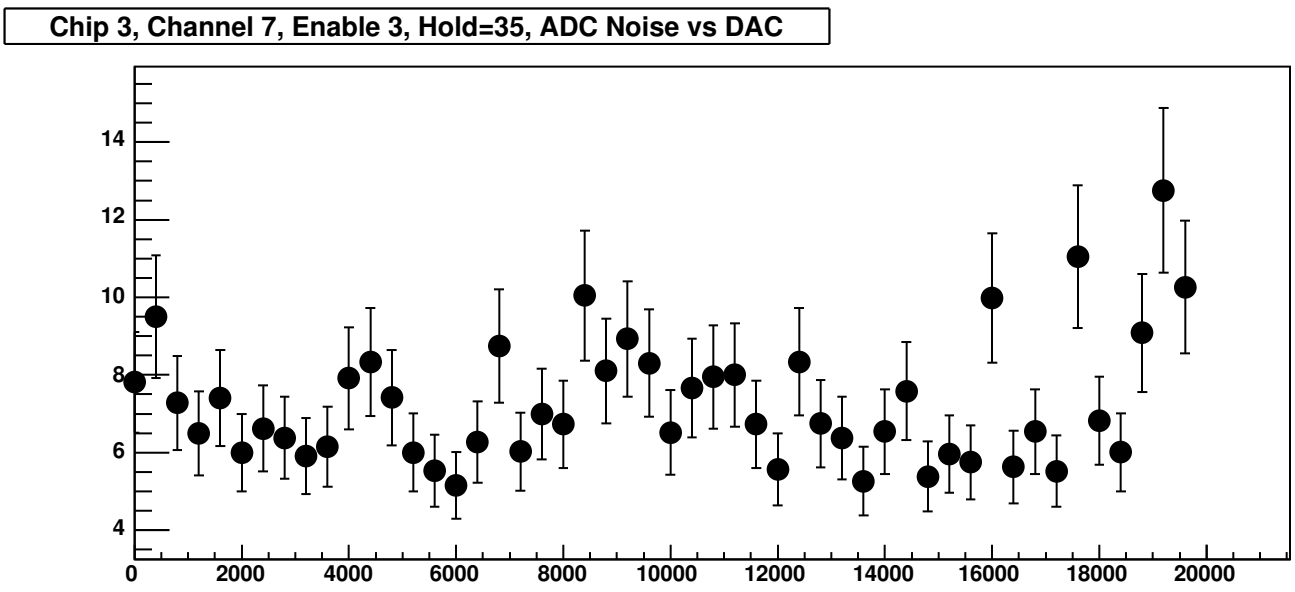

Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

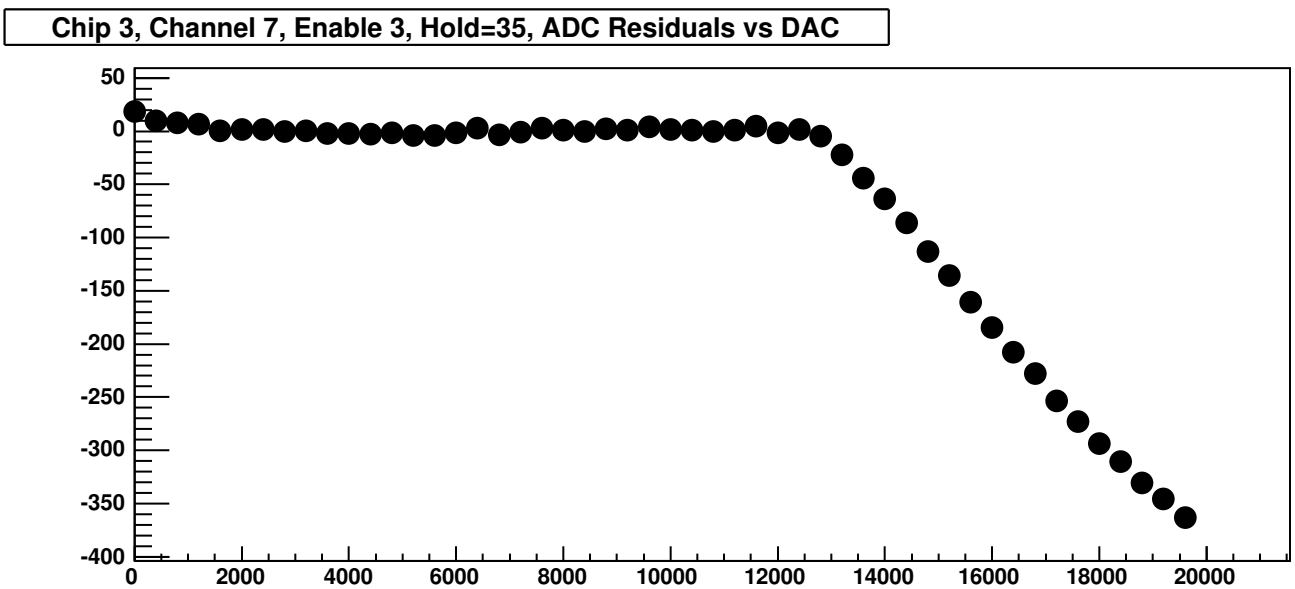

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 3470 / 23
p0 649.3 ± 0.7372
p1 2.506 ± 0.0001405
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

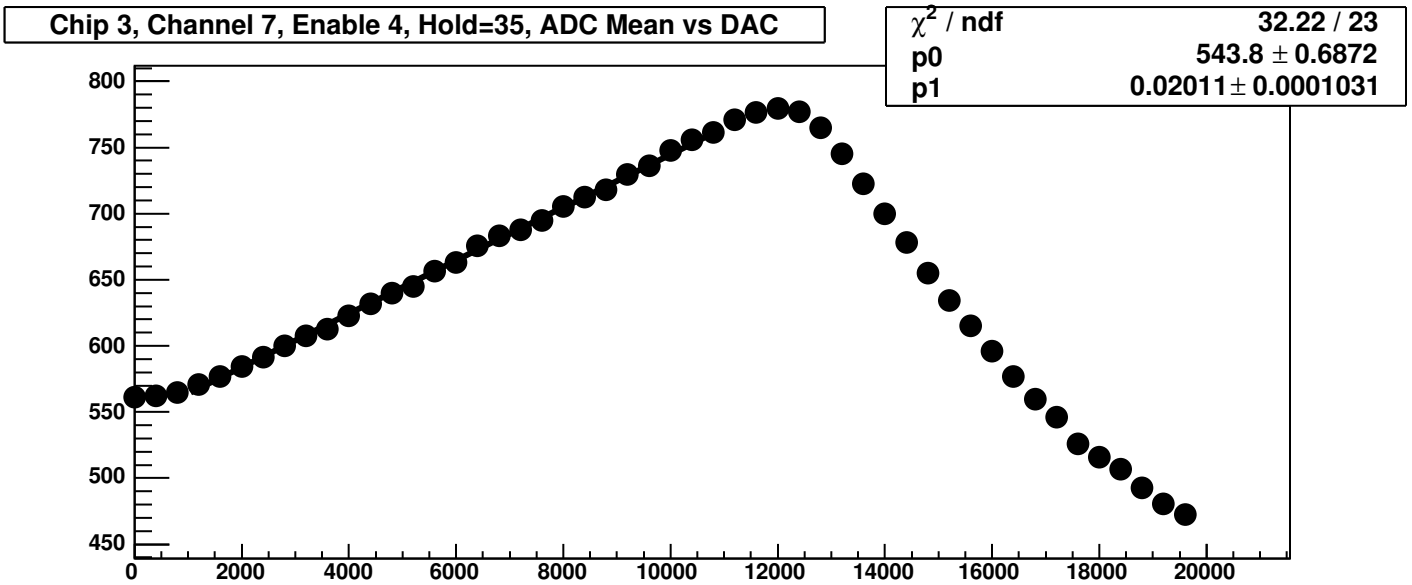

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

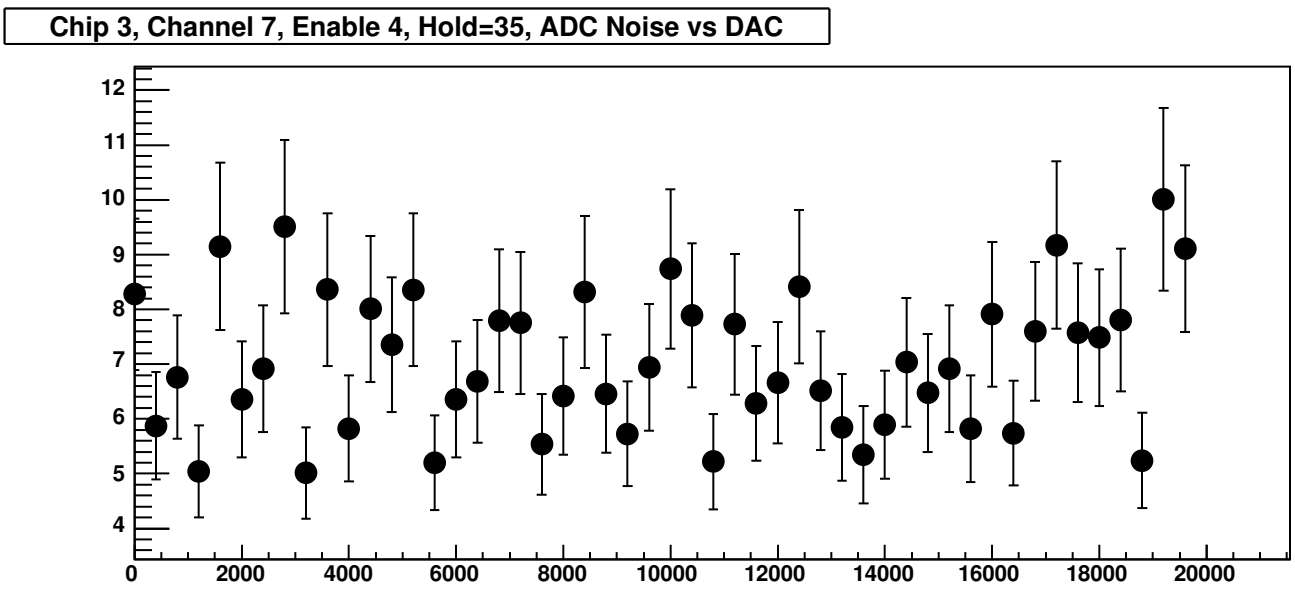

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

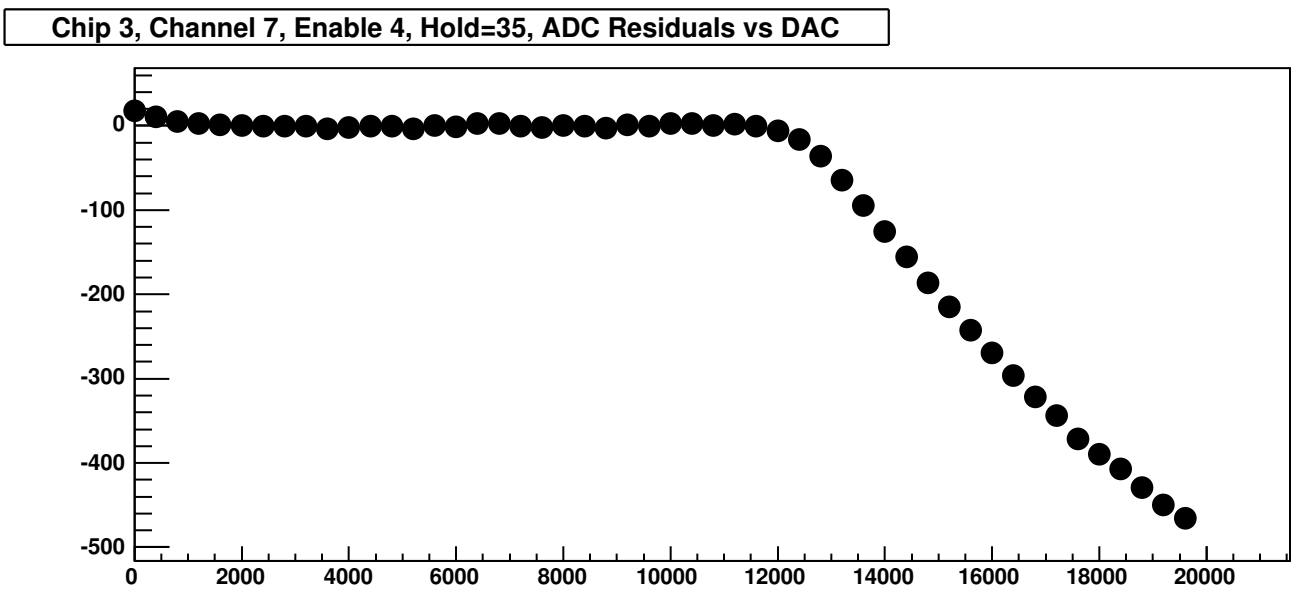

Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 70.02 / 23
p0 539.8 ± 0.7122
p1 0.01914 ± 0.0001132
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

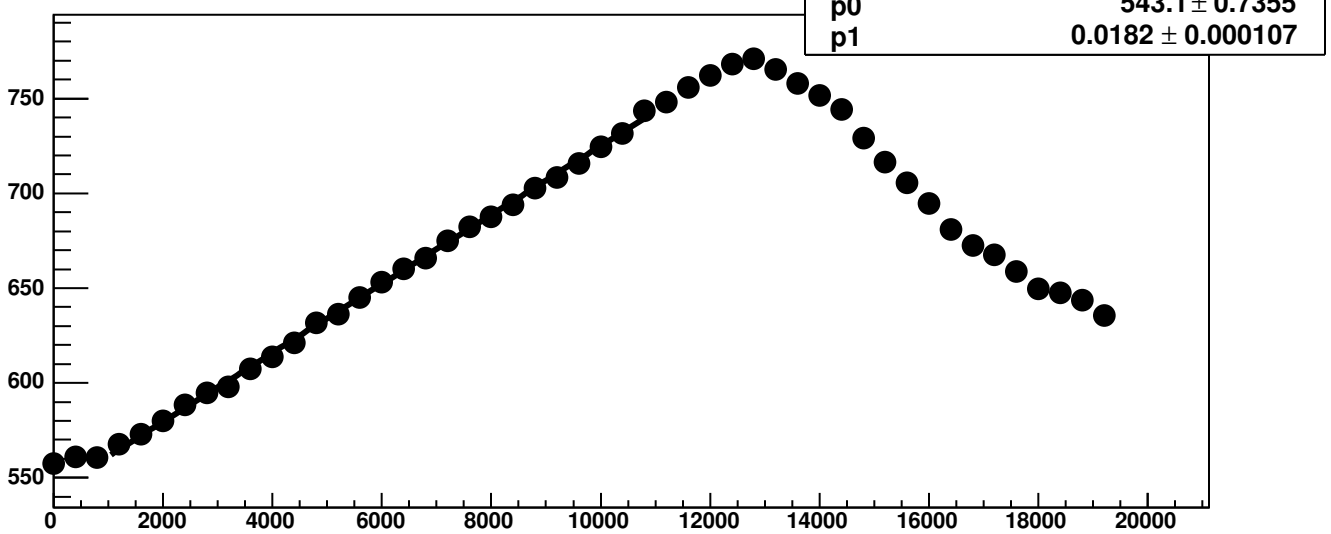

Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.22 / 23
p0 543.8 ± 0.6872
p1 0.02011 ± 0.0001031
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

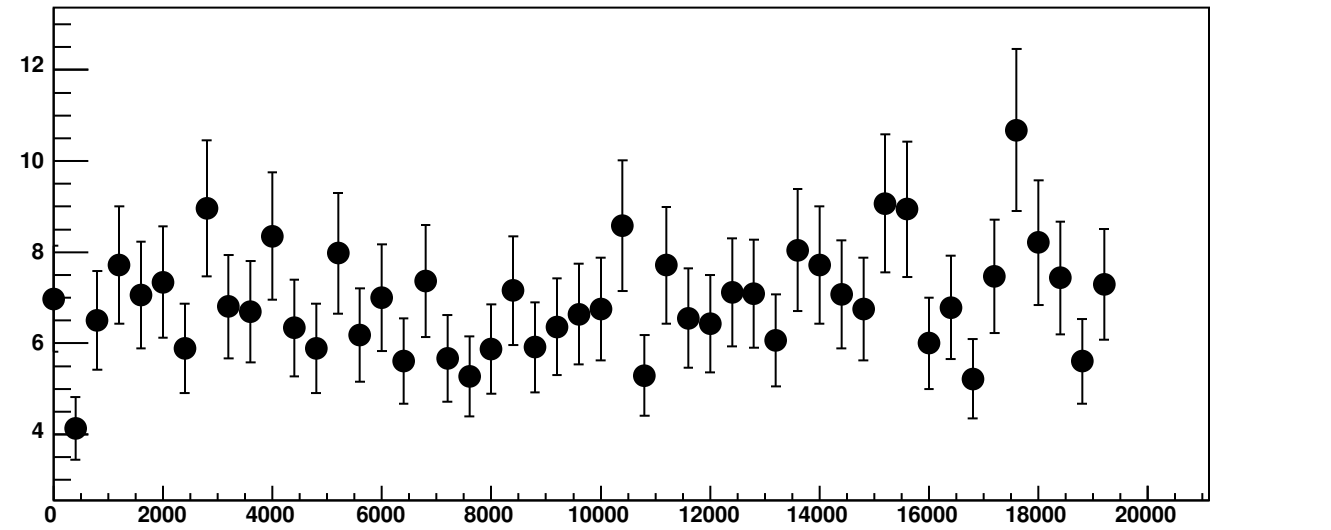

Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

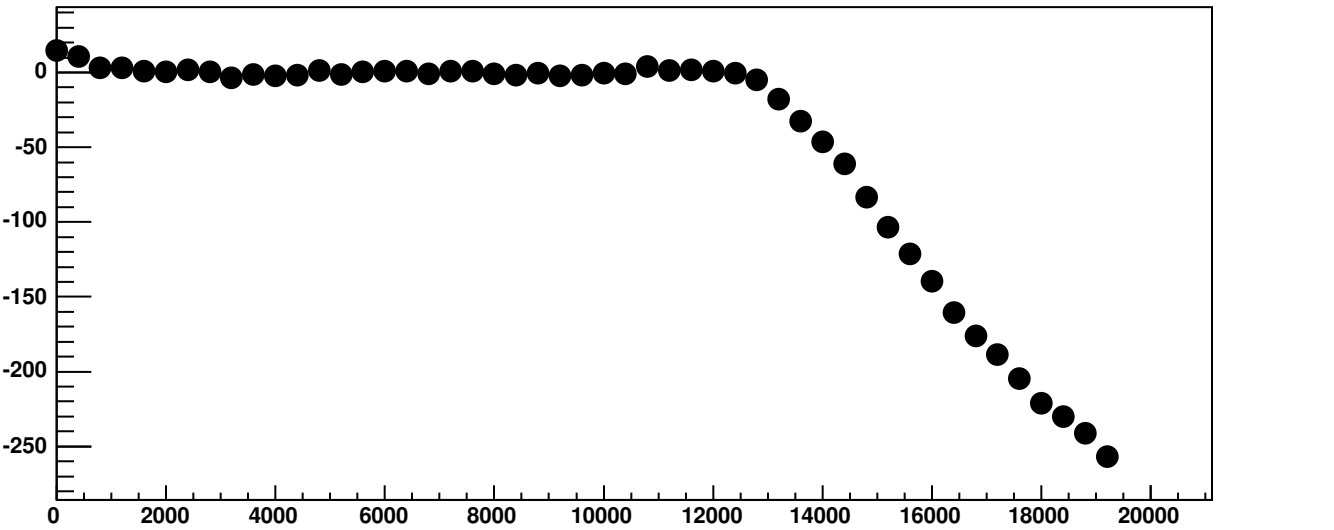
Chip 3, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



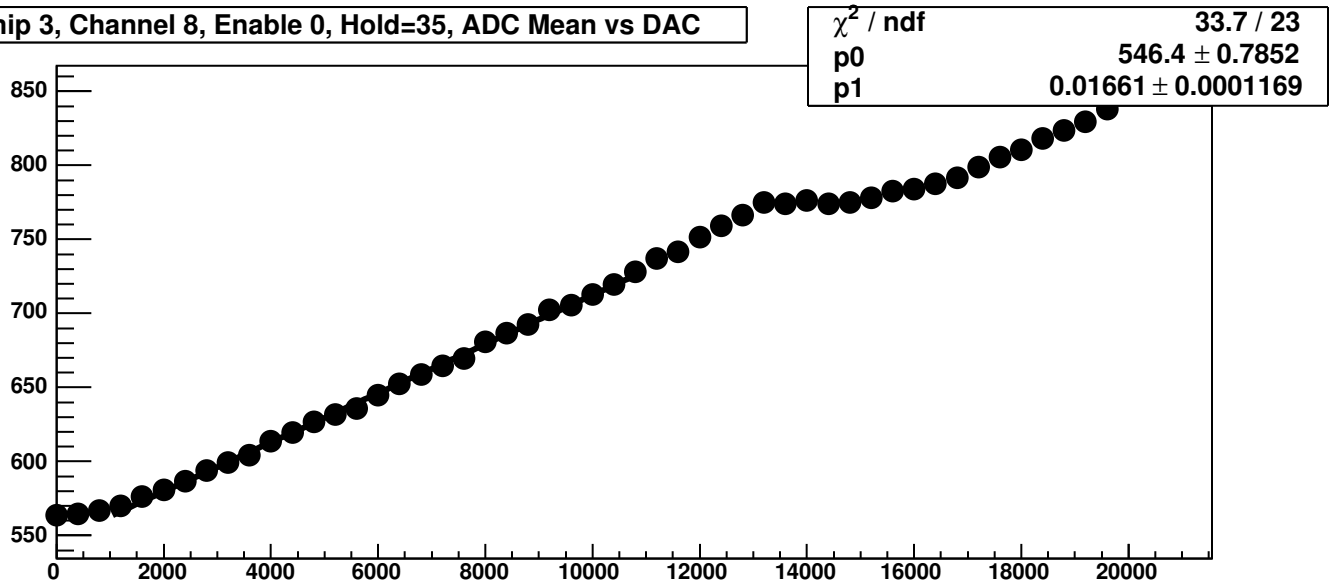
Chip 3, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



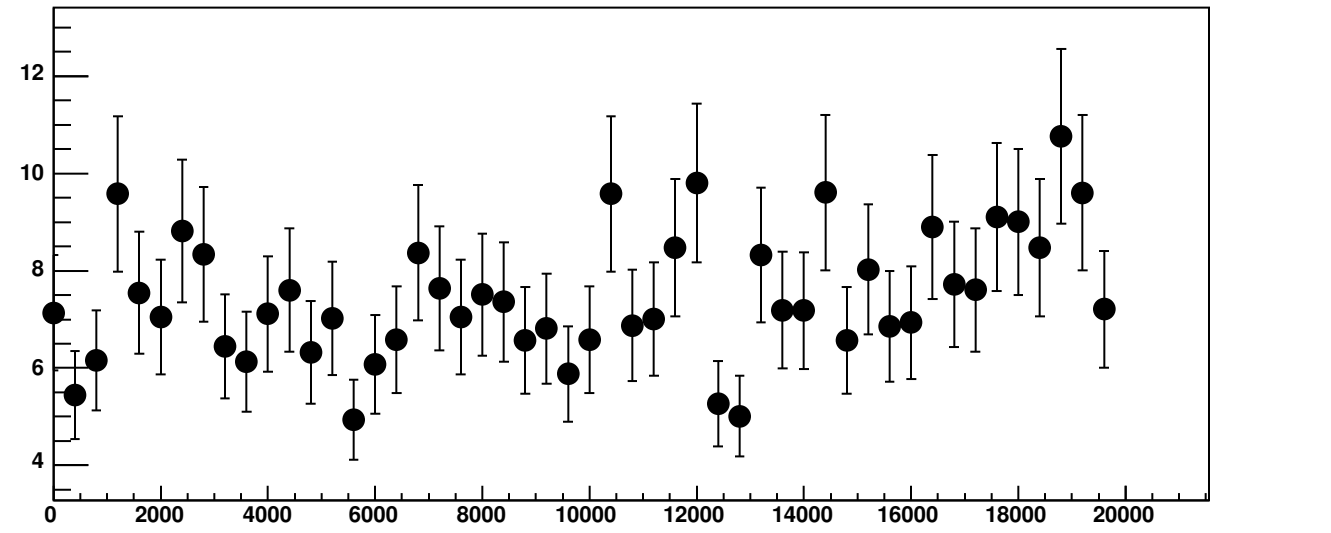
Chip 3, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



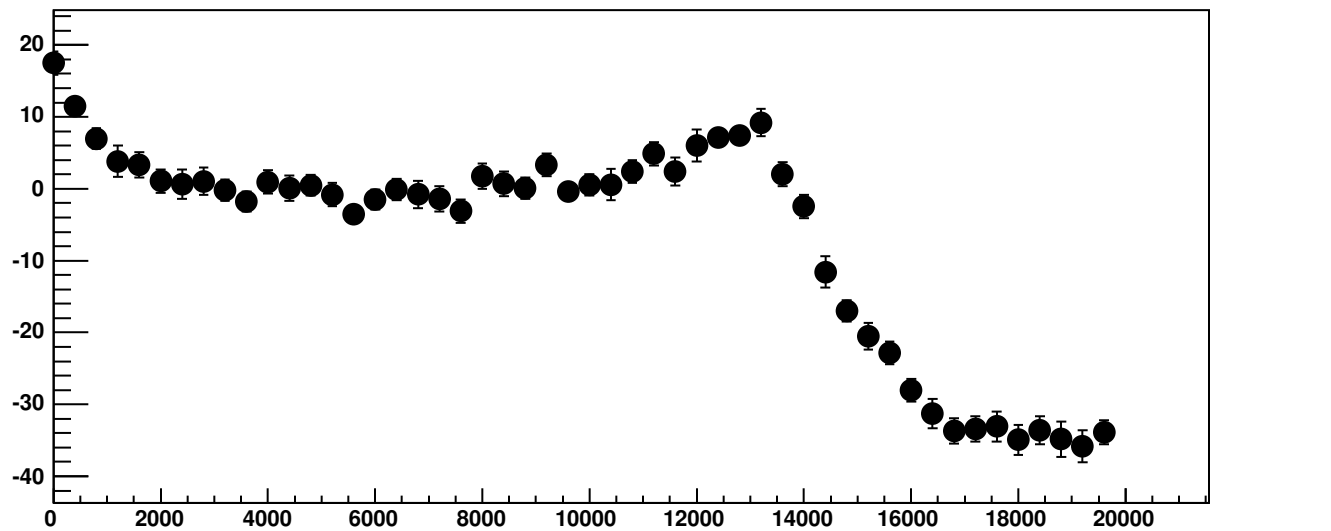
Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



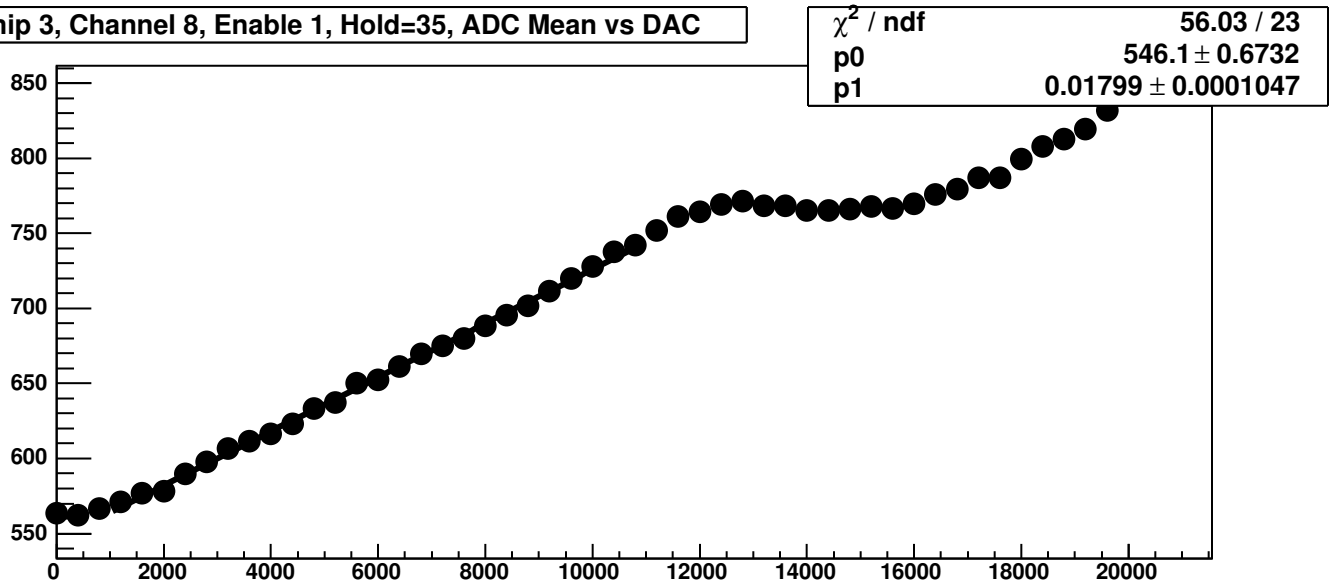
Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



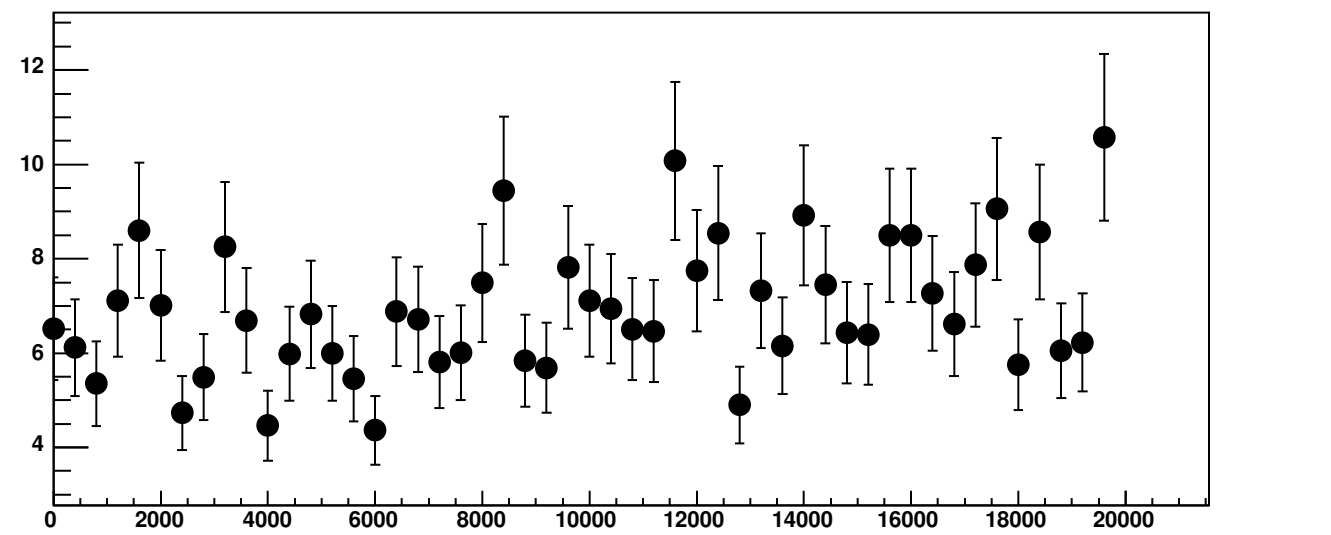
Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



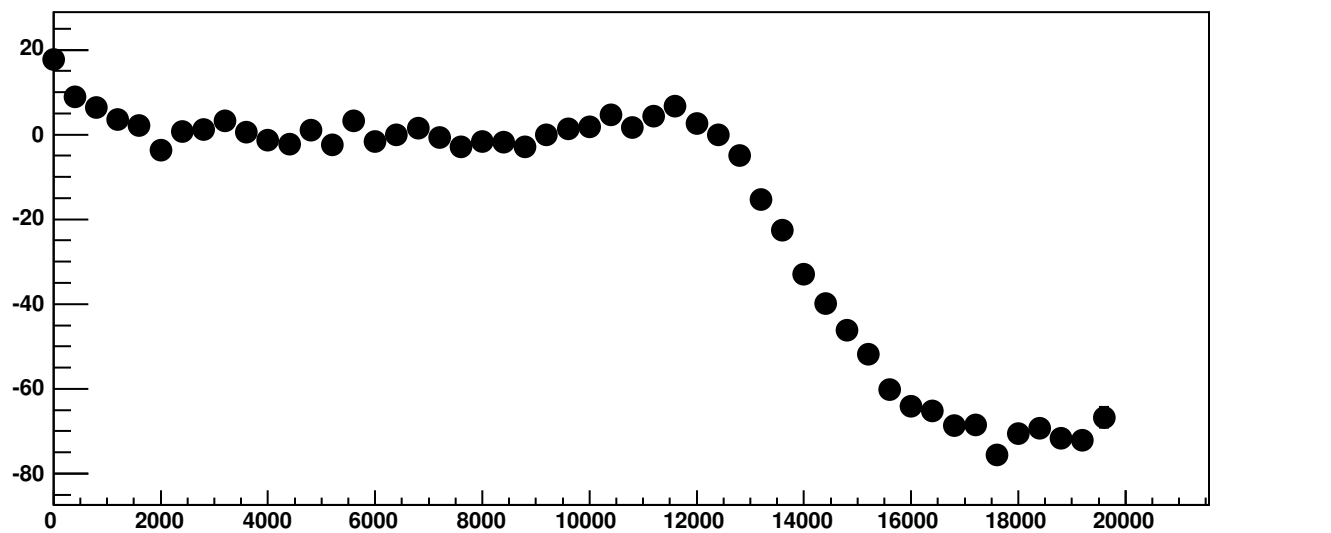
Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



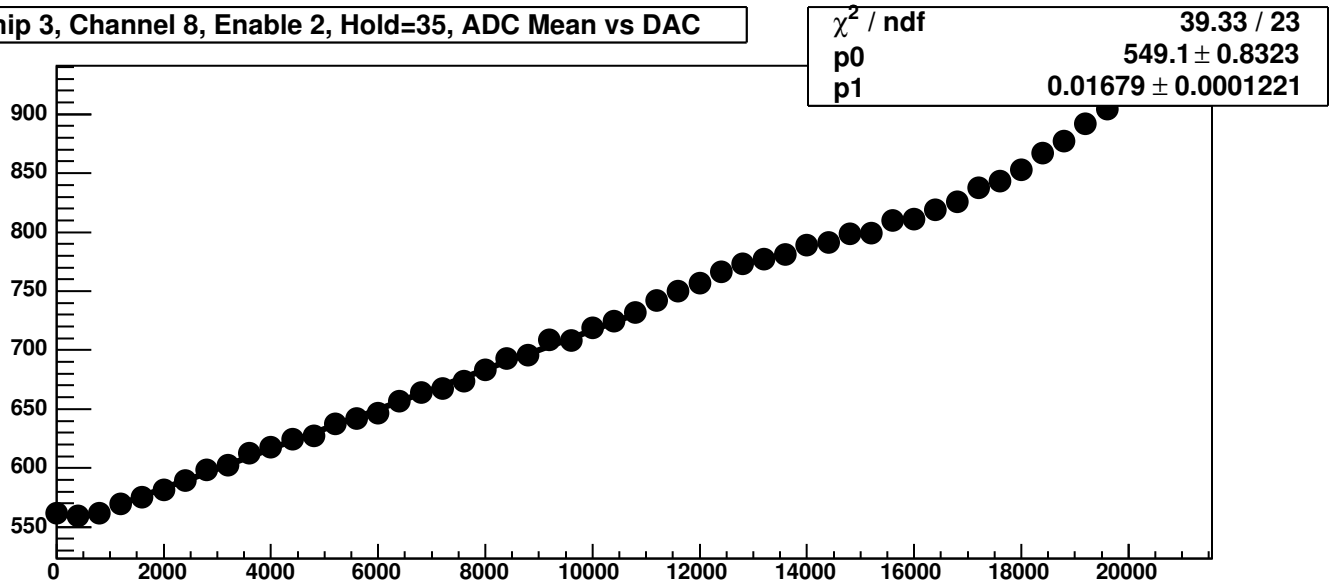
Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



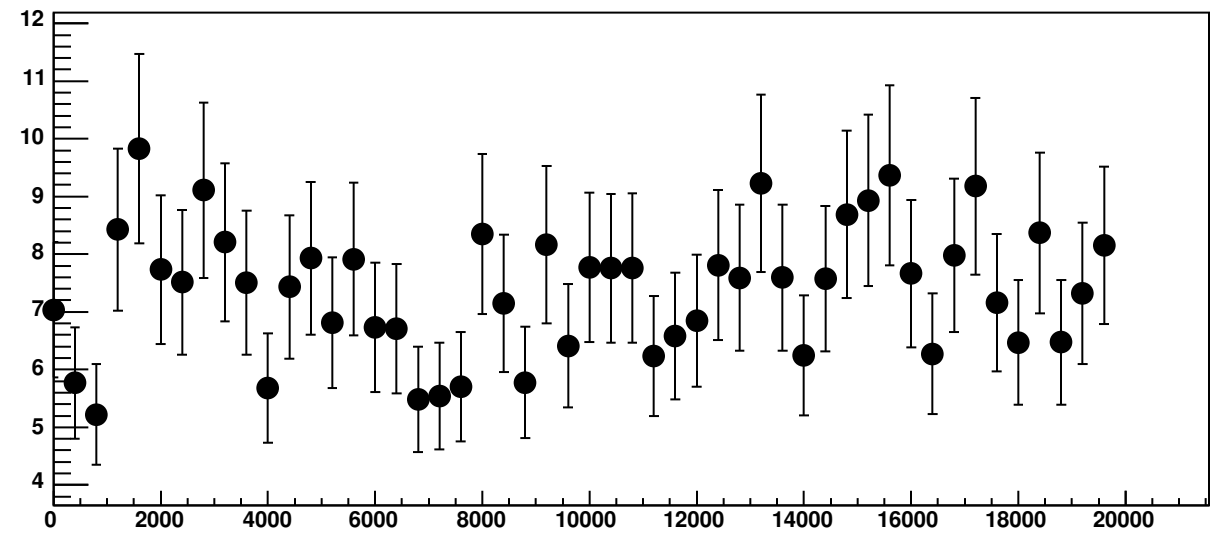
Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



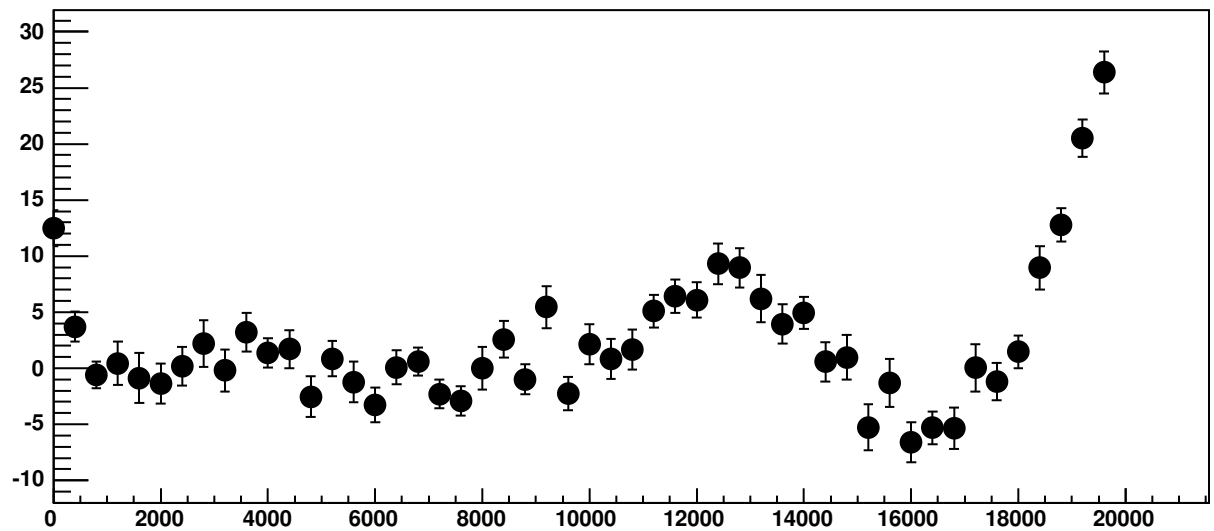
Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



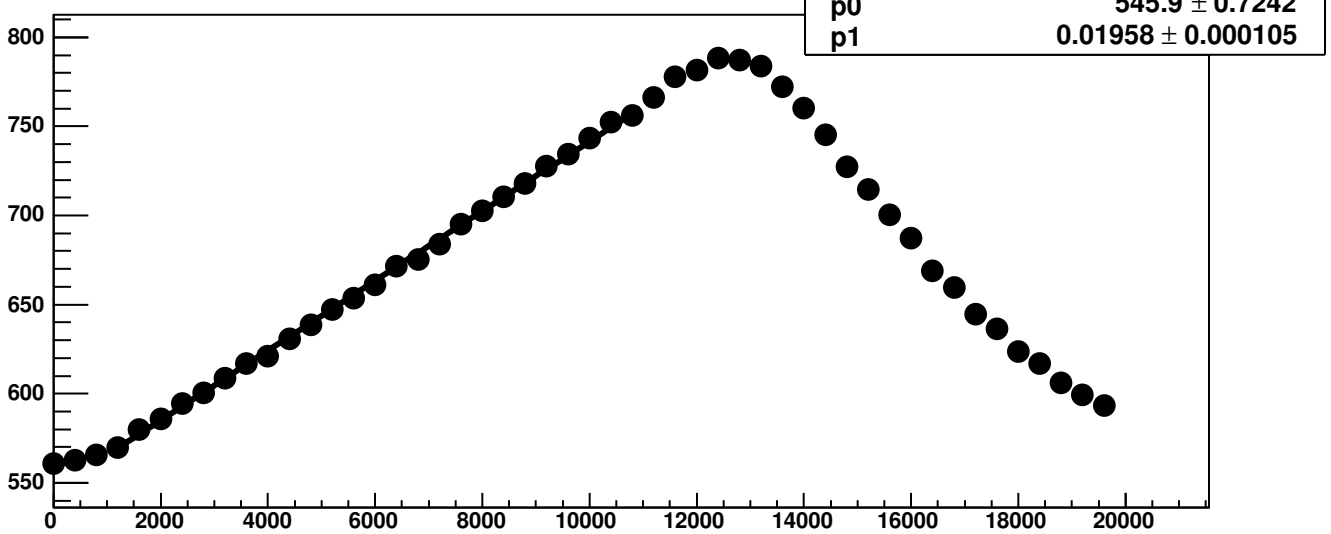
Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



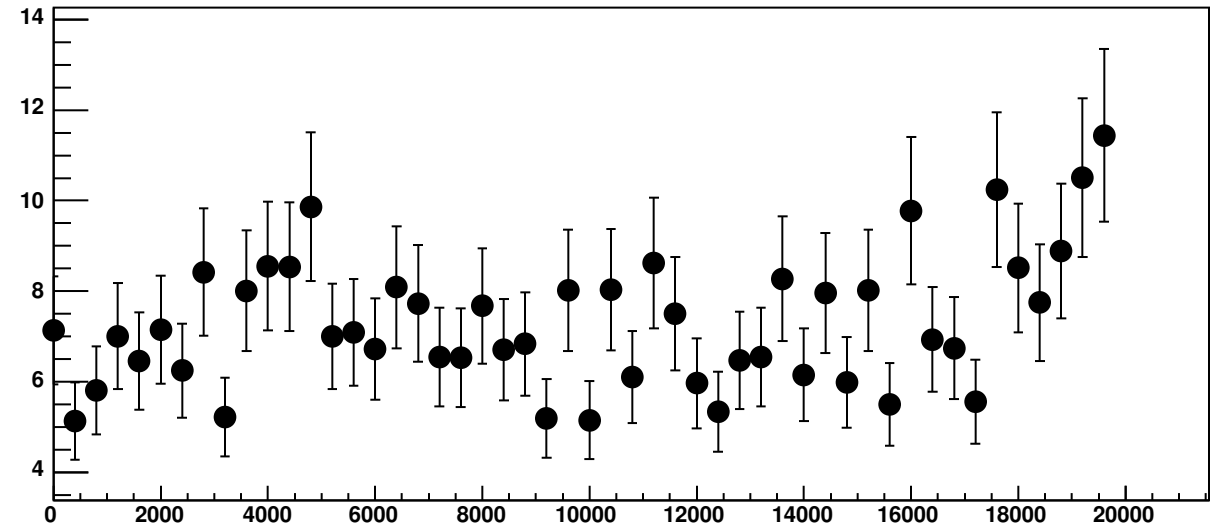
Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



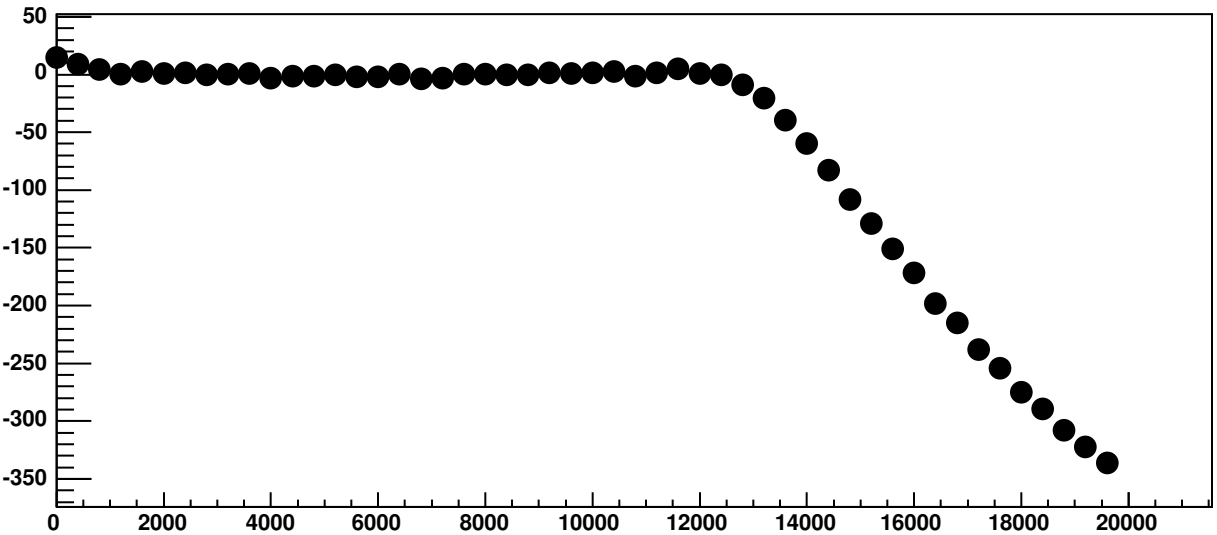
Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



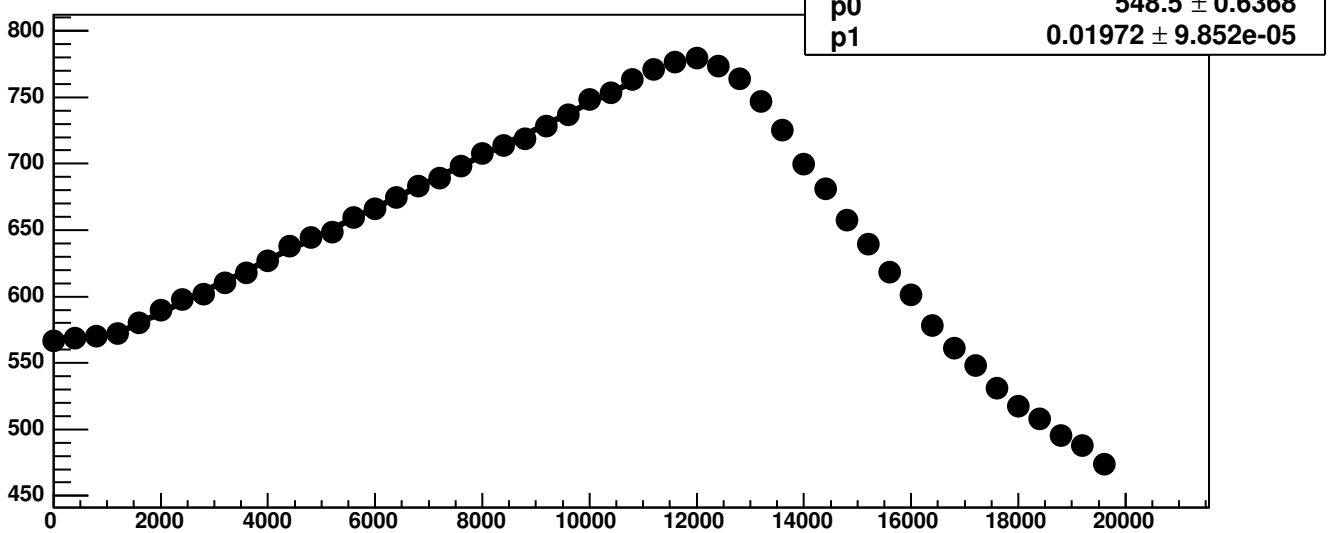
Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



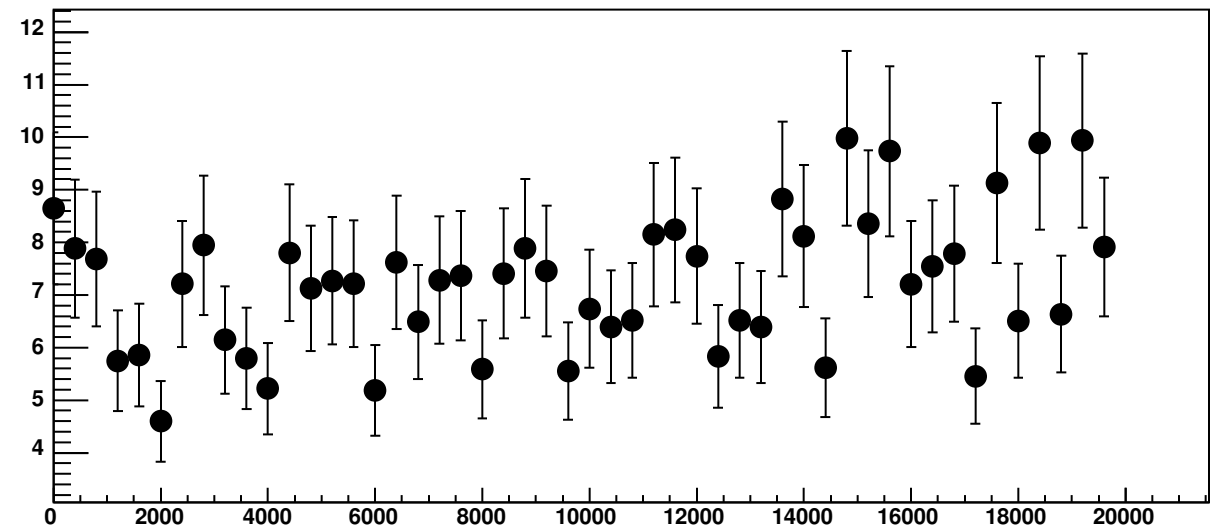
Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



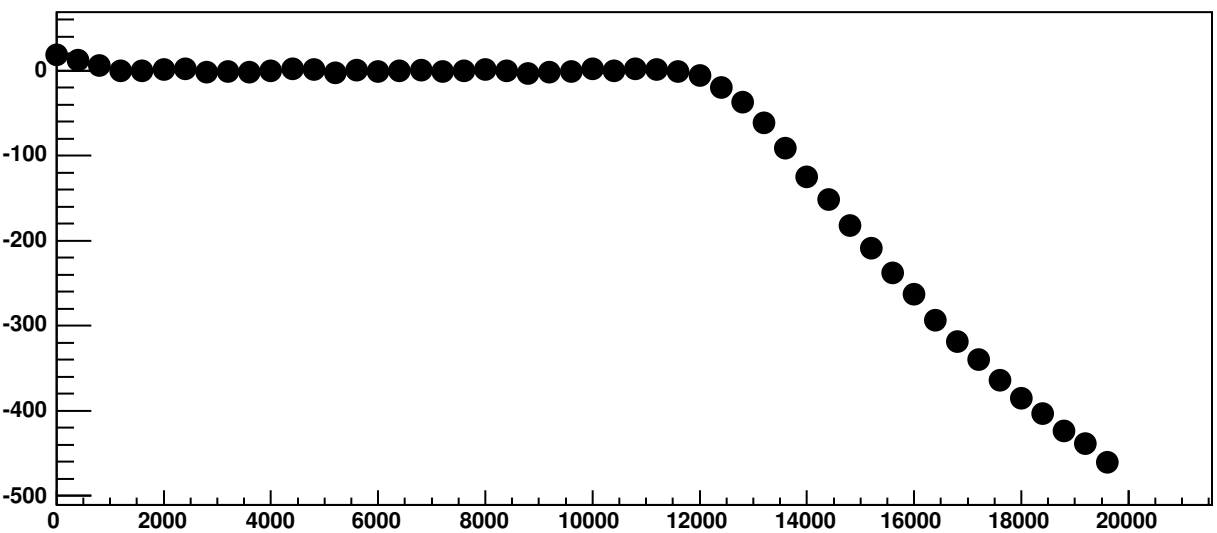
Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

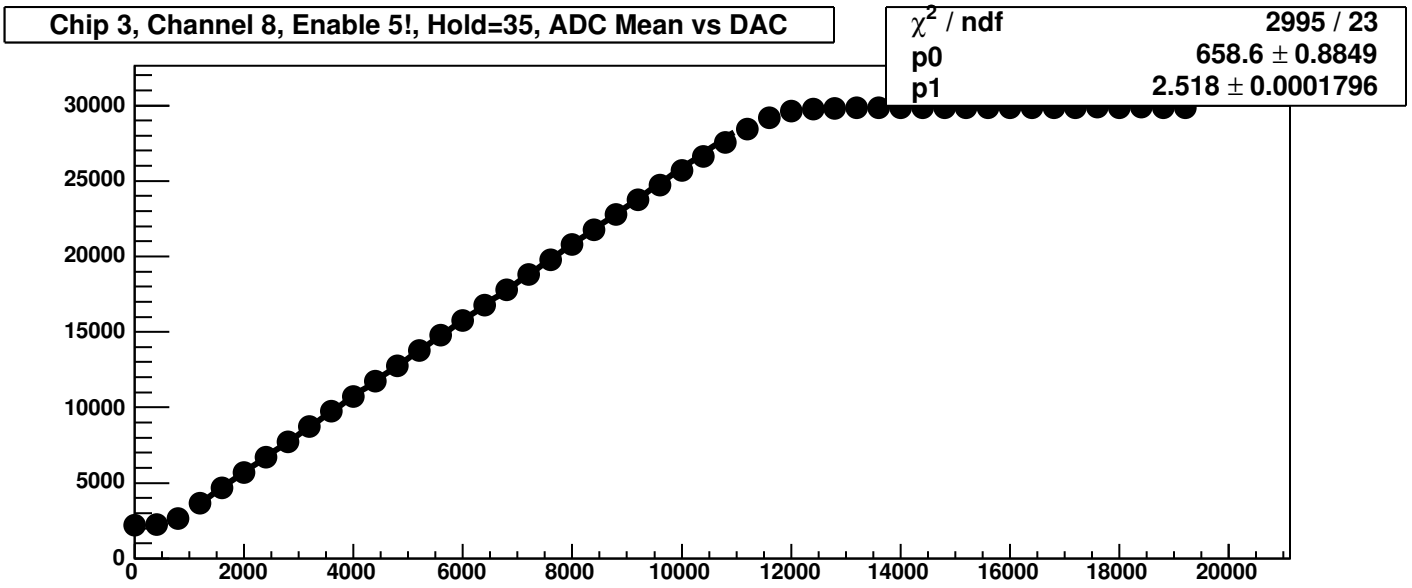


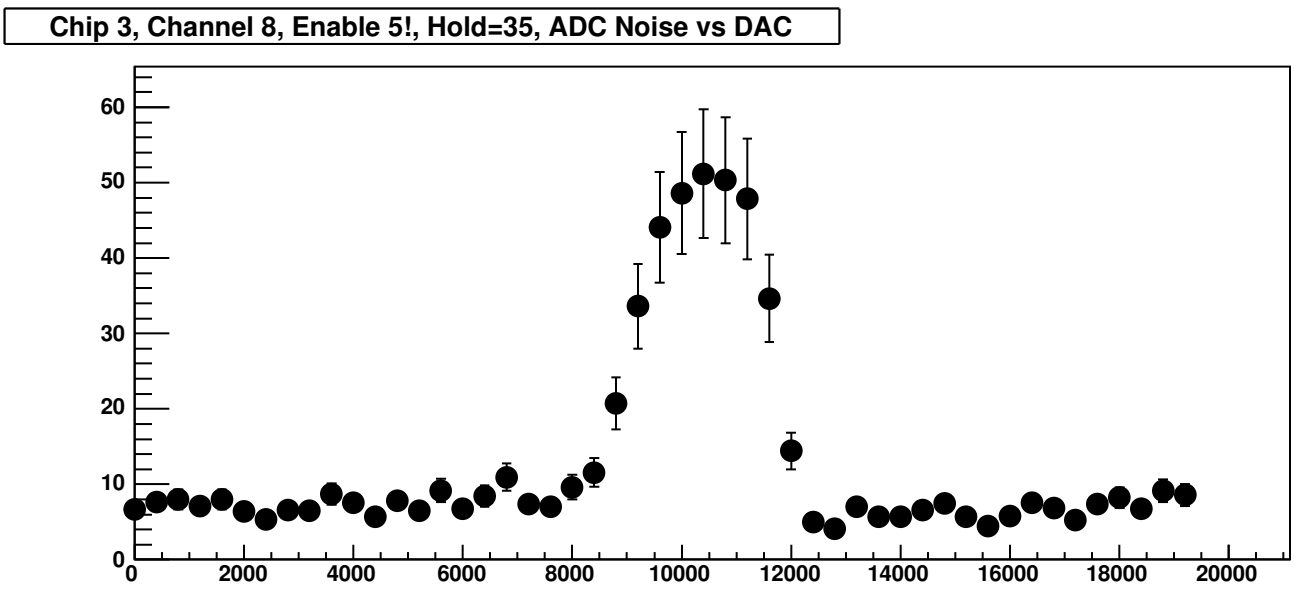
Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

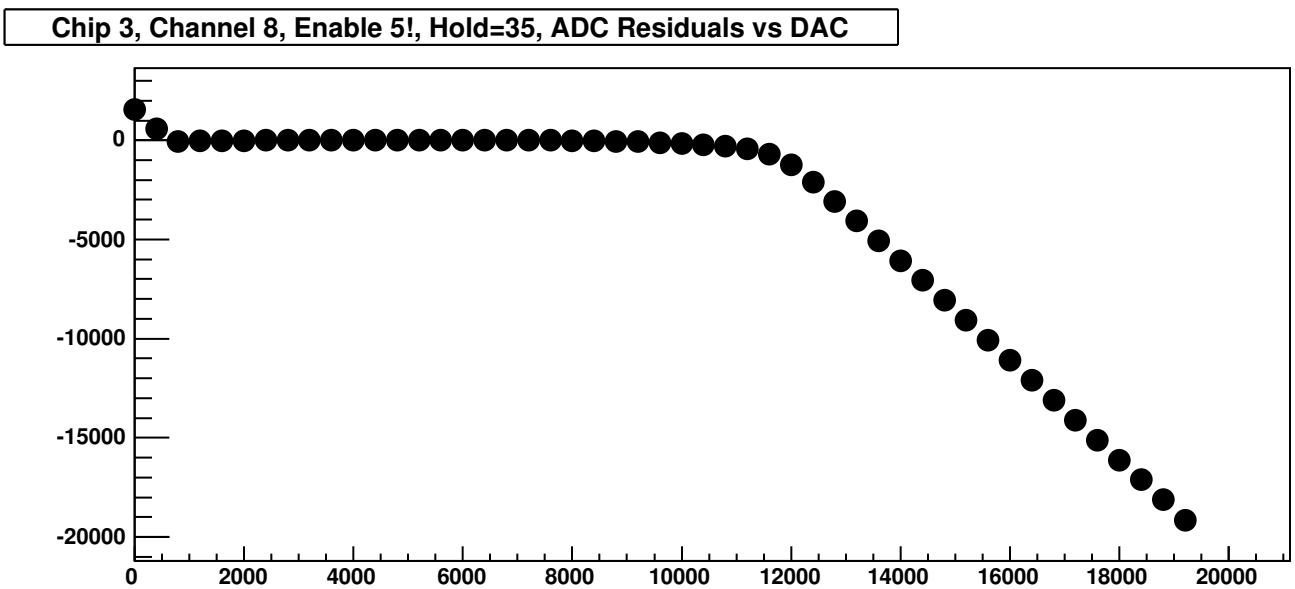


Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

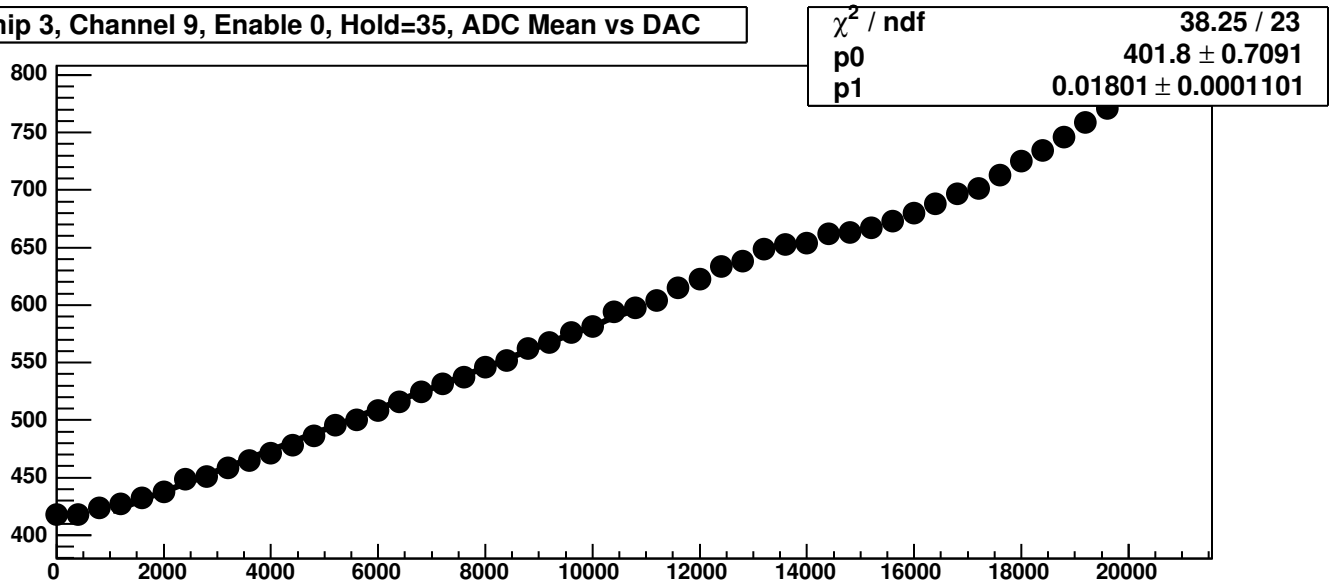



Chip 3, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2995 / 23
p0 658.6 ± 0.8849
p1 2.518 ± 0.0001796
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

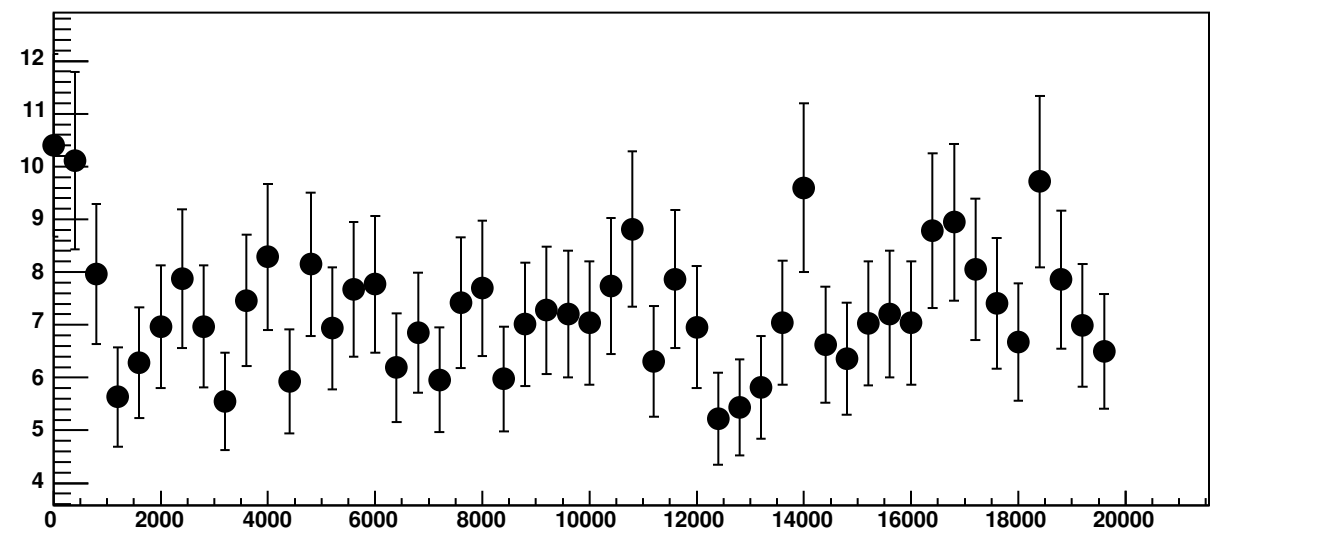

Chip 3, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

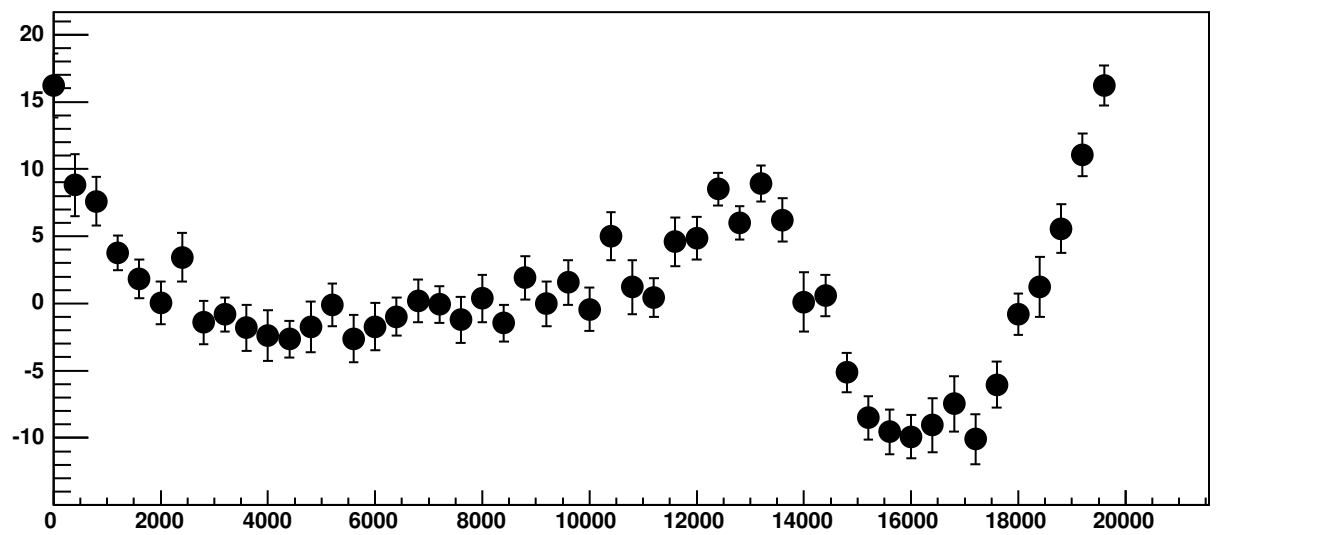
Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

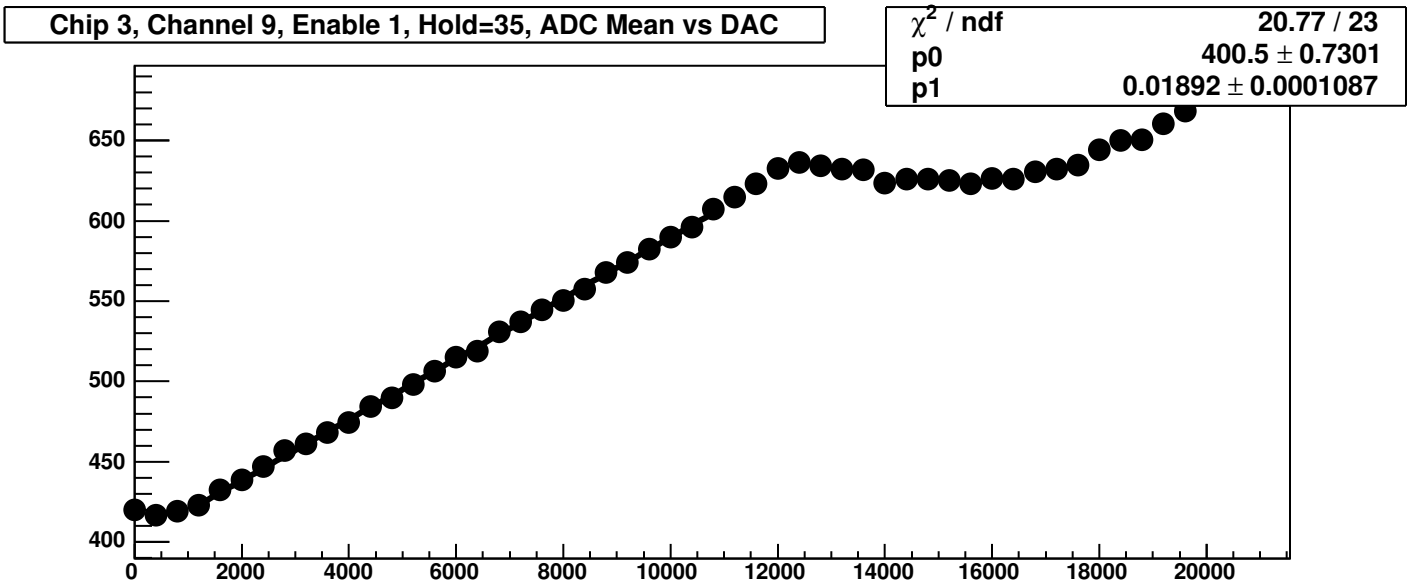


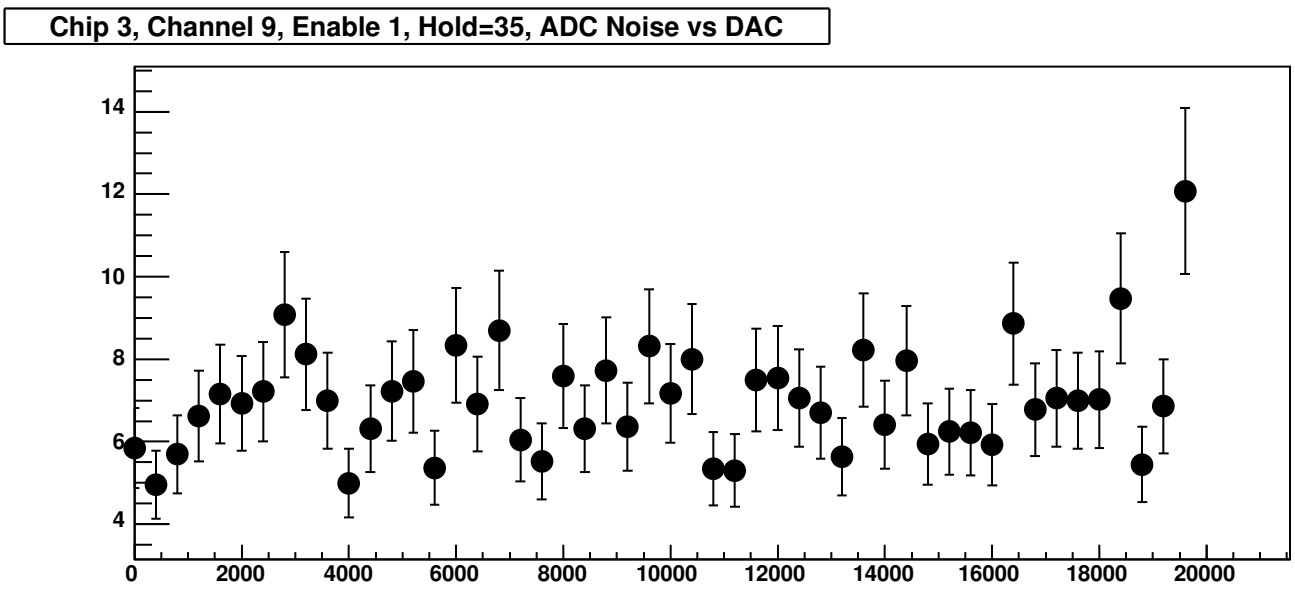
Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC

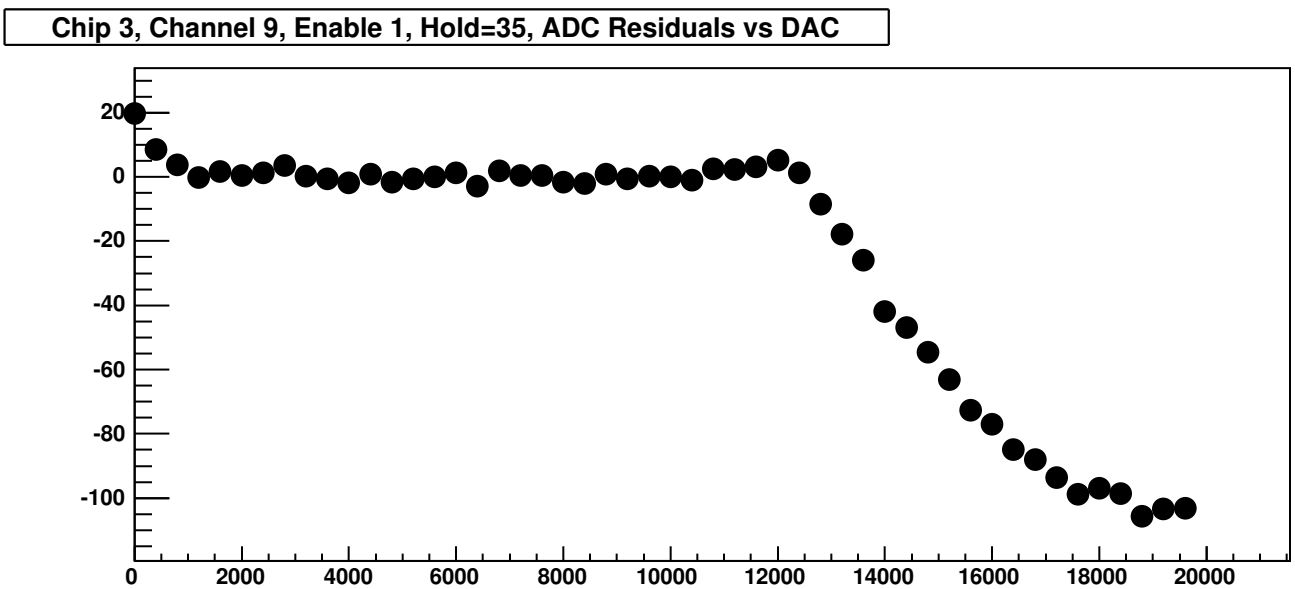


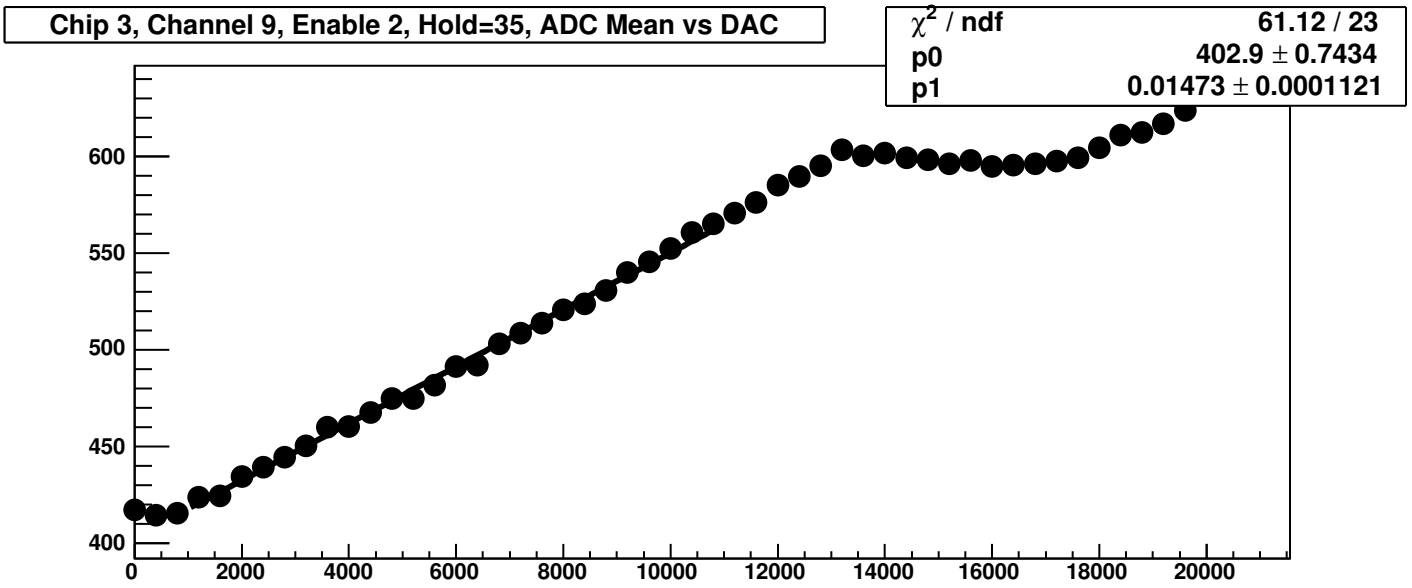
Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

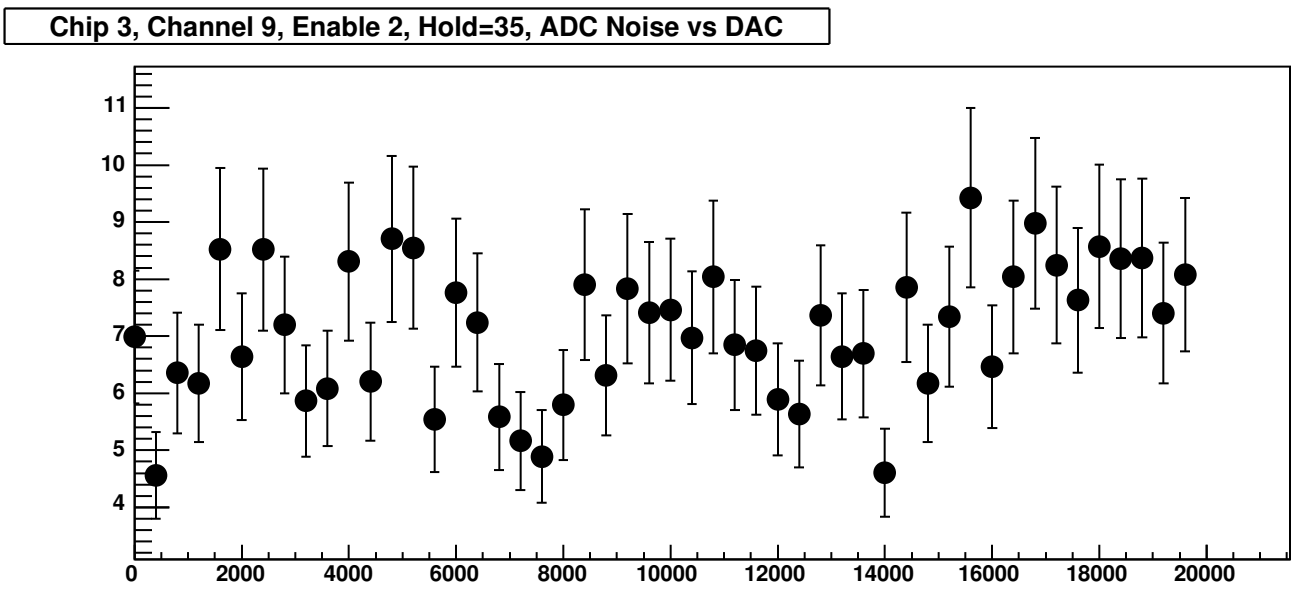


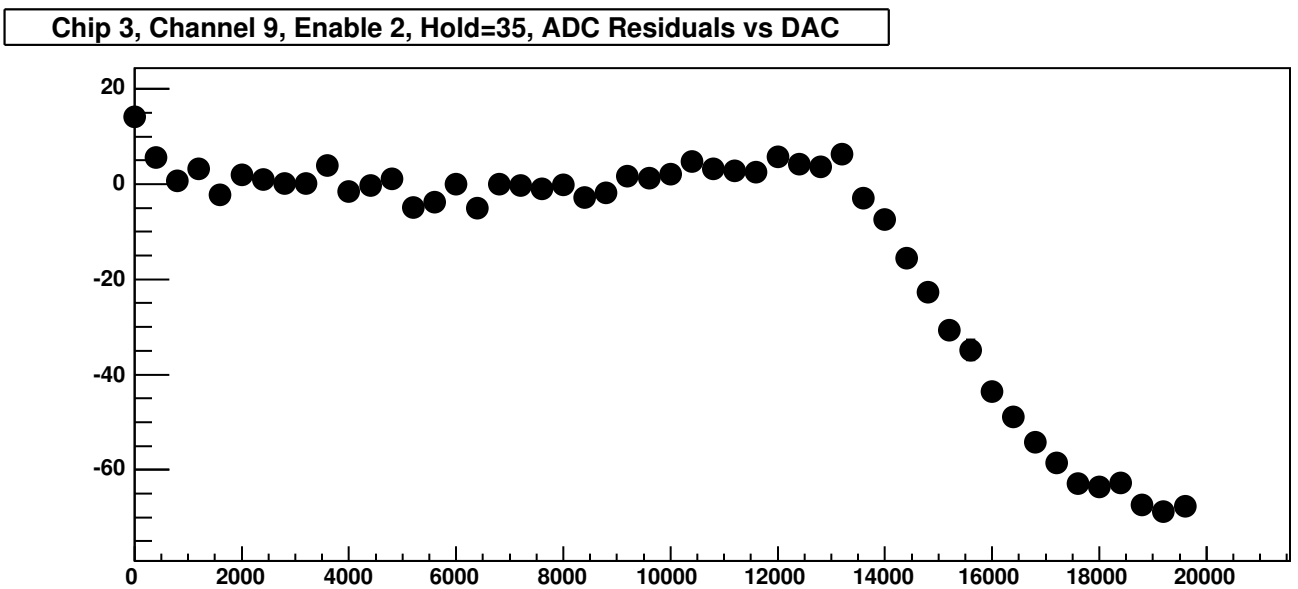

Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.77 / 23
p0 400.5 ± 0.7301
p1 0.01892 ± 0.0001087
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

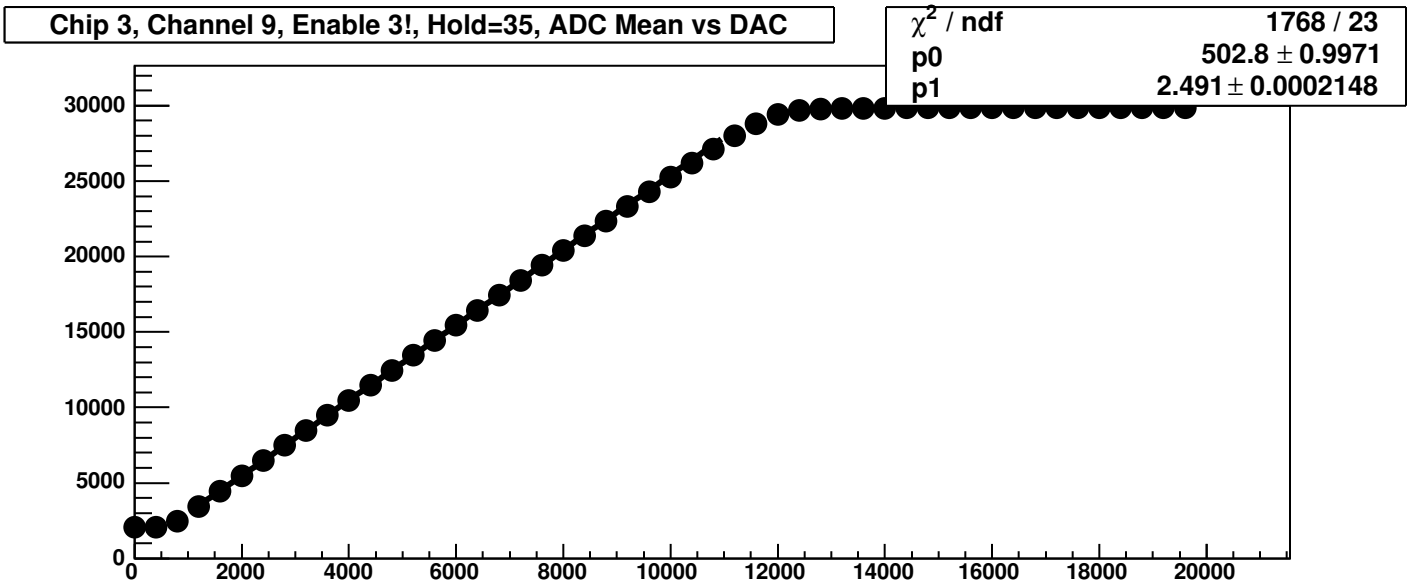

Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

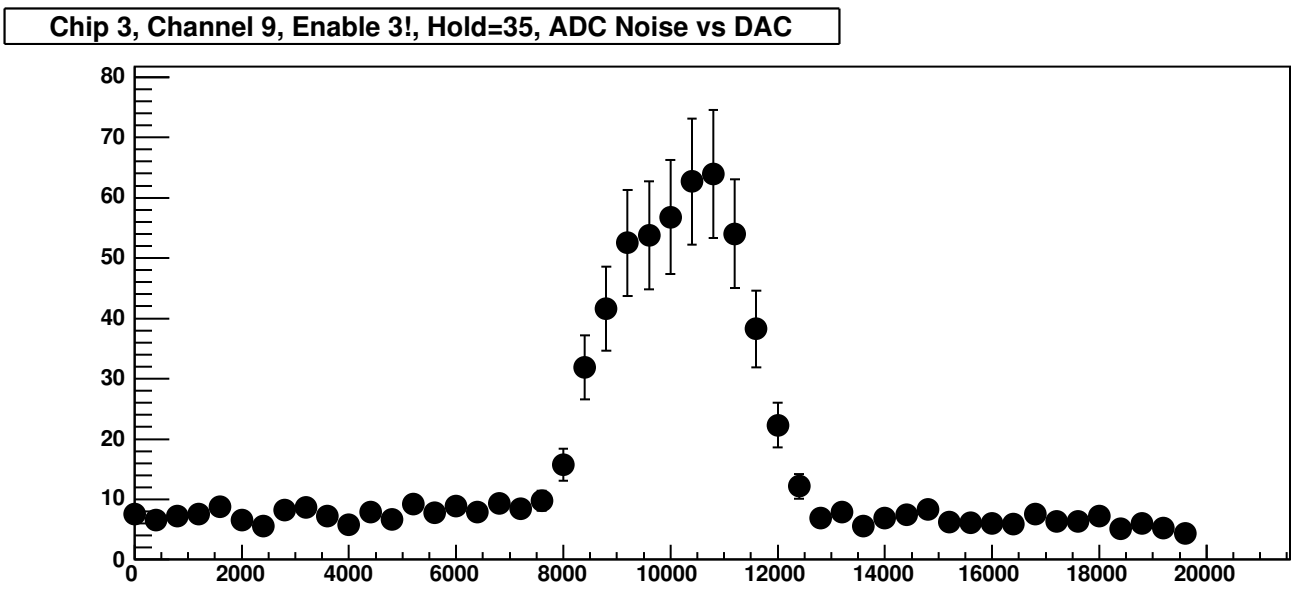

Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

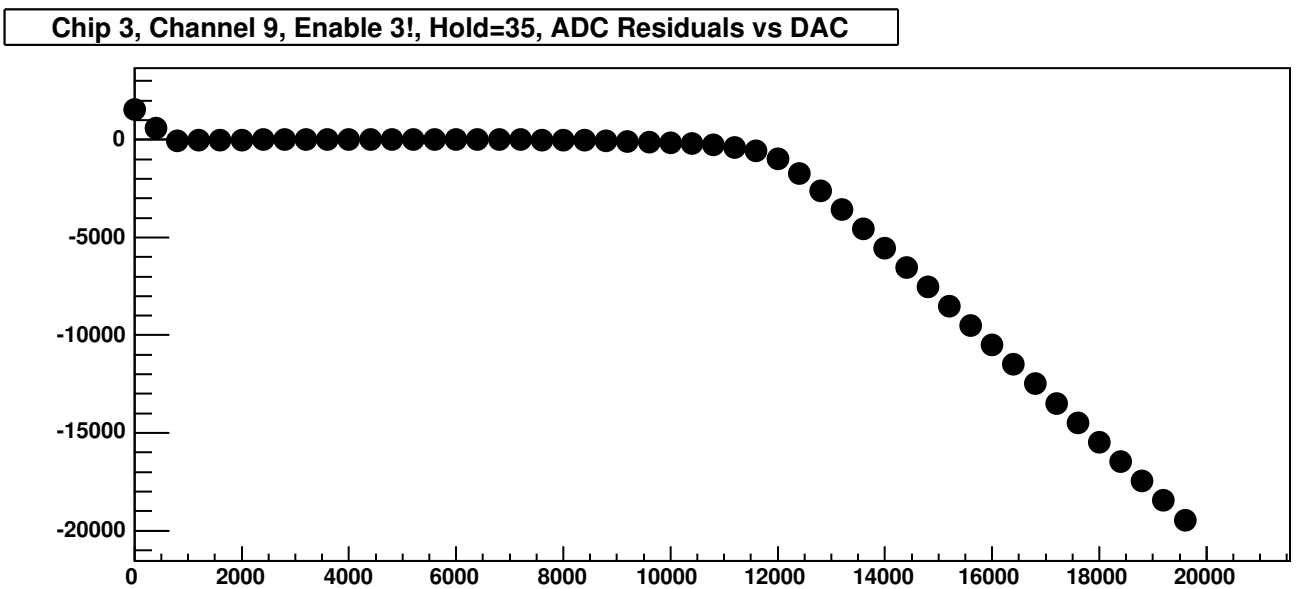

Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 61.12 / 23
p0 402.9 ± 0.7434
p1 0.01473 ± 0.0001121
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

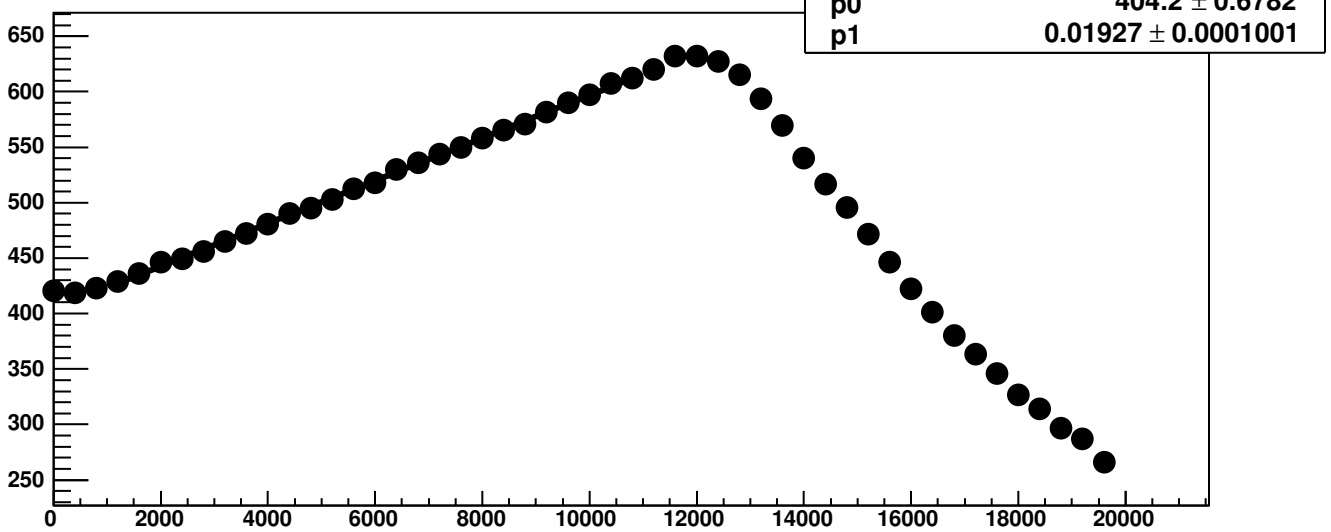

Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1768 / 23
p0 502.8 ± 0.9971
p1 2.491 ± 0.0002148
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

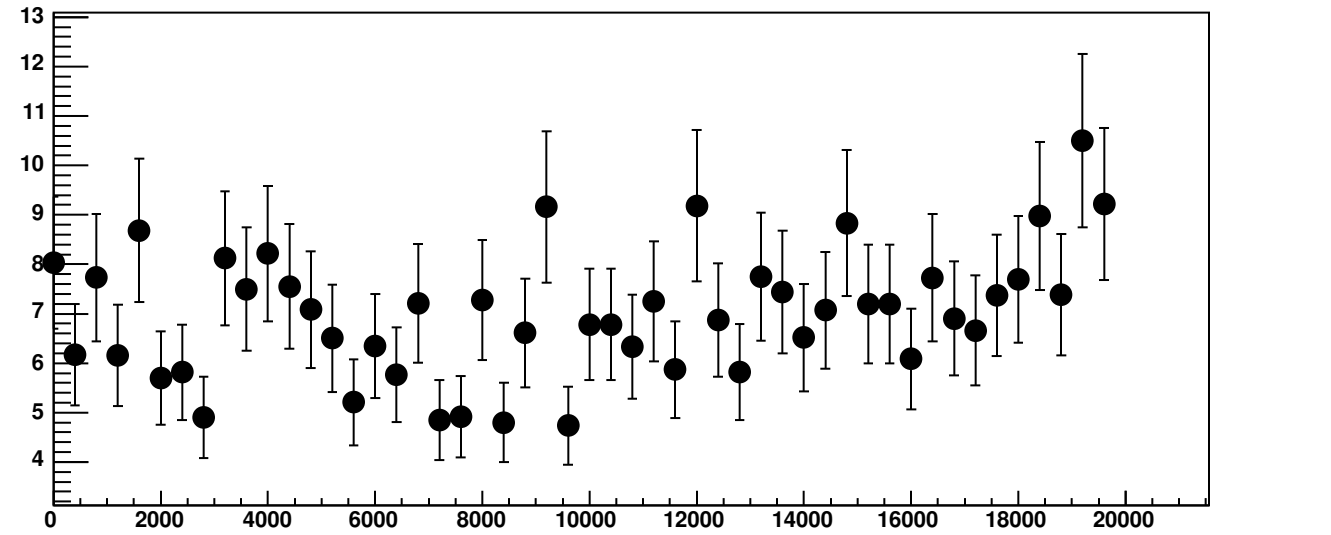

Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

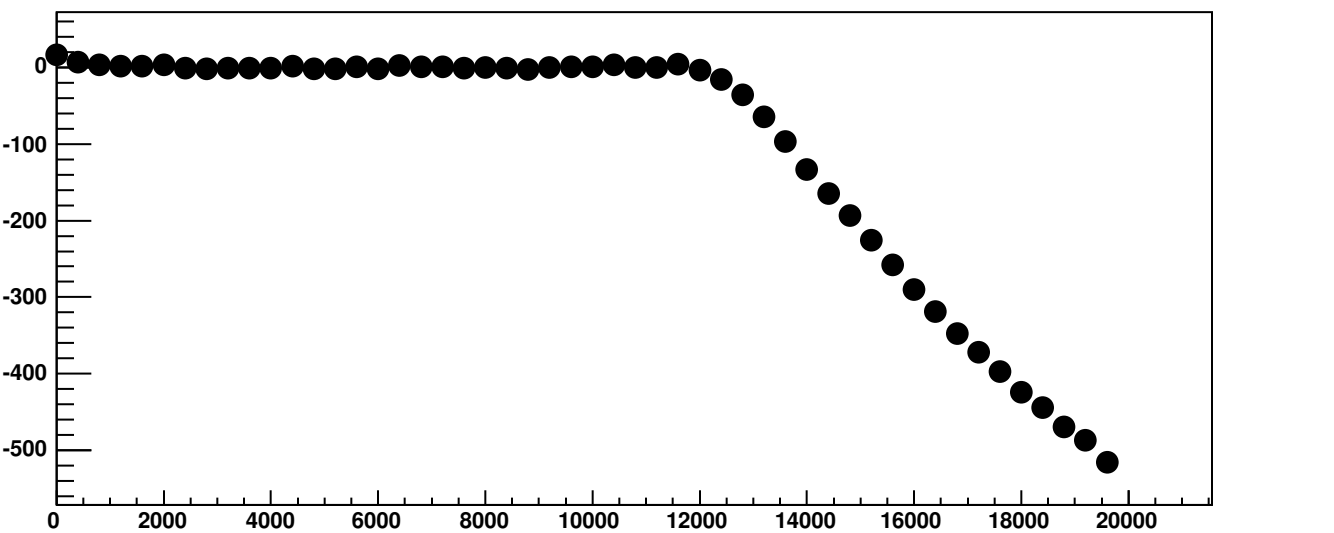
Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

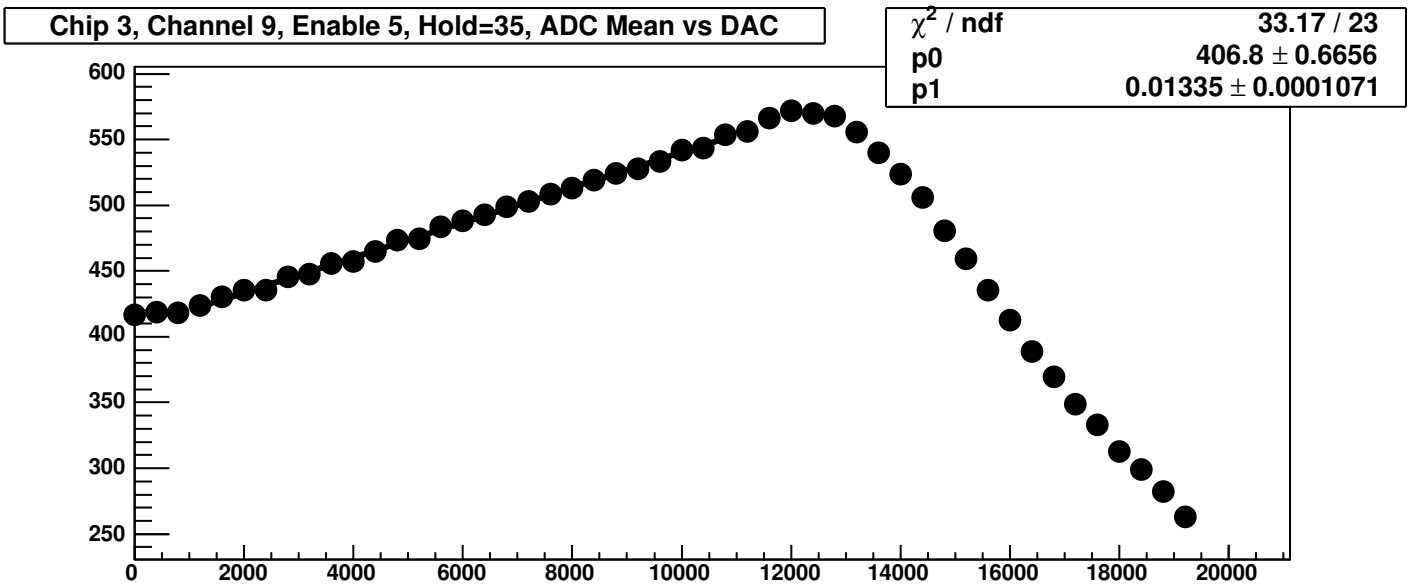


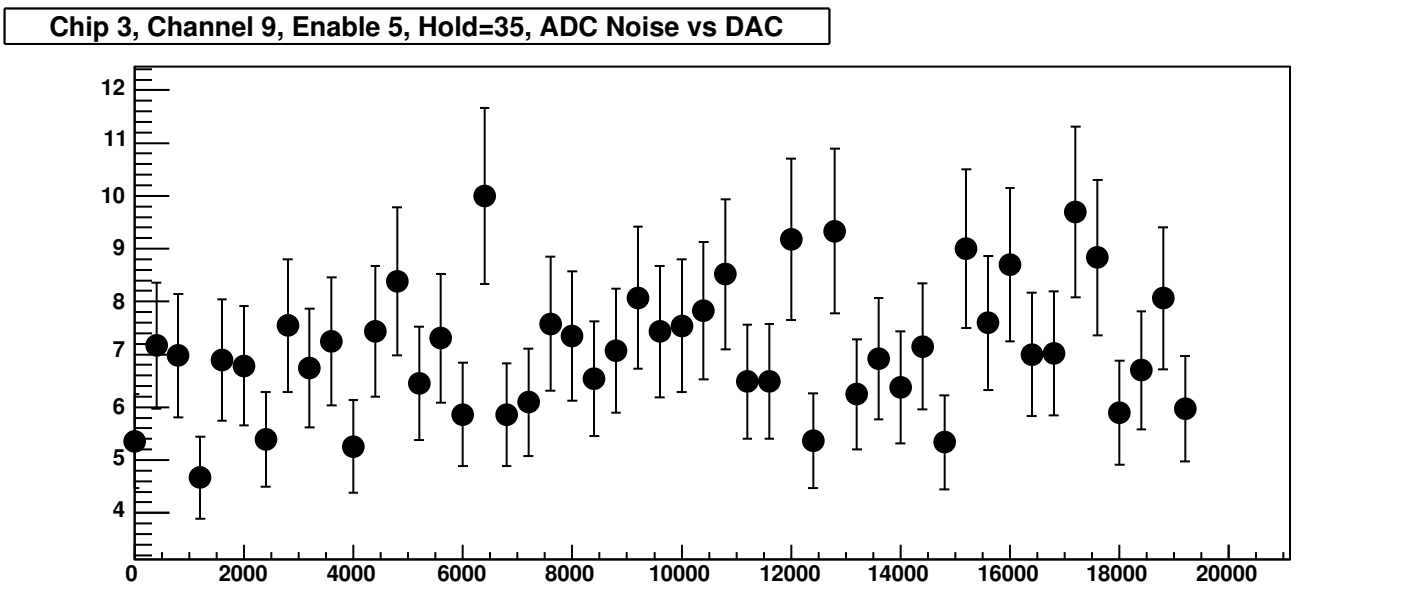
Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

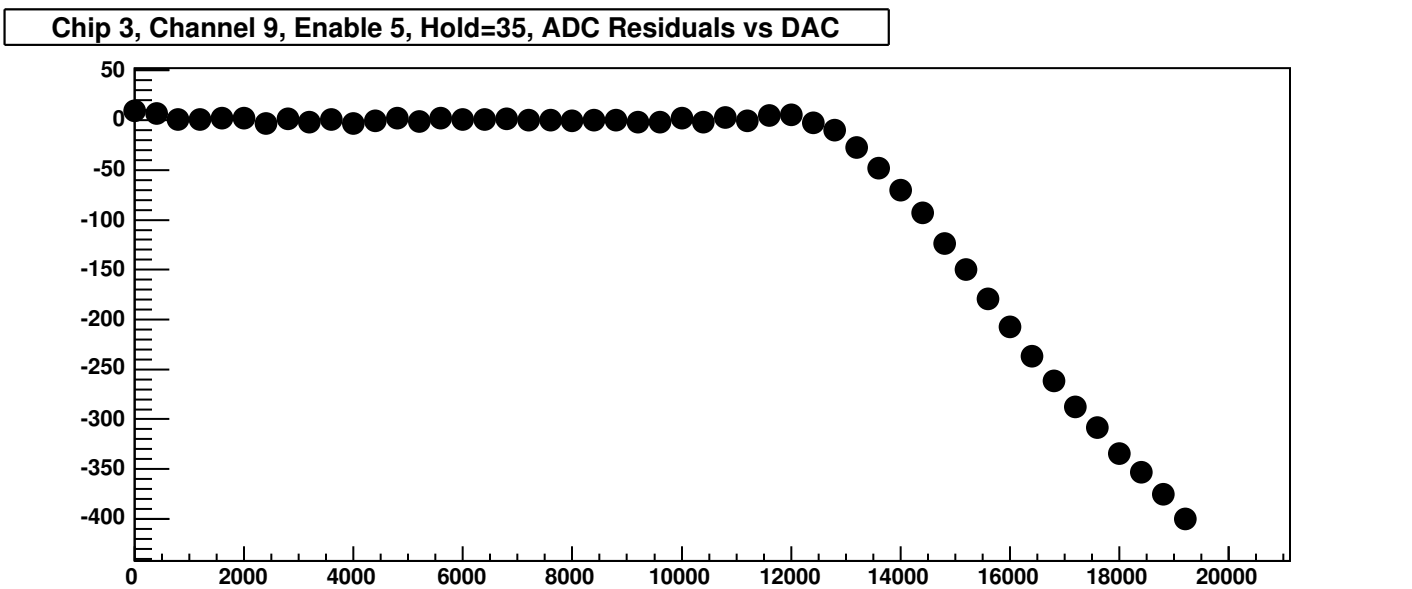


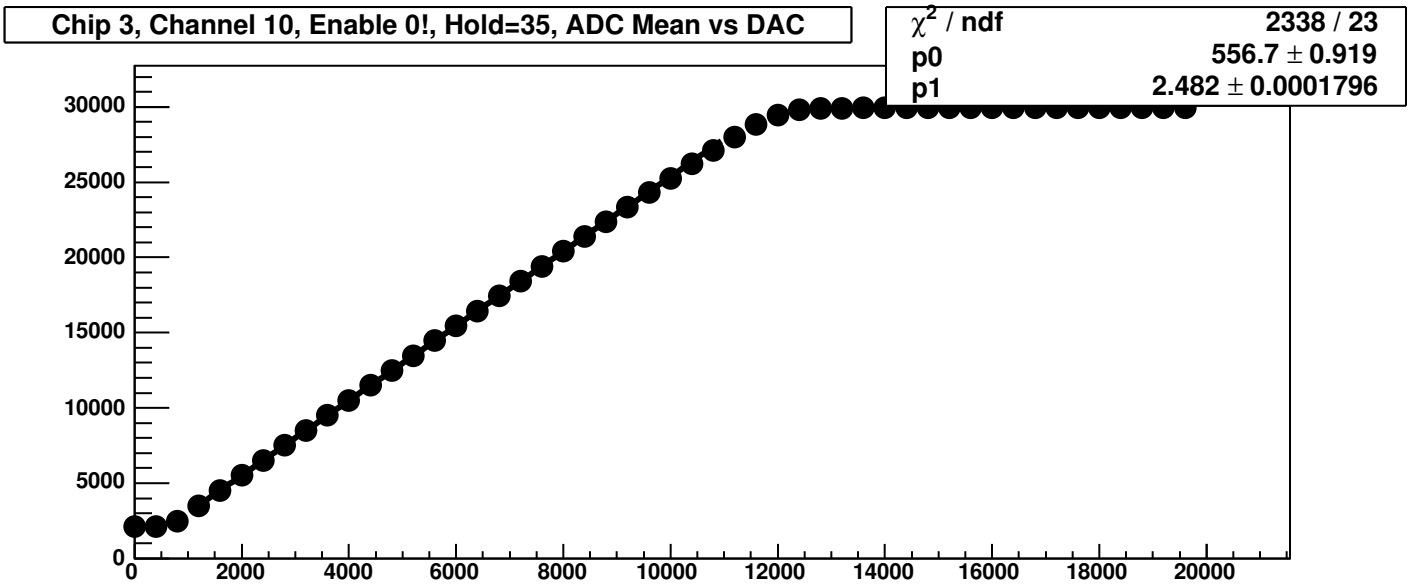
Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

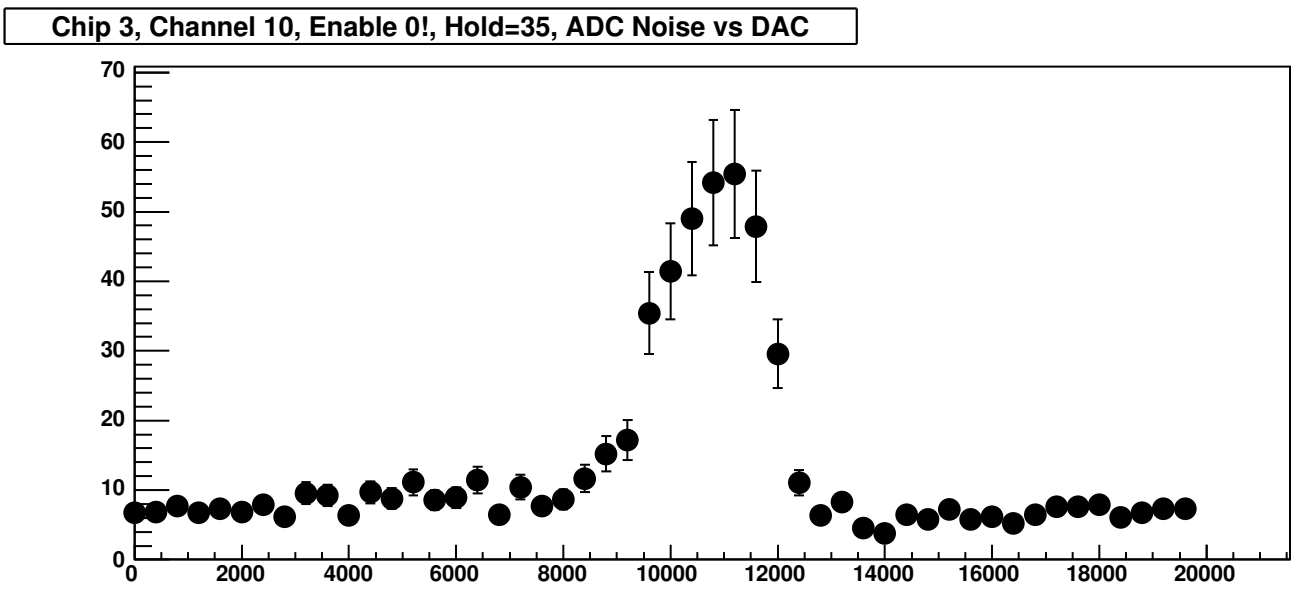


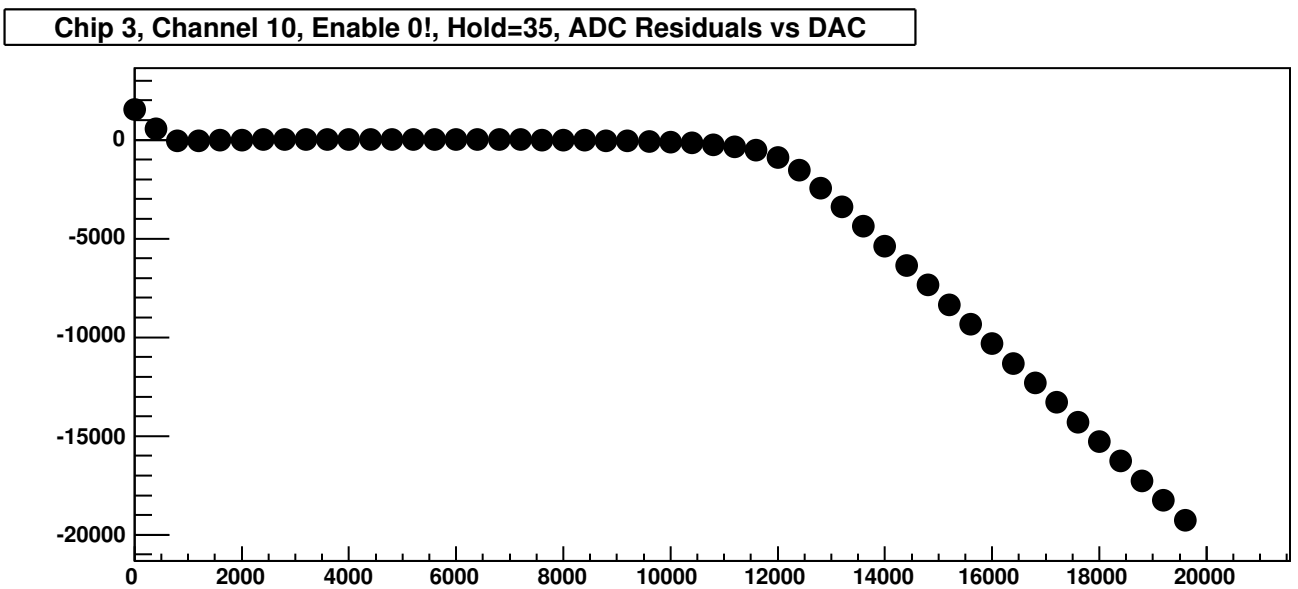

Chip 3, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 33.17 / 23
p0 406.8 ± 0.6656
p1 0.01335 ± 0.0001071
600
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

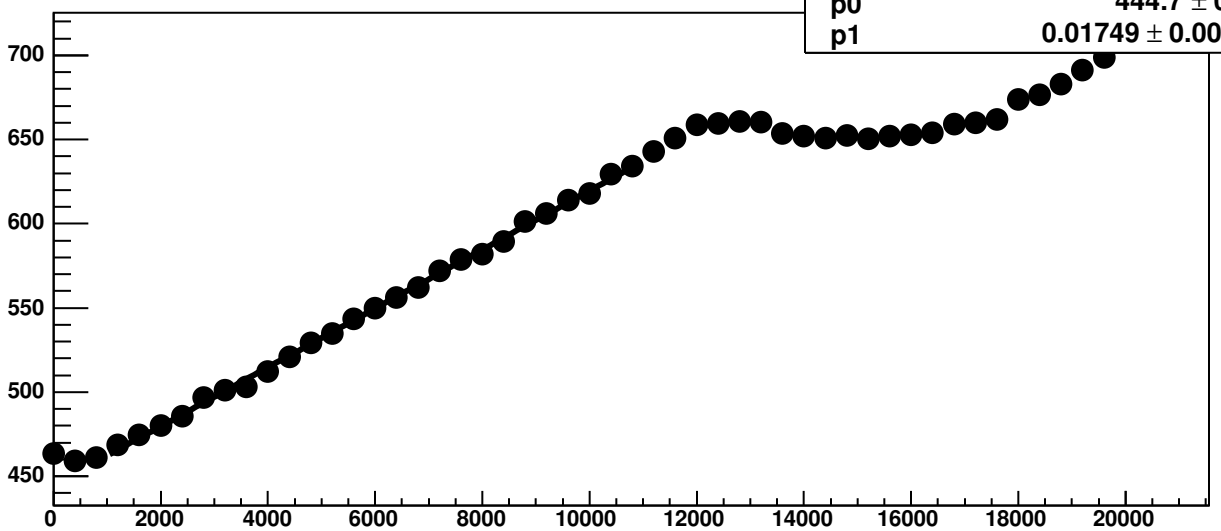

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 2338 / 23
p0 556.7 ± 0.919
p1 2.482 ± 0.0001796
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

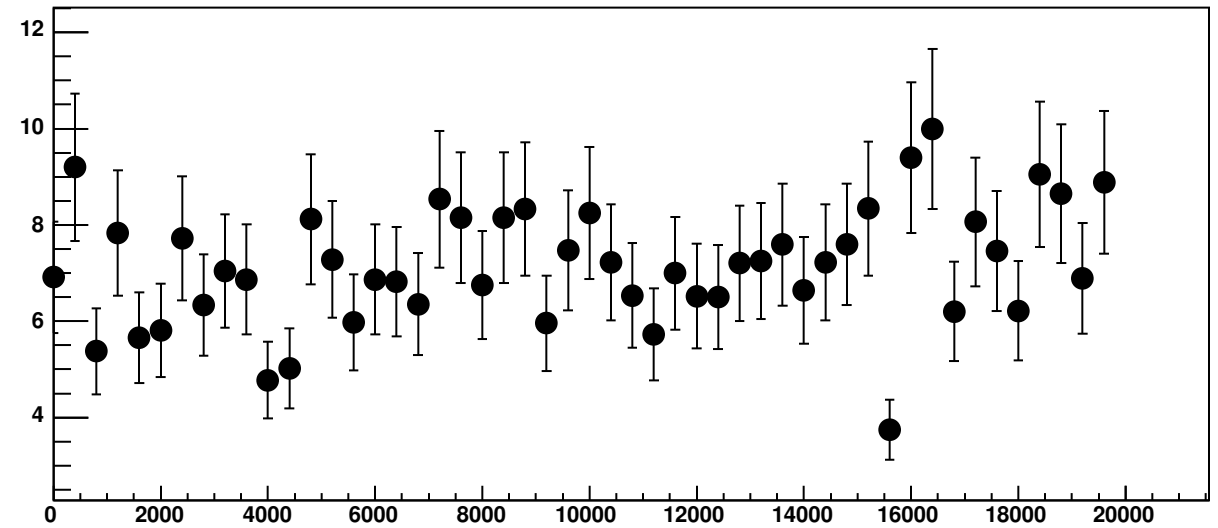

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

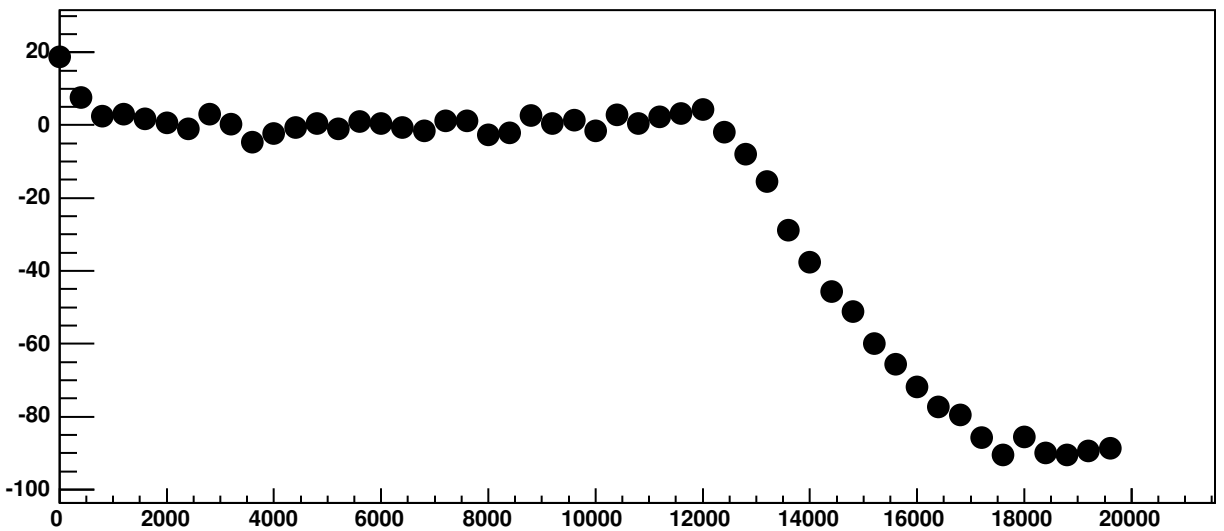
χ^2 / ndf	36.08 / 23
p0	444.7 ± 0.6872
p1	0.01749 ± 0.0001082

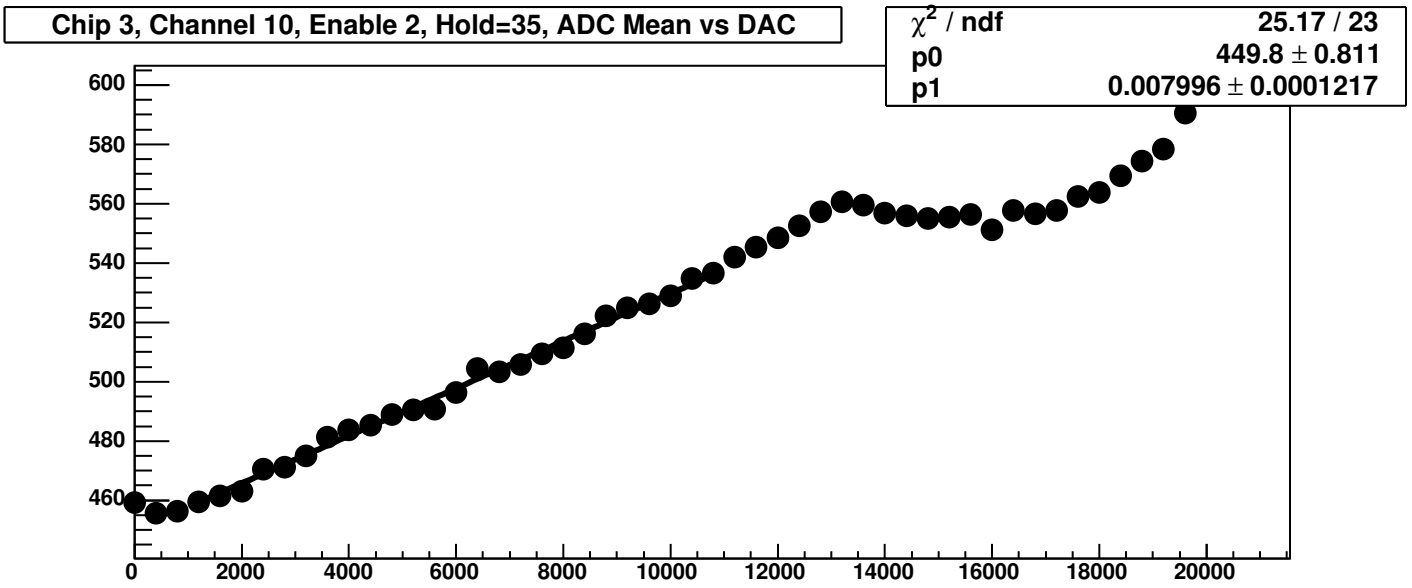


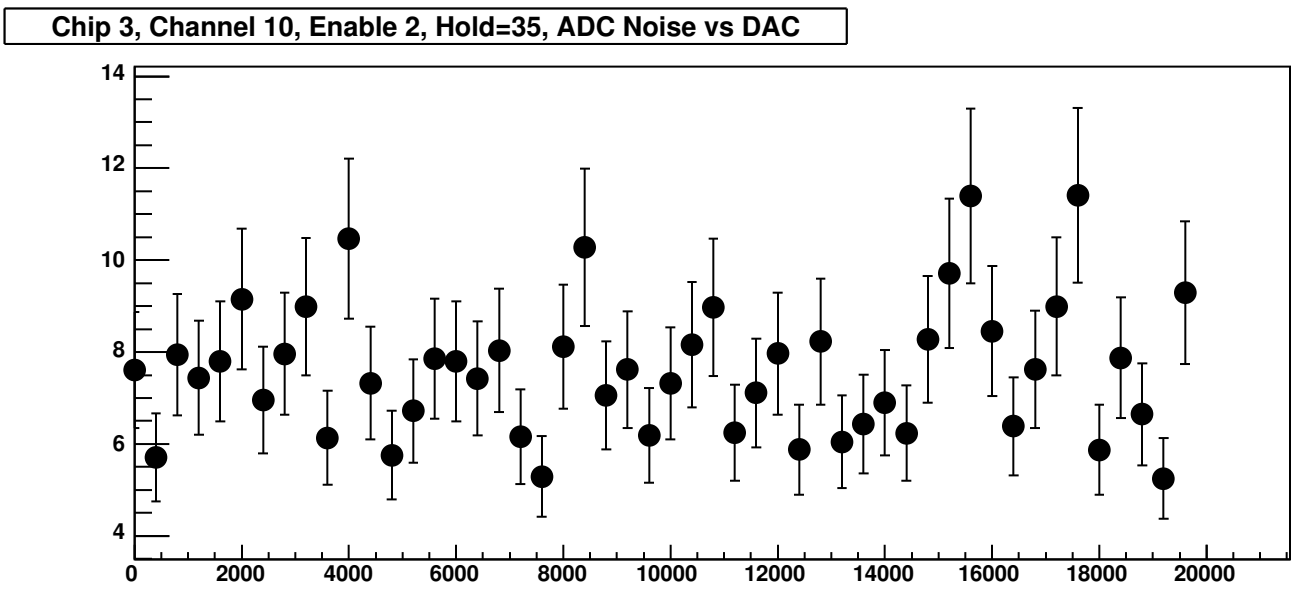
Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC

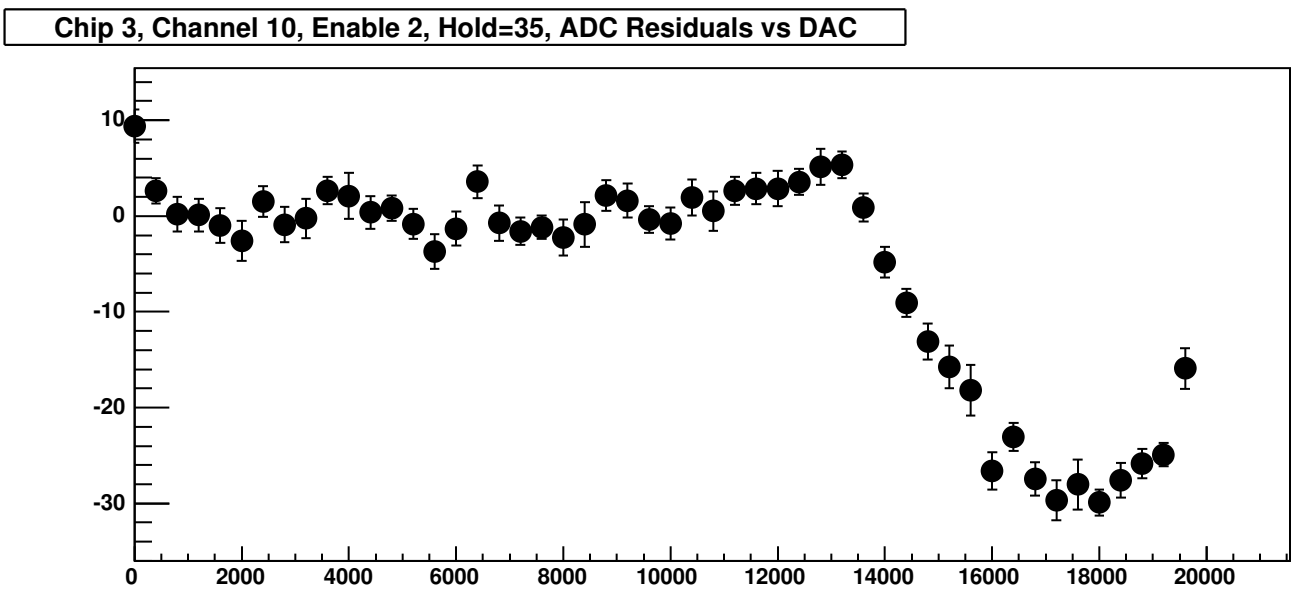


Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

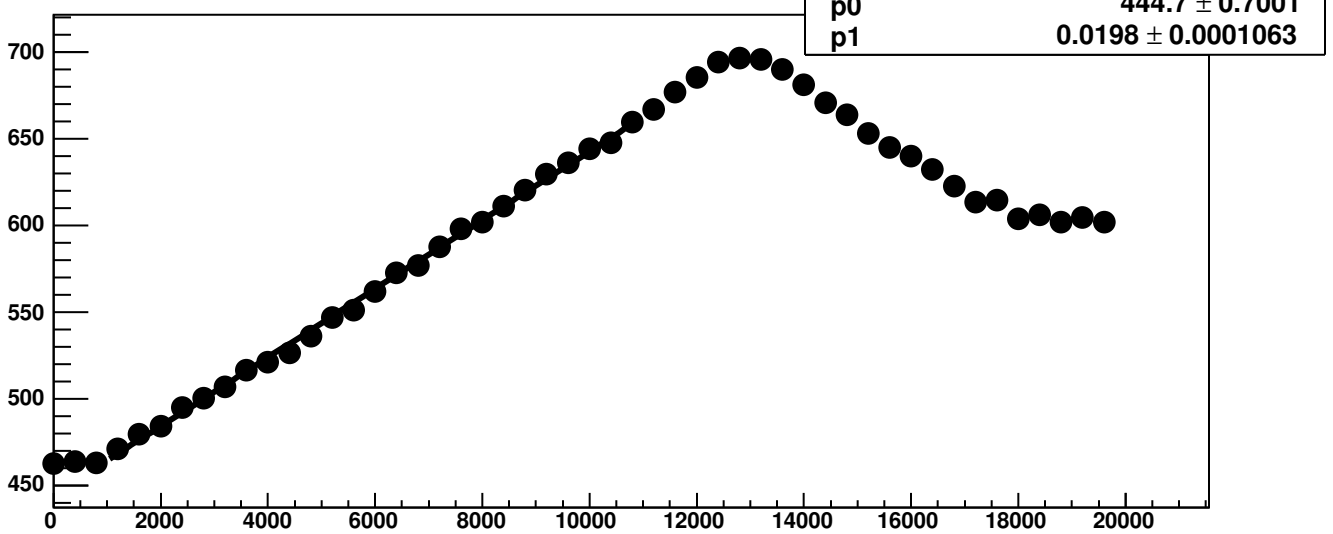



Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.17 / 23
p0 449.8 ± 0.811
p1 0.007996 ± 0.0001217
600
580
560
540
520
500
480
460
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

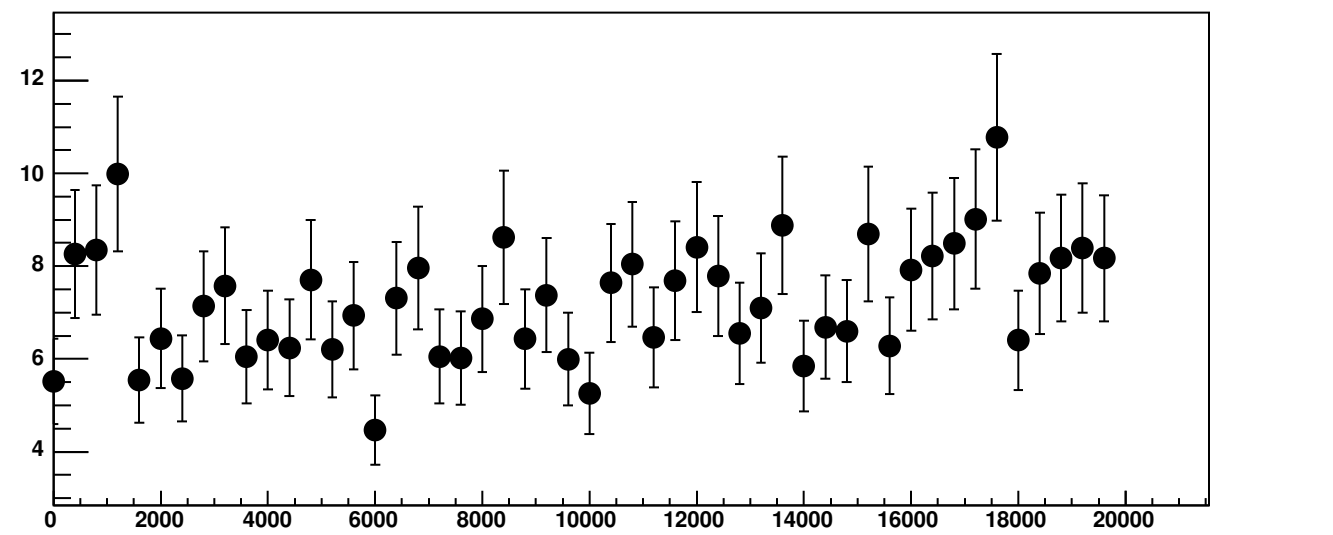

Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

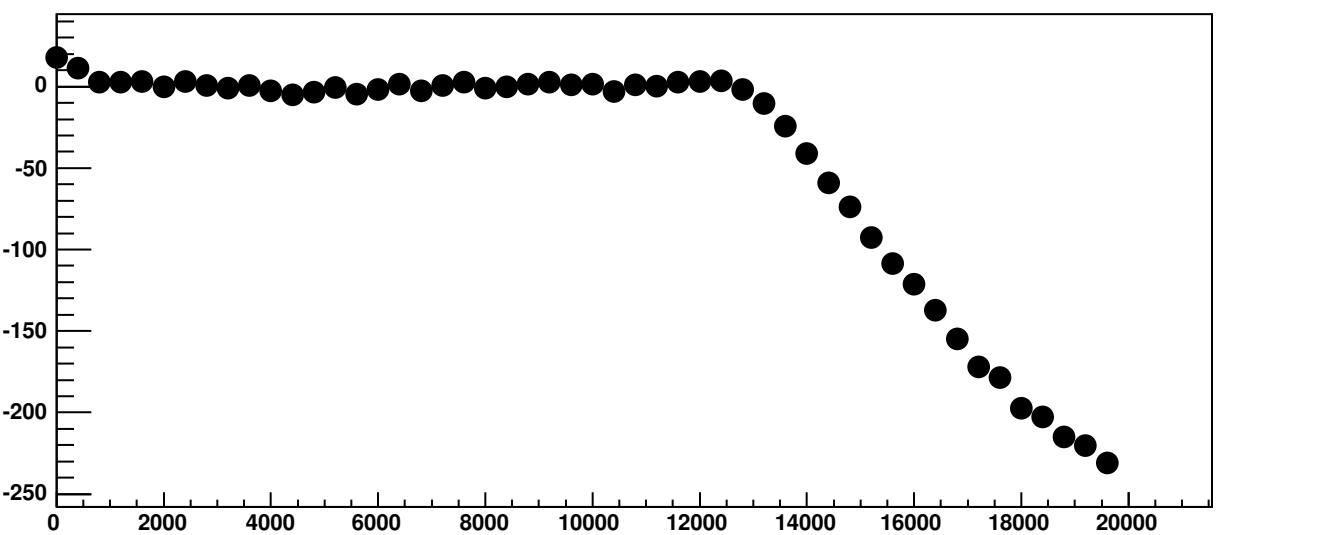
Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

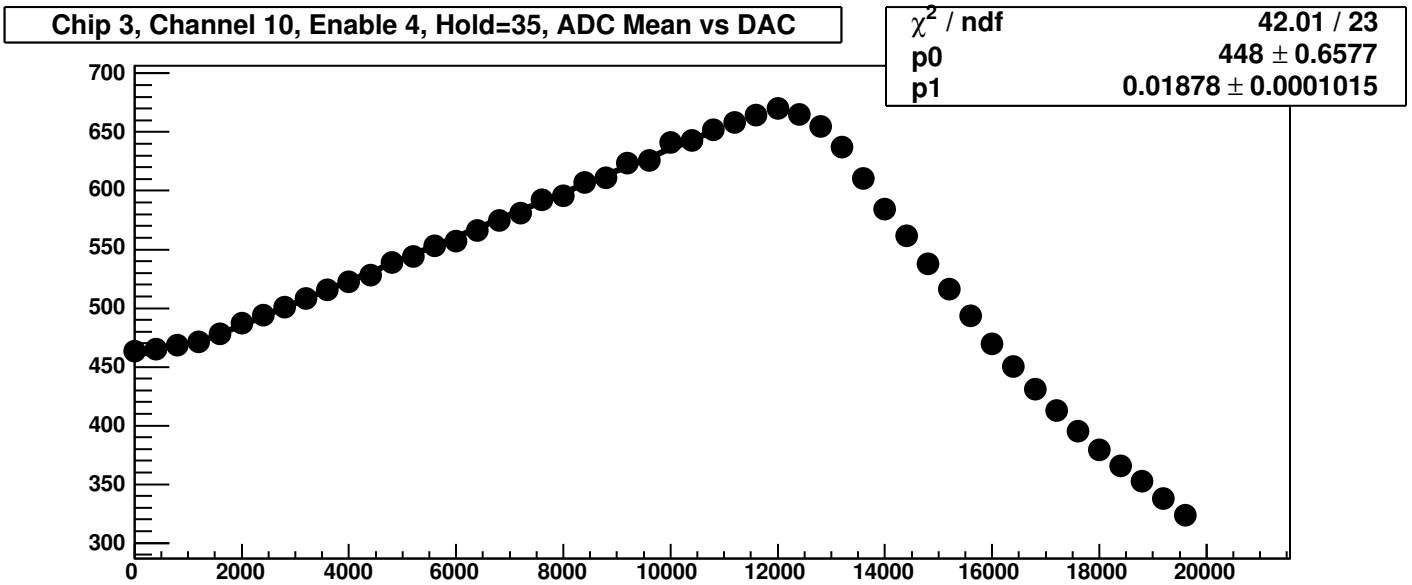


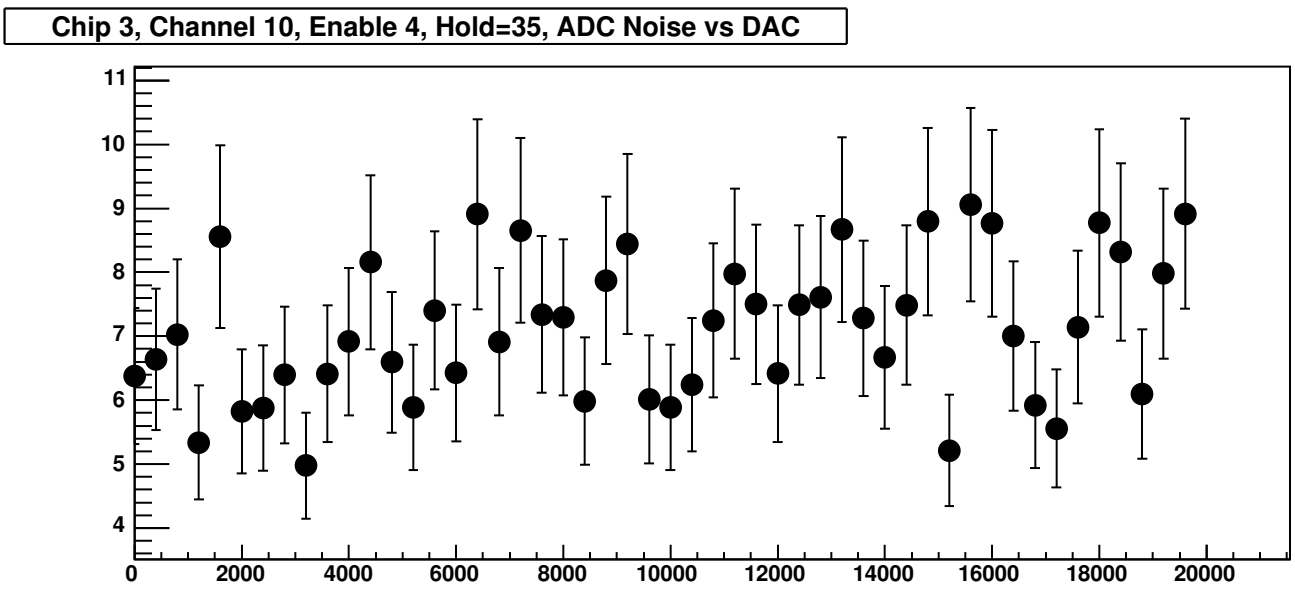
Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC

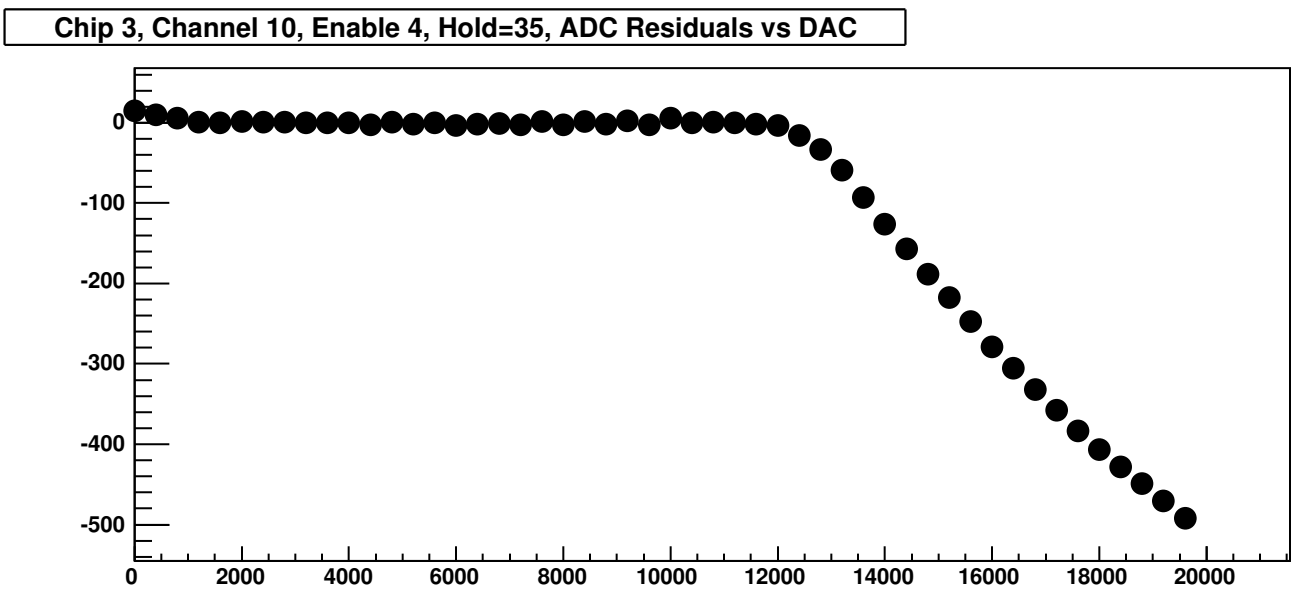


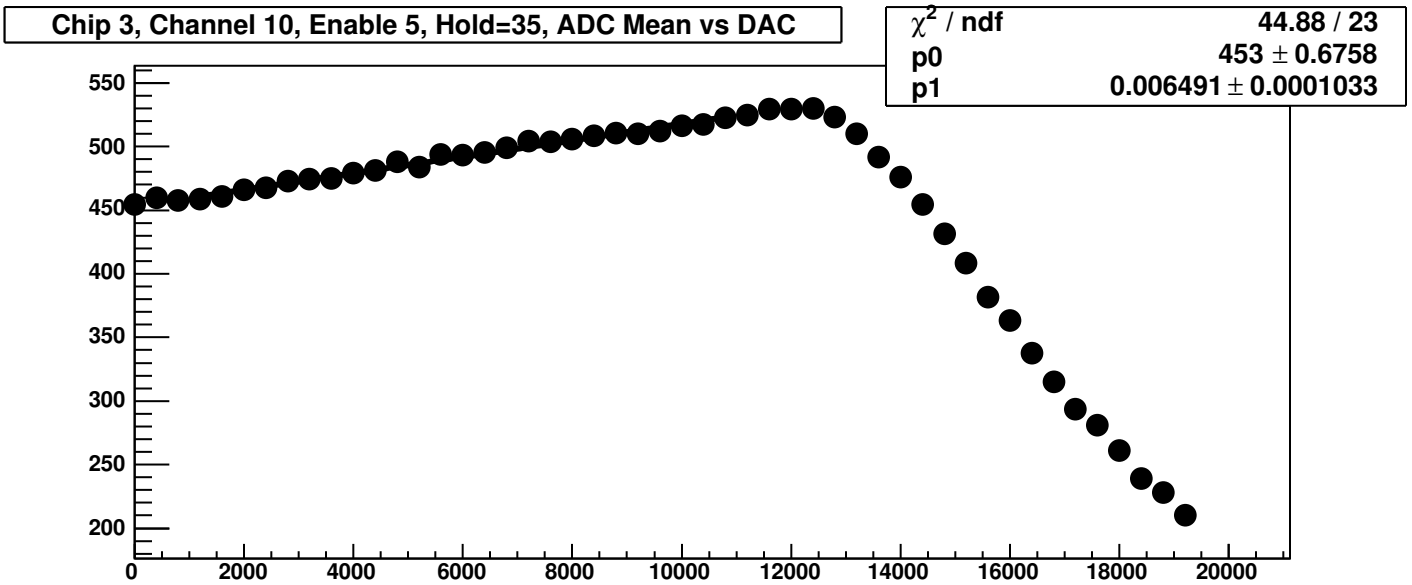
Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

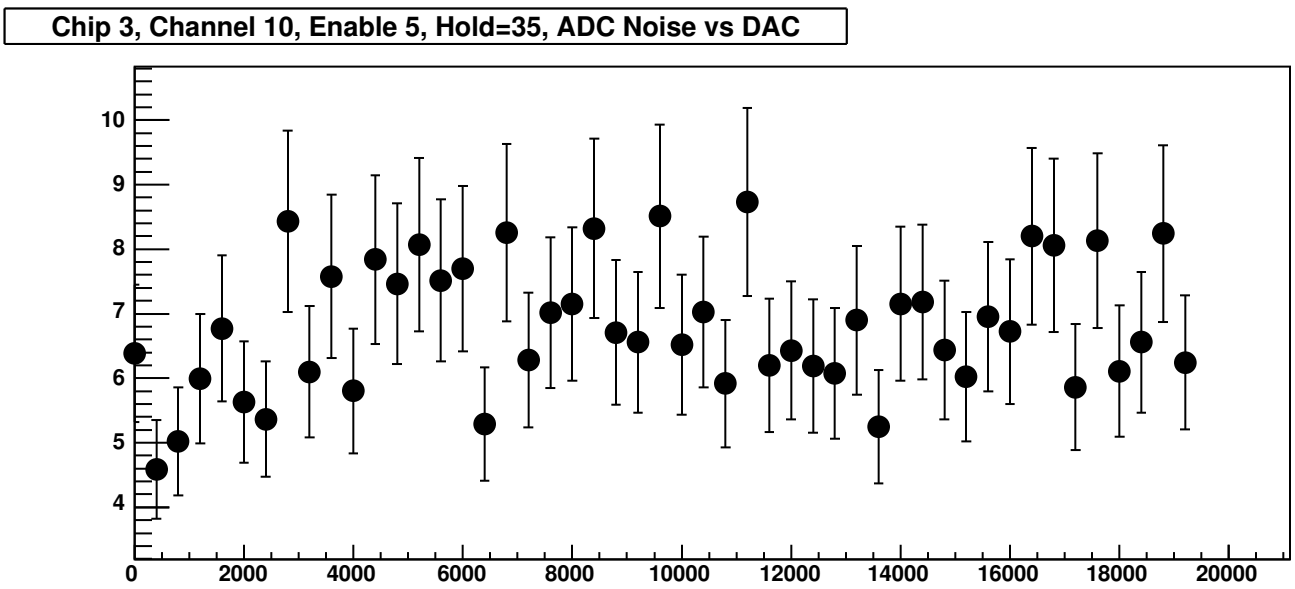


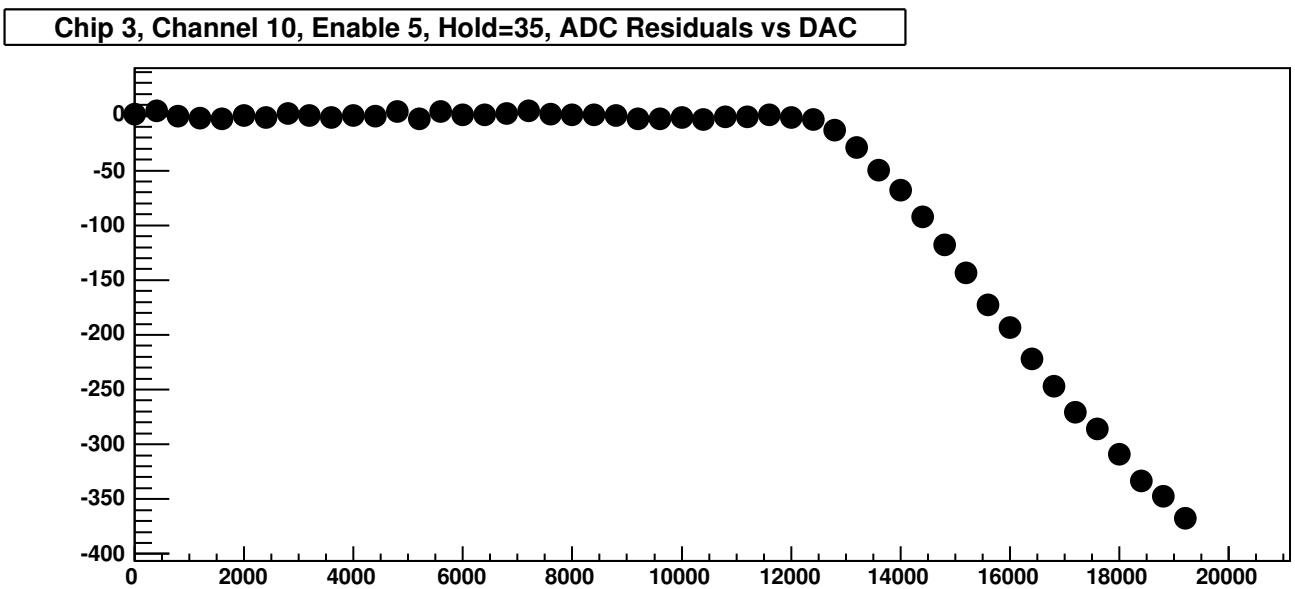

Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 42.01 / 23
p0 448 ± 0.6577
p1 0.01878 ± 0.0001015
700
650
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

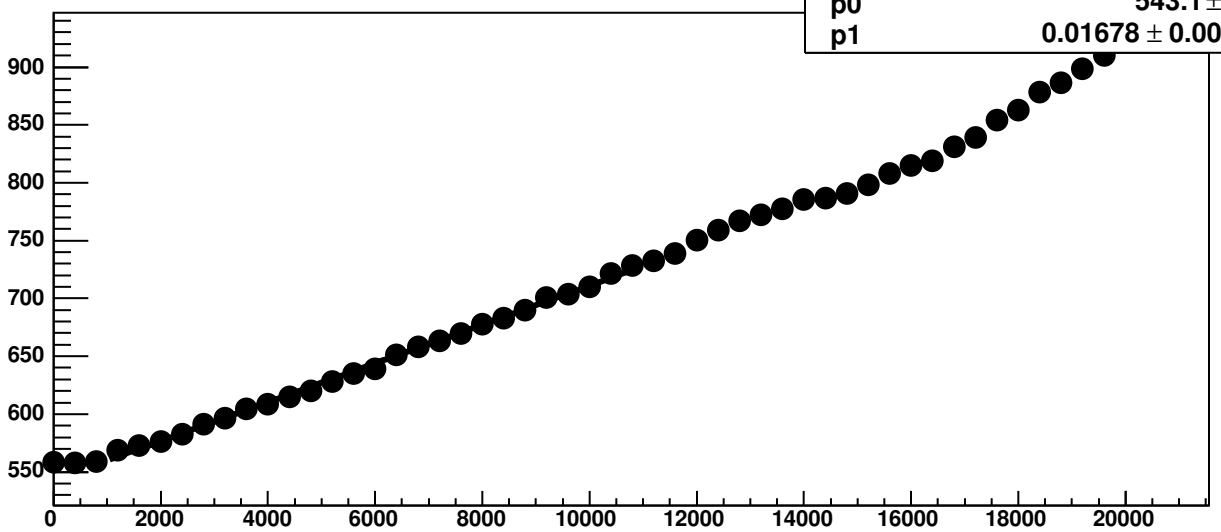

Chip 3, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 44.88 / 23
p0 453 ± 0.6758
p1 0.006491 ± 0.0001033
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

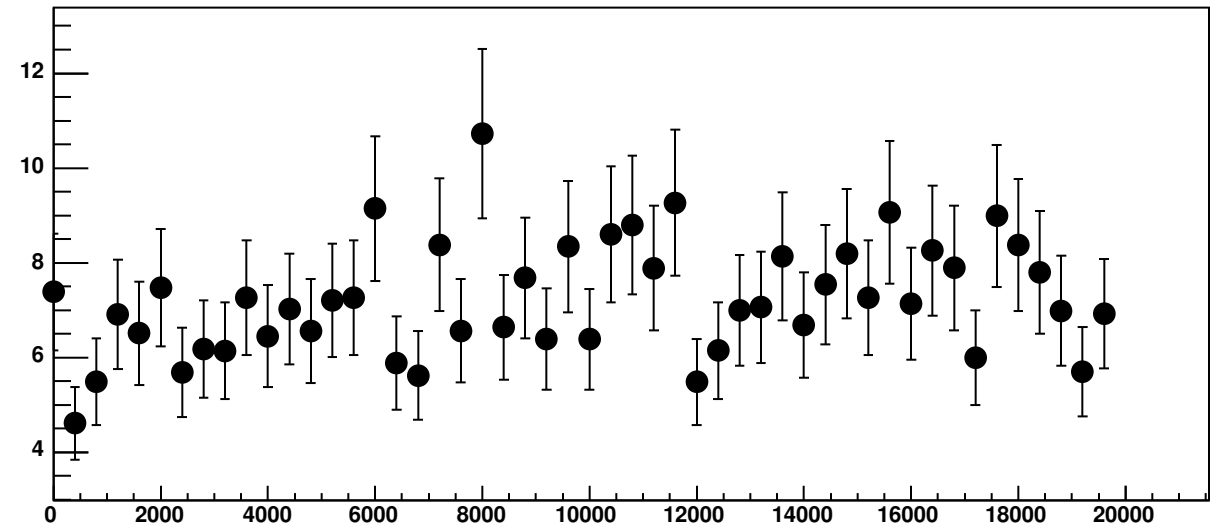

Chip 3, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

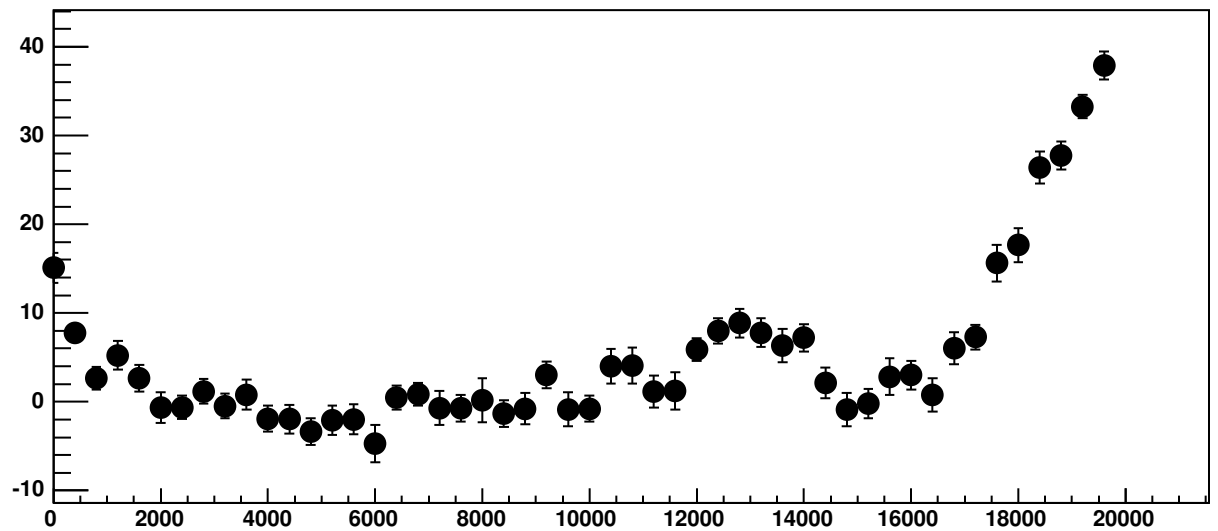
χ^2 / ndf	46.42 / 23
p0	543.1 ± 0.715
p1	0.01678 ± 0.0001126

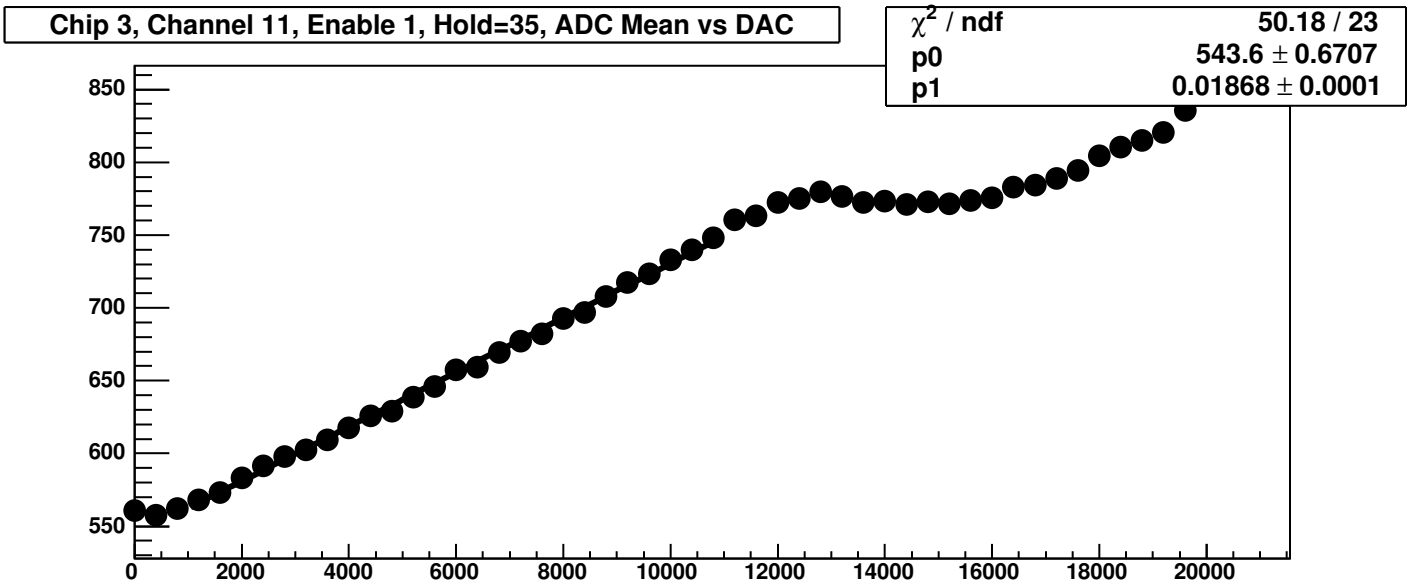


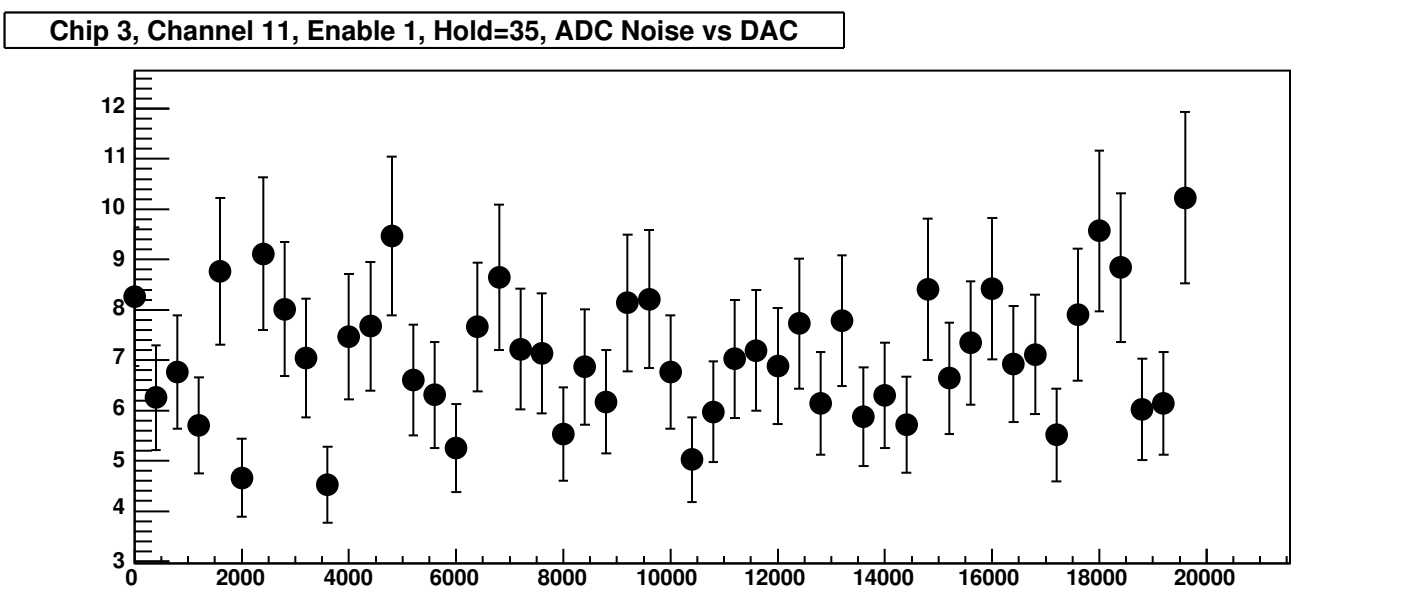
Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC

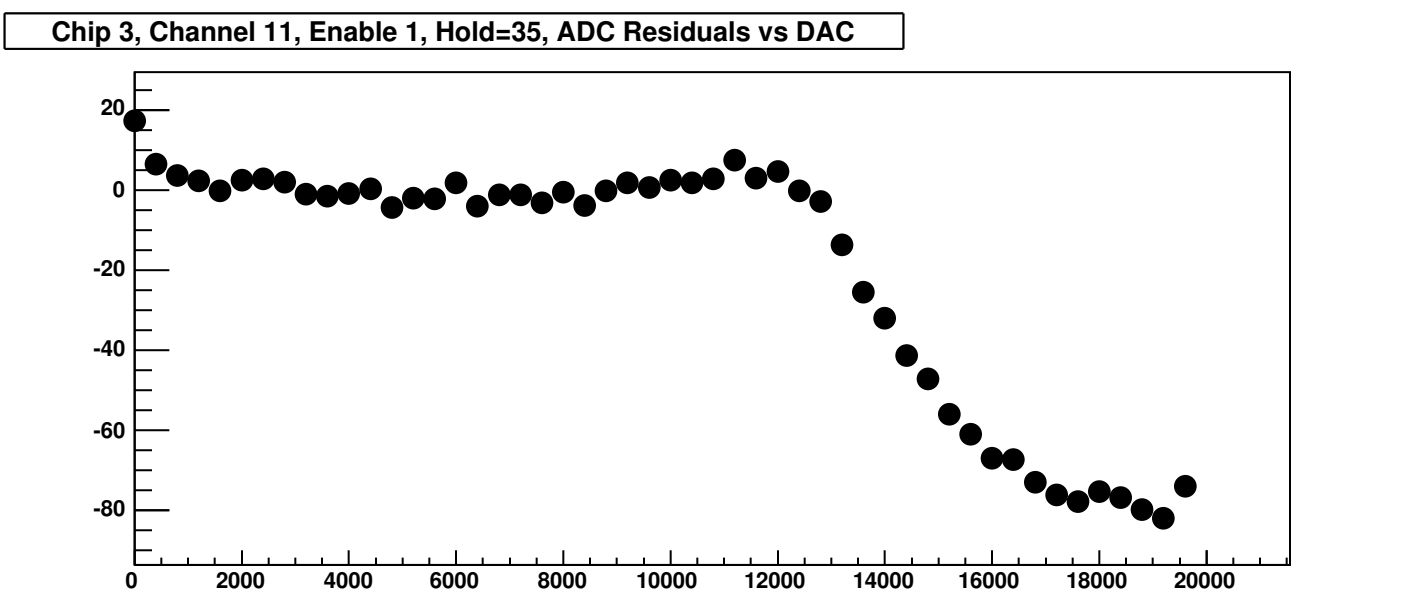


Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



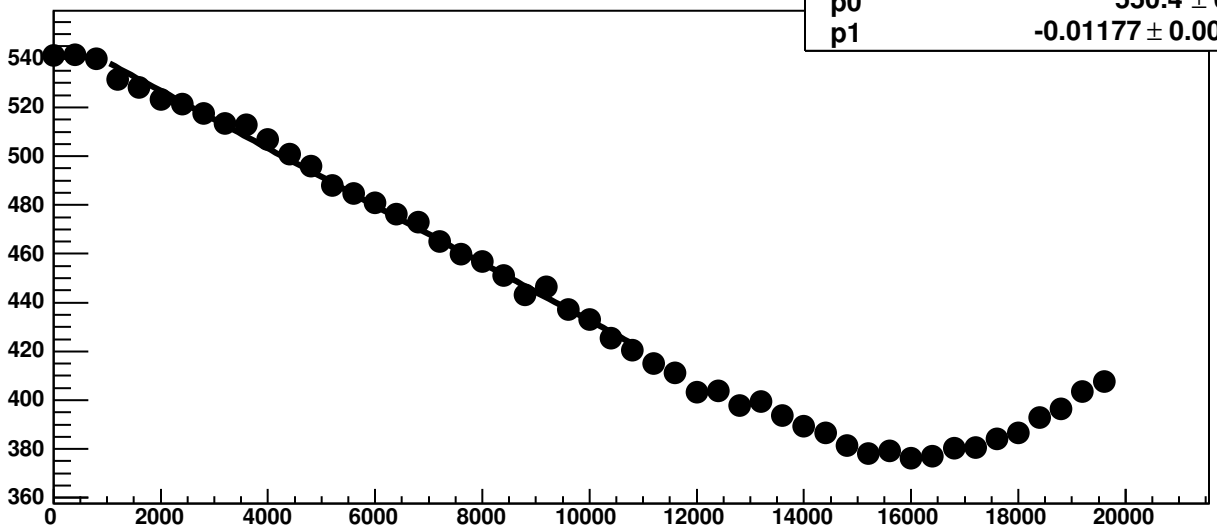

Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 50.18 / 23
p0 543.6 ± 0.6707
p1 0.01868 ± 0.0001
850
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

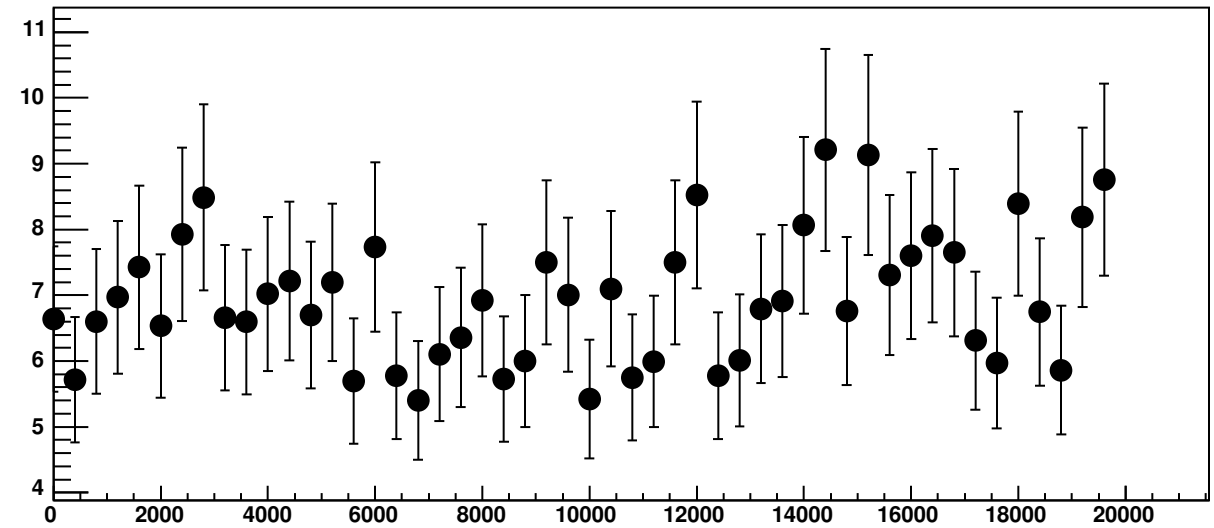

Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

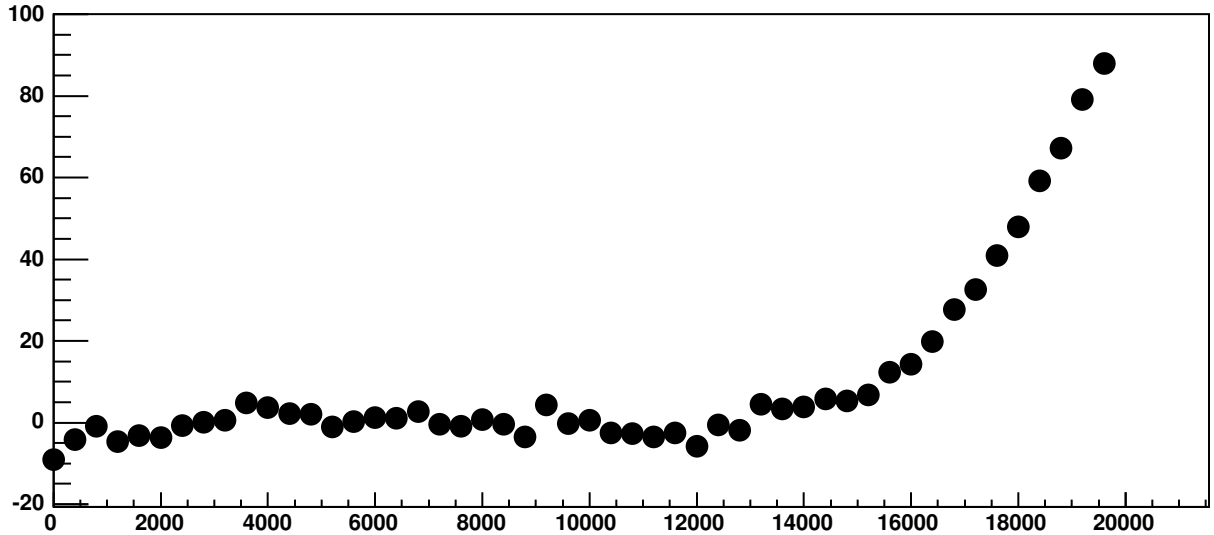
χ^2 / ndf	63.9 / 23
p0	550.4 ± 0.7356
p1	-0.01177 ± 0.0001063

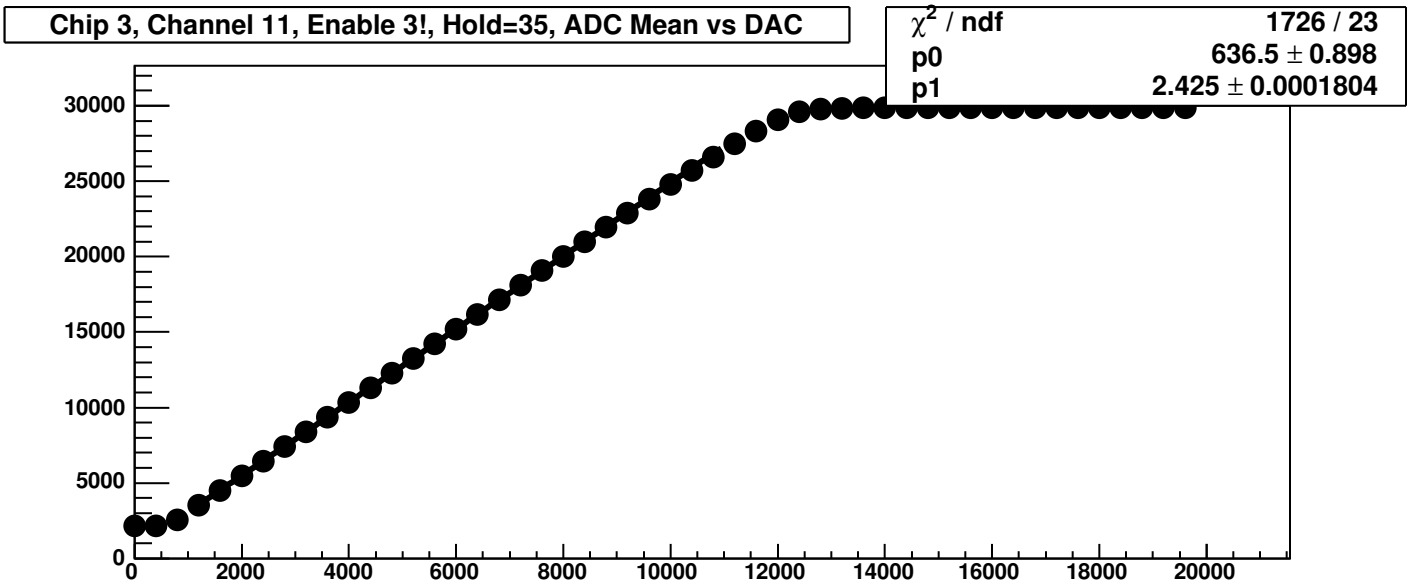


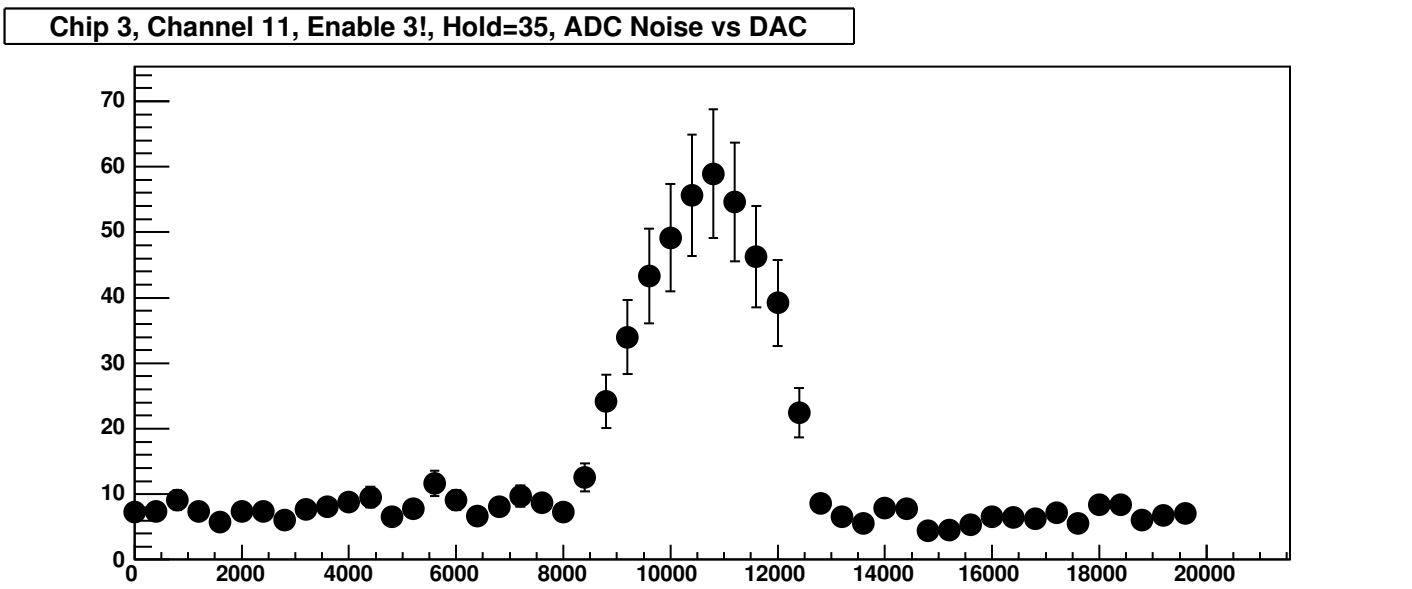
Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

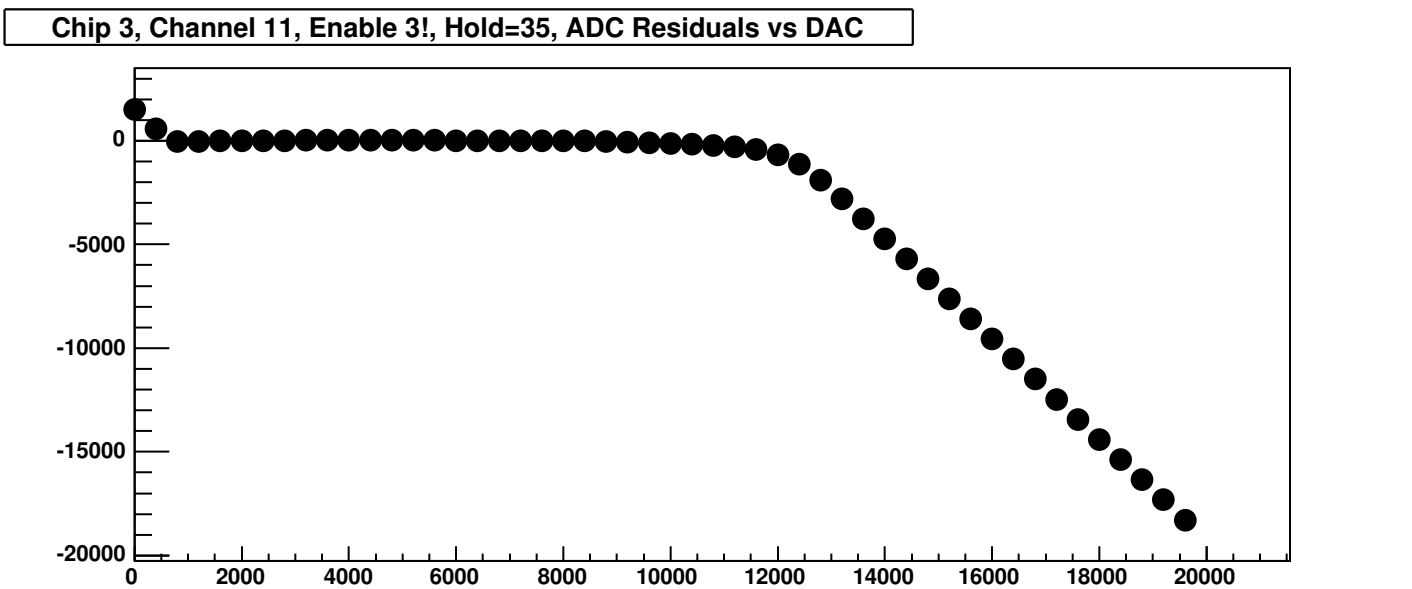


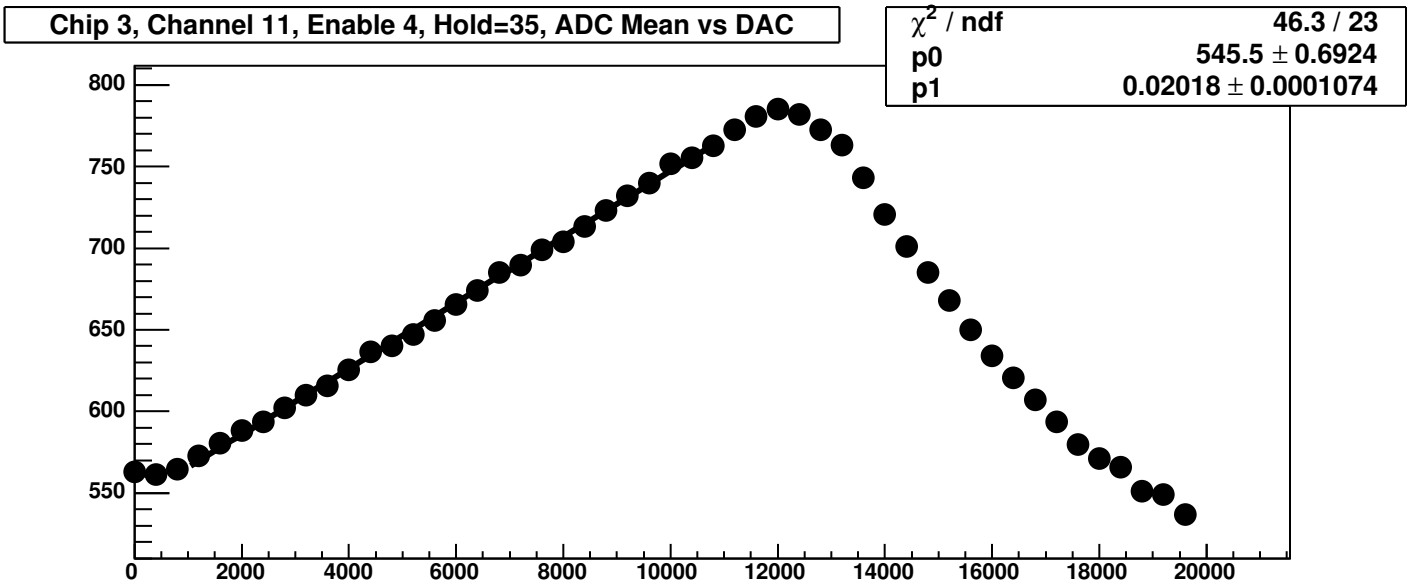
Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

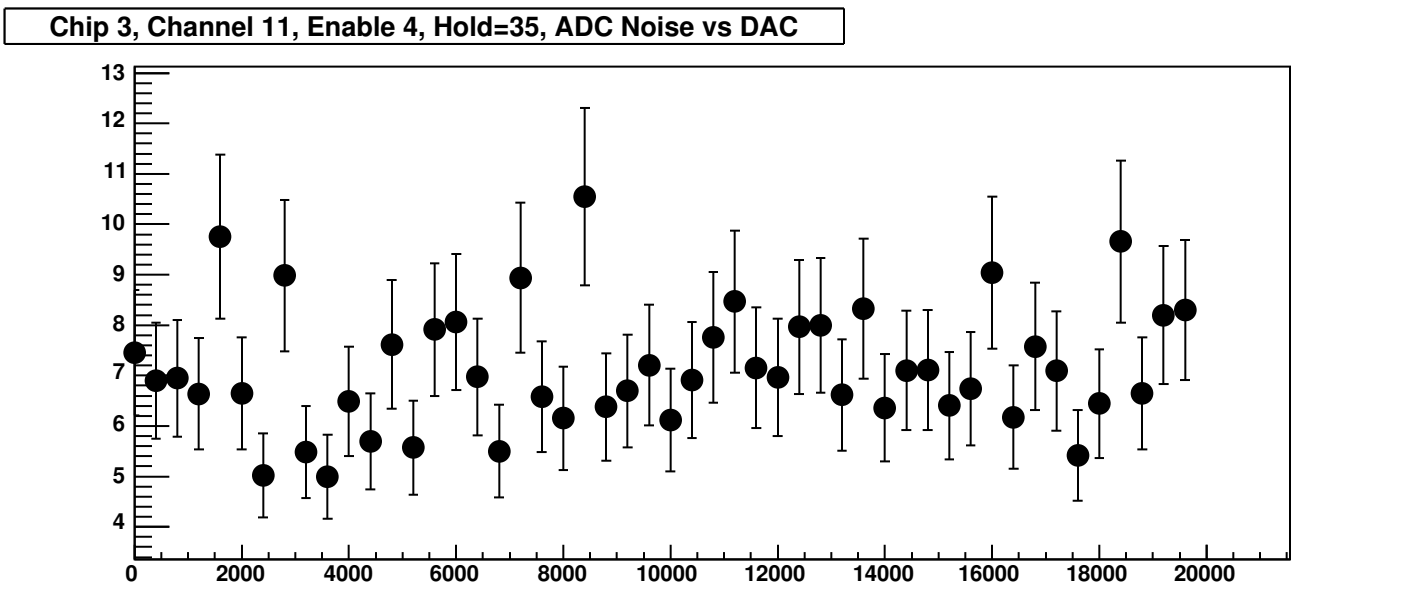


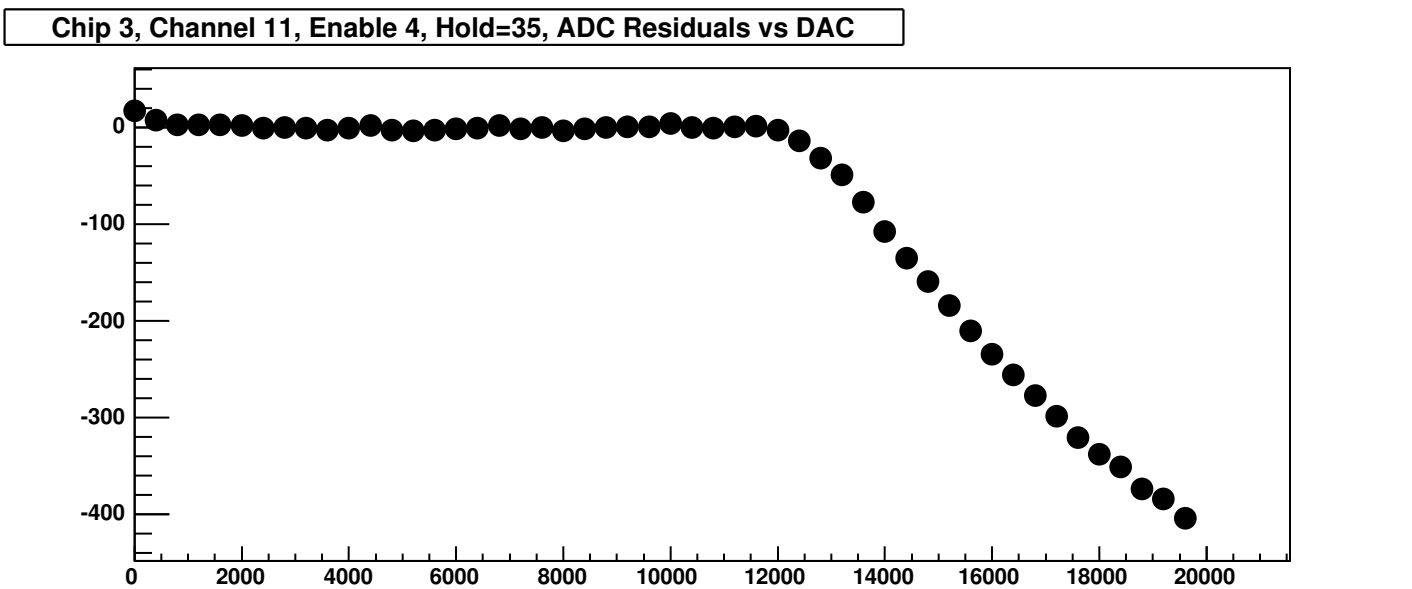

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1726 / 23
p0 636.5 ± 0.898
p1 2.425 ± 0.0001804
30000
25000
20000
15000
10000
5000
0
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

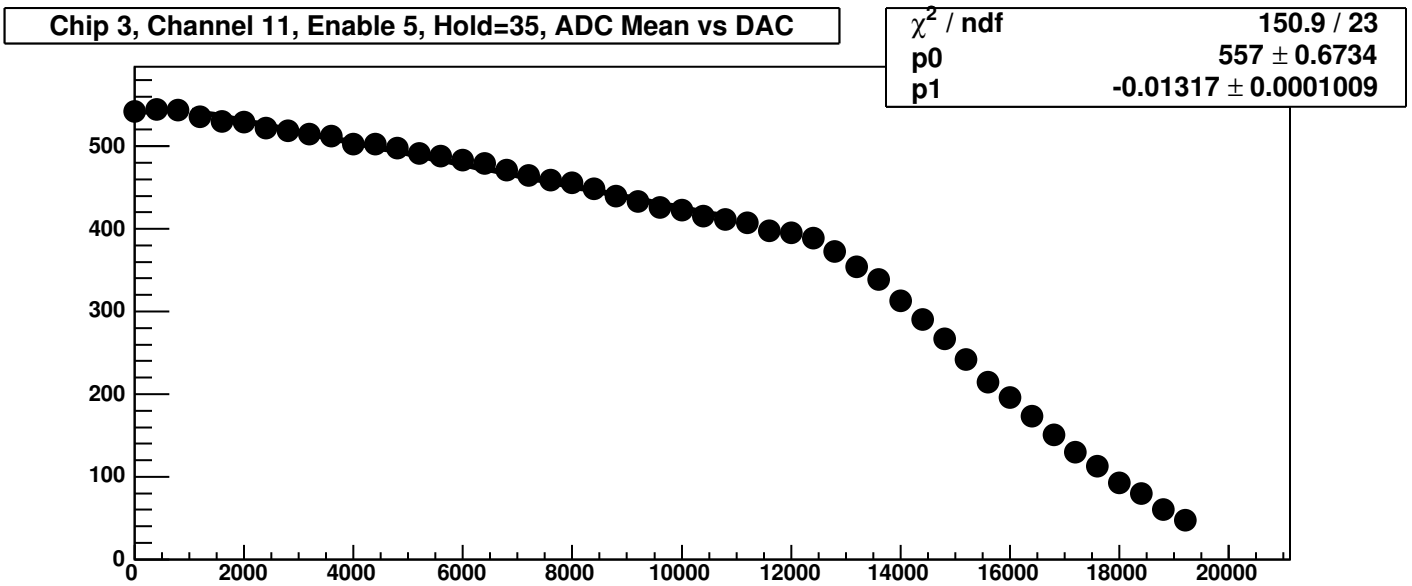

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

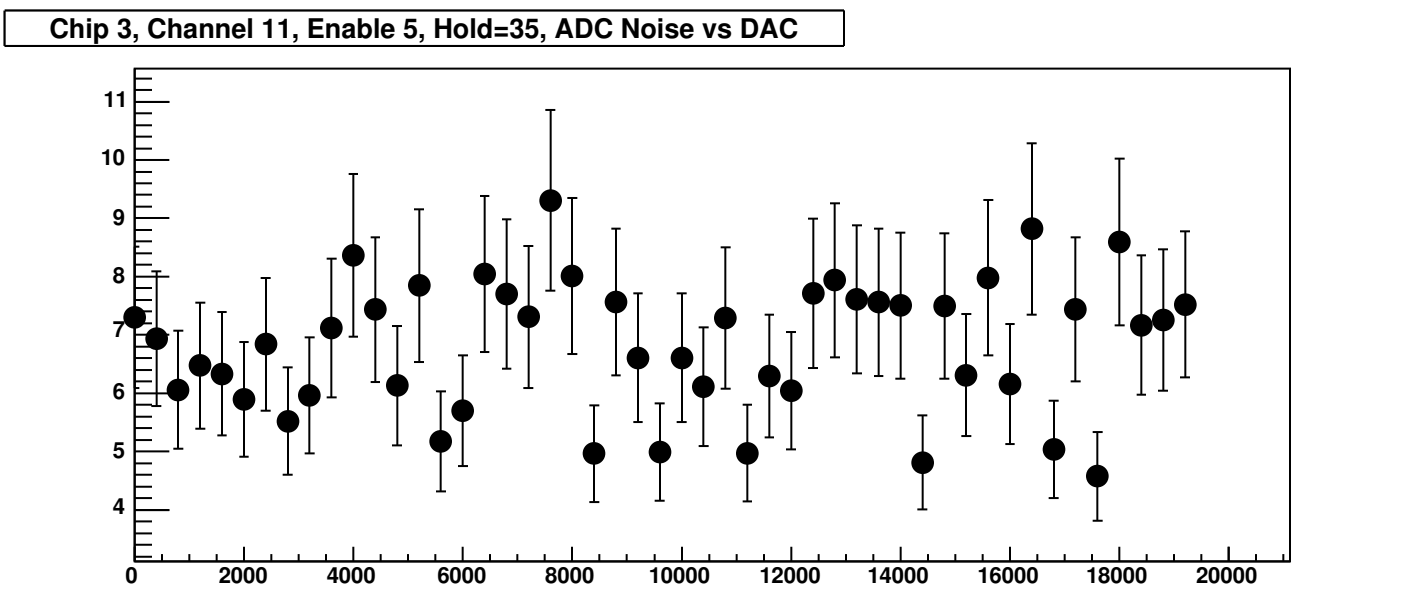

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

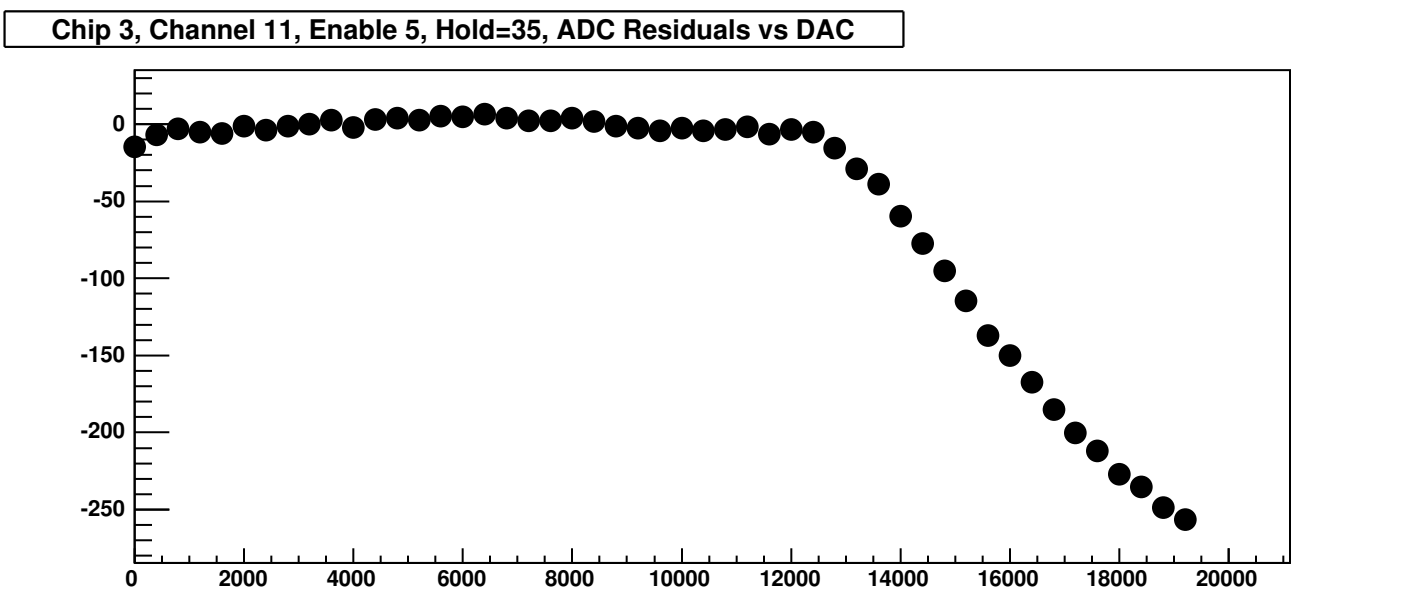

Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 46.3 / 23
p0 545.5 ± 0.6924
p1 0.02018 ± 0.0001074
800
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

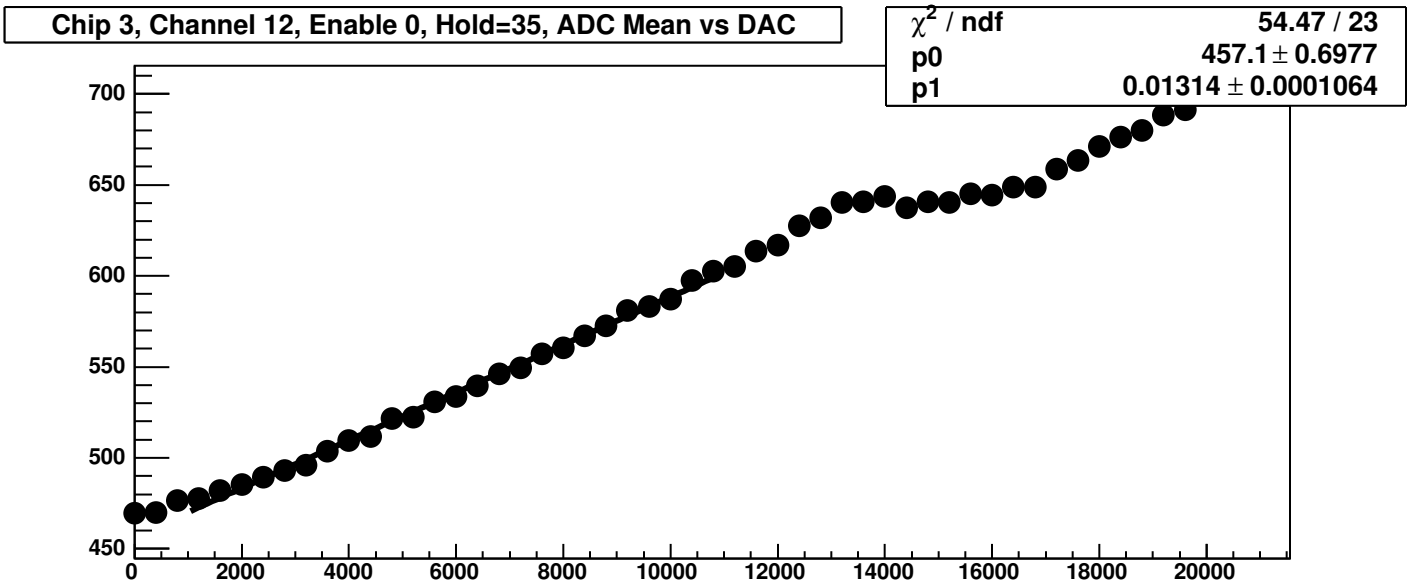

Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

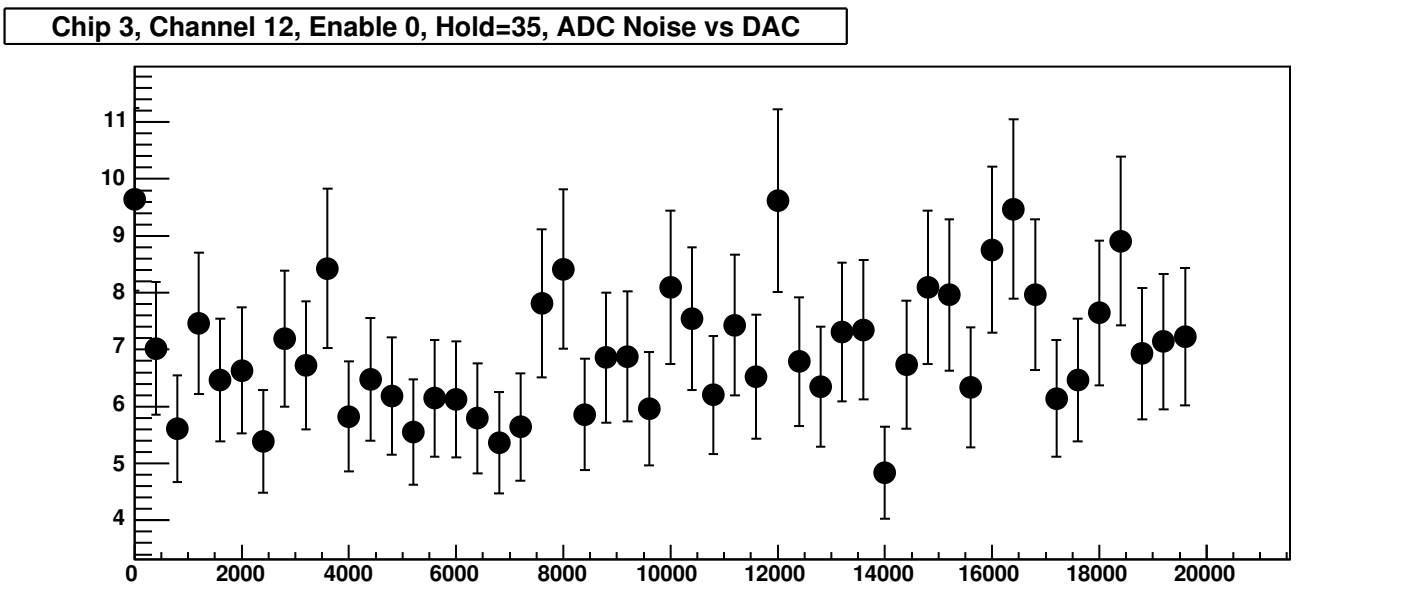

Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

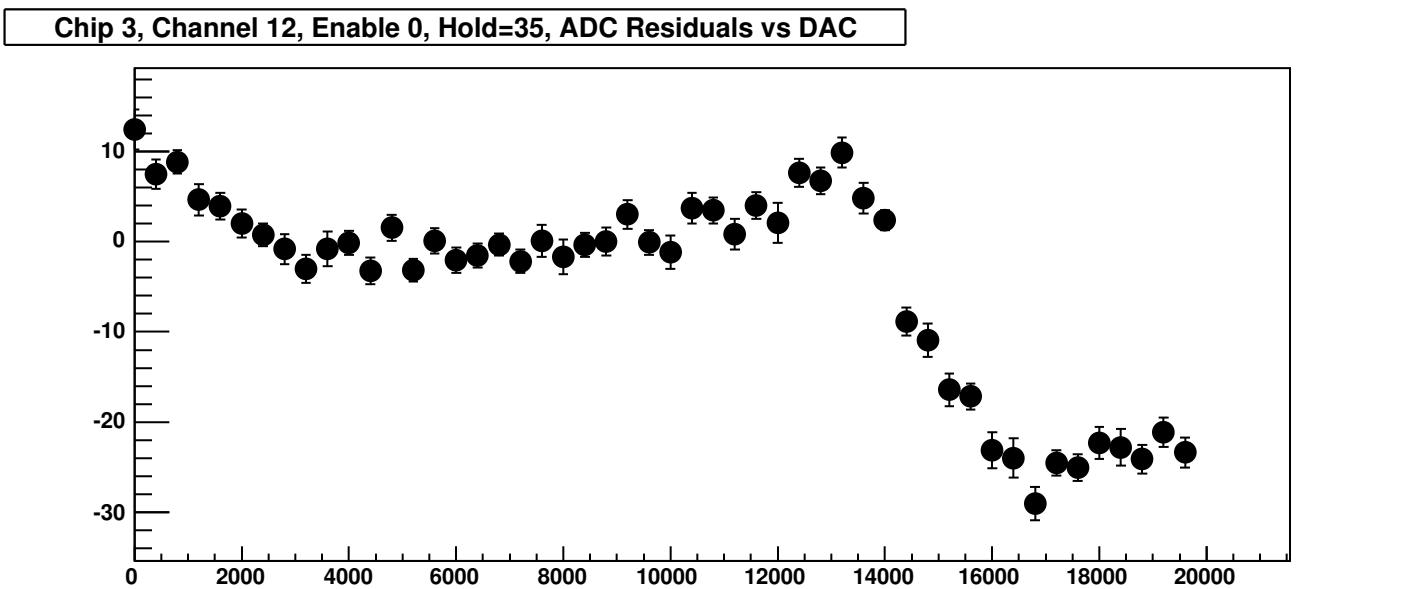

Chip 3, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 150.9 / 23
p0 557 ± 0.6734
p1 -0.01317 ± 0.0001009
0
100
200
300
400
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

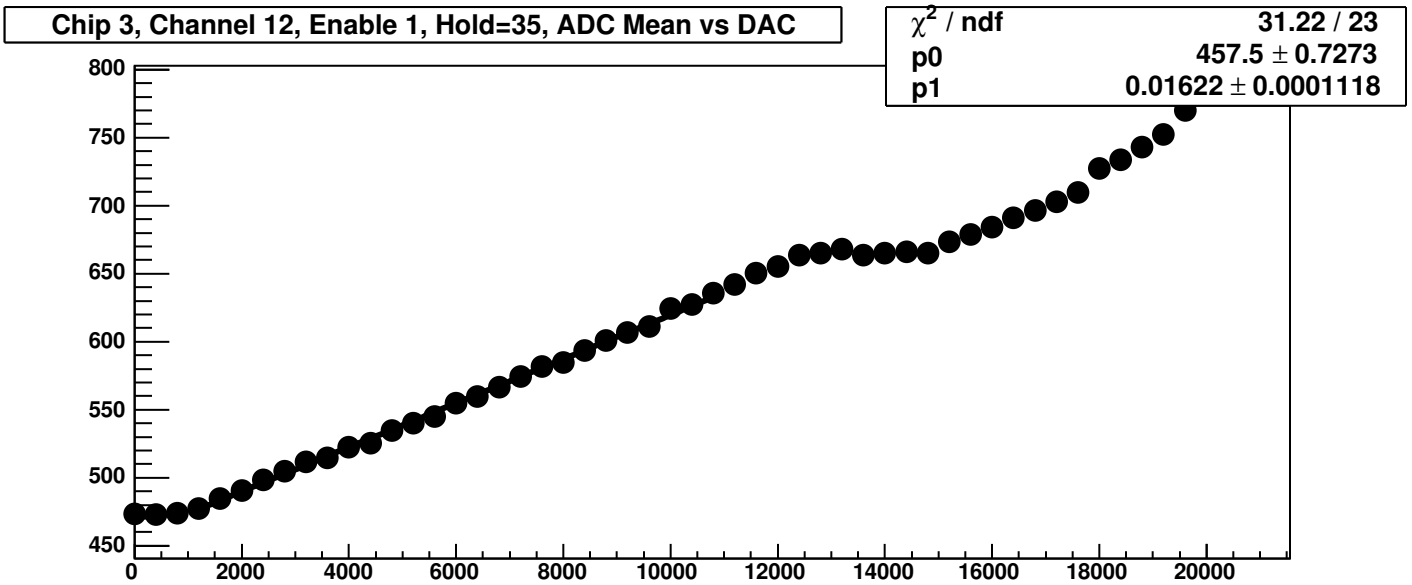

Chip 3, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
4
5
6
7
8
9
10
11
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

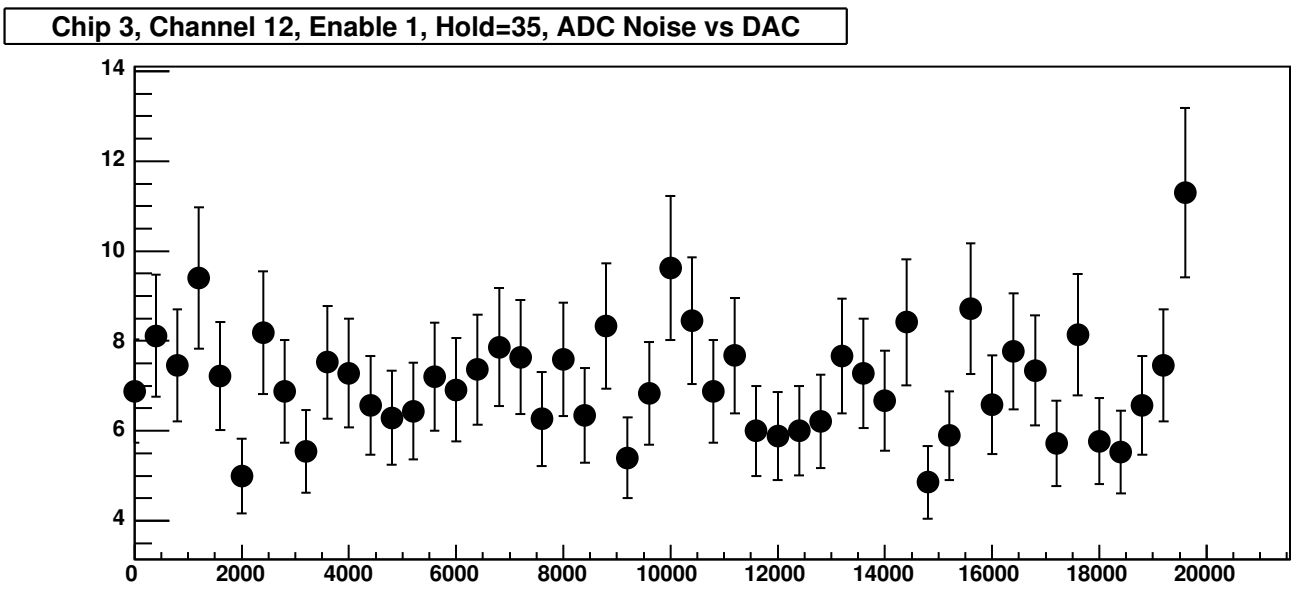

Chip 3, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
-250
-200
-150
-100
-50
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

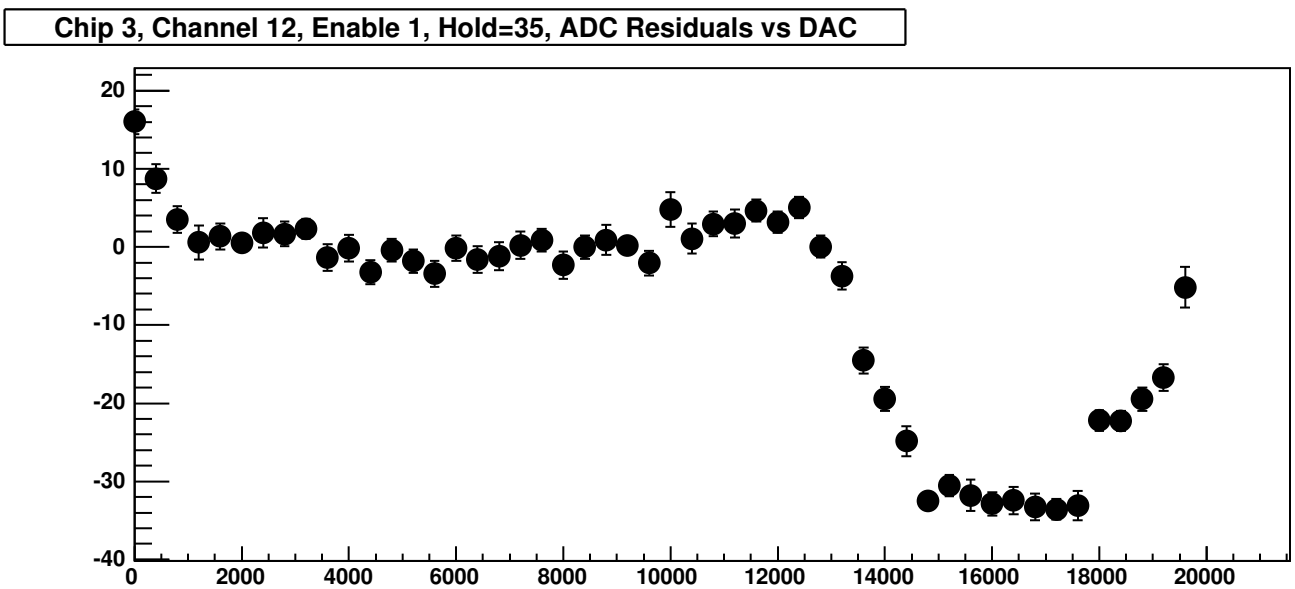

Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 54.47 / 23
p0 457.1 ± 0.6977
p1 0.01314 ± 0.0001064
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

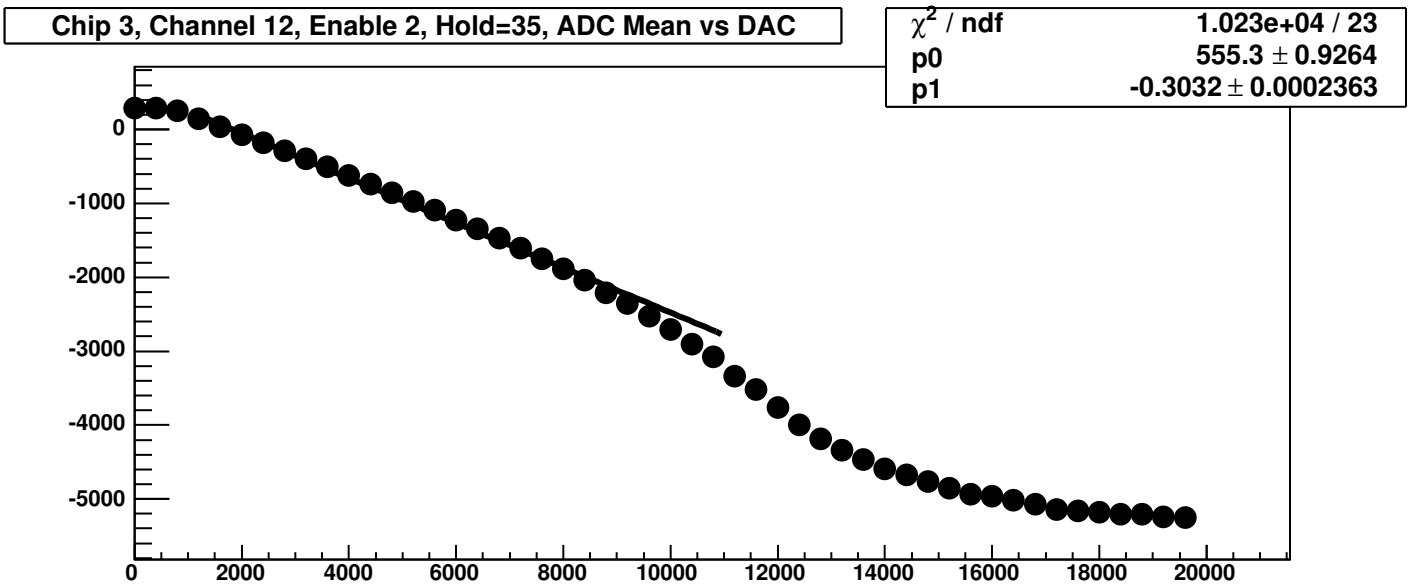

Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

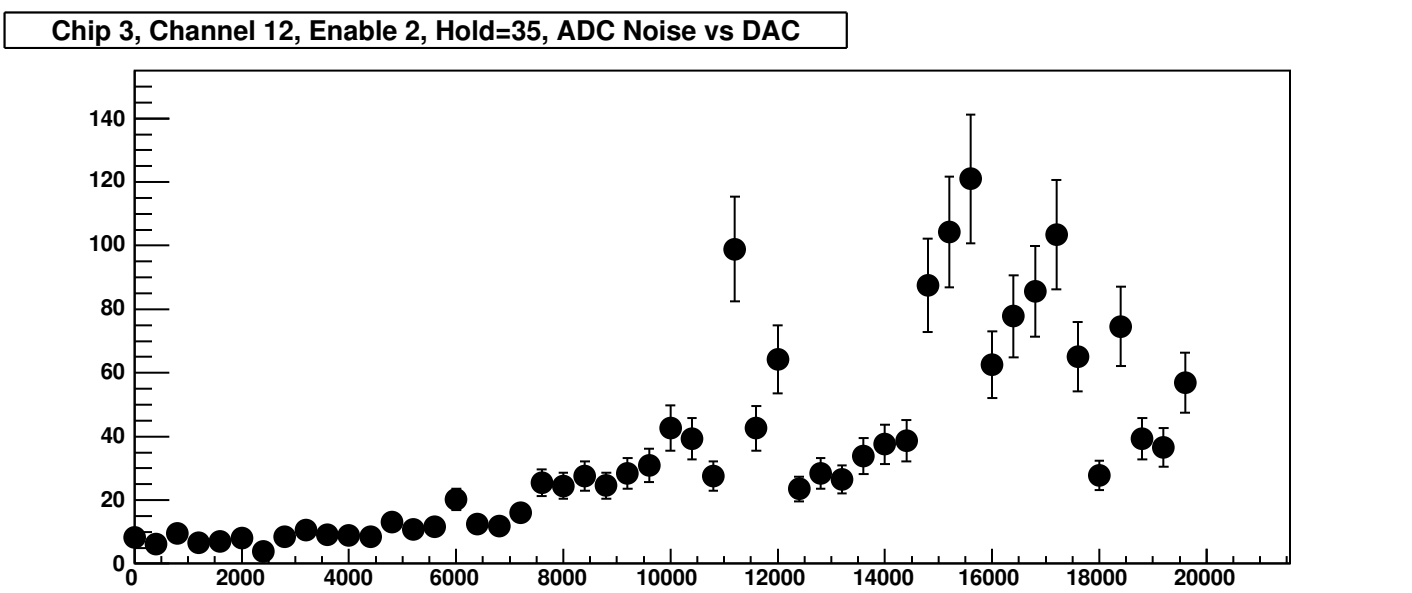

Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

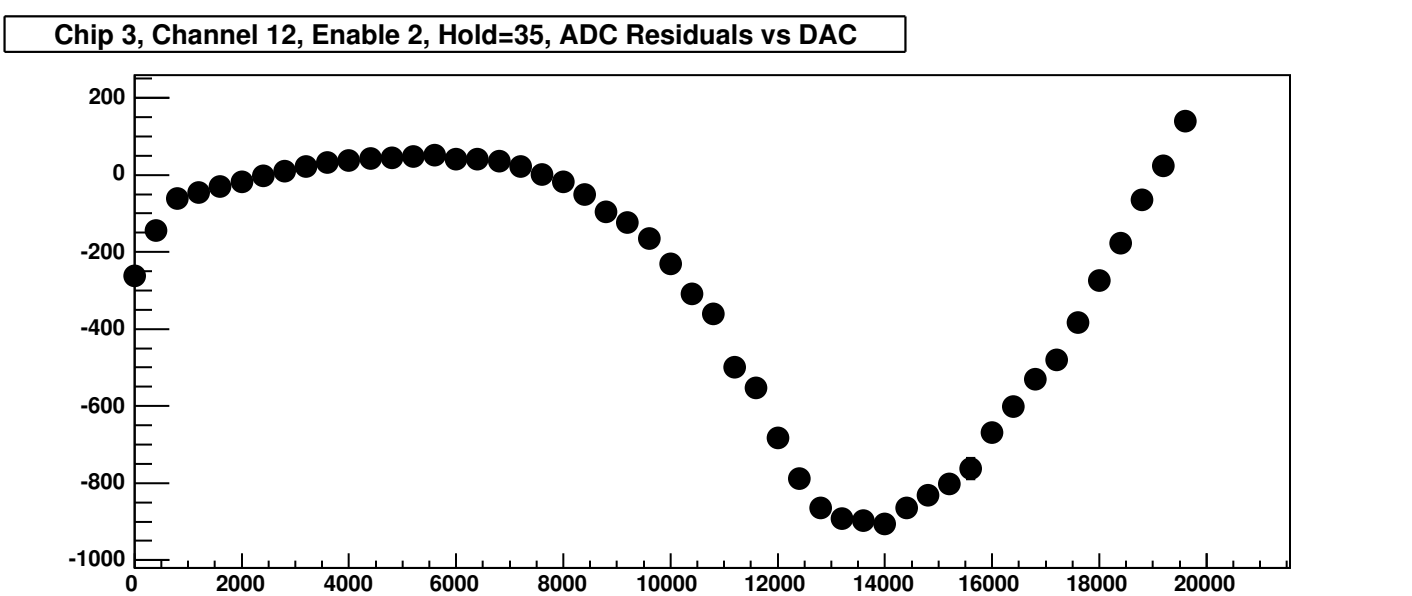

Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.22 / 23
p0 457.5 ± 0.7273
p1 0.01622 ± 0.0001118
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

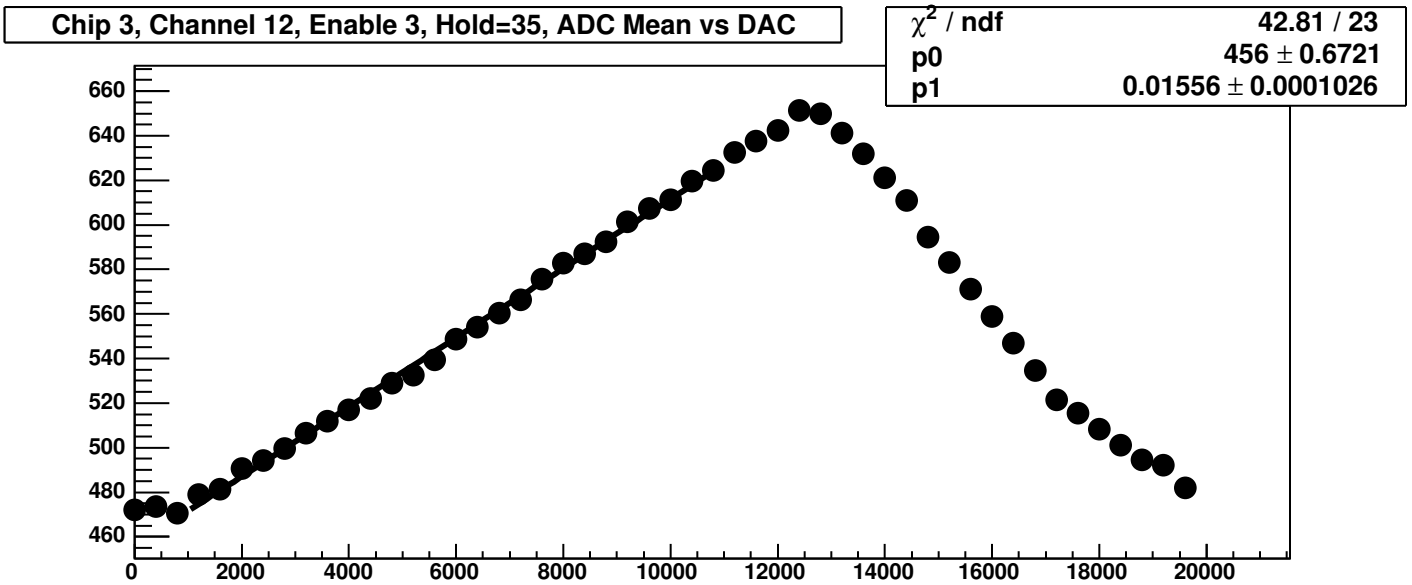

Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

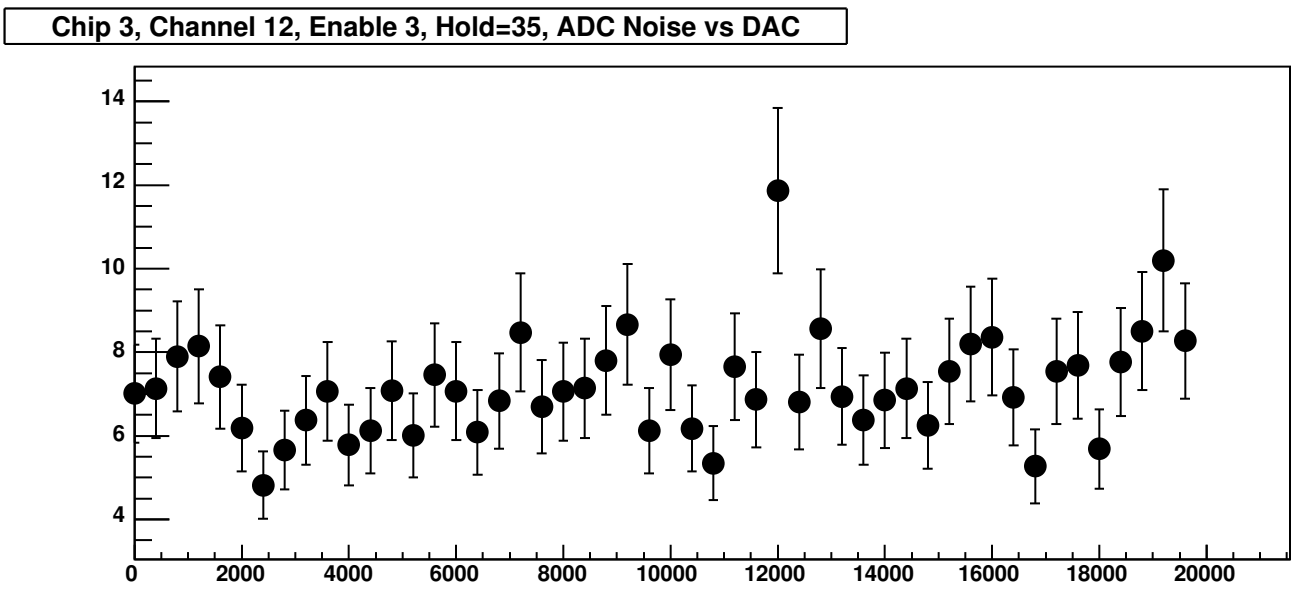

Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

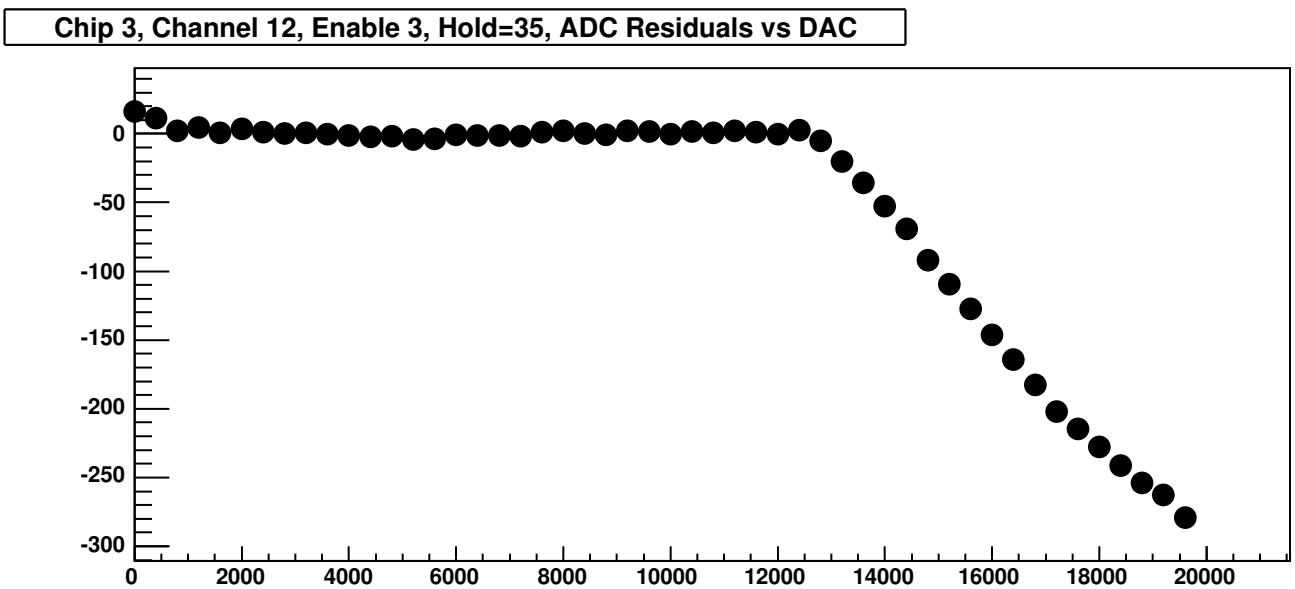

Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.023e+04 / 23
p0 555.3 ± 0.9264
p1 -0.3032 ± 0.0002363
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

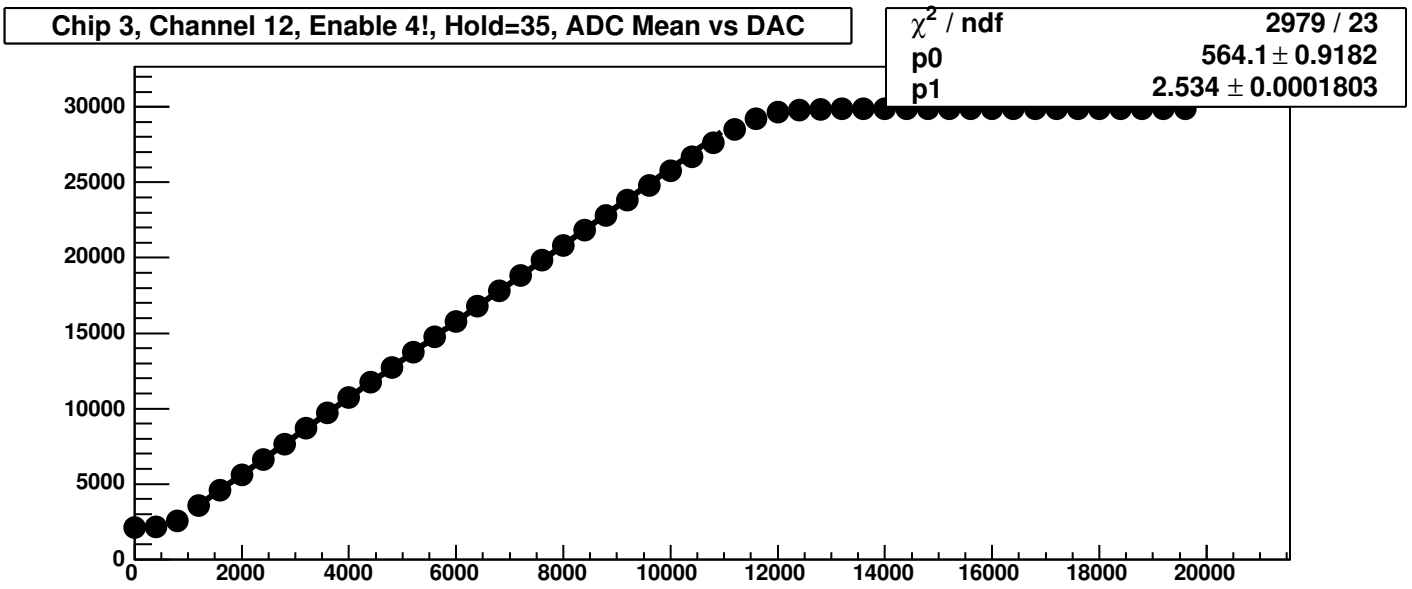

Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

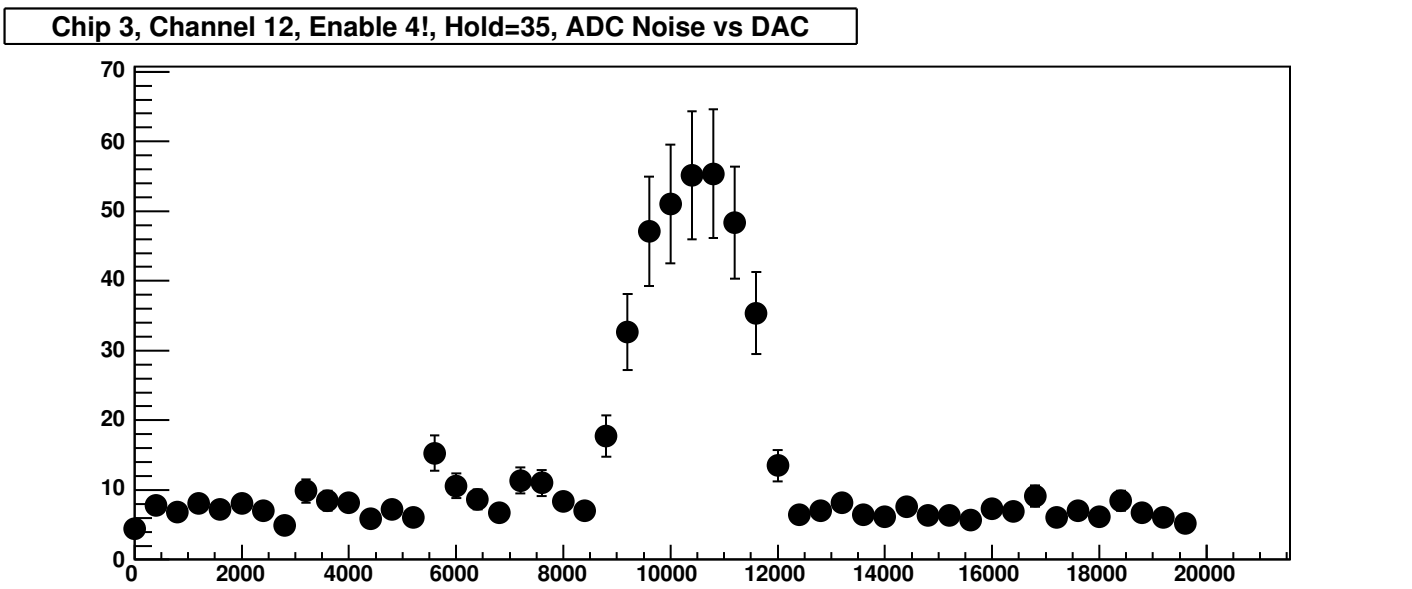

Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

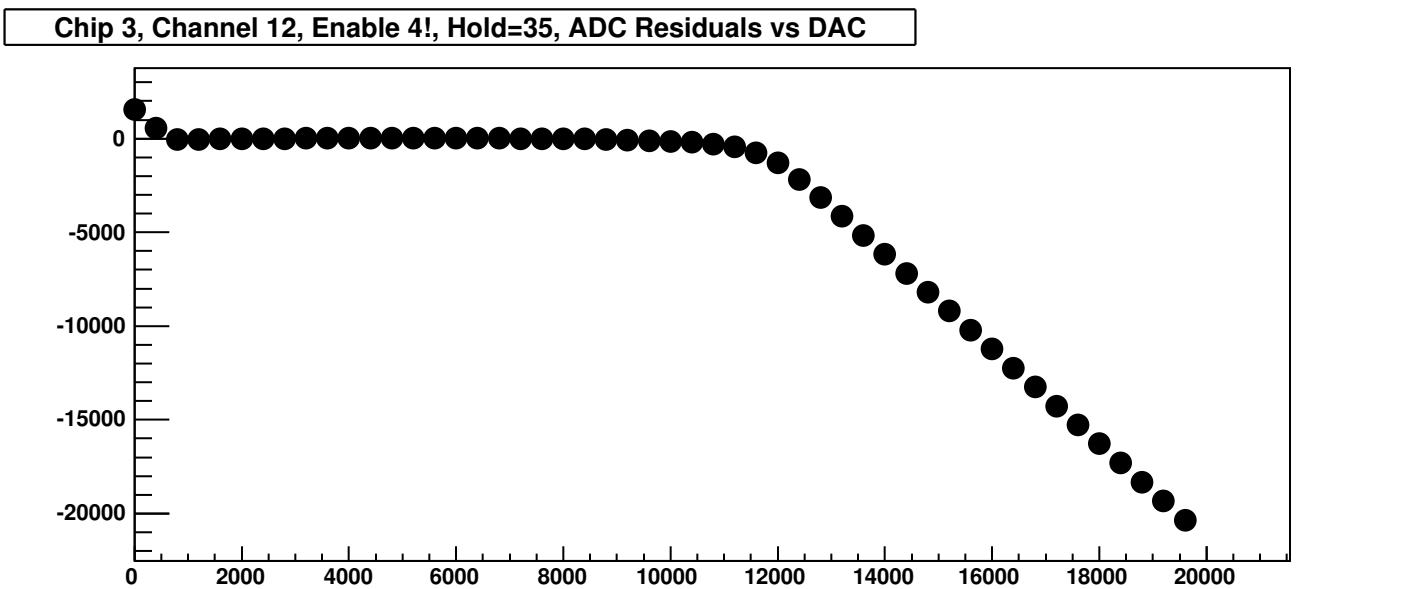

Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.81 / 23
p0 456 ± 0.6721
p1 0.01556 ± 0.0001026
660
640
620
600
580
560
540
520
500
480
460
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

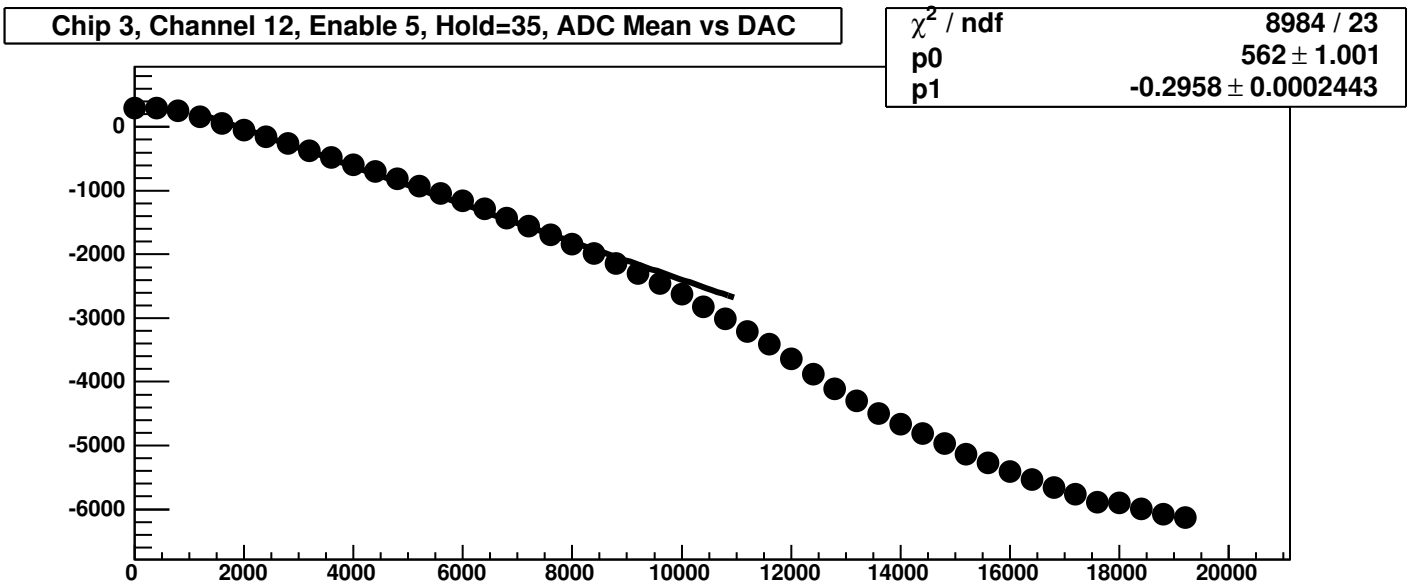

Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

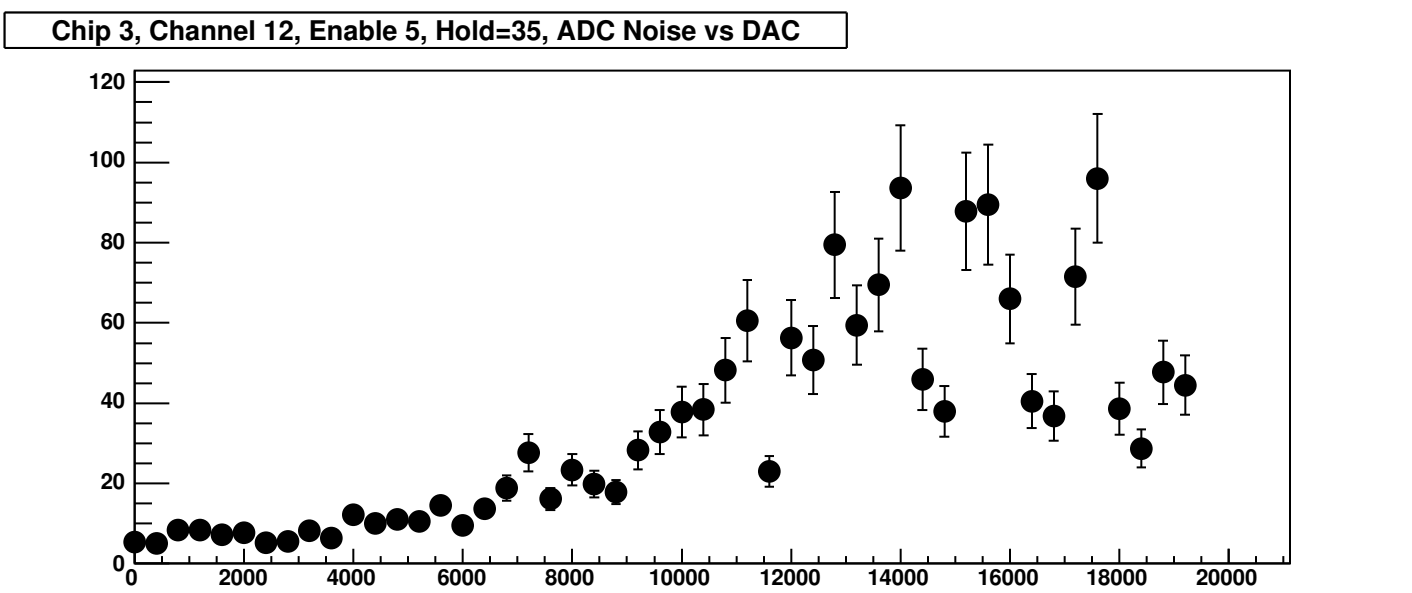

Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

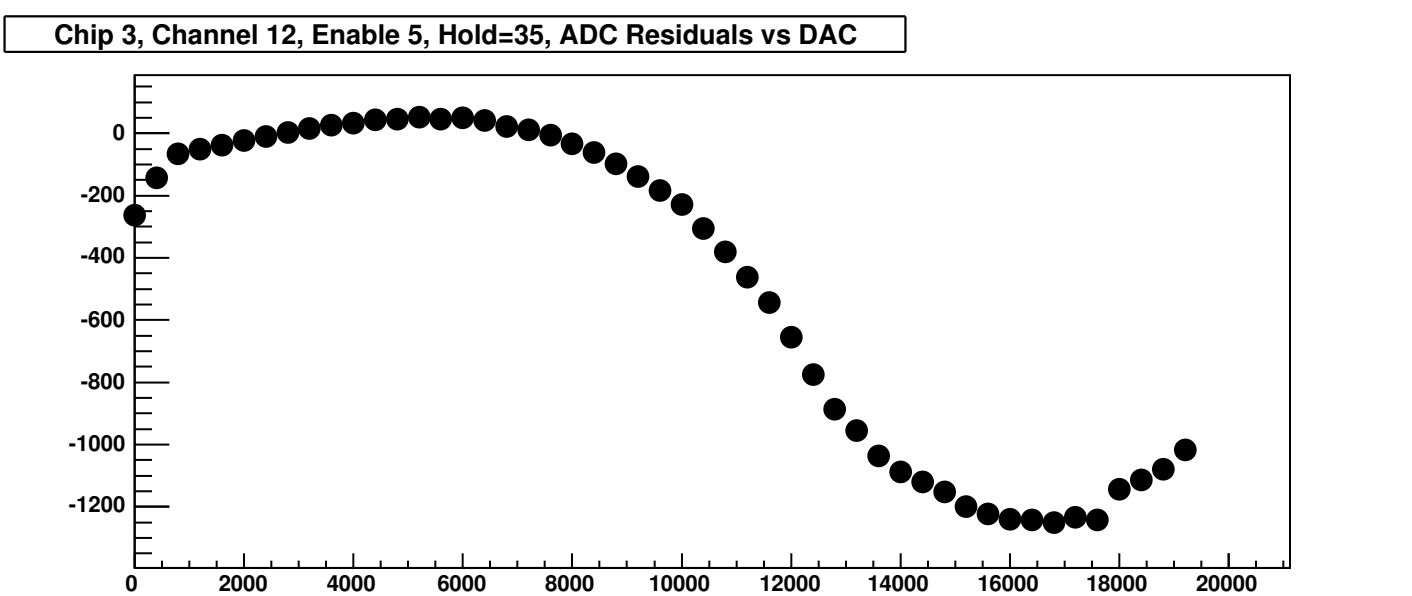

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 2979 / 23
p0 564.1 ± 0.9182
p1 2.534 ± 0.0001803
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

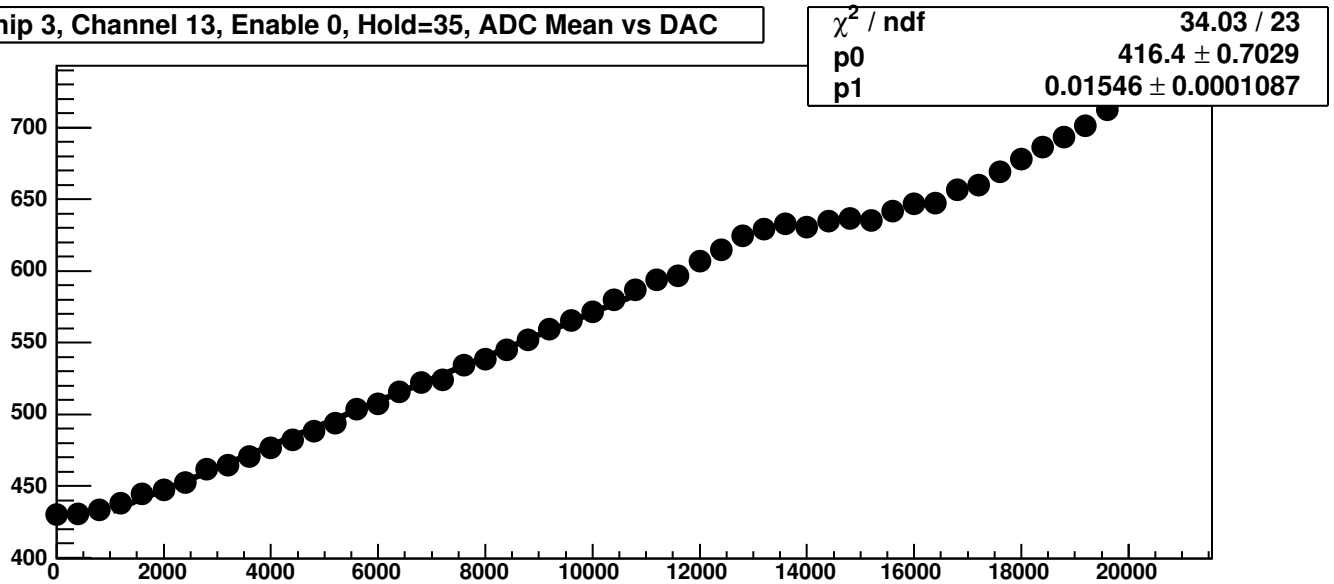

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 8984 / 23
p0 562 ± 1.001
p1 -0.2958 ± 0.0002443
0
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

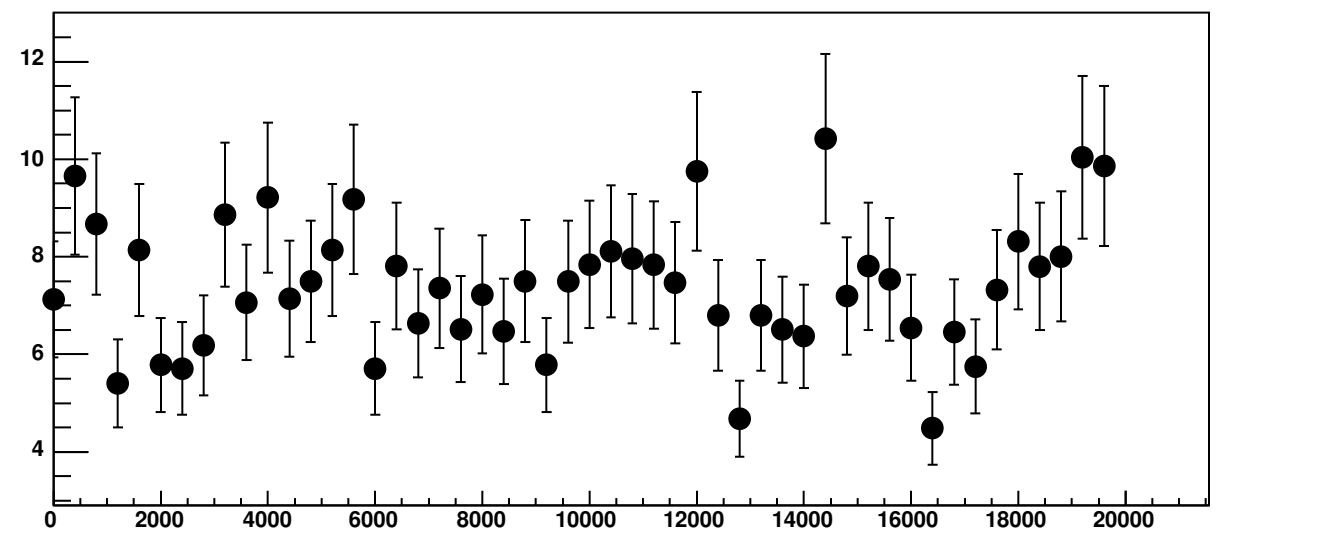

Chip 3, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
-1000
-1200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

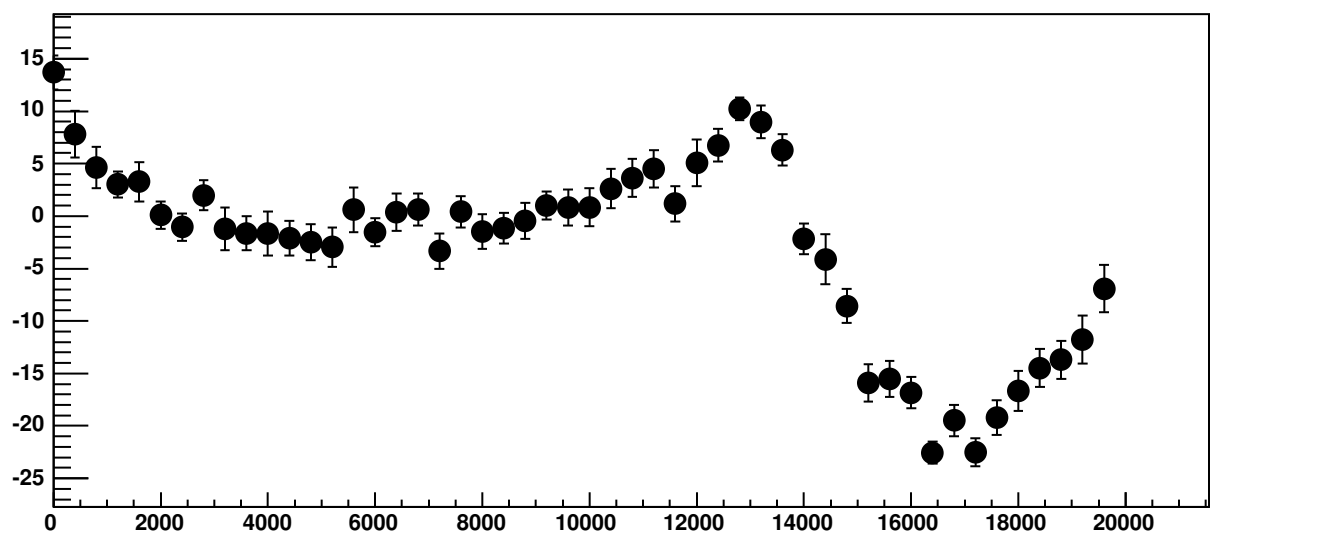
Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

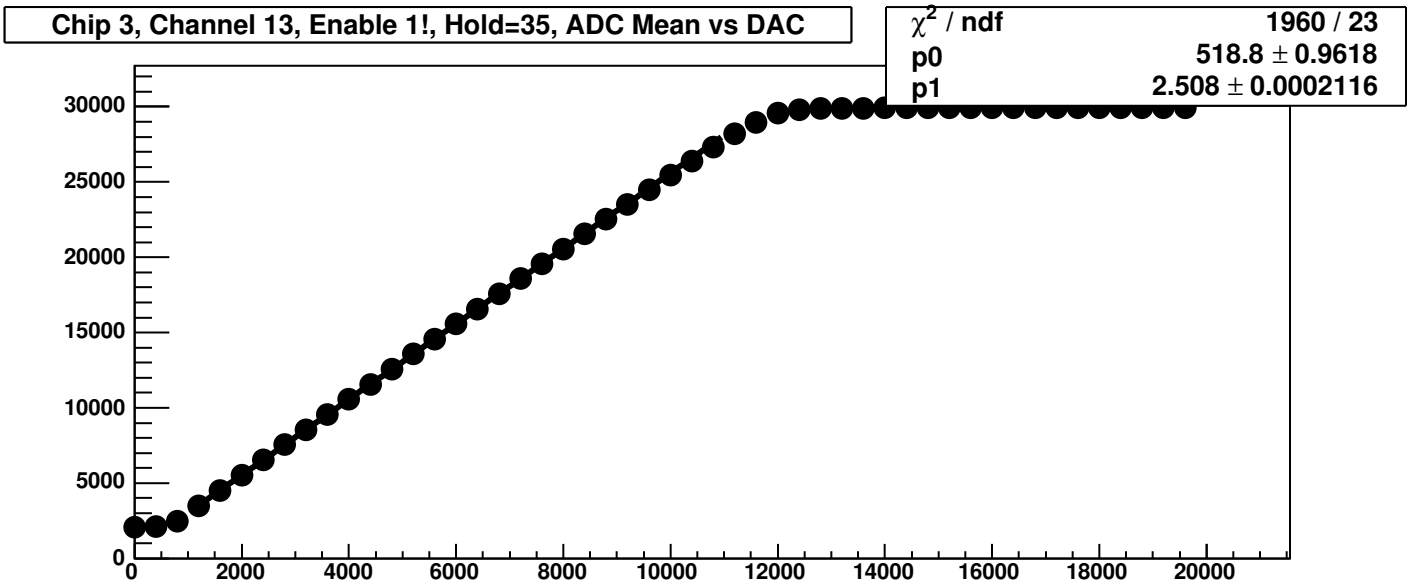


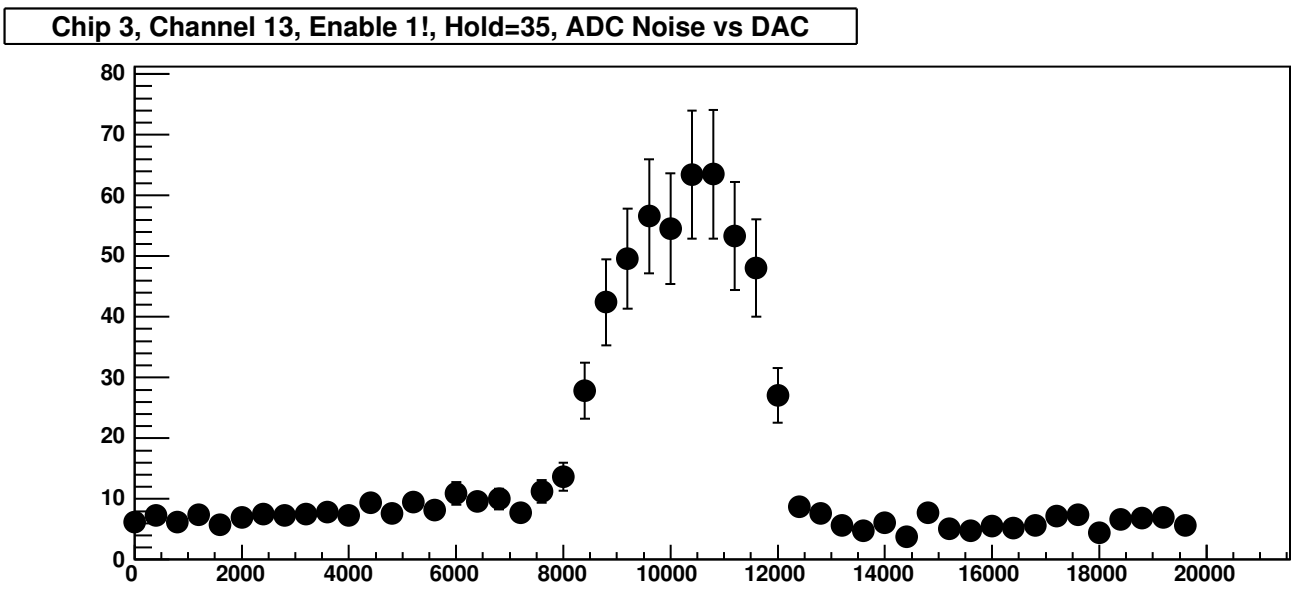
Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

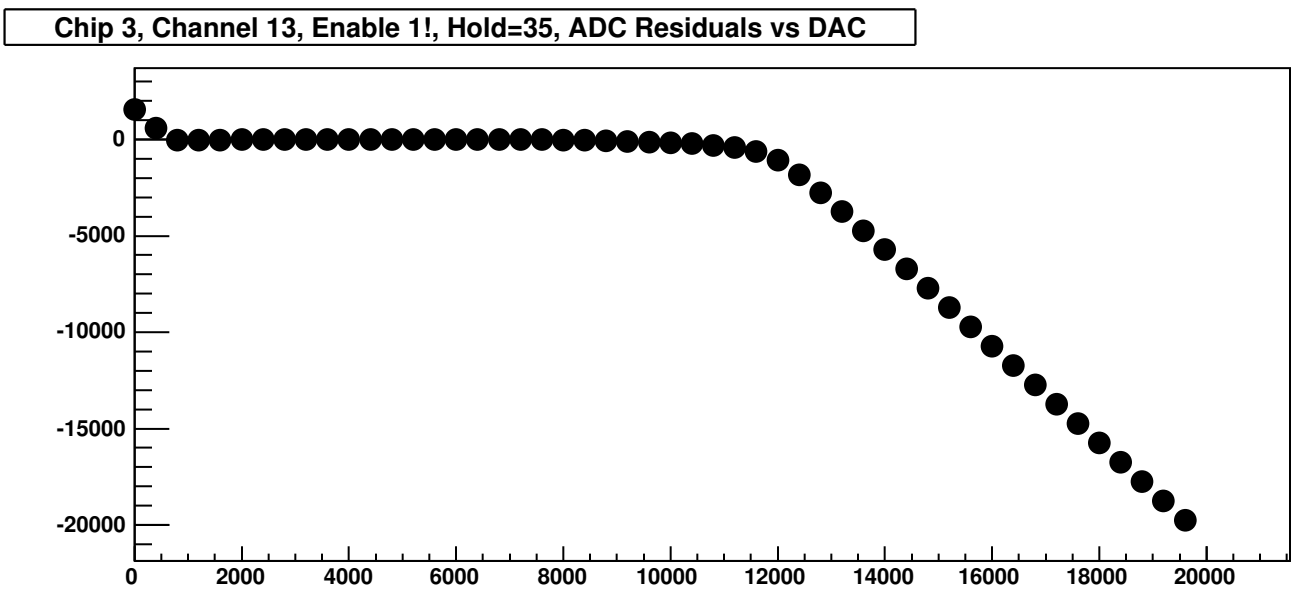


Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



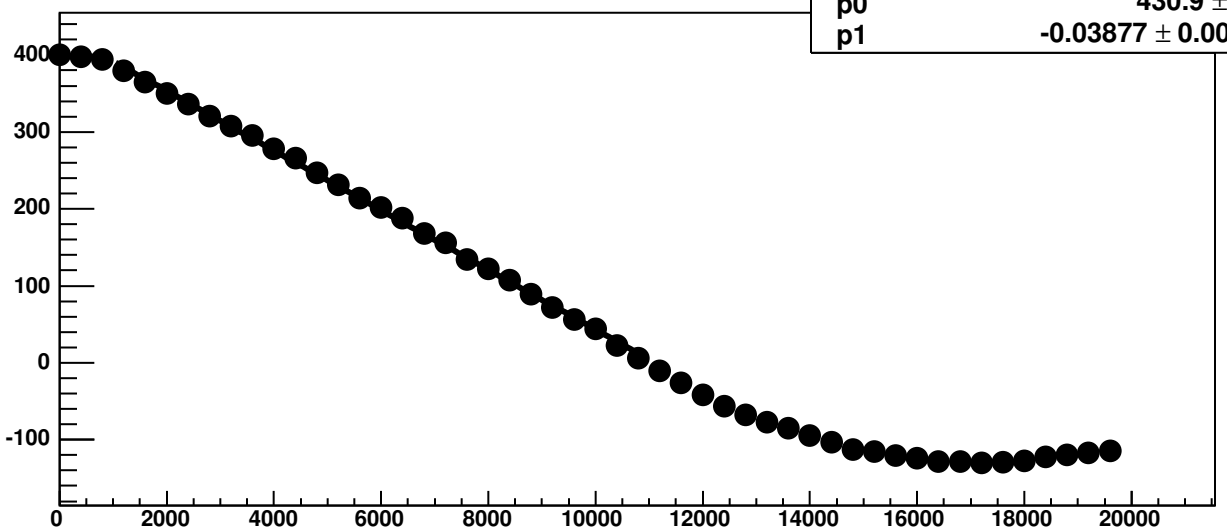

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1960 / 23
p0 518.8 ± 0.9618
p1 2.508 ± 0.0002116
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

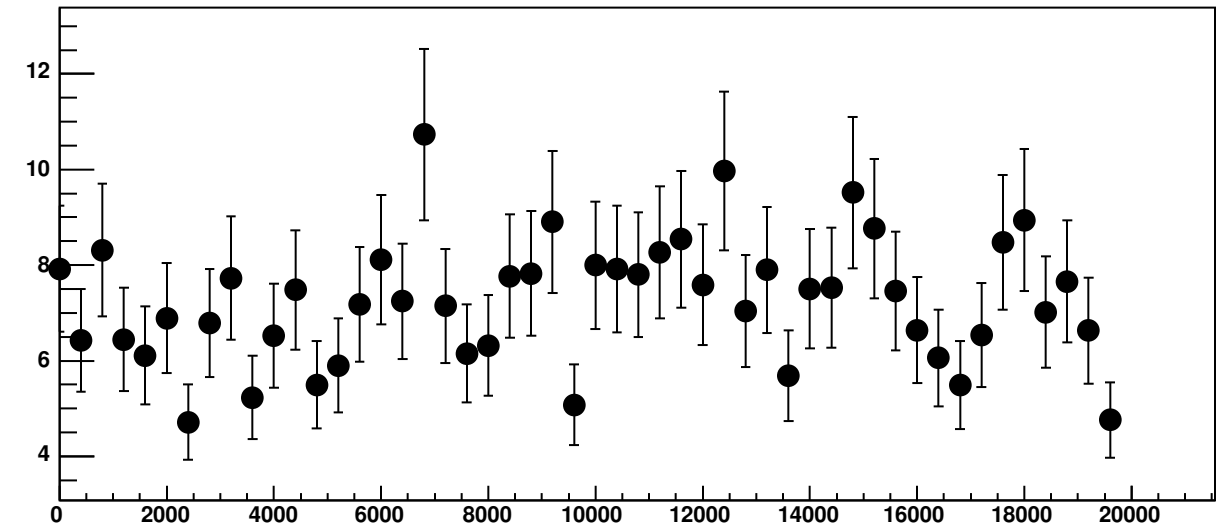

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

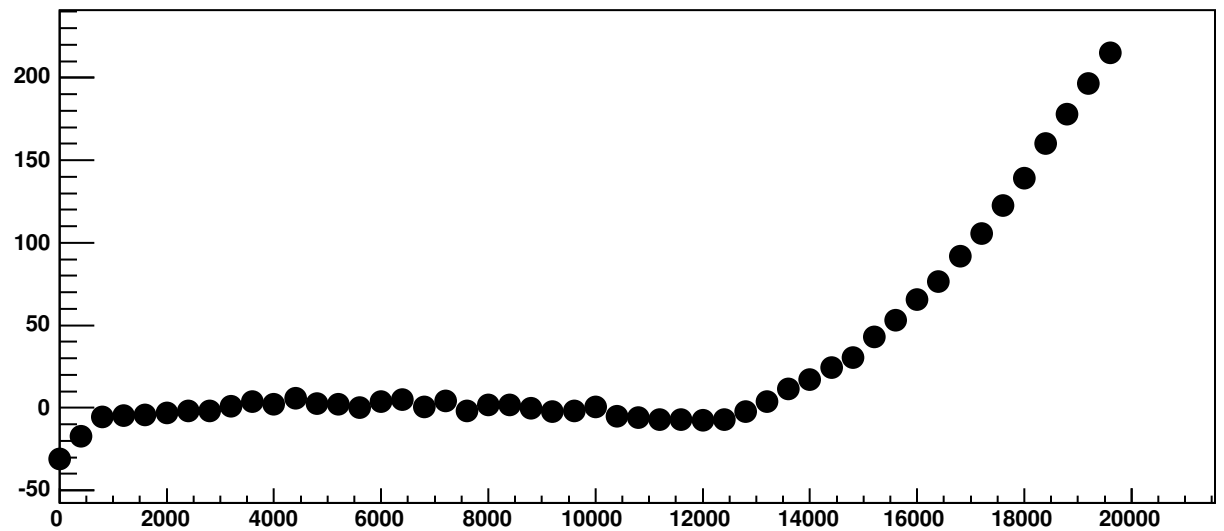
χ^2 / ndf	105.7 / 23
p0	430.9 ± 0.669
p1	-0.03877 ± 0.0001061

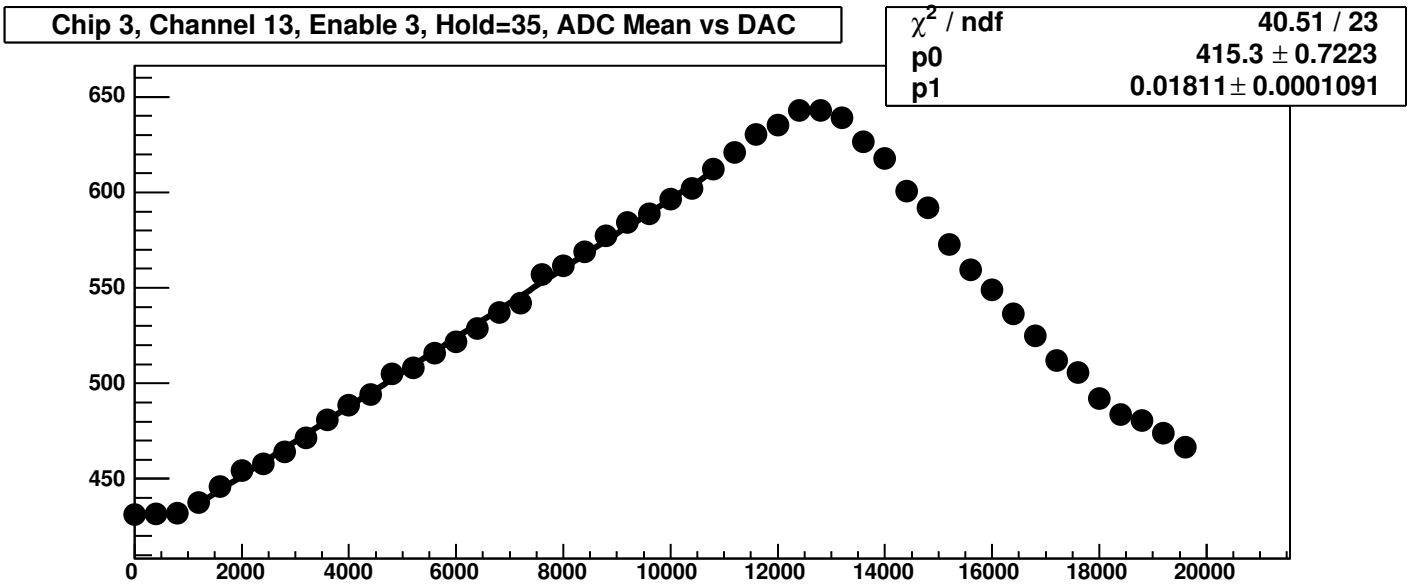


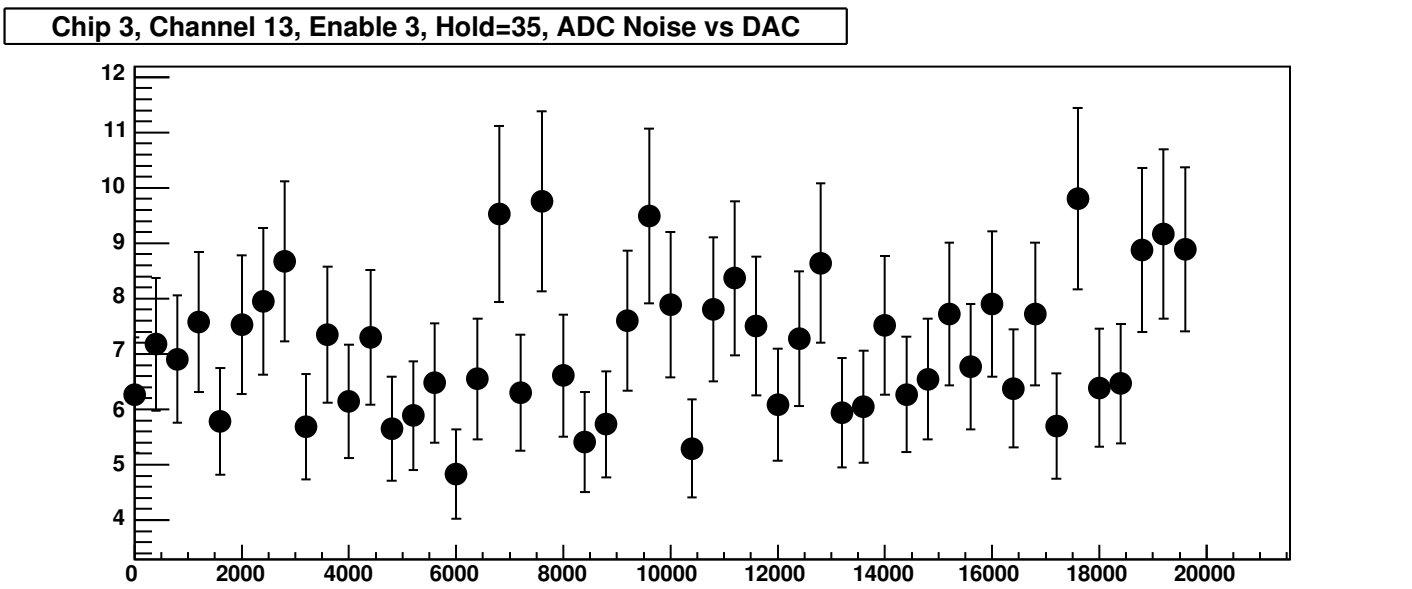
Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC

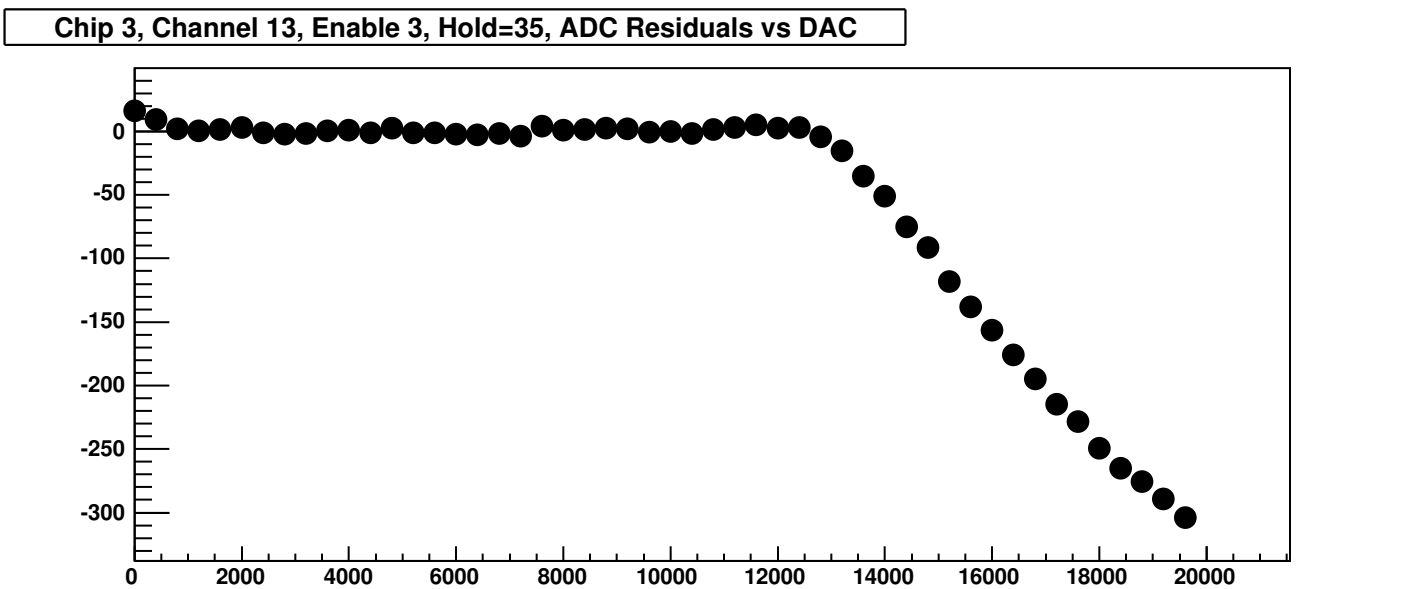


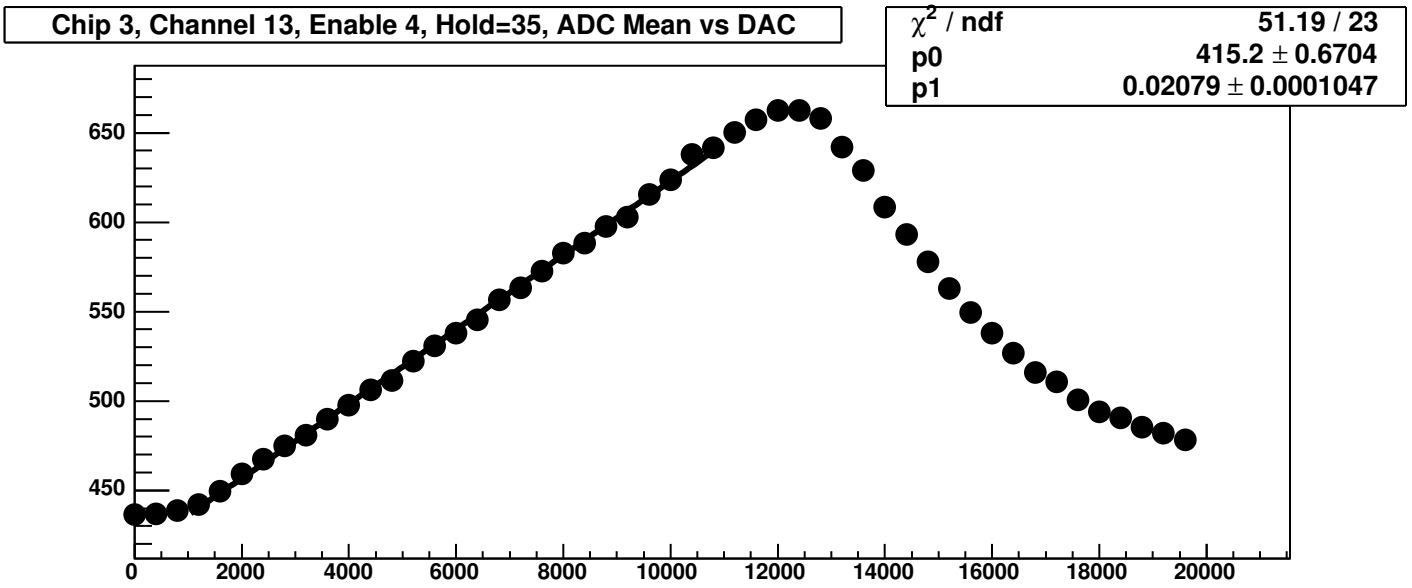
Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

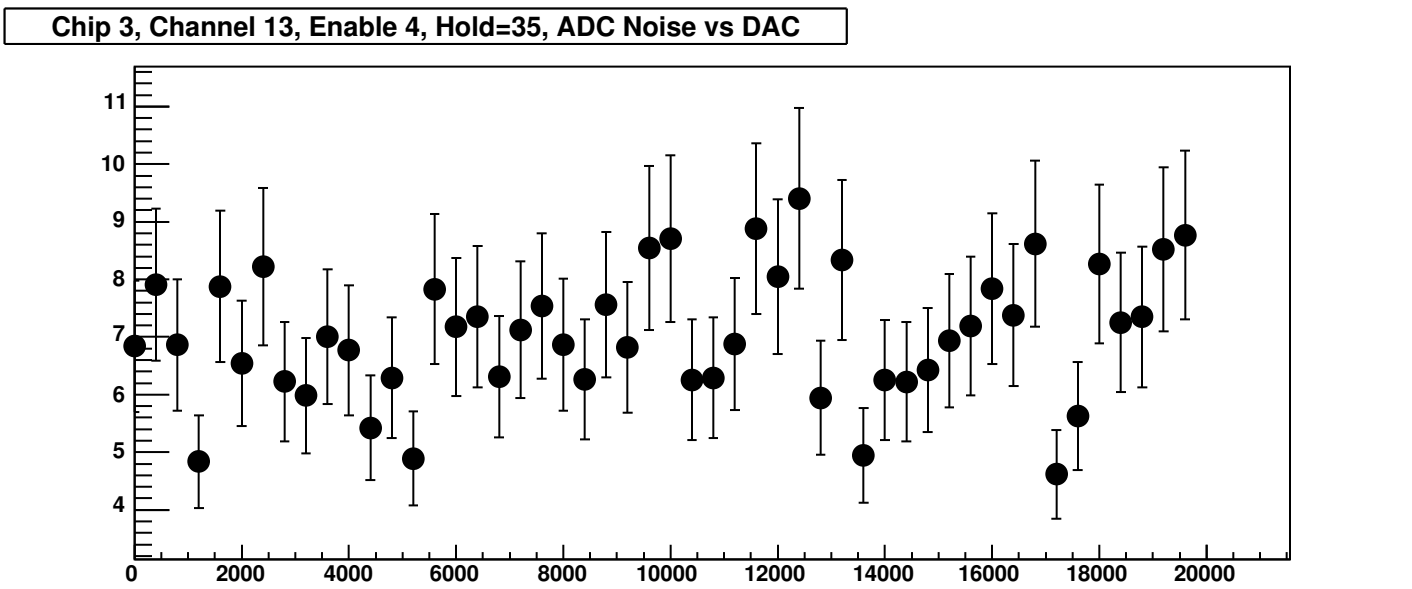


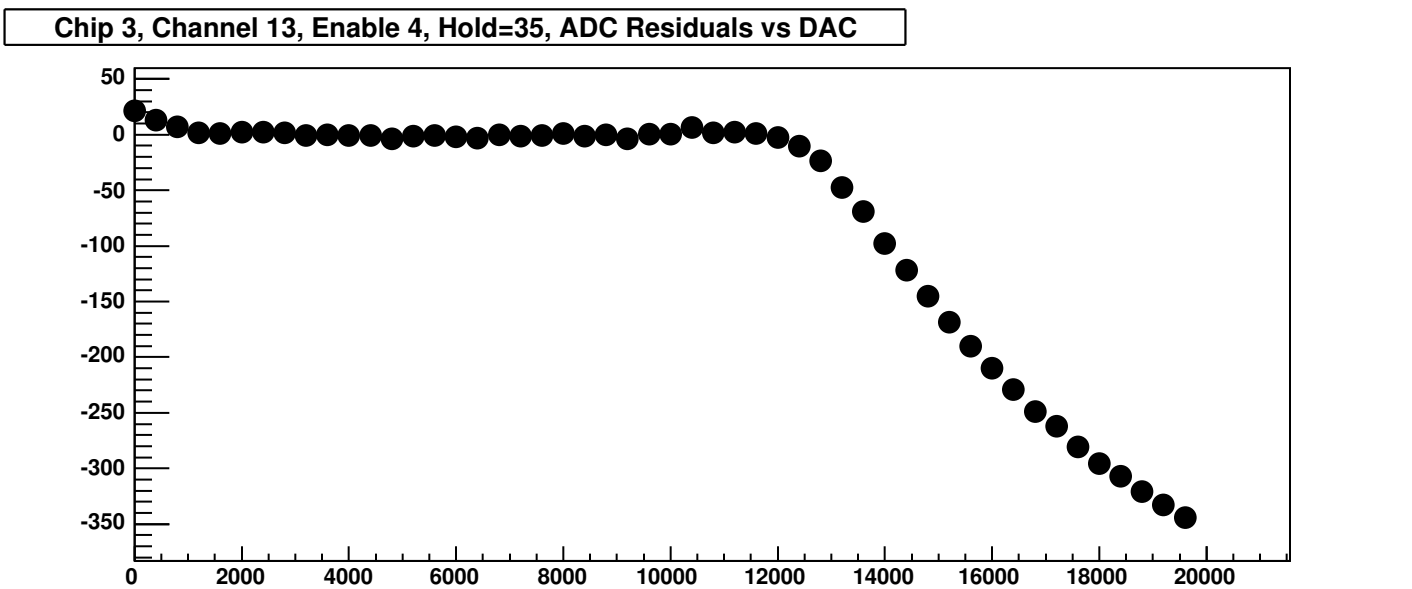

Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.51 / 23
p0 415.3 ± 0.7223
p1 0.01811 ± 0.0001091
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

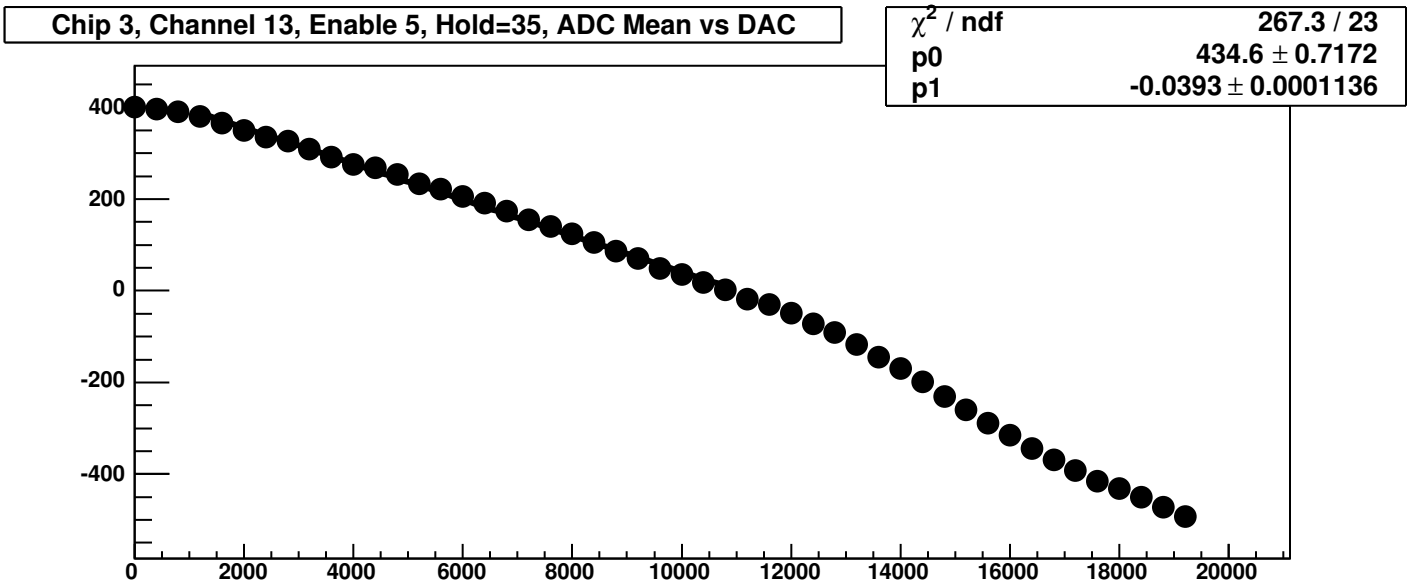

Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

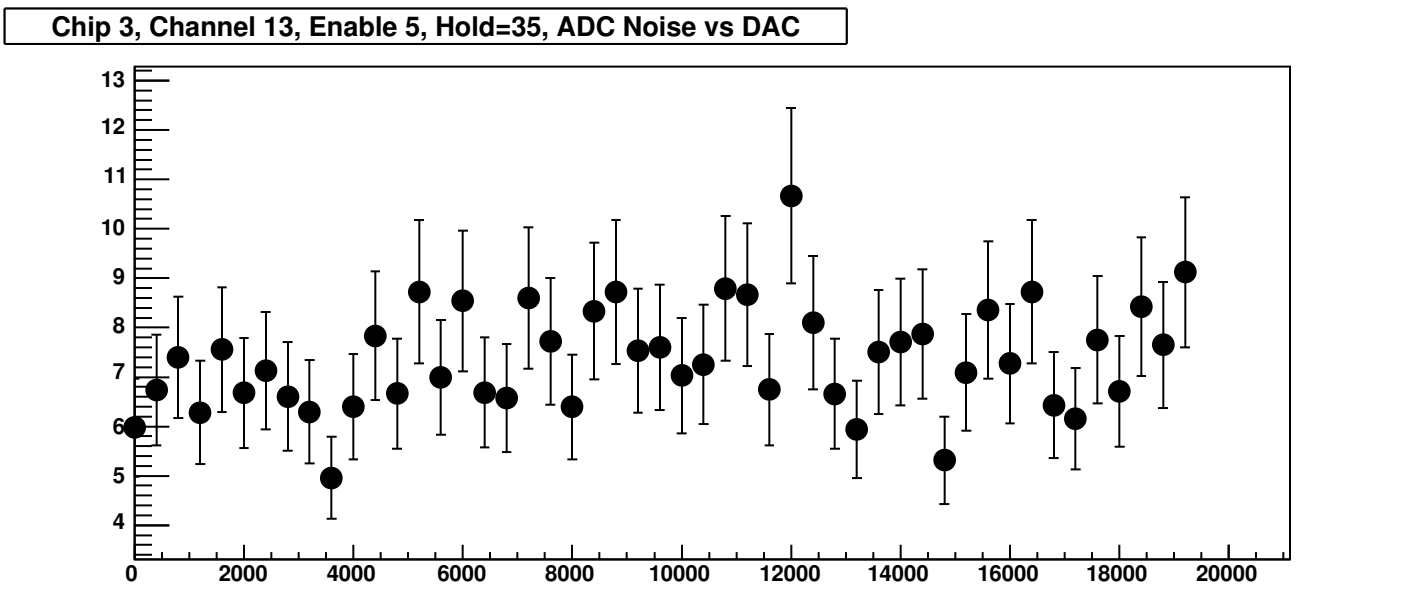

Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

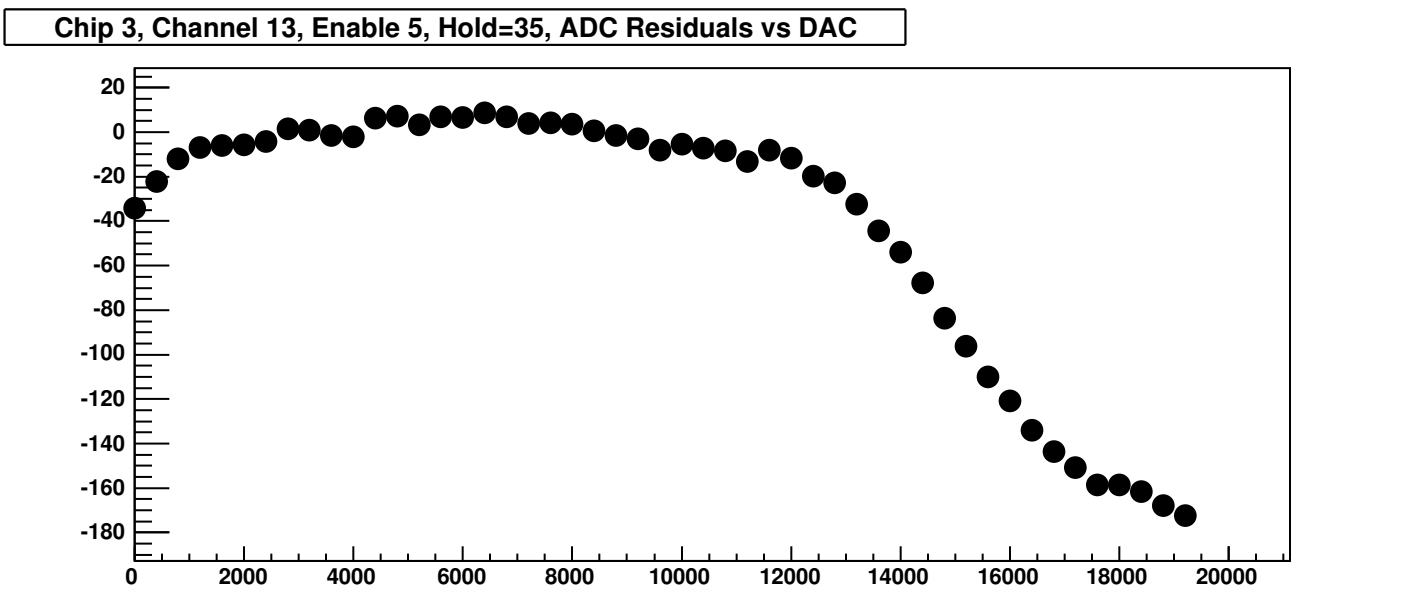

Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 51.19 / 23
p0 415.2 ± 0.6704
p1 0.02079 ± 0.0001047
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

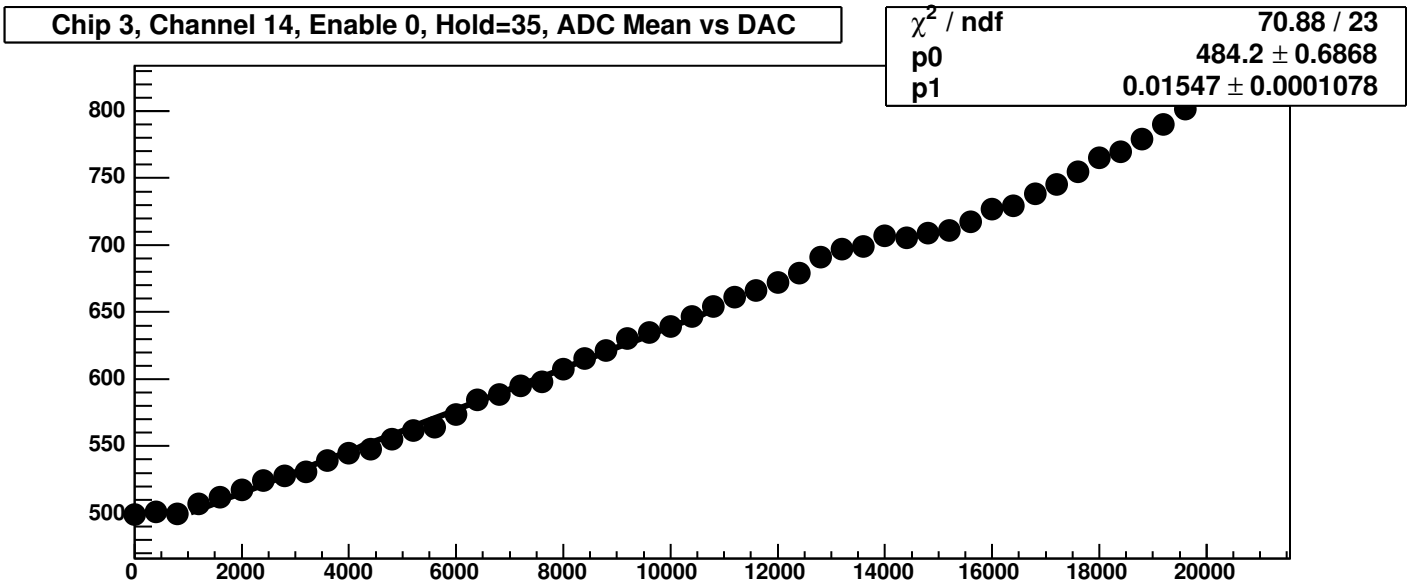

Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

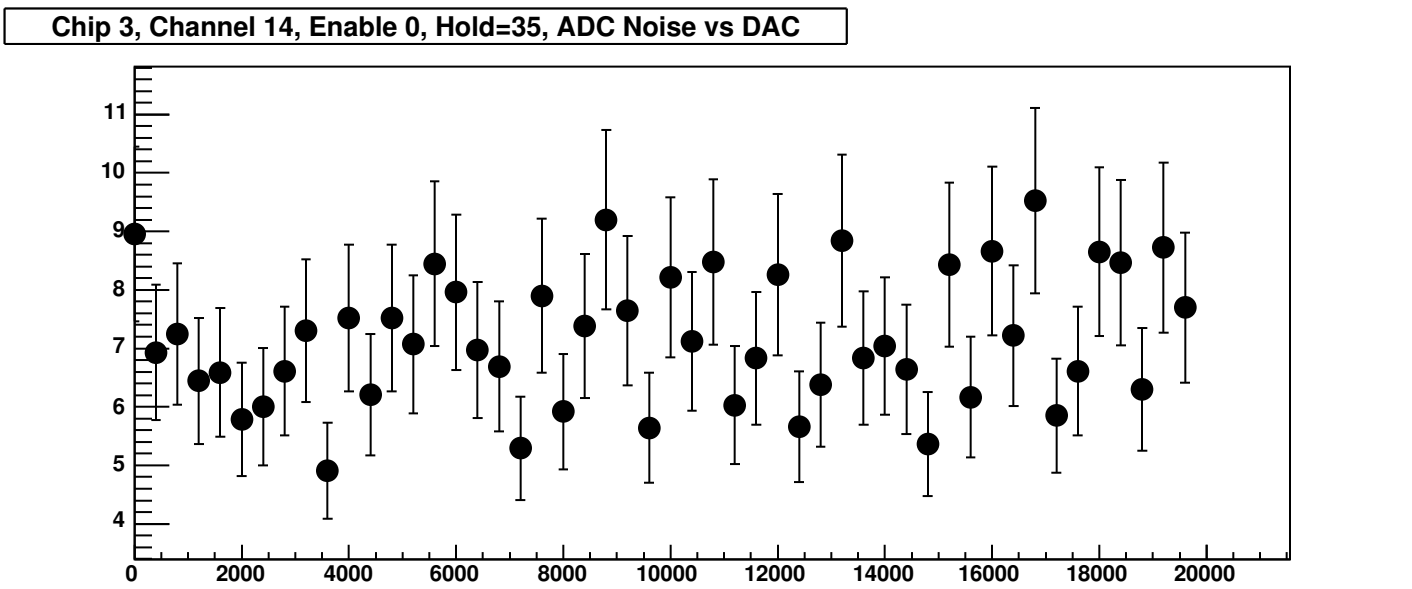

Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

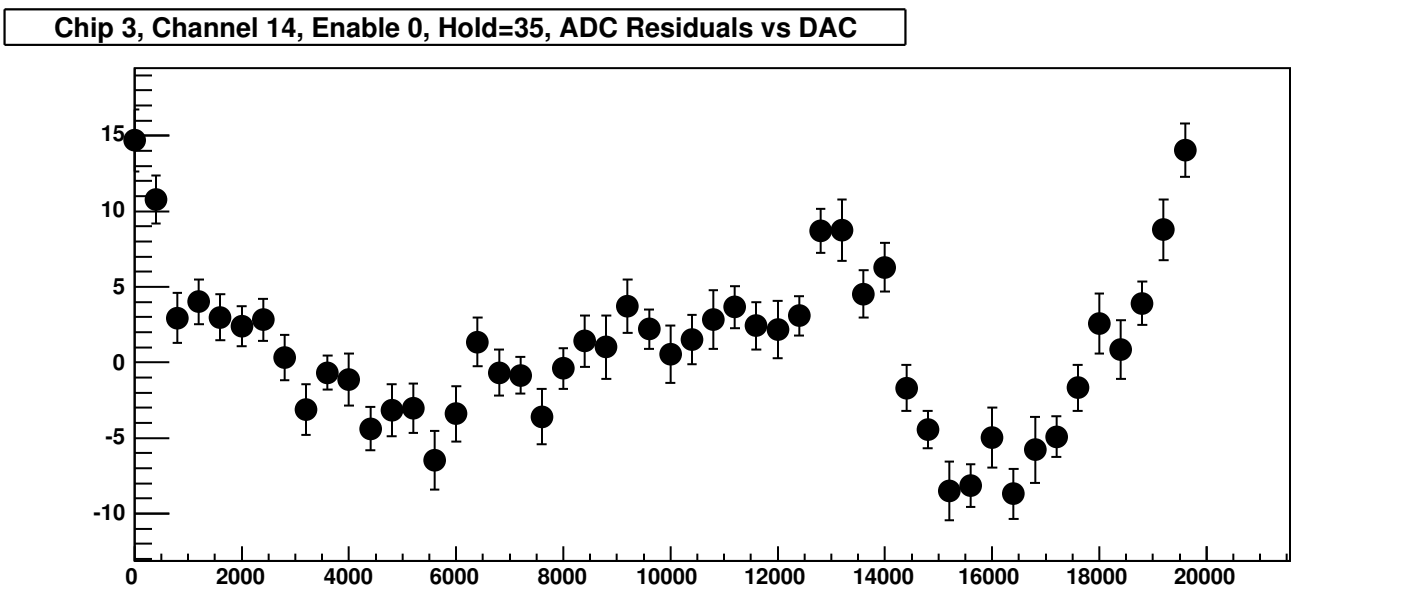

Chip 3, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 267.3 / 23
p0 434.6 ± 0.7172
p1 -0.0393 ± 0.0001136
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

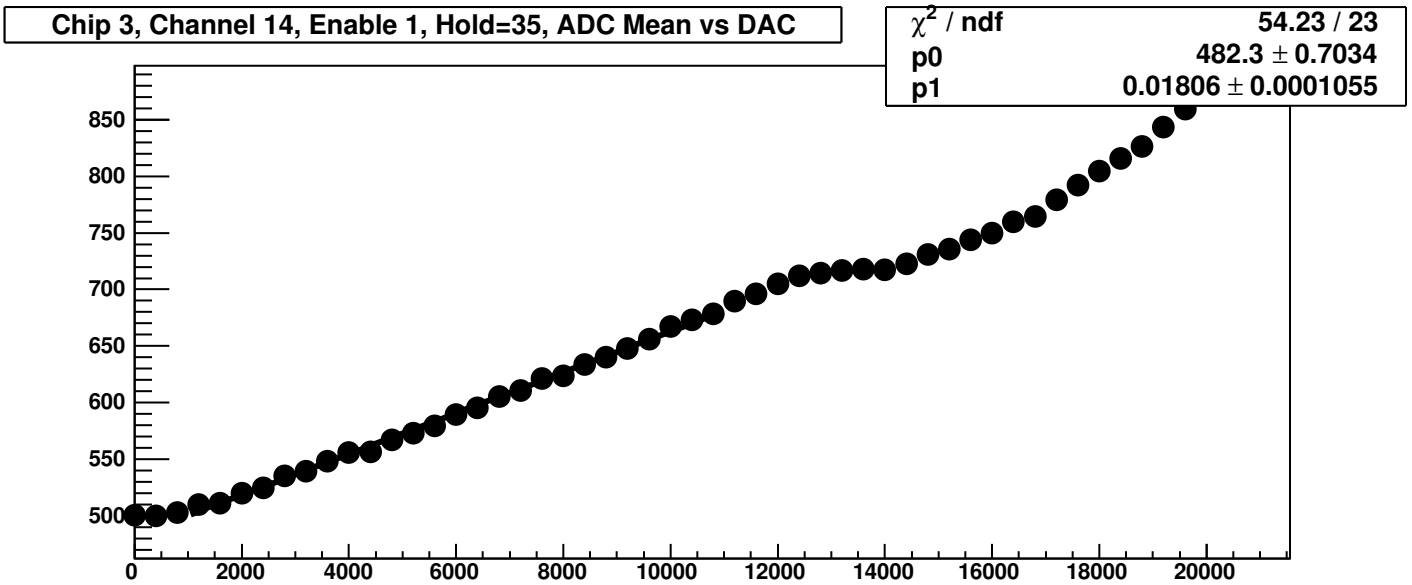

Chip 3, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

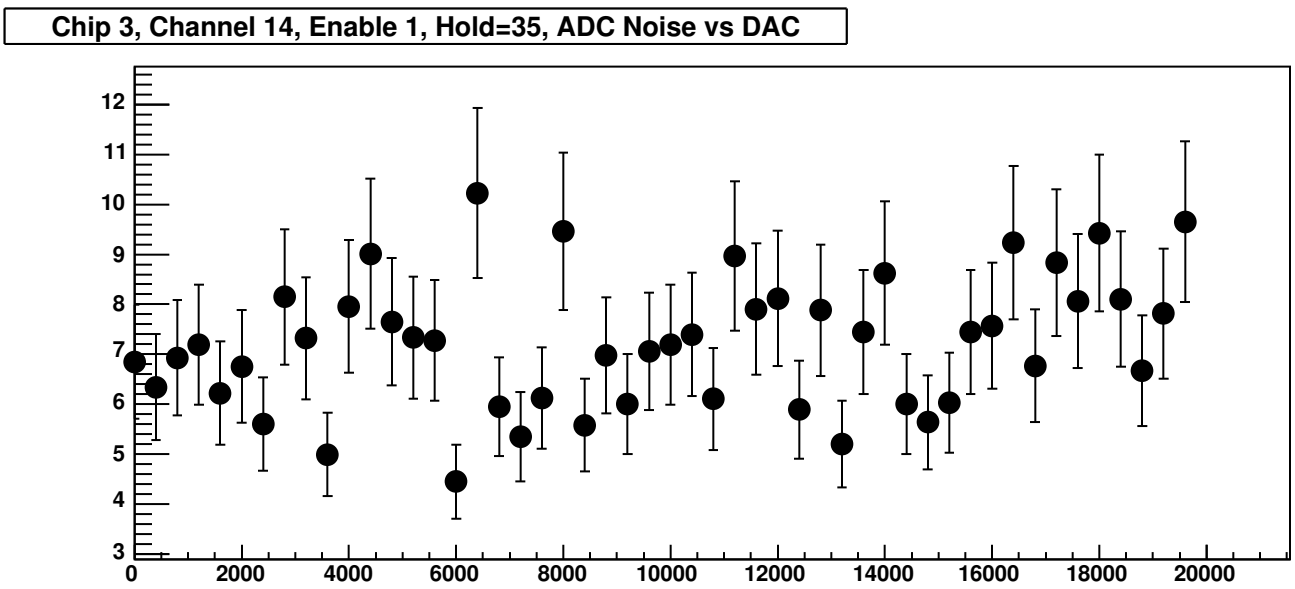

Chip 3, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

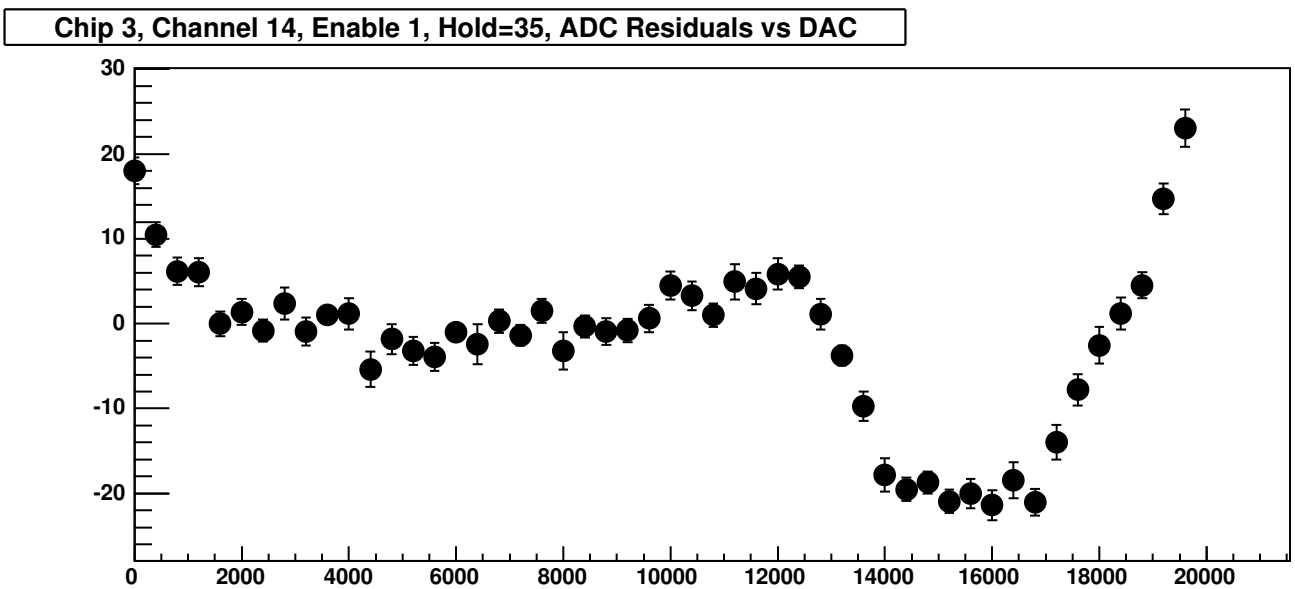

Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 70.88 / 23
p0 484.2 ± 0.6868
p1 0.01547 ± 0.0001078
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

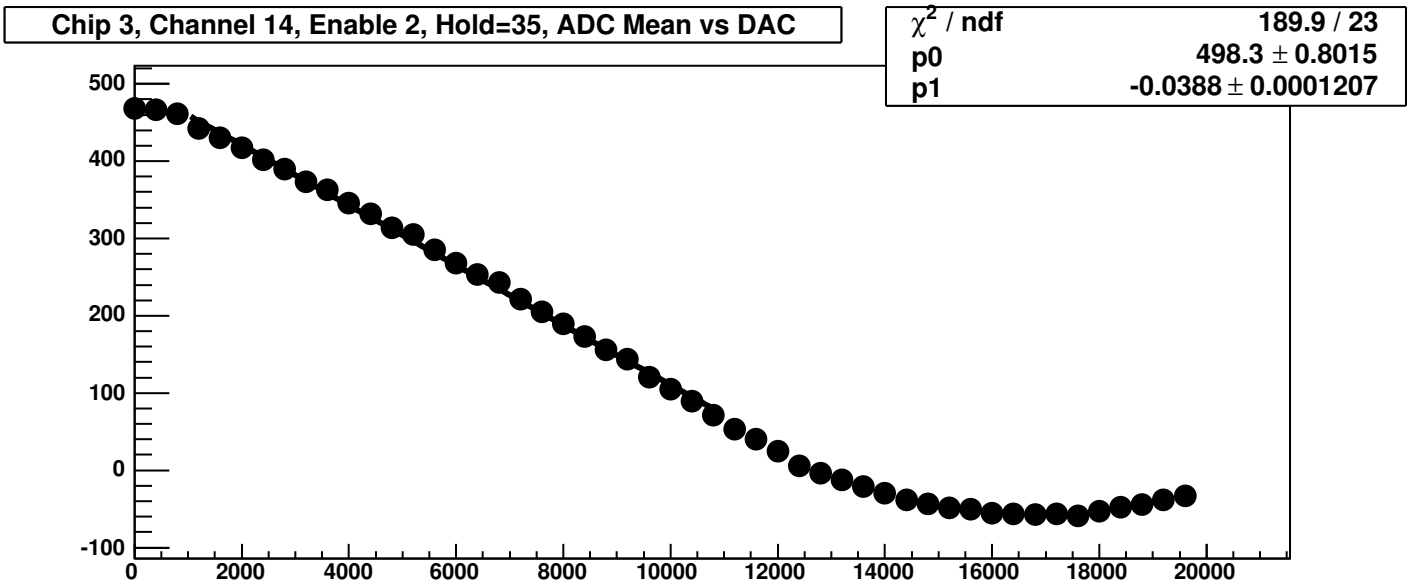

Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

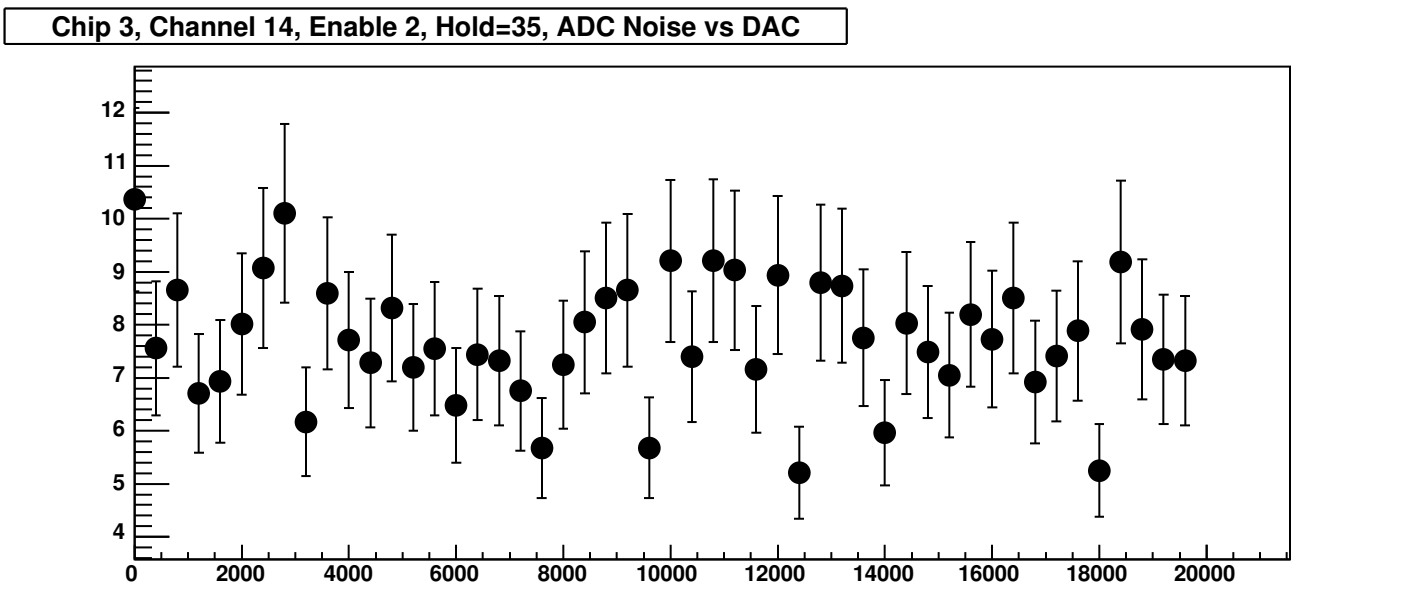

Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

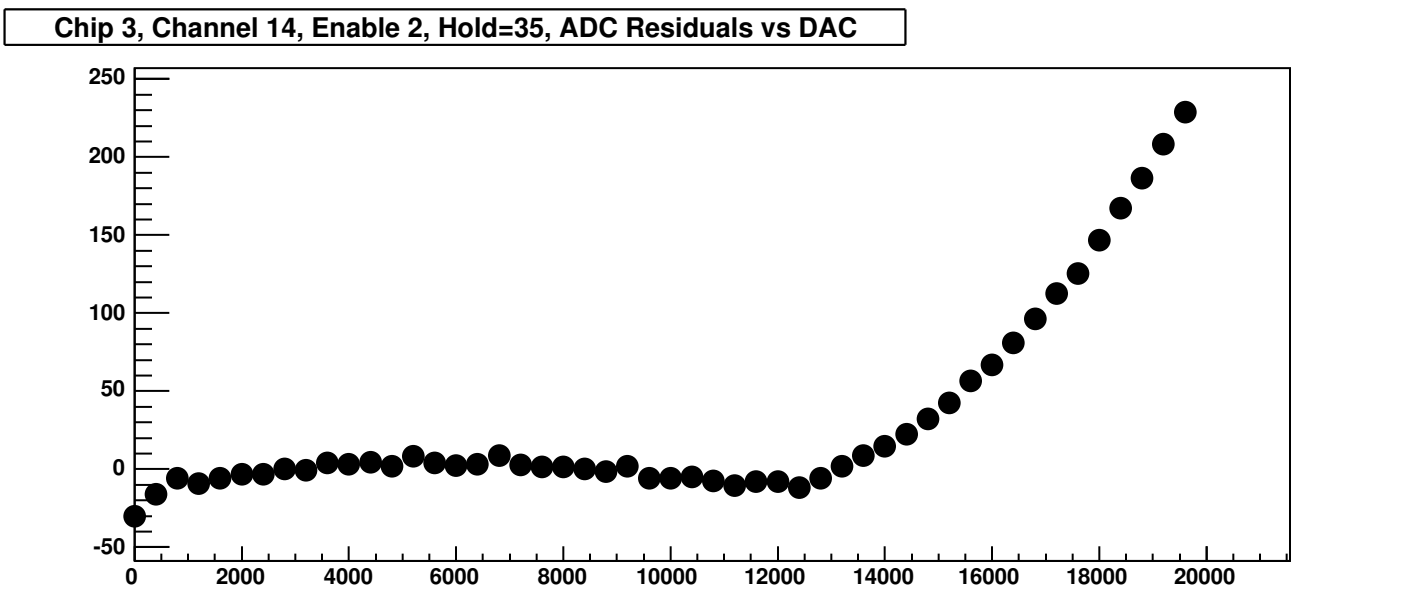

Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 54.23 / 23
p0 482.3 ± 0.7034
p1 0.01806 ± 0.0001055
850
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

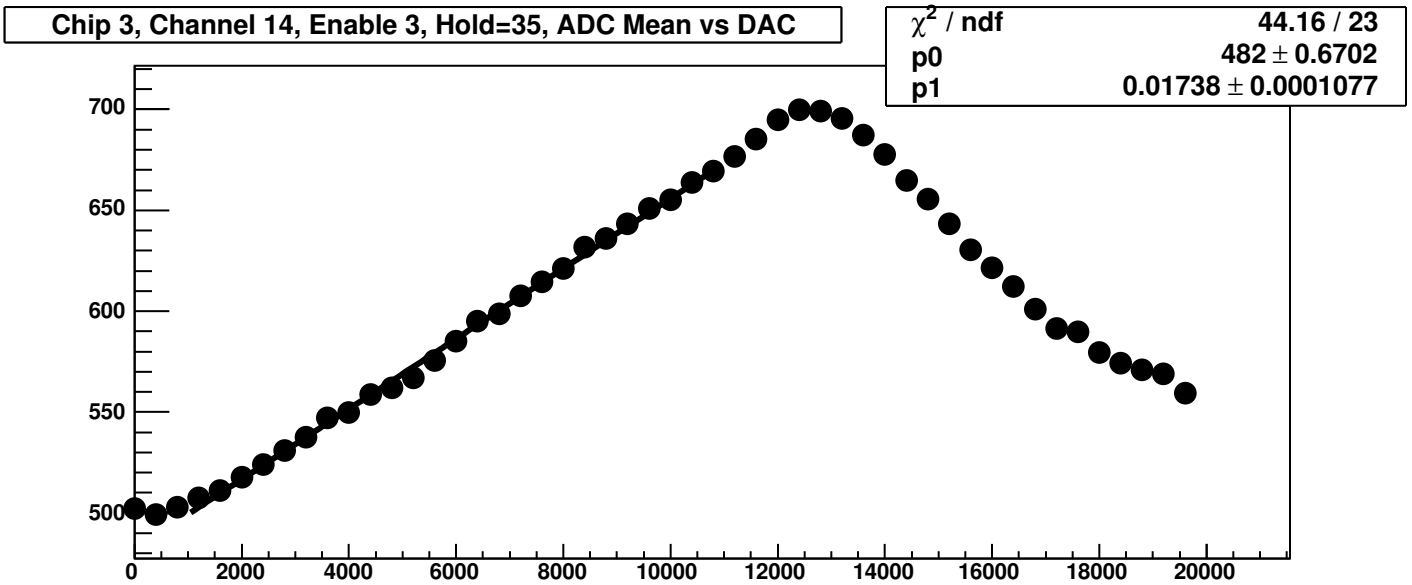

Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

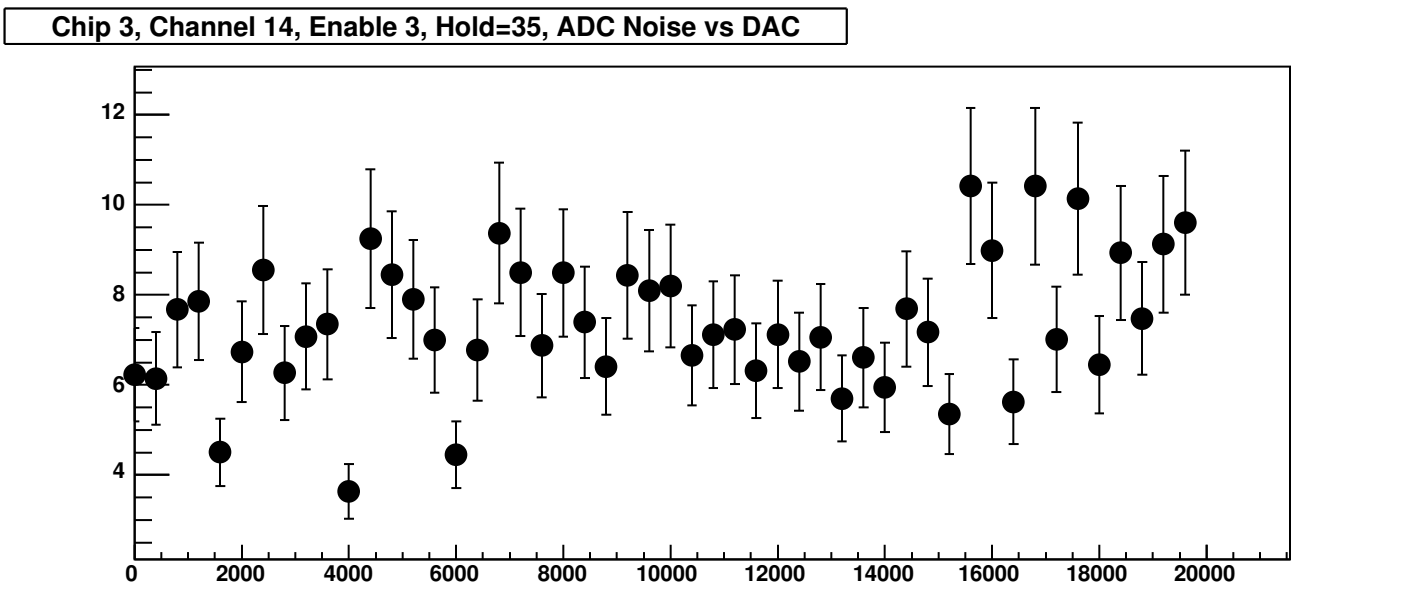

Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

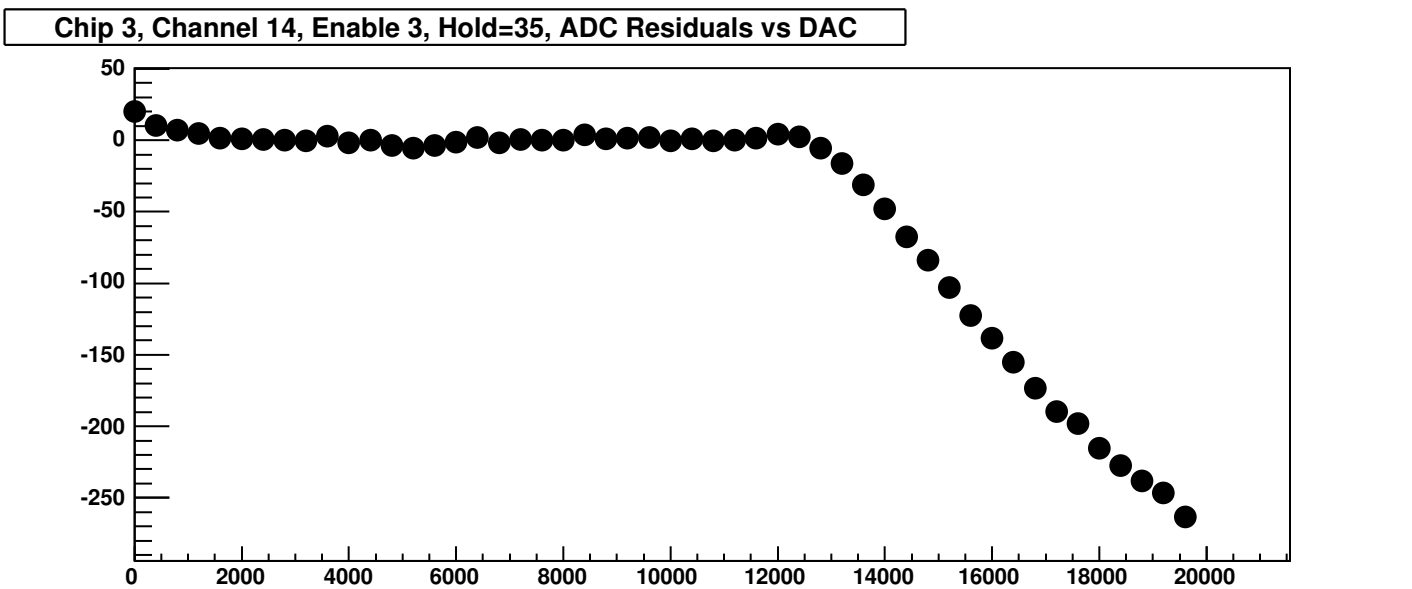

Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 189.9 / 23
p0 498.3 ± 0.8015
p1 -0.0388 ± 0.0001207
500
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

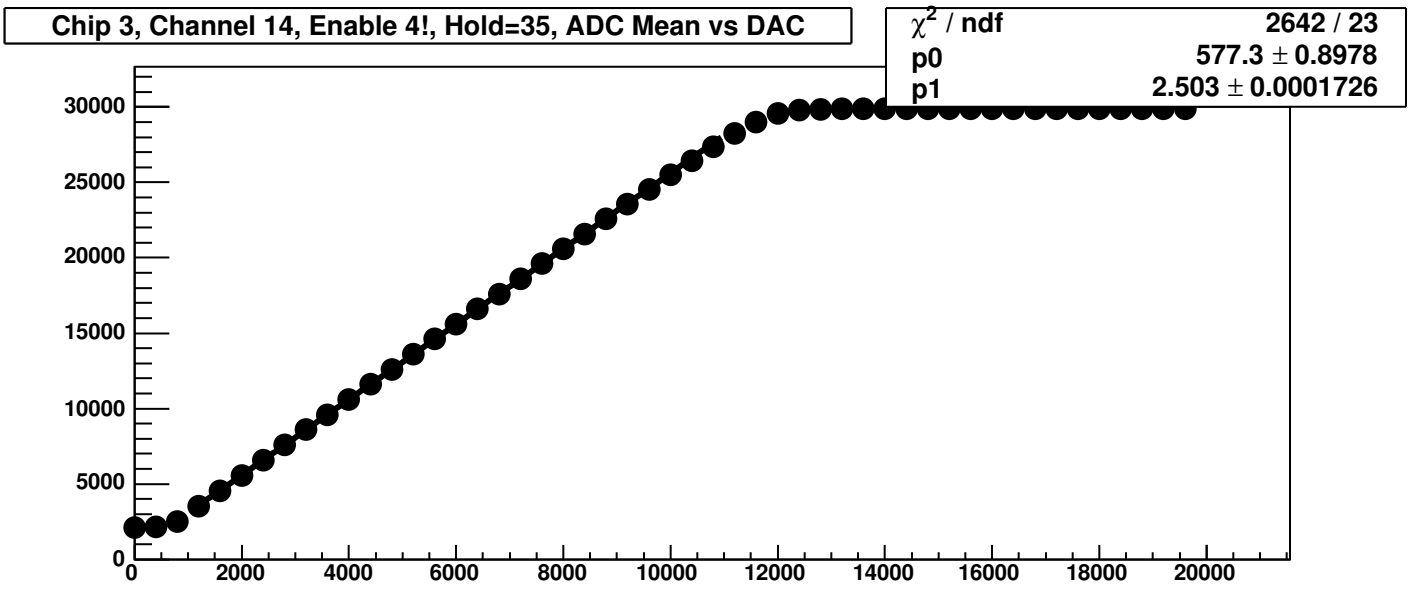

Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

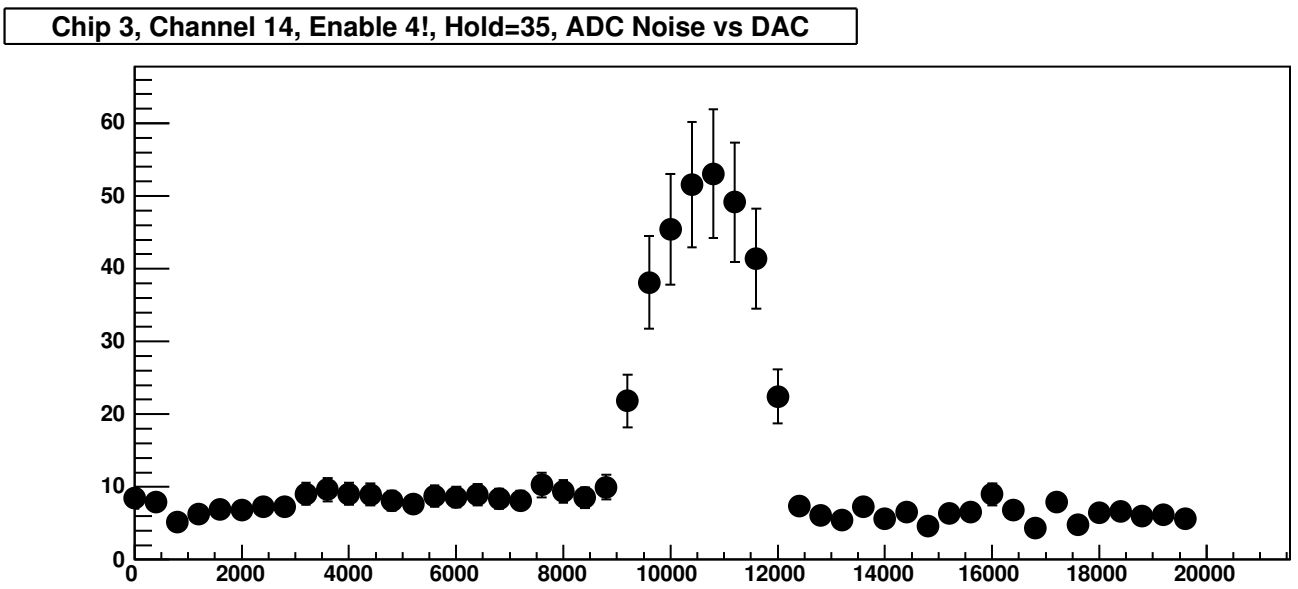

Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

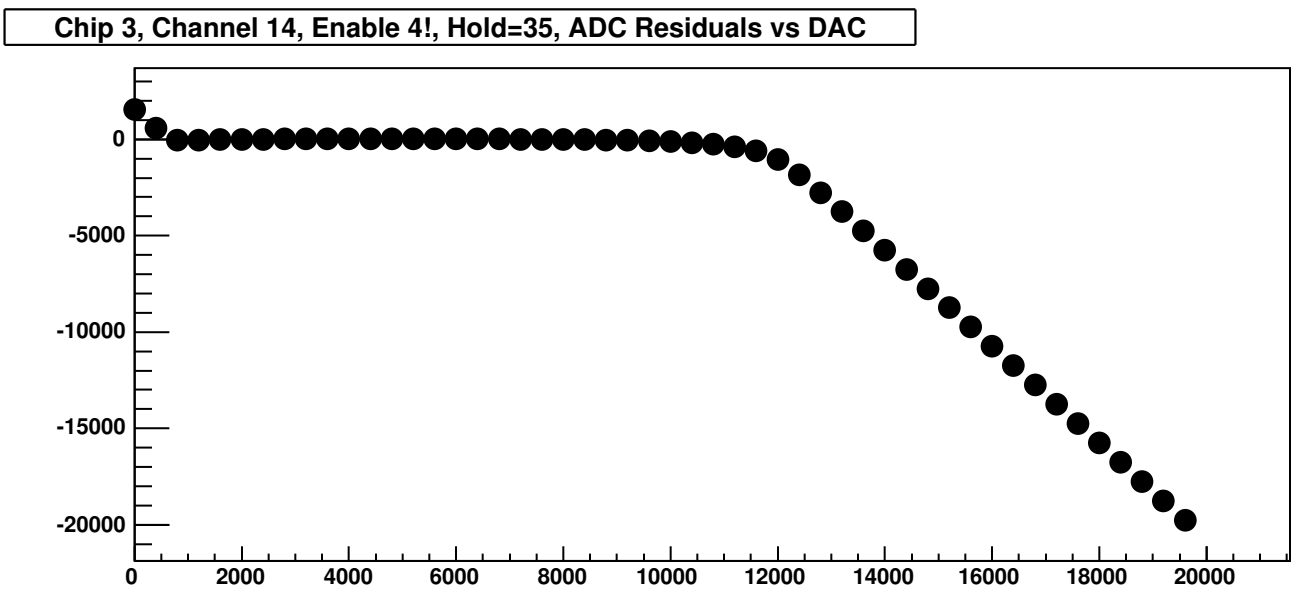

Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.16 / 23
p0 482 ± 0.6702
p1 0.01738 ± 0.0001077
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

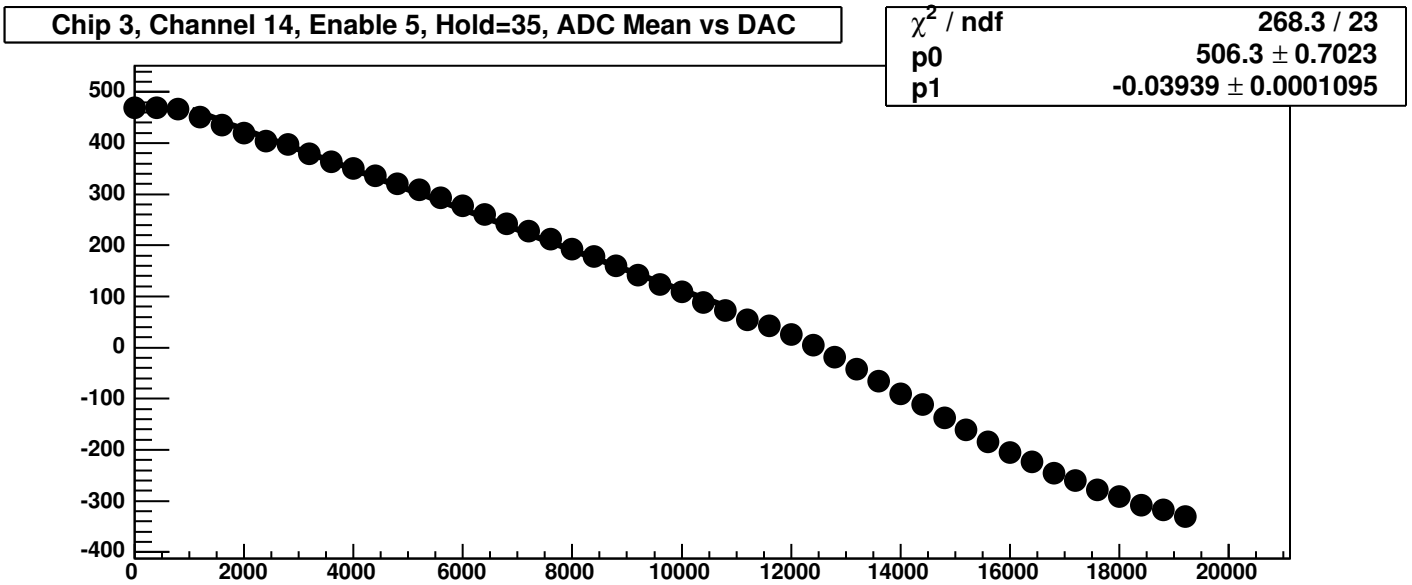

Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

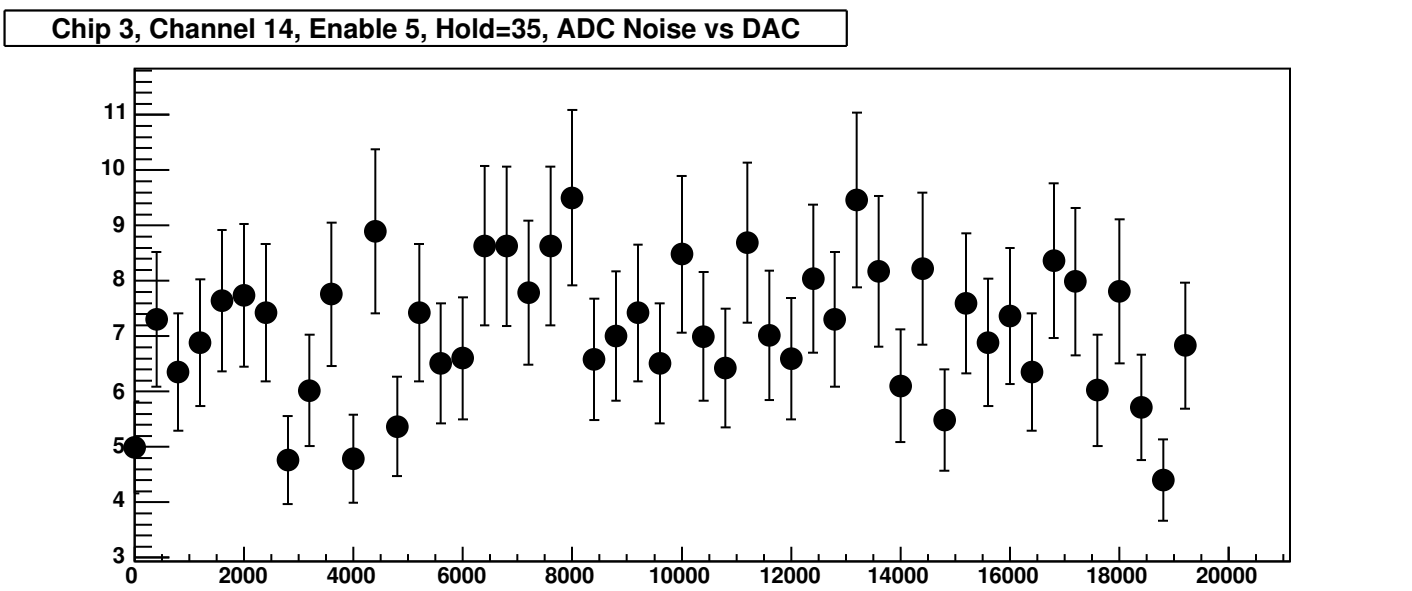

Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

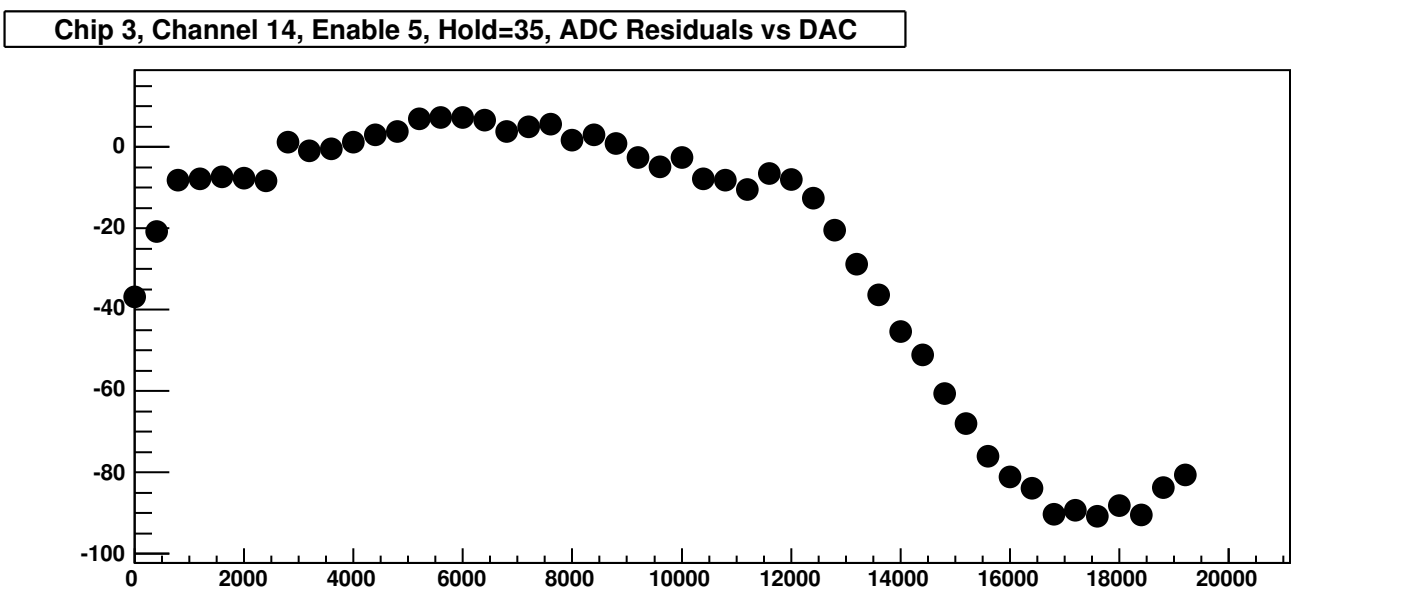

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 2642 / 23
p0 577.3 ± 0.8978
p1 2.503 ± 0.0001726
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

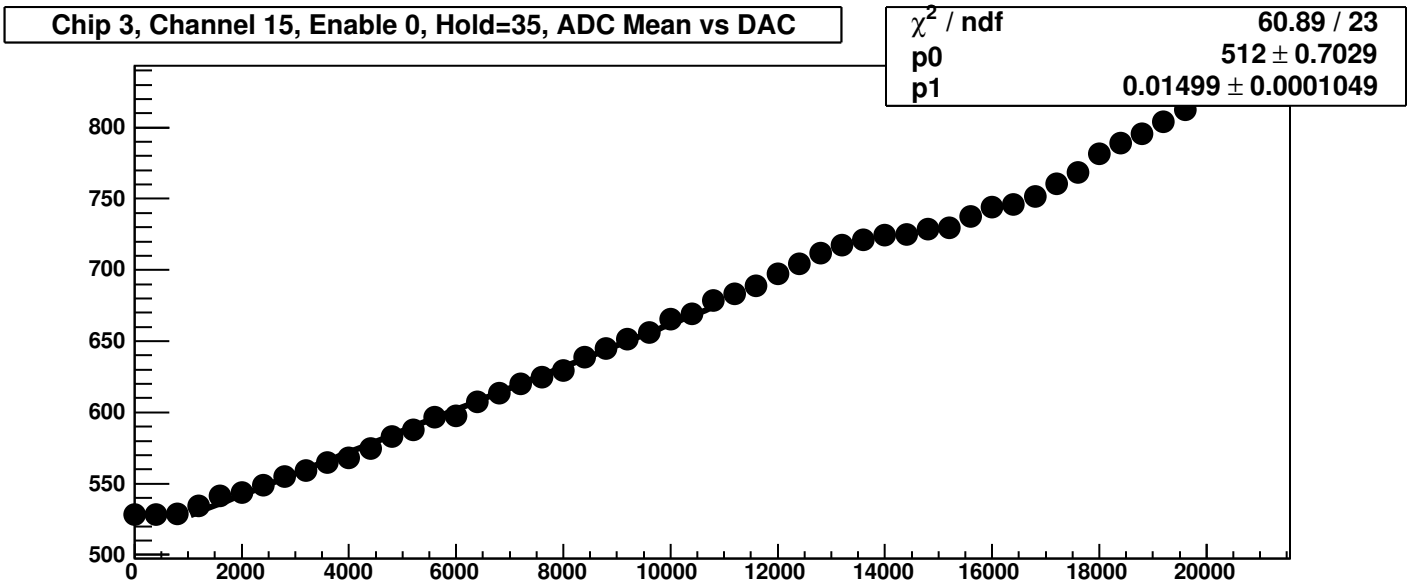

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

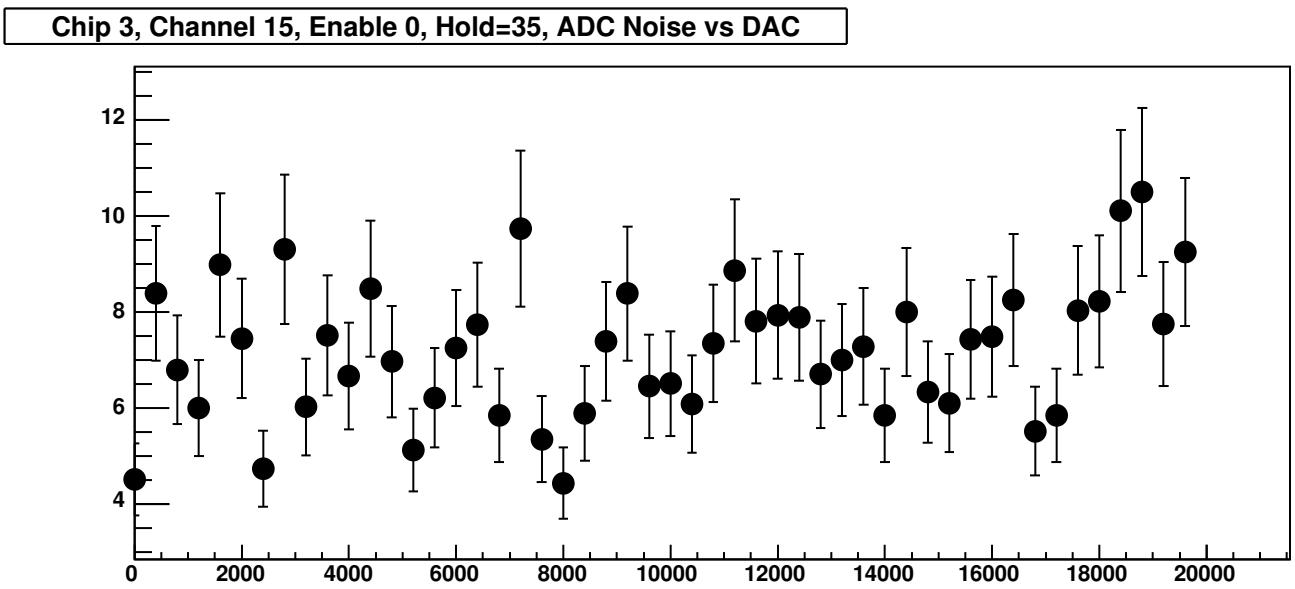

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

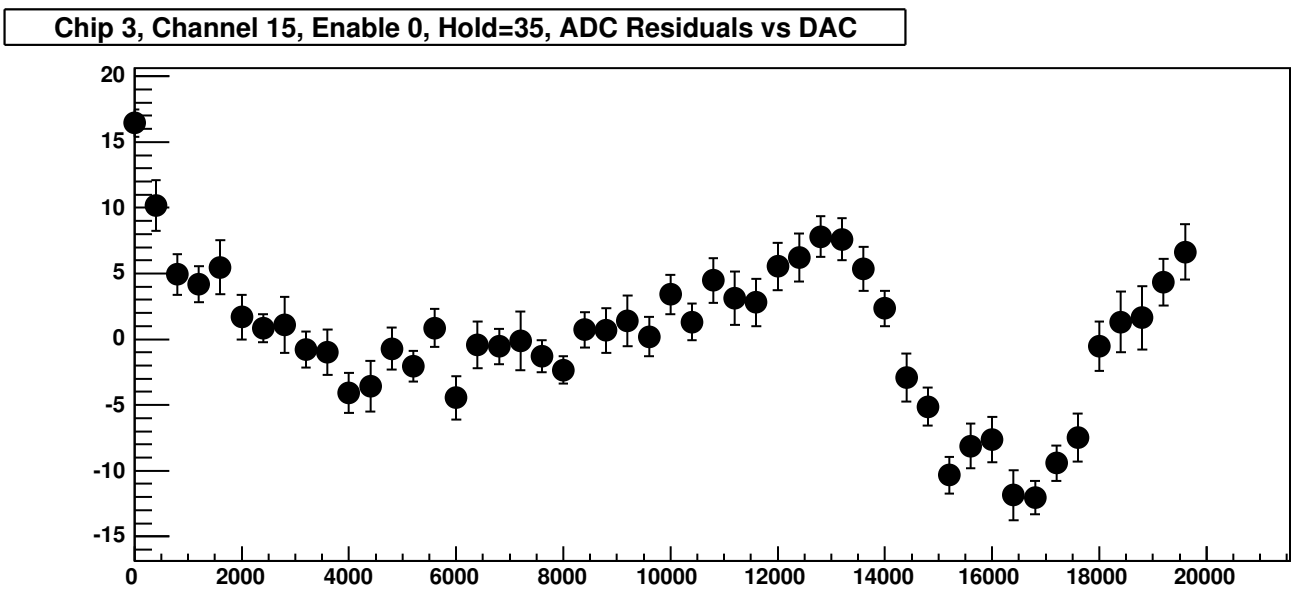

Chip 3, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 268.3 / 23
p0 506.3 ± 0.7023
p1 -0.03939 ± 0.0001095
500
400
300
200
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

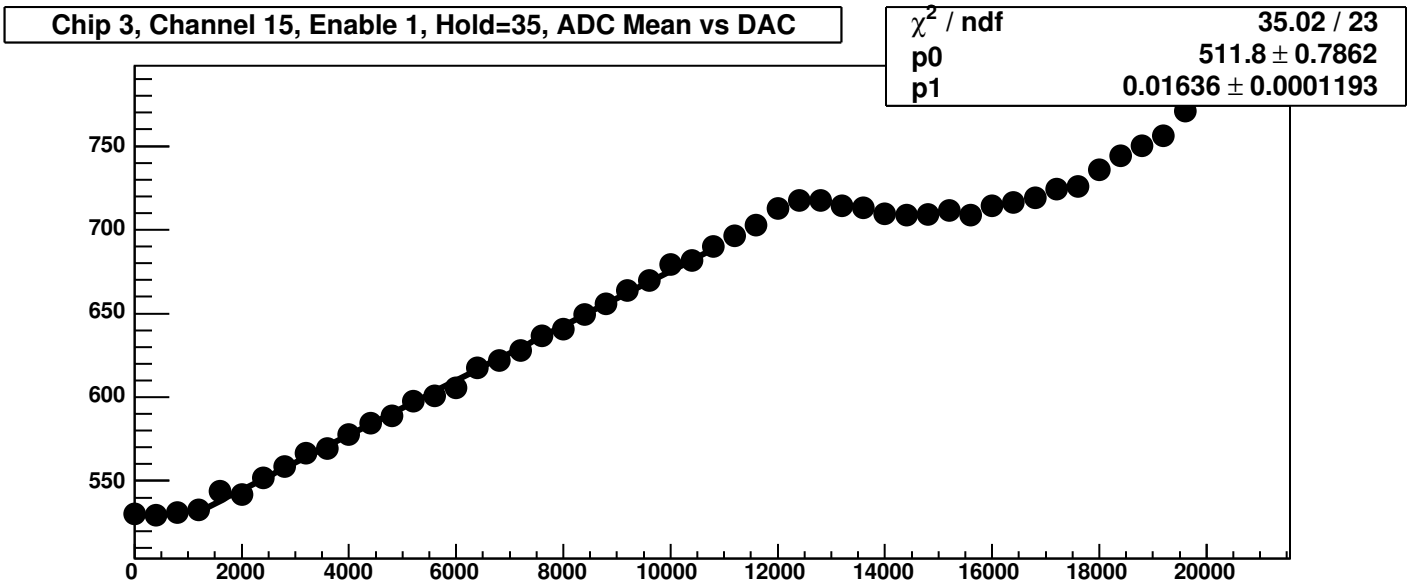

Chip 3, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

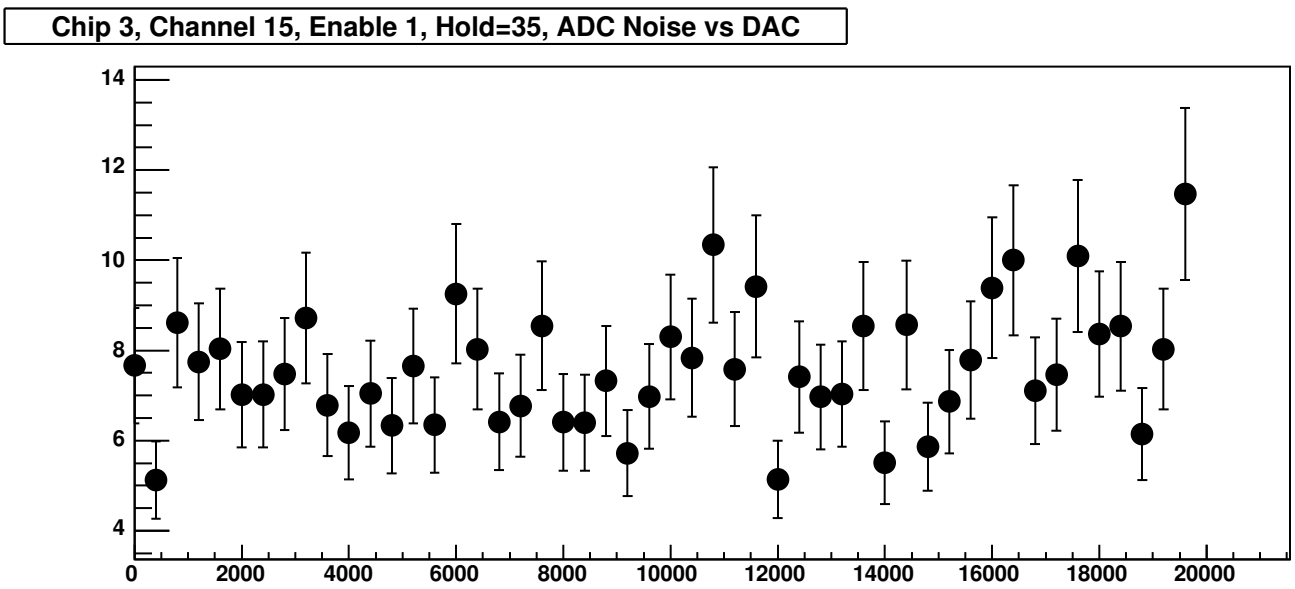

Chip 3, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

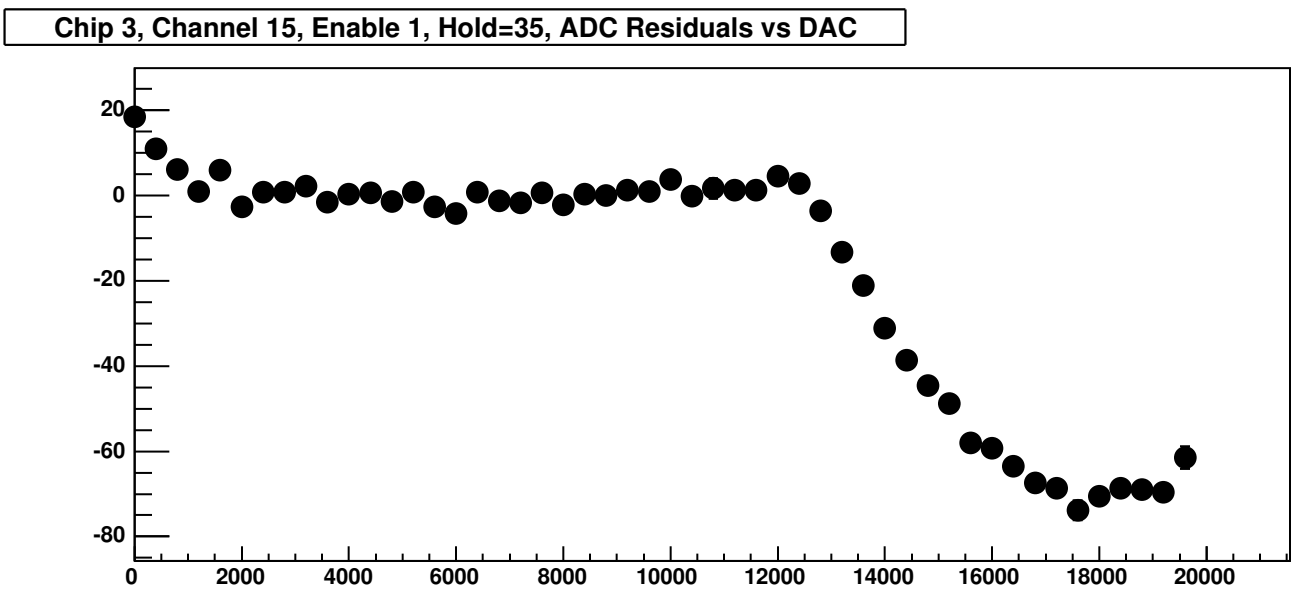

Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 60.89 / 23
p0 512 ± 0.7029
p1 0.01499 ± 0.0001049
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

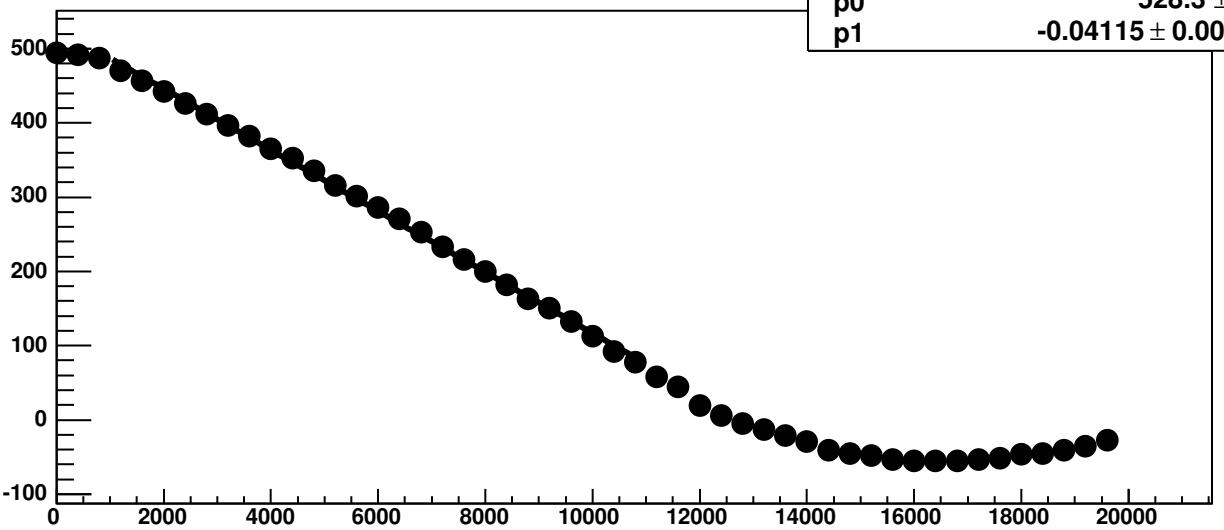

Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.02 / 23
p0 511.8 ± 0.7862
p1 0.01636 ± 0.0001193
750
700
650
600
550
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

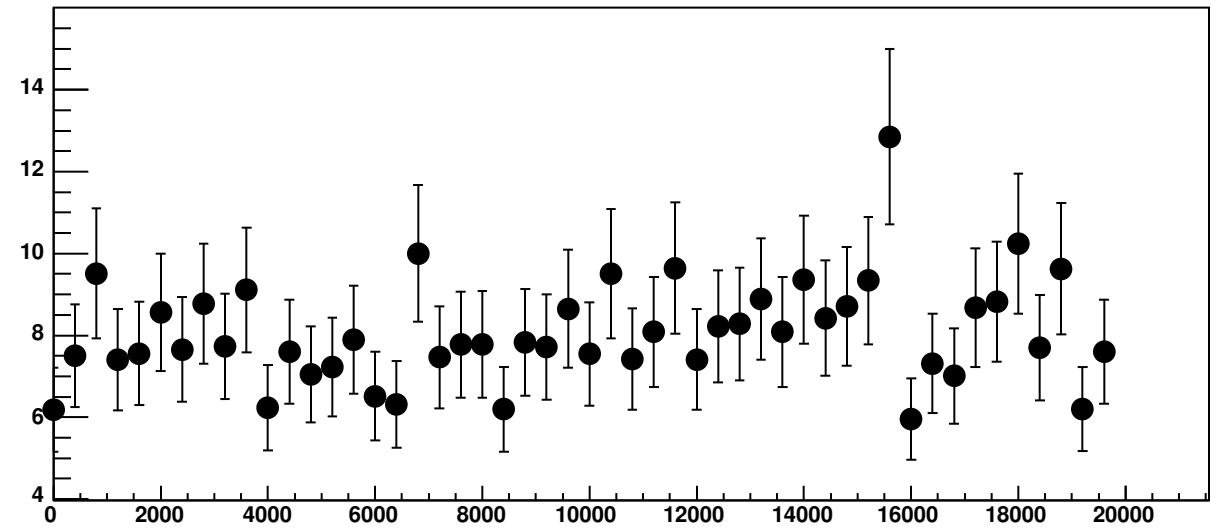

Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

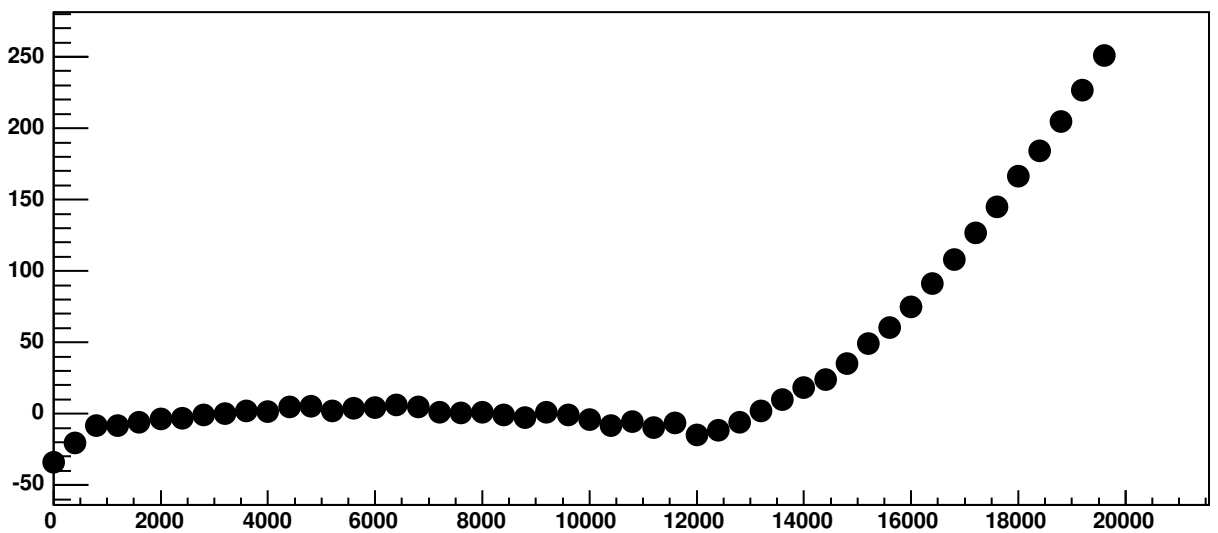
χ^2 / ndf	135.6 / 23
p0	528.3 ± 0.821
p1	-0.04115 ± 0.0001243

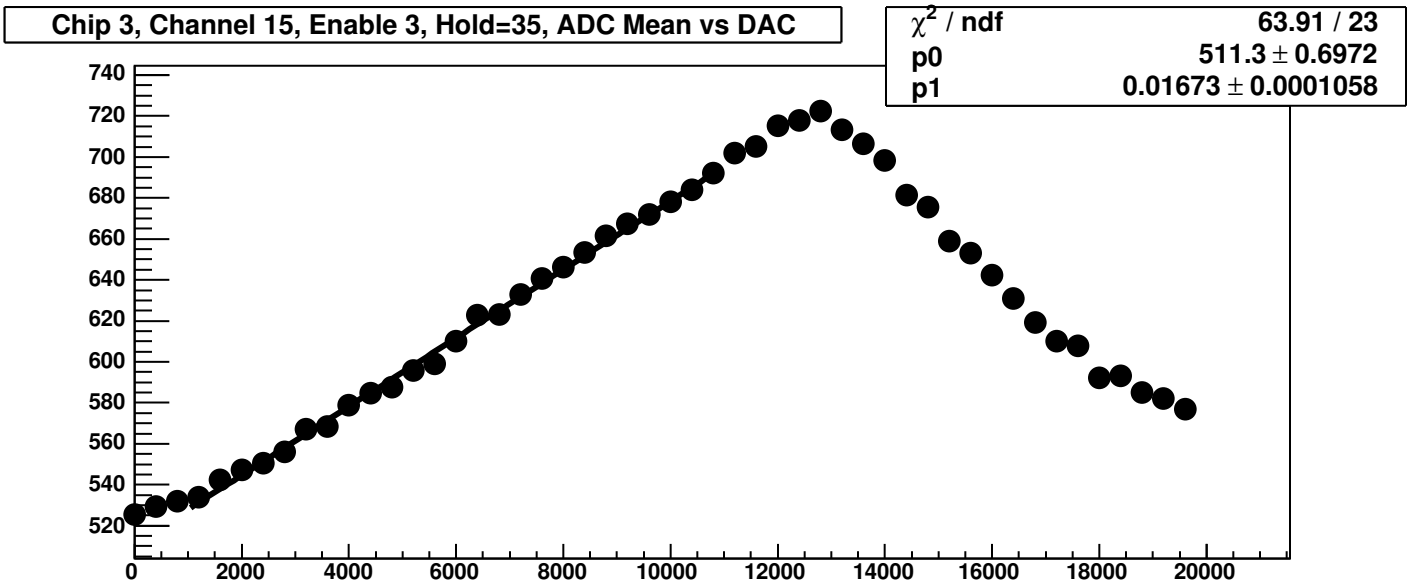


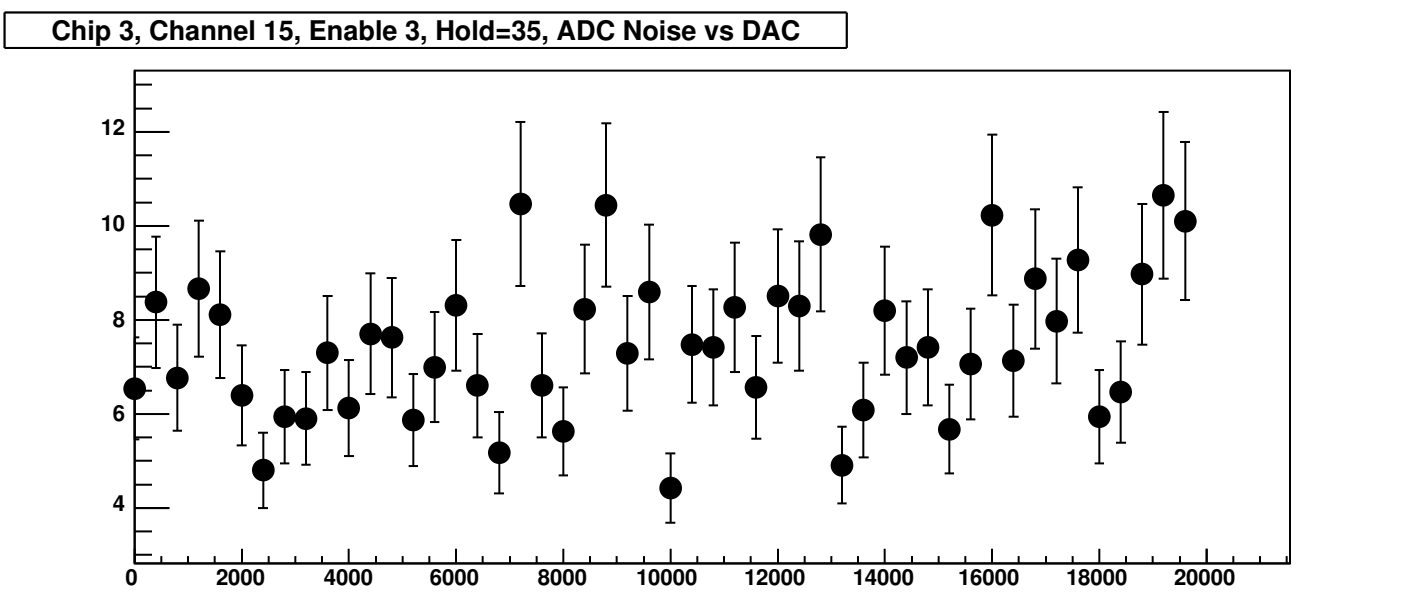
Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC

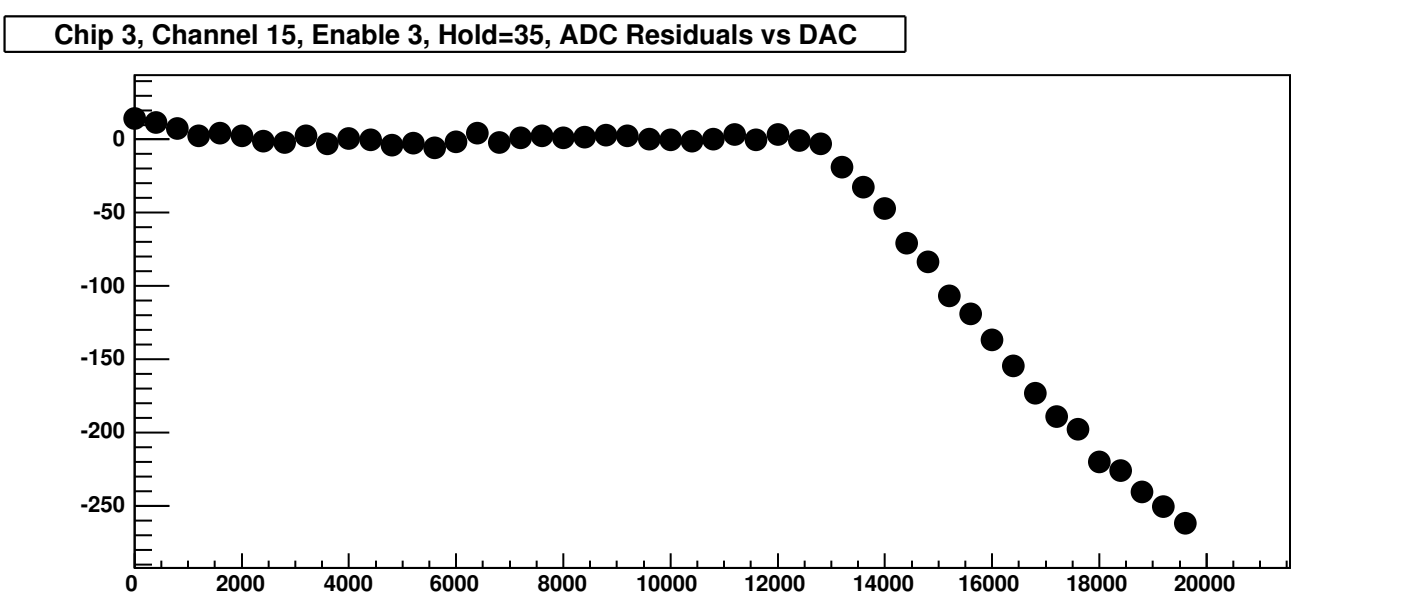


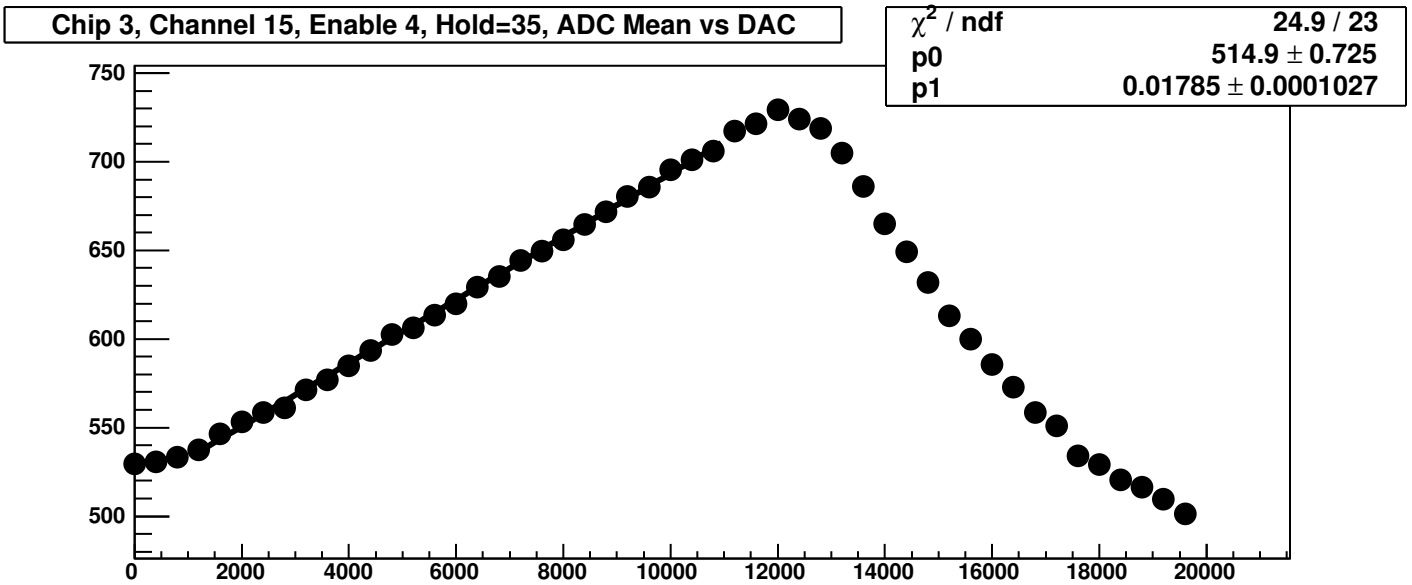
Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

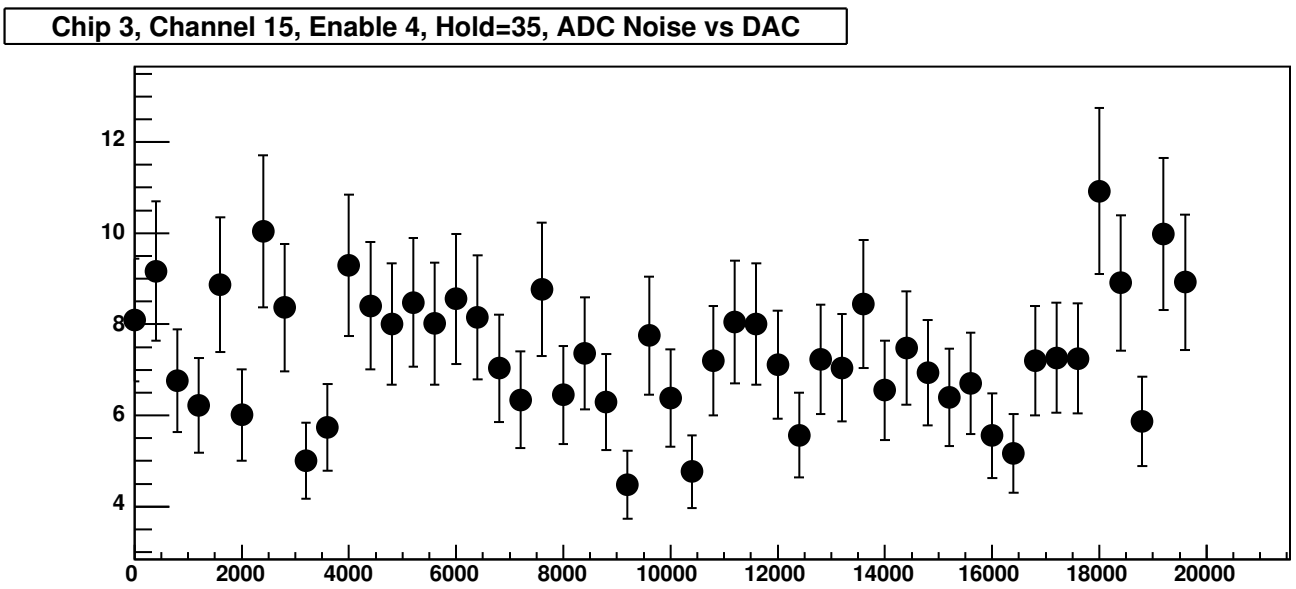


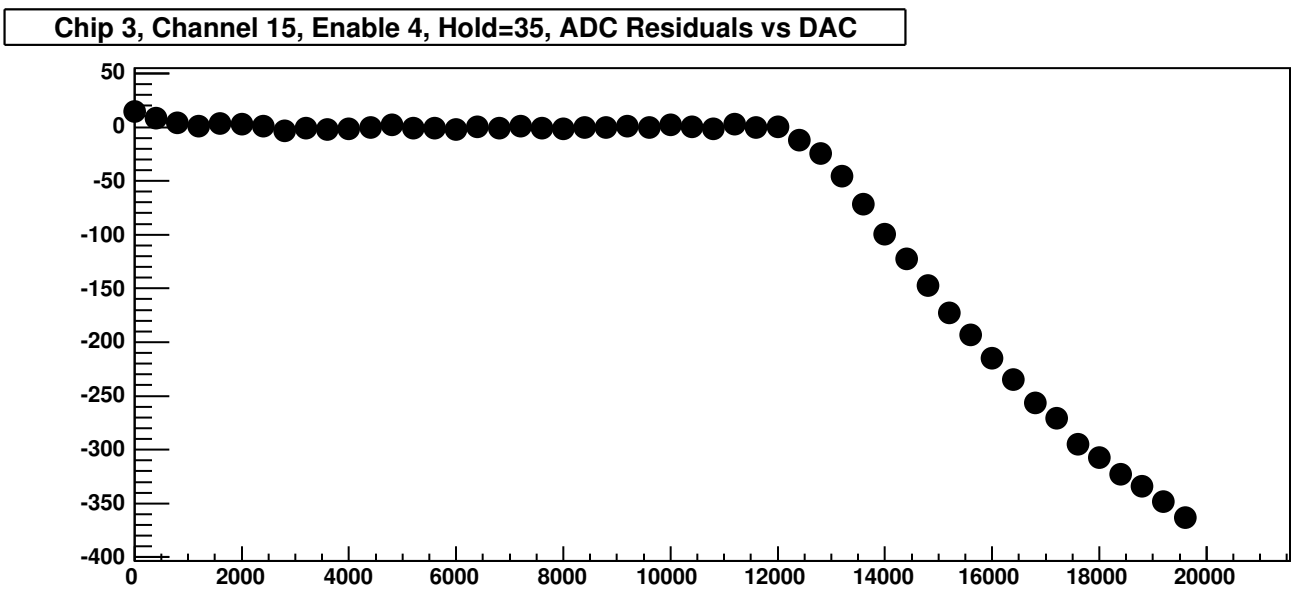

Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 63.91 / 23
p0 511.3 ± 0.6972
p1 0.01673 ± 0.0001058
740
720
700
680
660
640
620
600
580
560
540
520
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

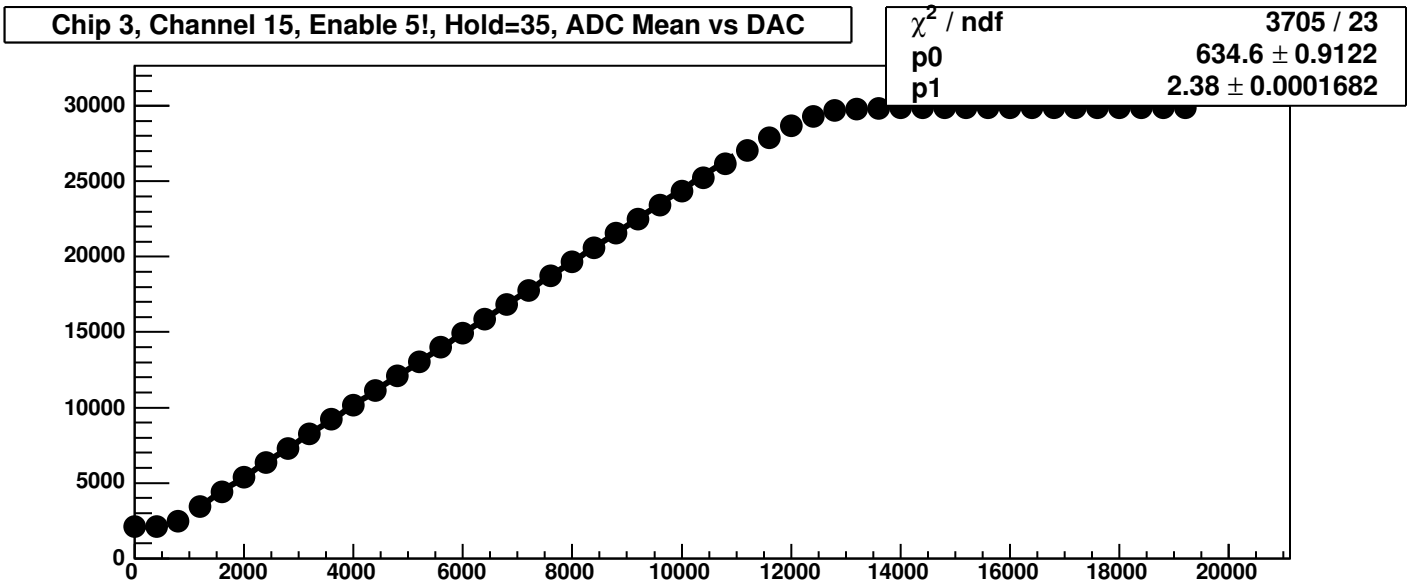

Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

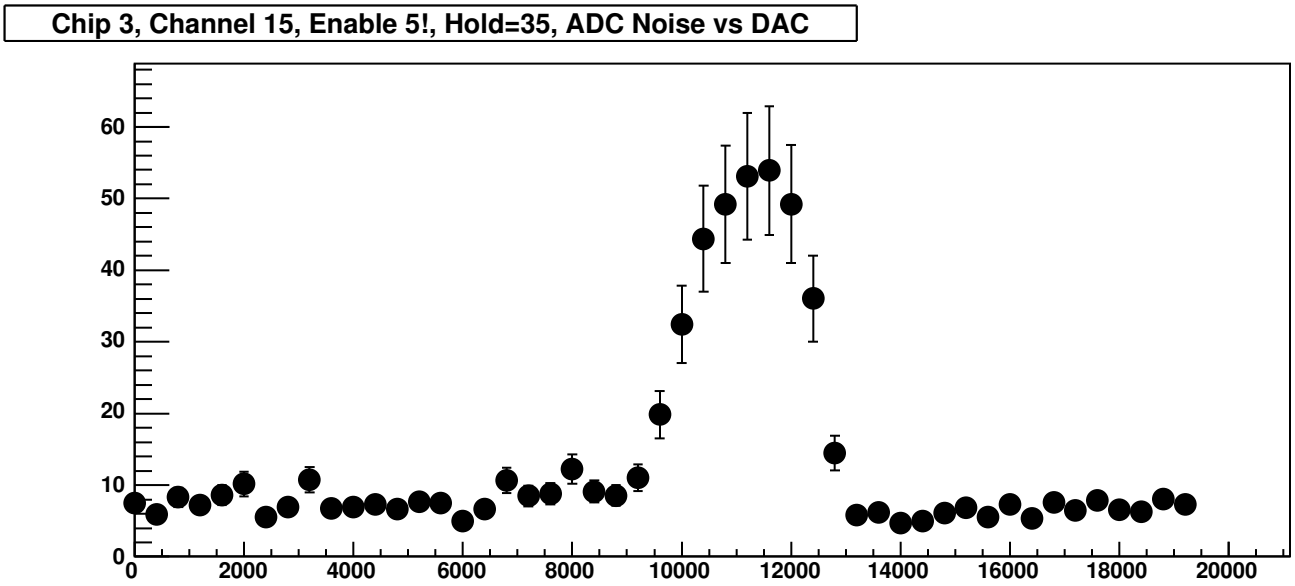

Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

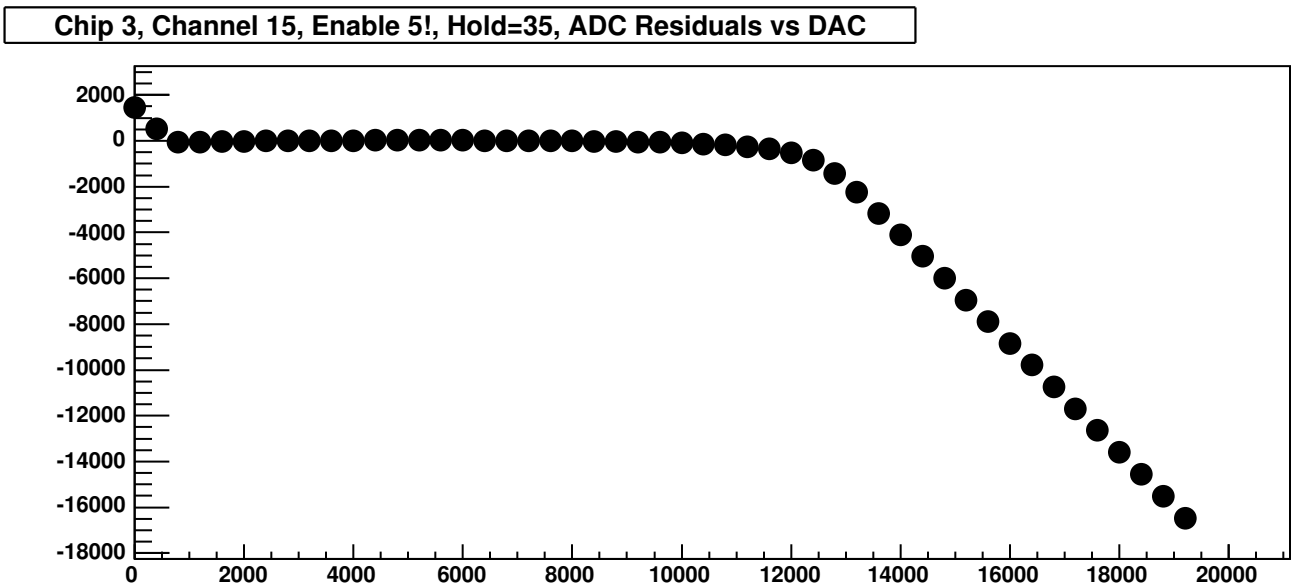

Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.9 / 23
p0 514.9 ± 0.725
p1 0.01785 ± 0.0001027
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

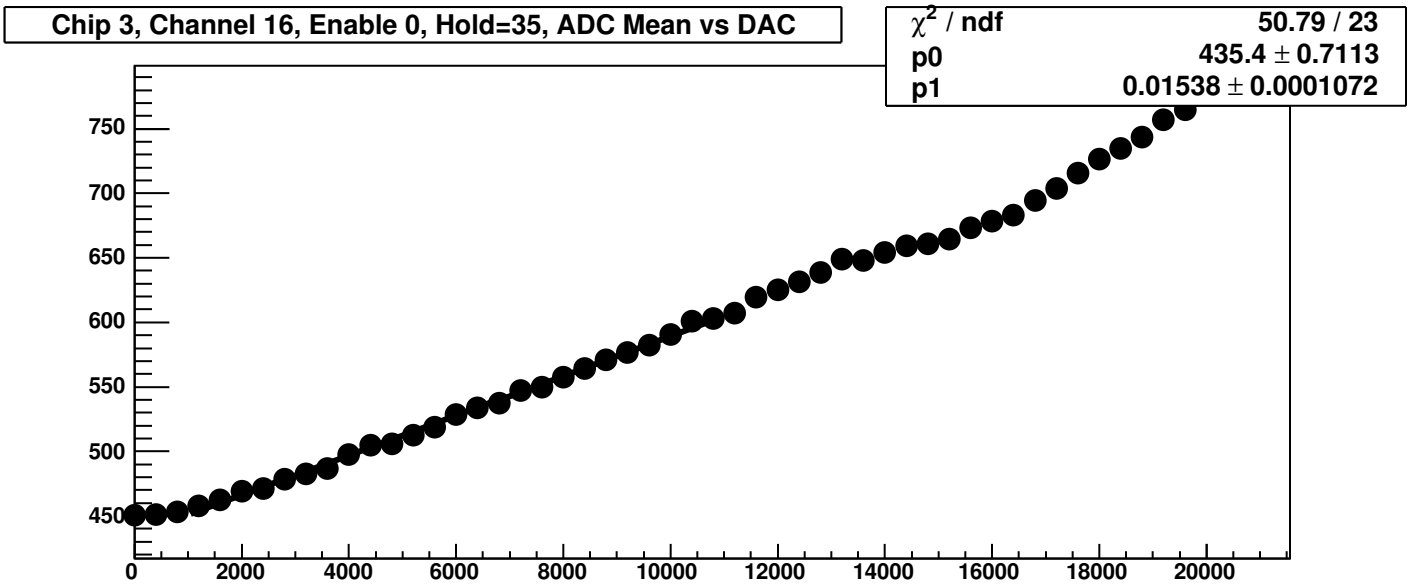

Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

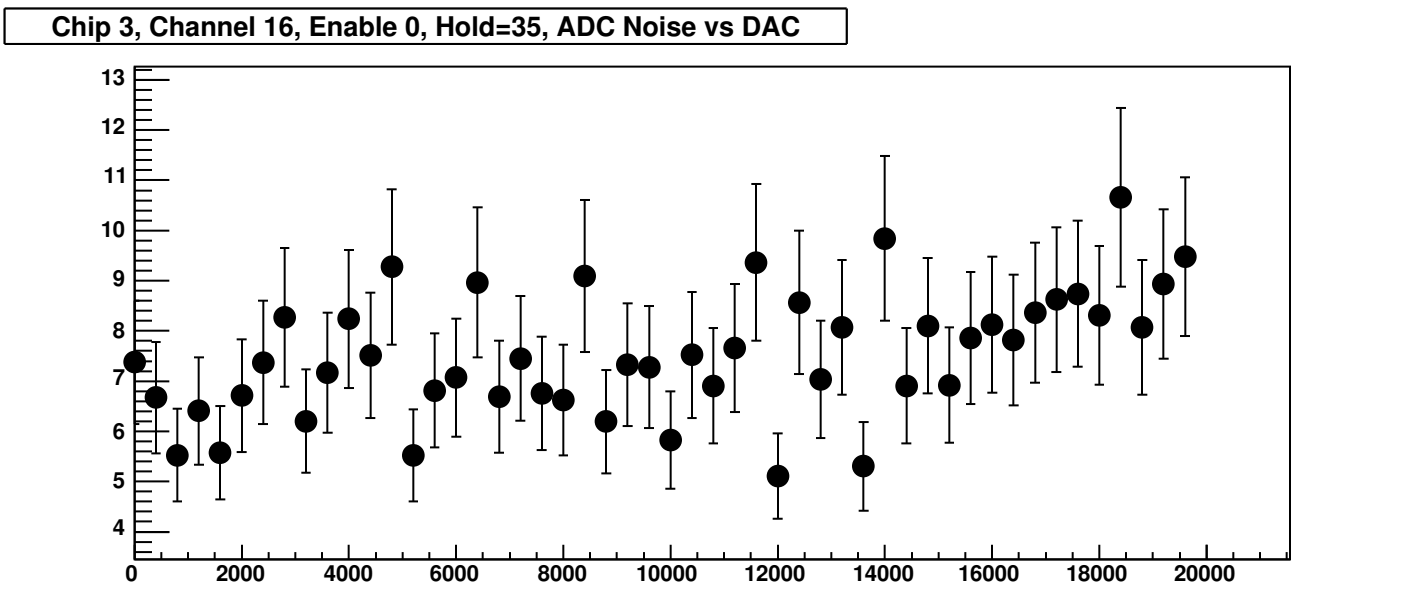

Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

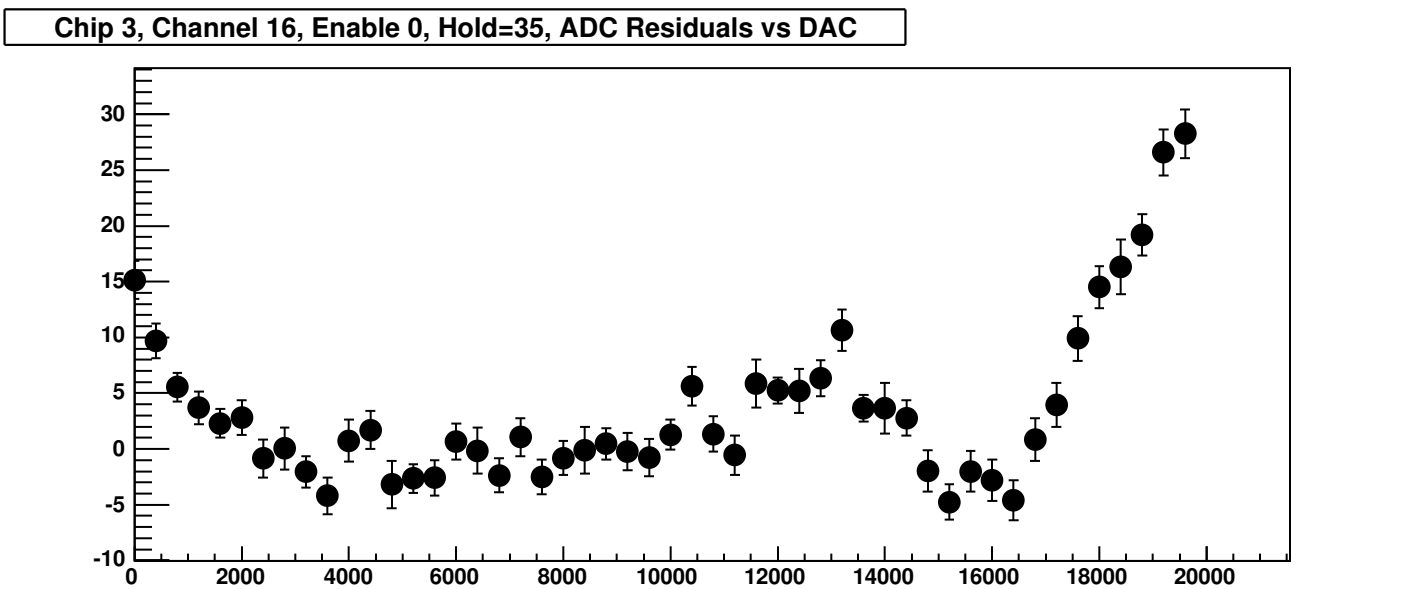

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3705 / 23
p0 634.6 ± 0.9122
p1 2.38 ± 0.0001682
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

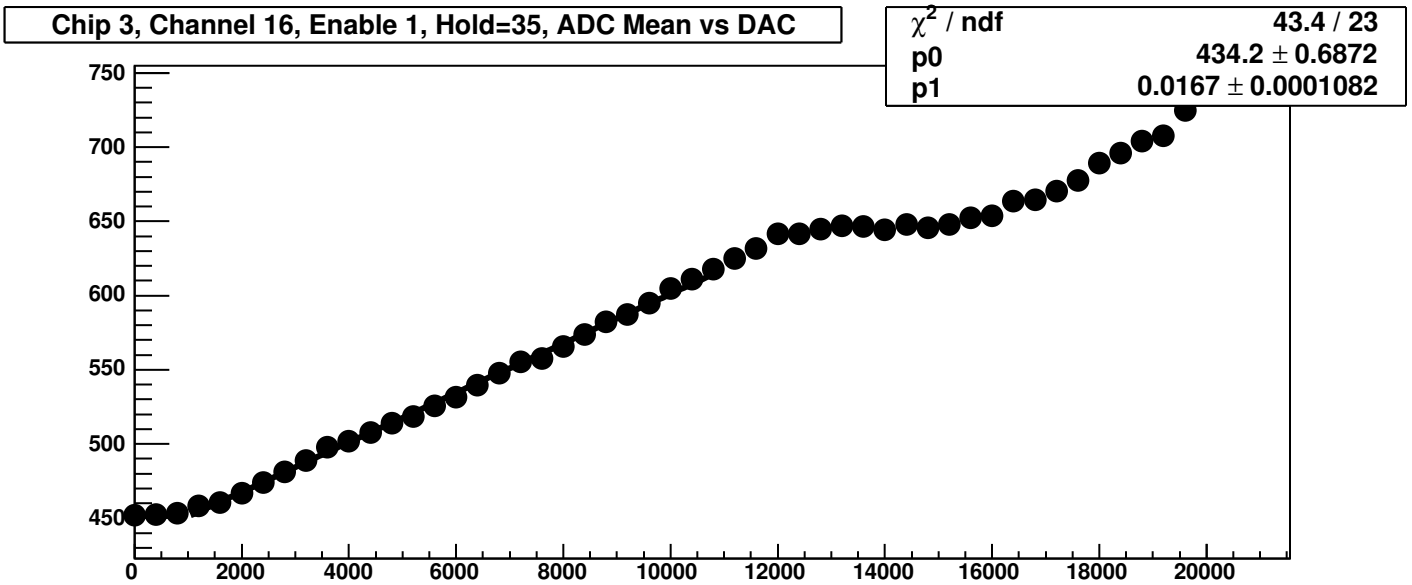

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

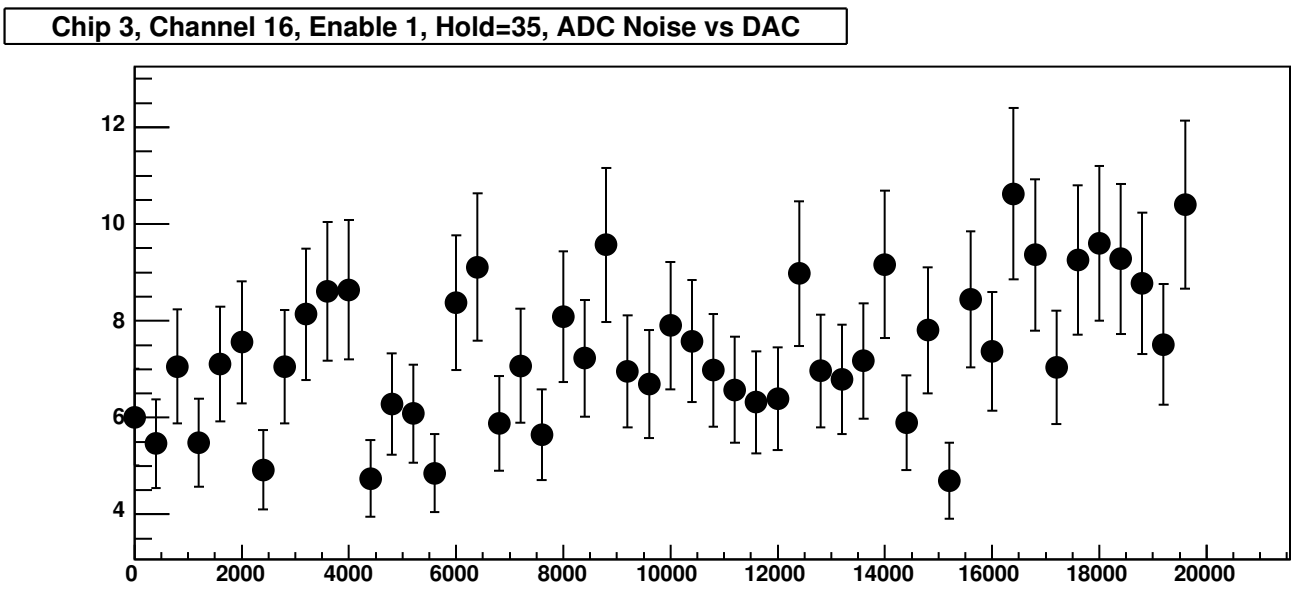

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
-18000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

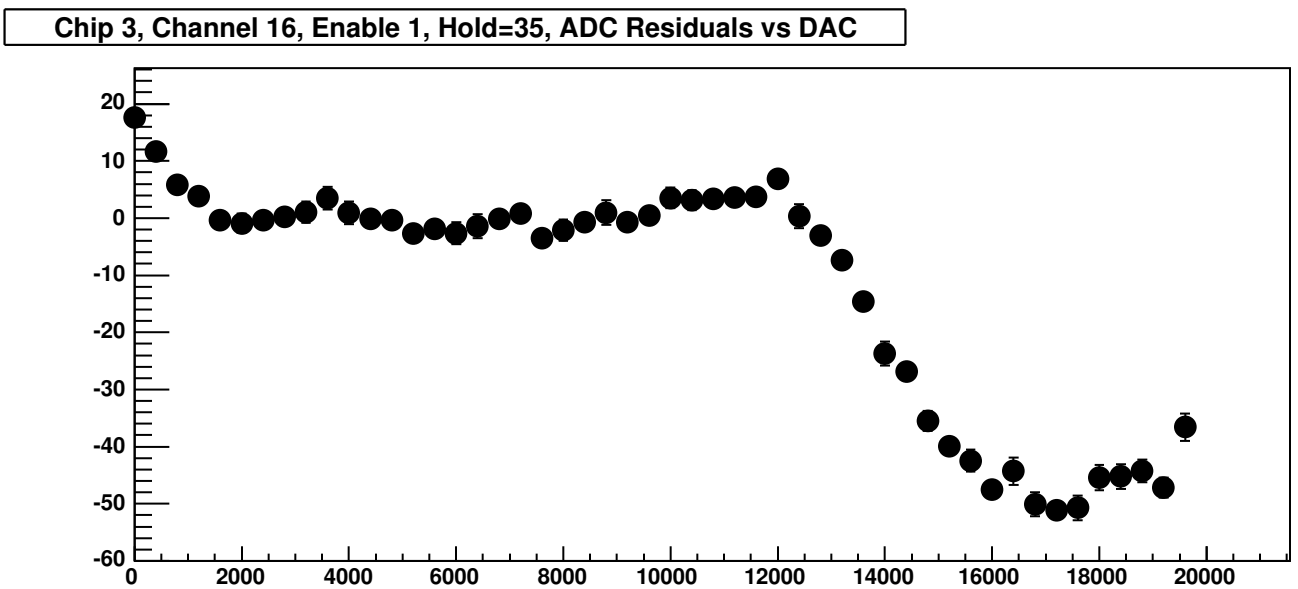

Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 50.79 / 23
p0 435.4 ± 0.7113
p1 0.01538 ± 0.0001072
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

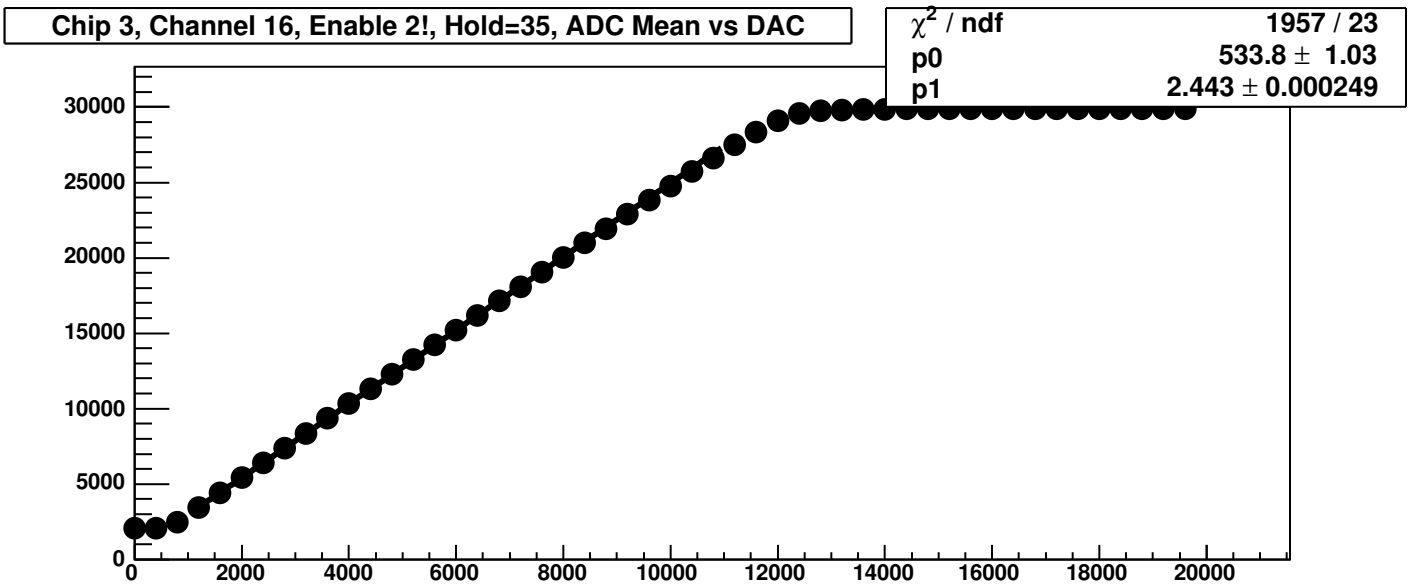

Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

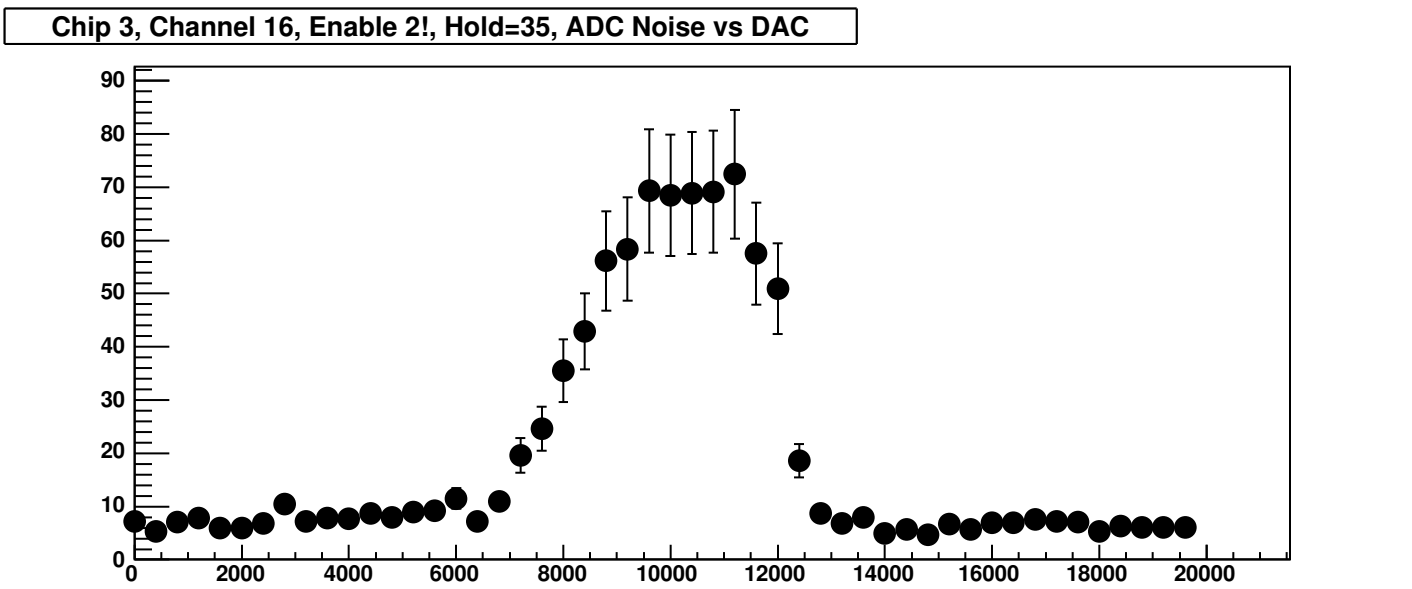

Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

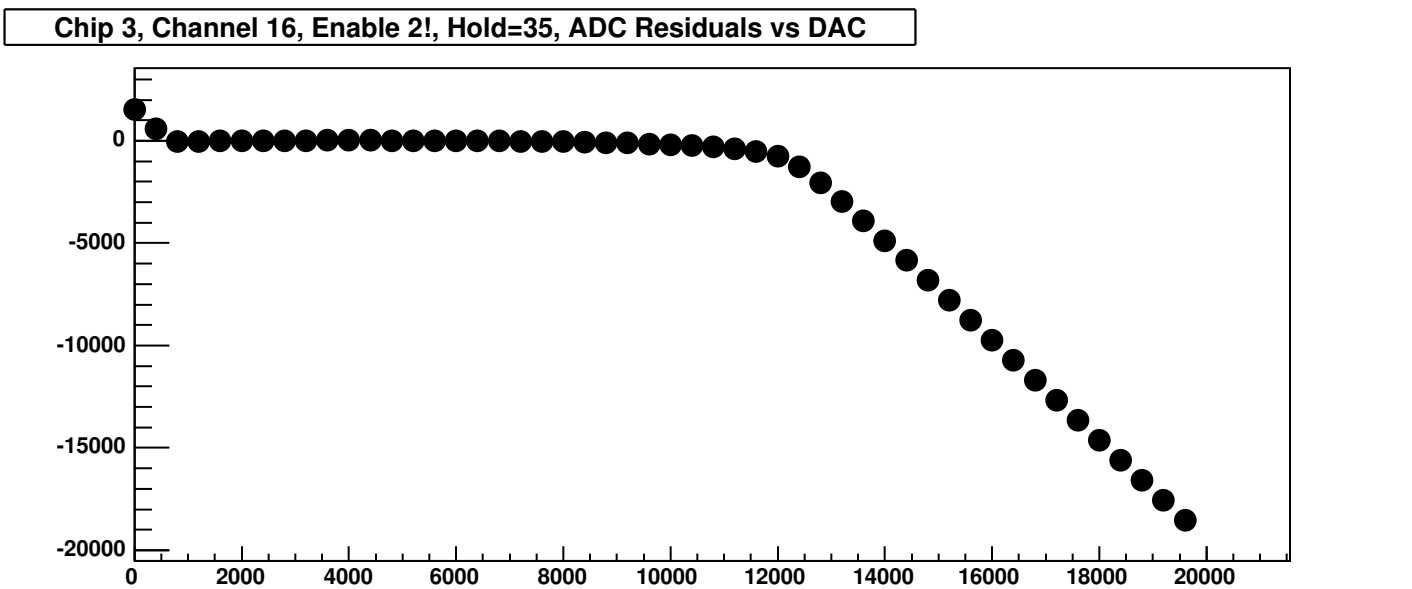

Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.4 / 23
p0 434.2 ± 0.6872
p1 0.0167 ± 0.0001082
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

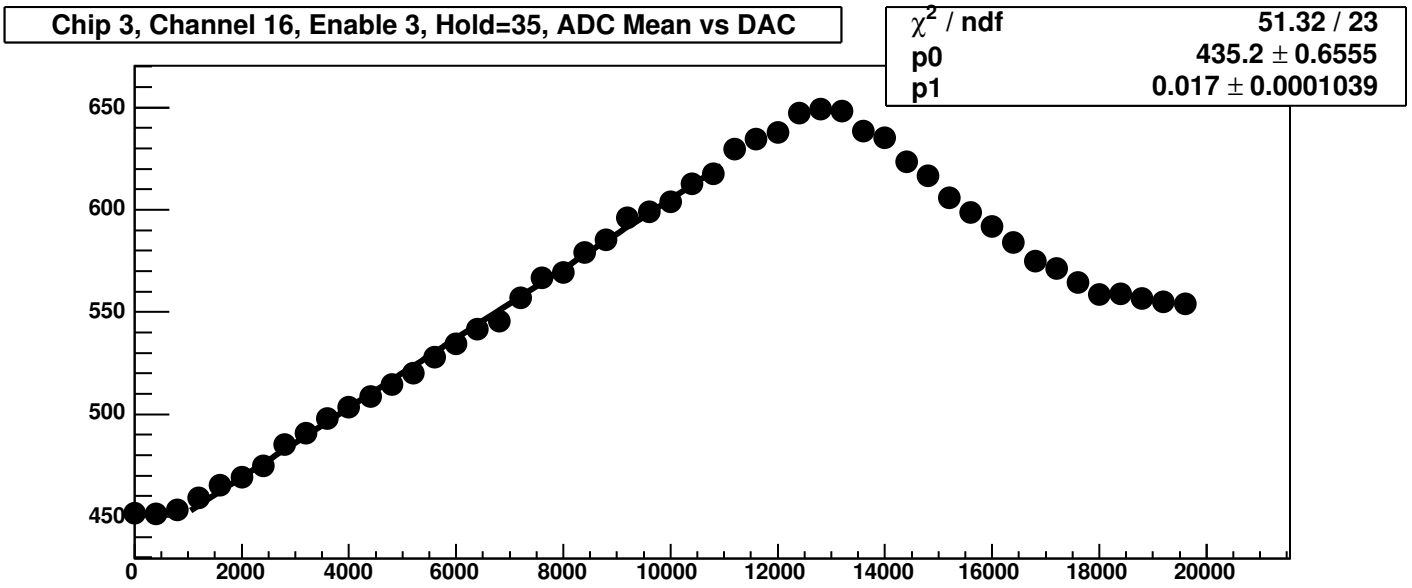

Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

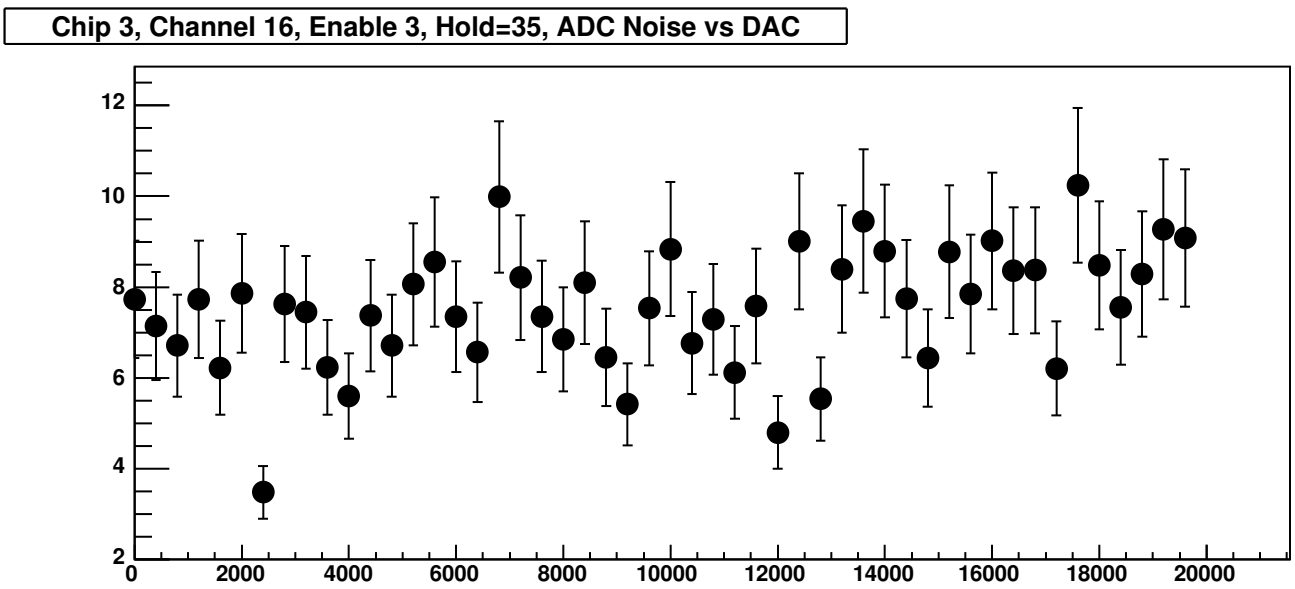

Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
-60
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

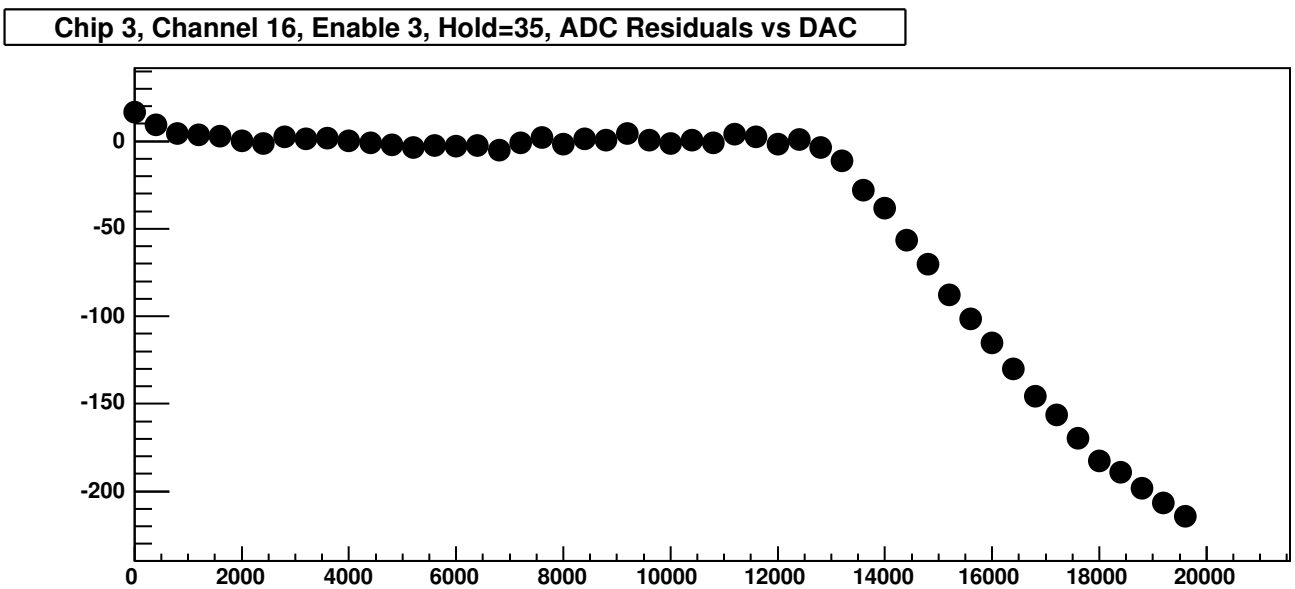

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1957 / 23
p0 533.8 ± 1.03
p1 2.443 ± 0.000249
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

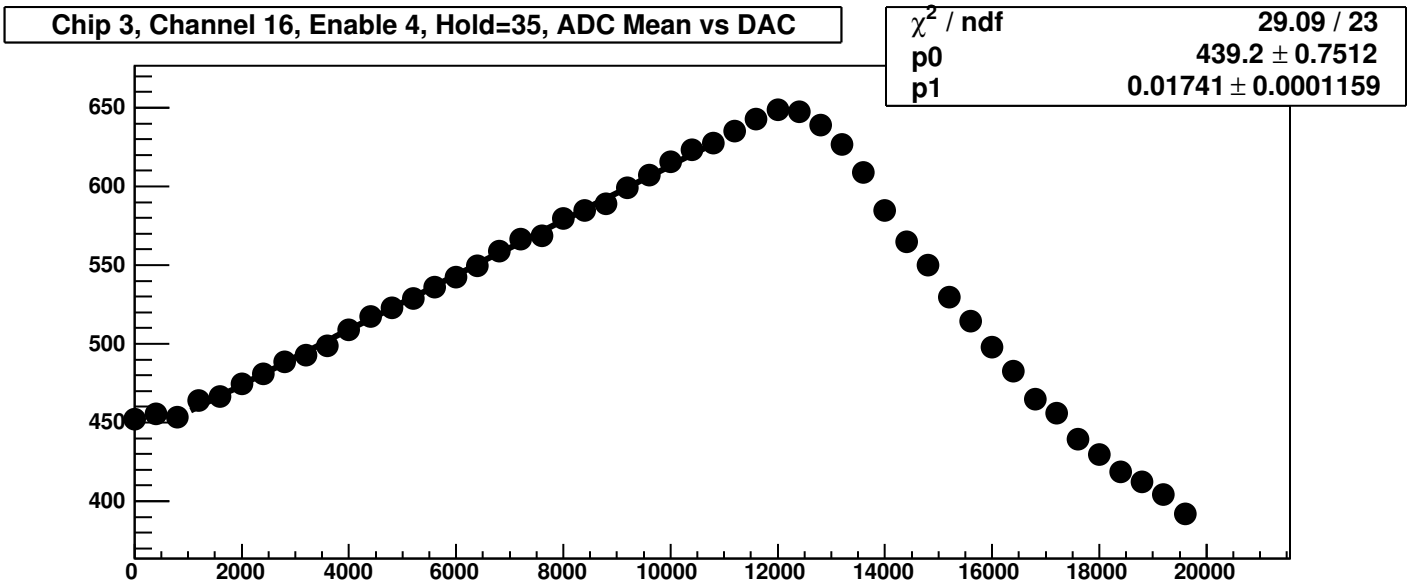

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

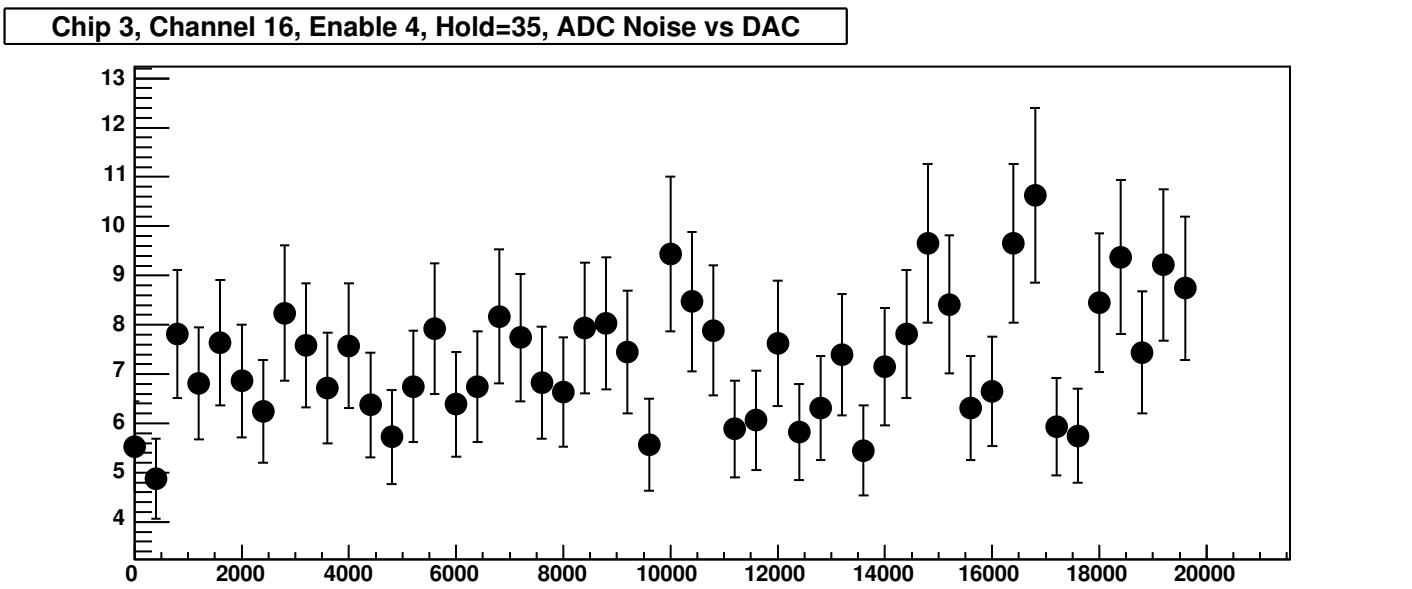

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

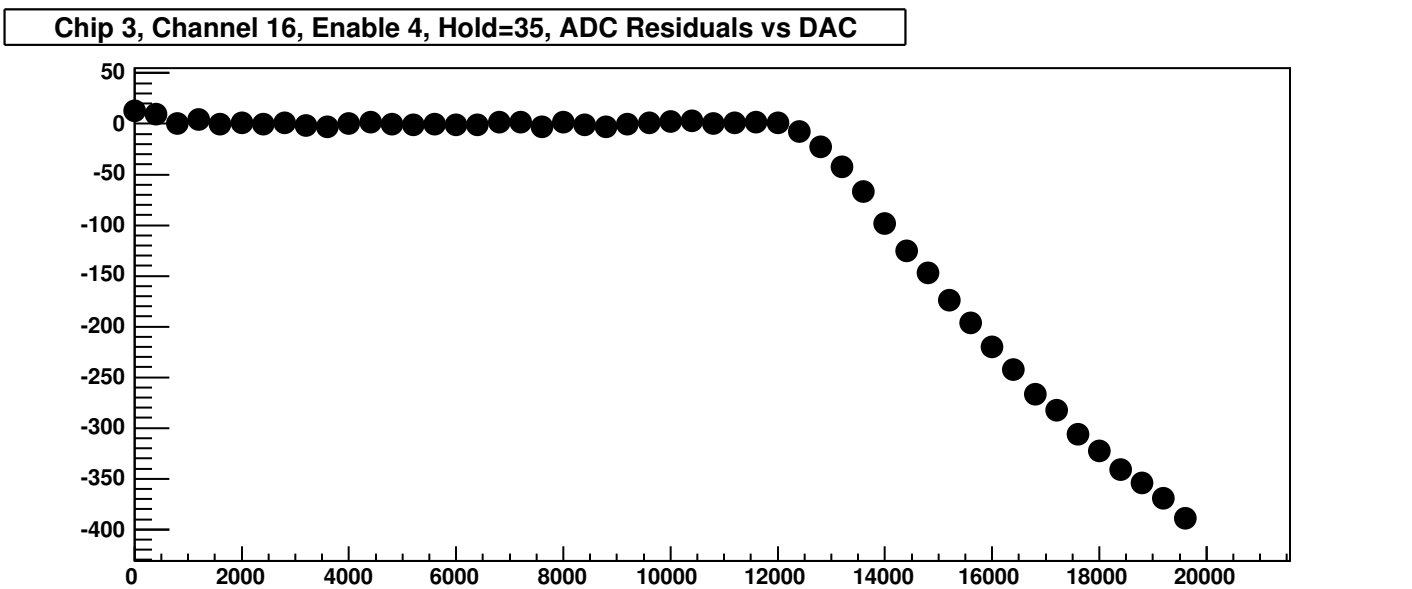

Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 51.32 / 23
p0 435.2 ± 0.6555
p1 0.017 ± 0.0001039
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

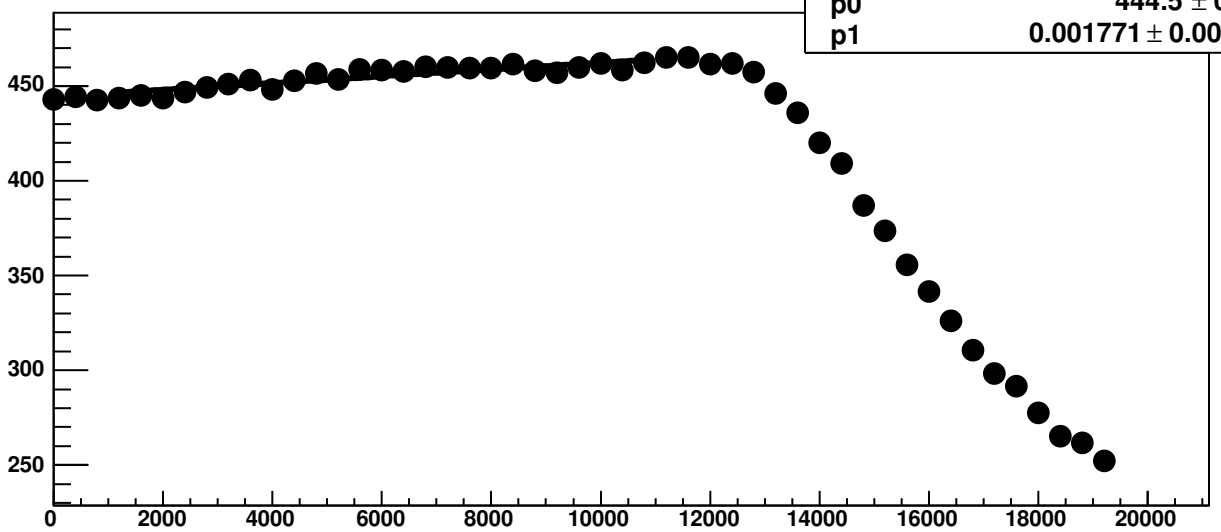

Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.09 / 23
p0 439.2 ± 0.7512
p1 0.01741 ± 0.0001159
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

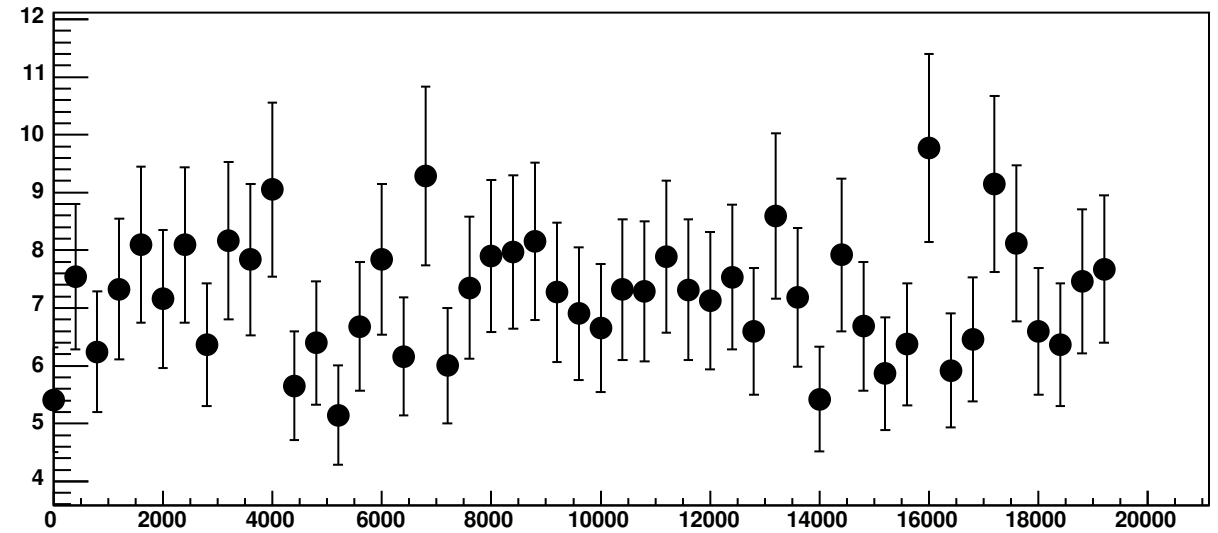

Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 3, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

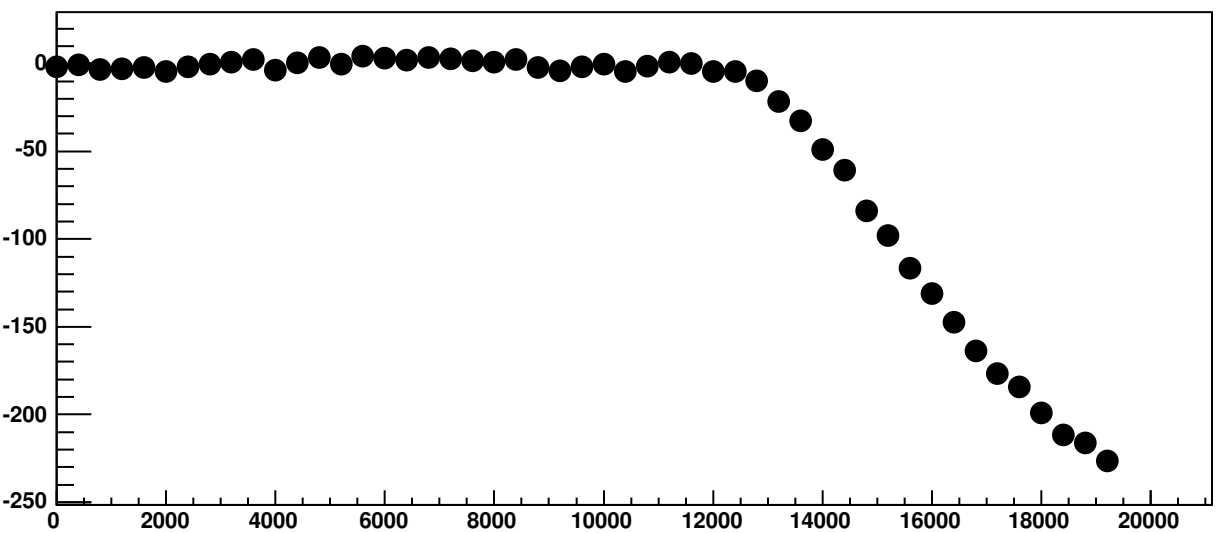
χ^2 / ndf	62.51 / 23
p0	444.5 ± 0.7704
p1	0.001771 ± 0.0001164

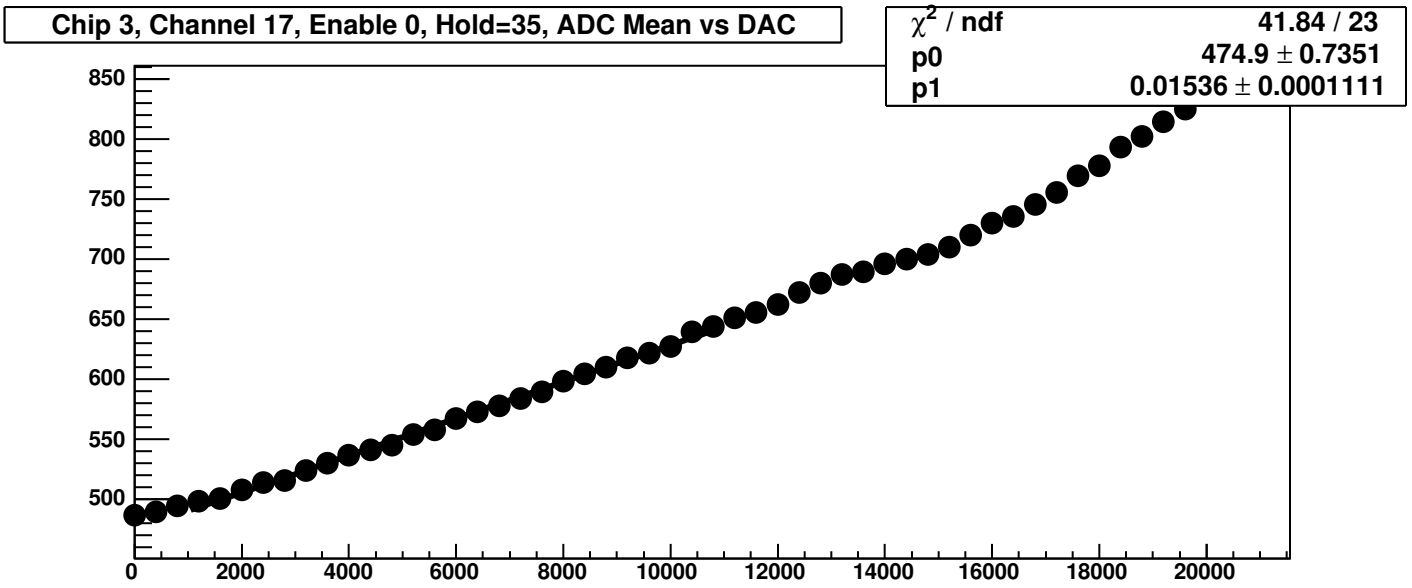


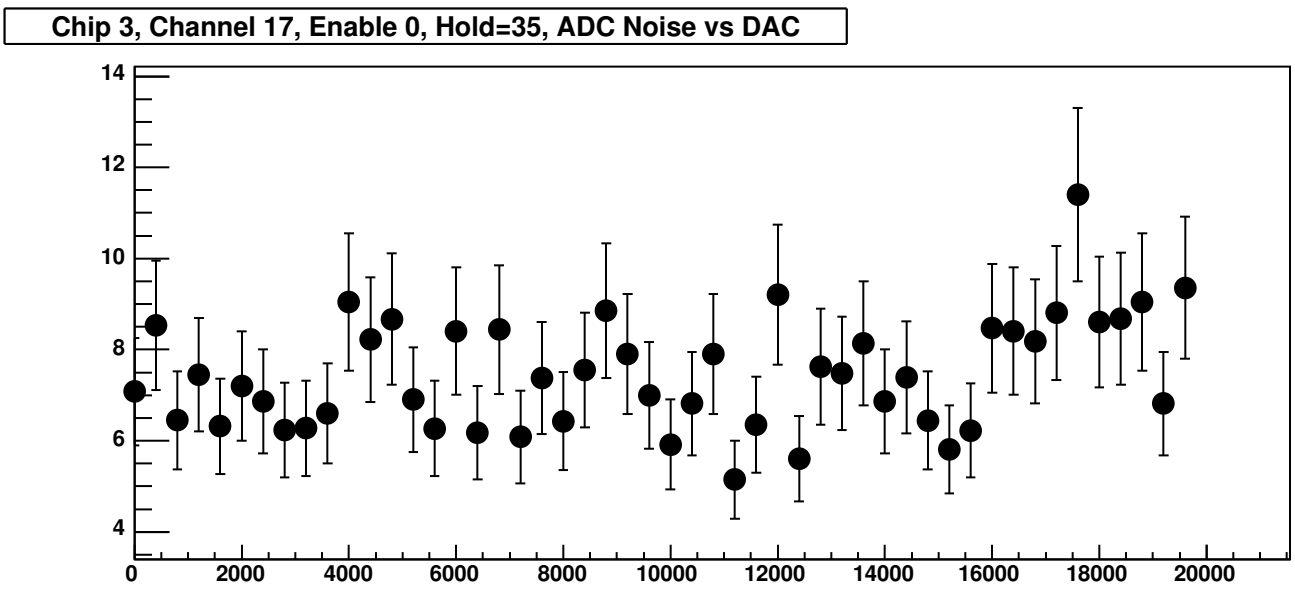
Chip 3, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC

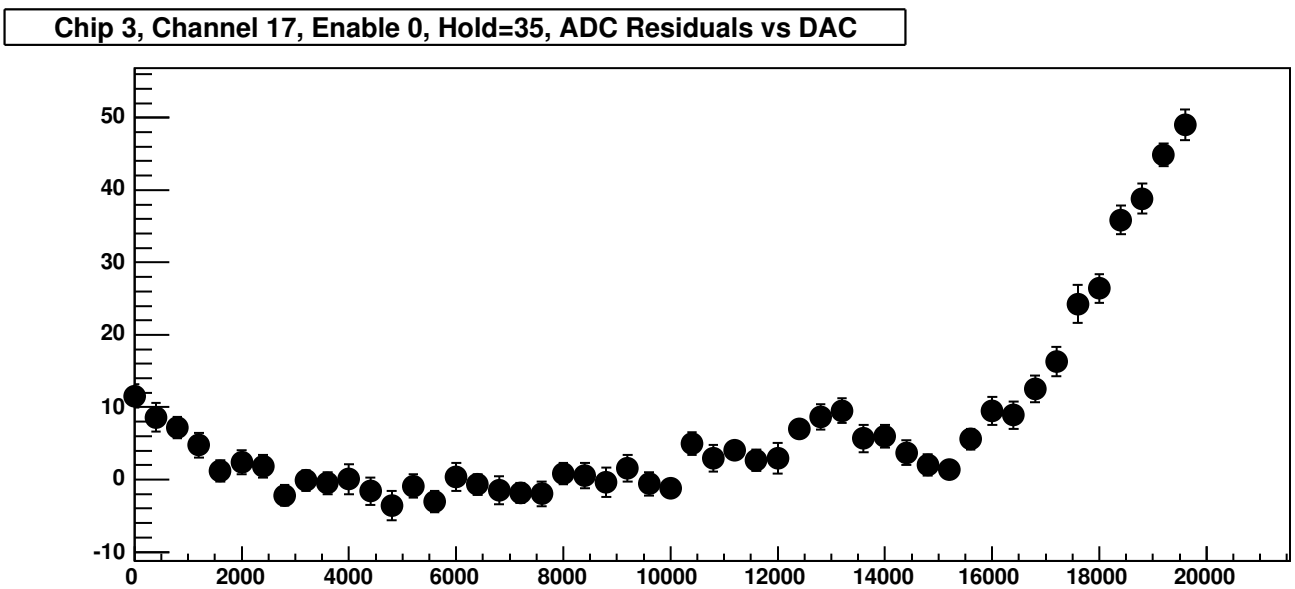


Chip 3, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

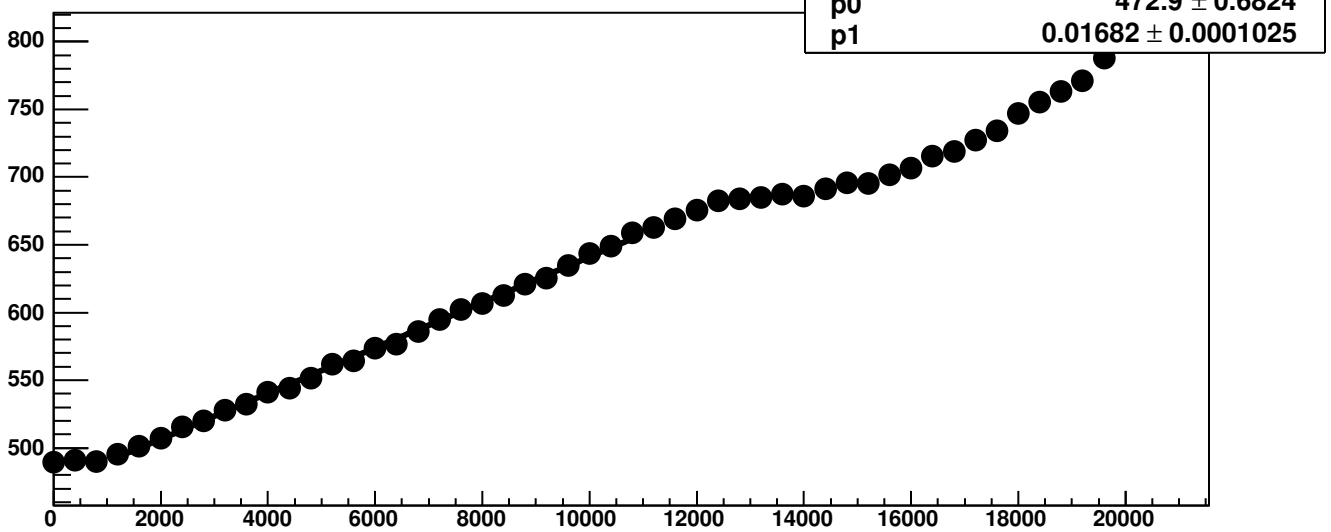



Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 41.84 / 23
p0 474.9 ± 0.7351
p1 0.01536 ± 0.0001111
850
800
750
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

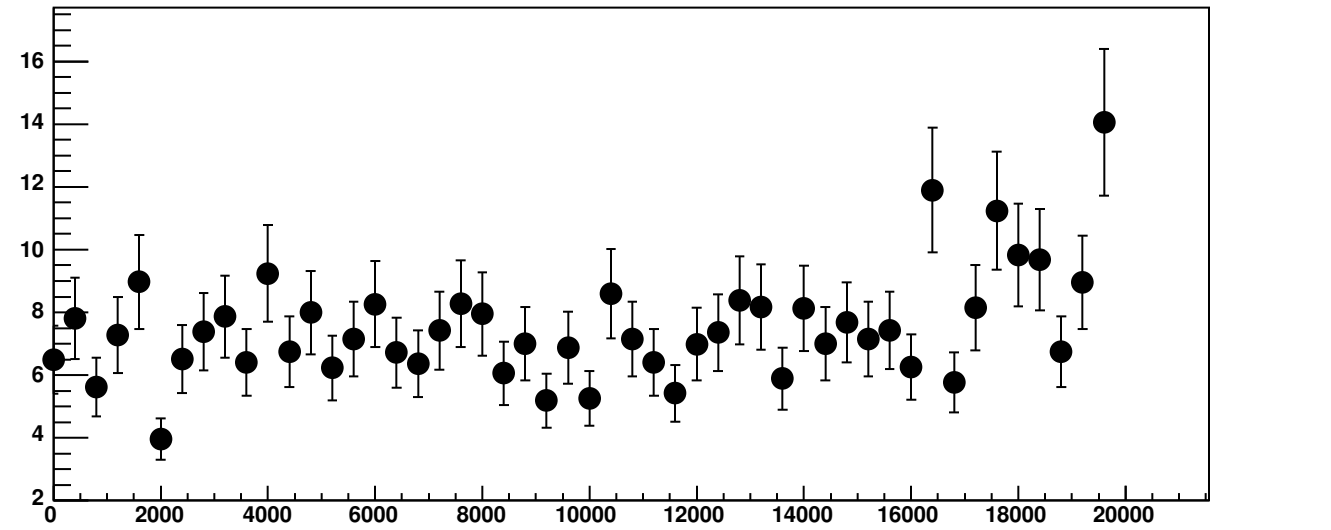

Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

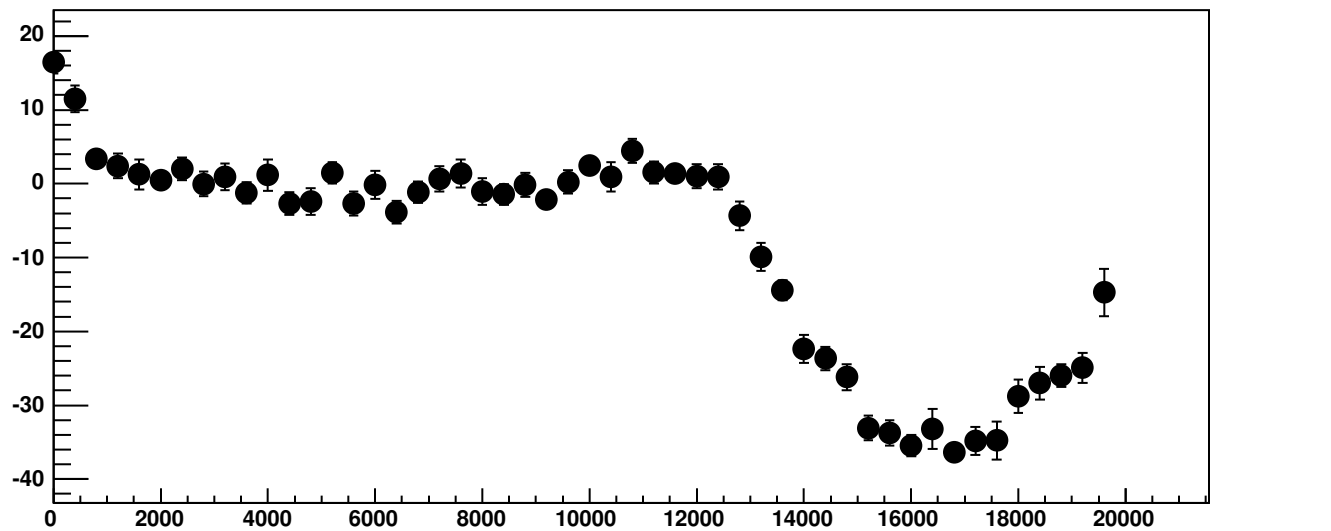
Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

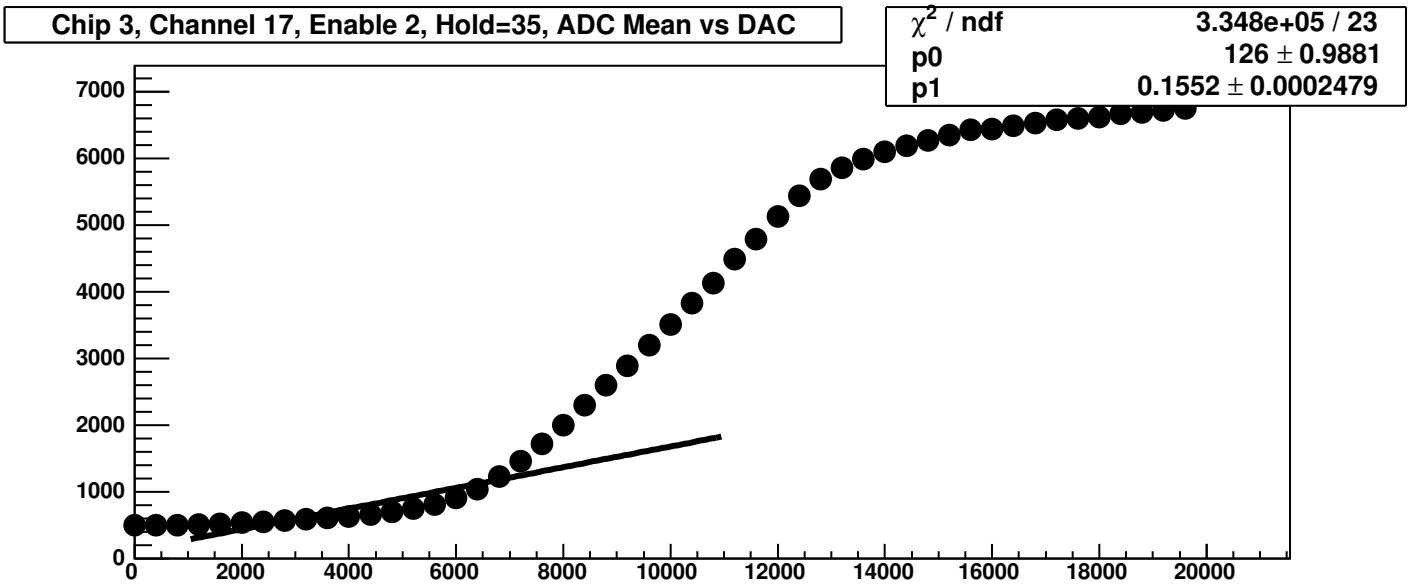


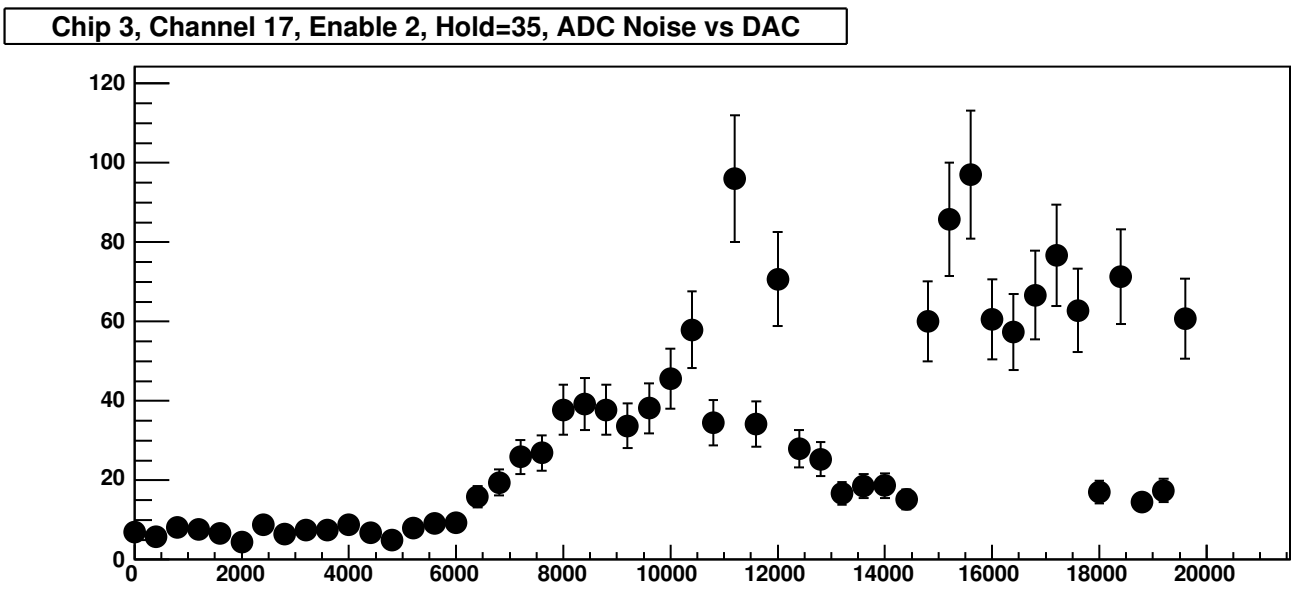
Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

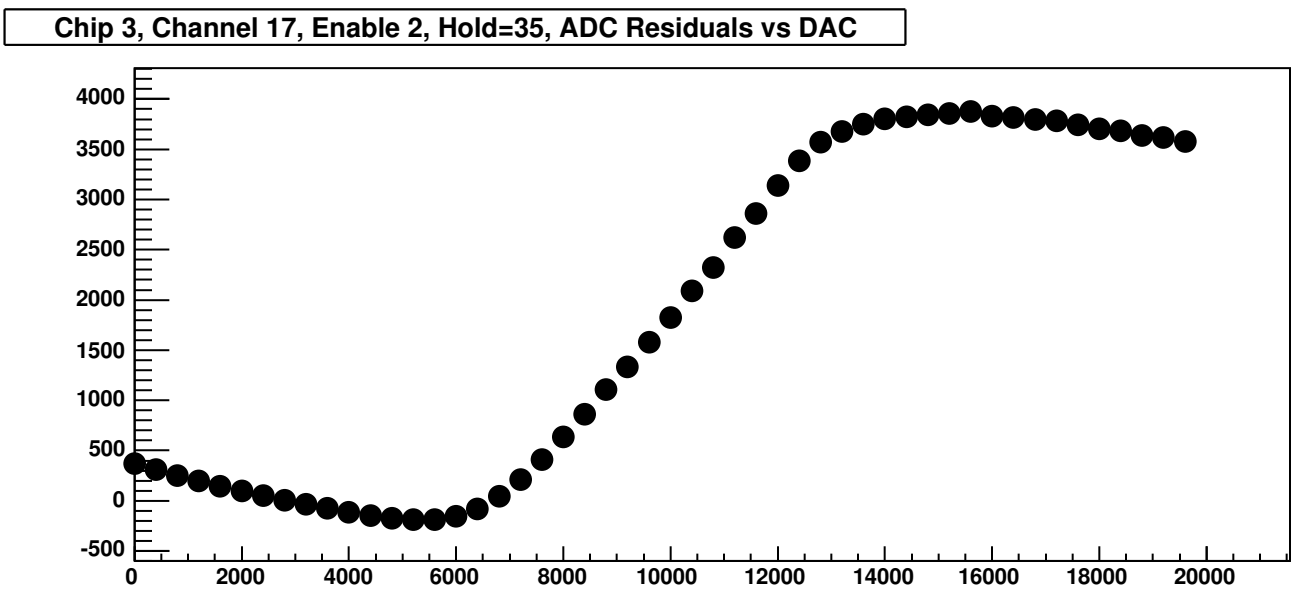


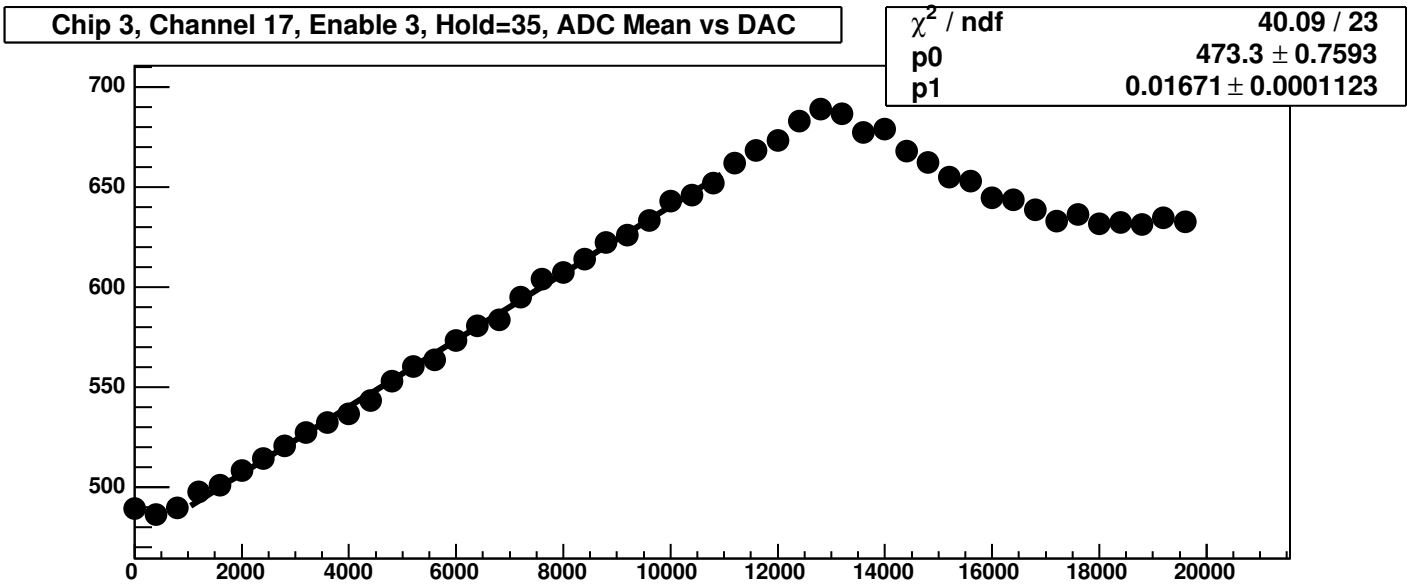
Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

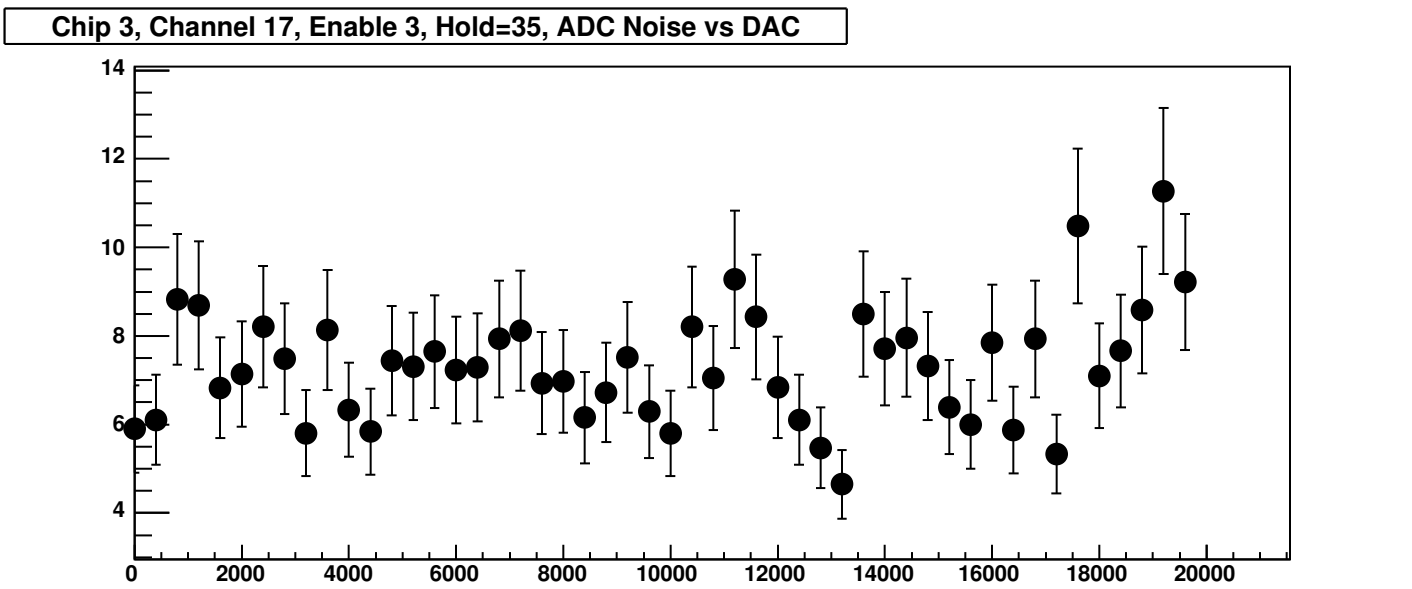


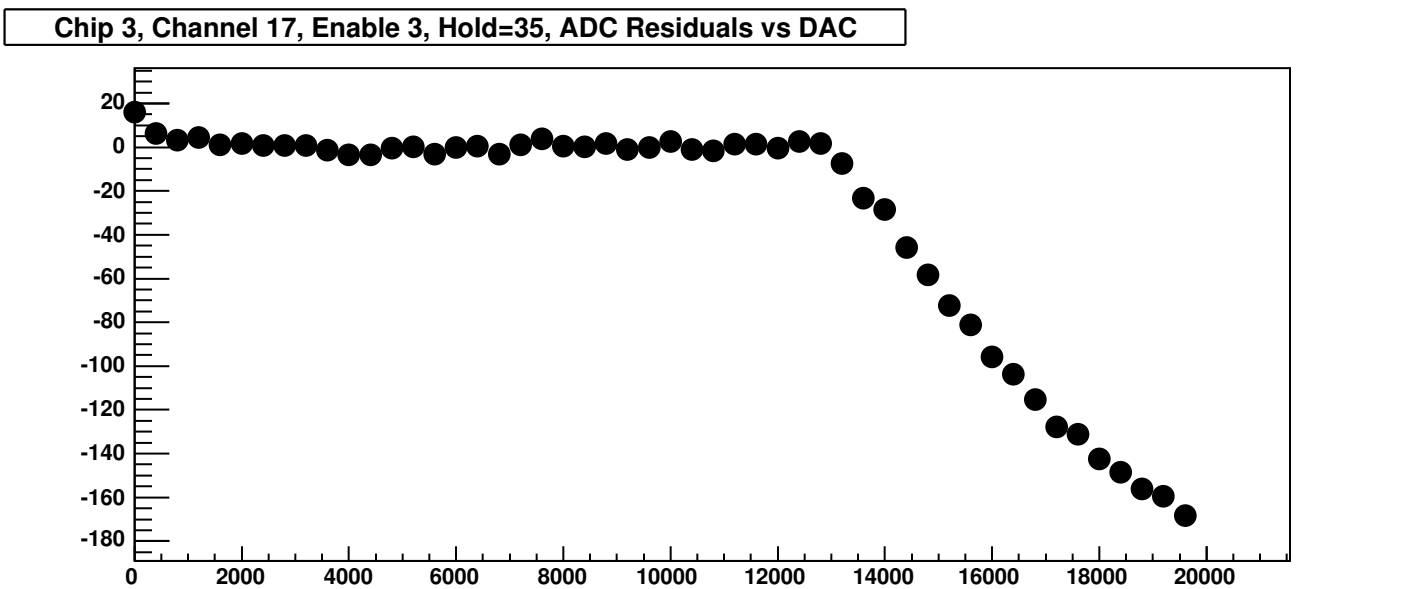

Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3.348e+05 / 23
p0 126 ± 0.9881
p1 0.1552 ± 0.0002479
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

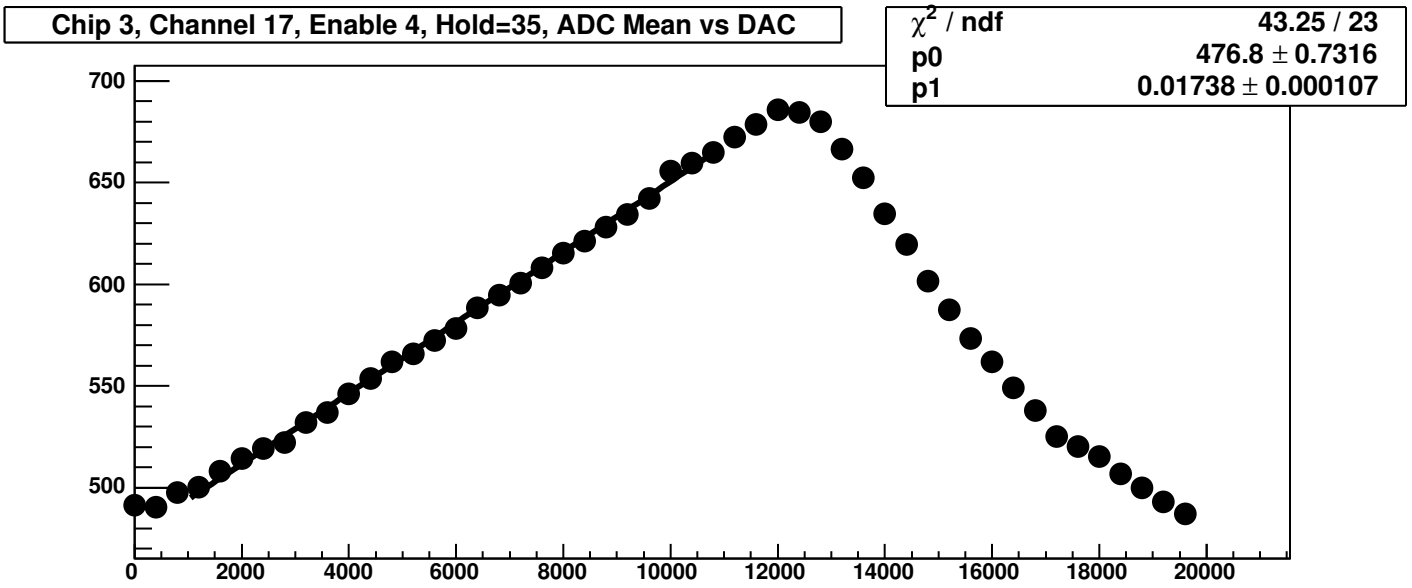

Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

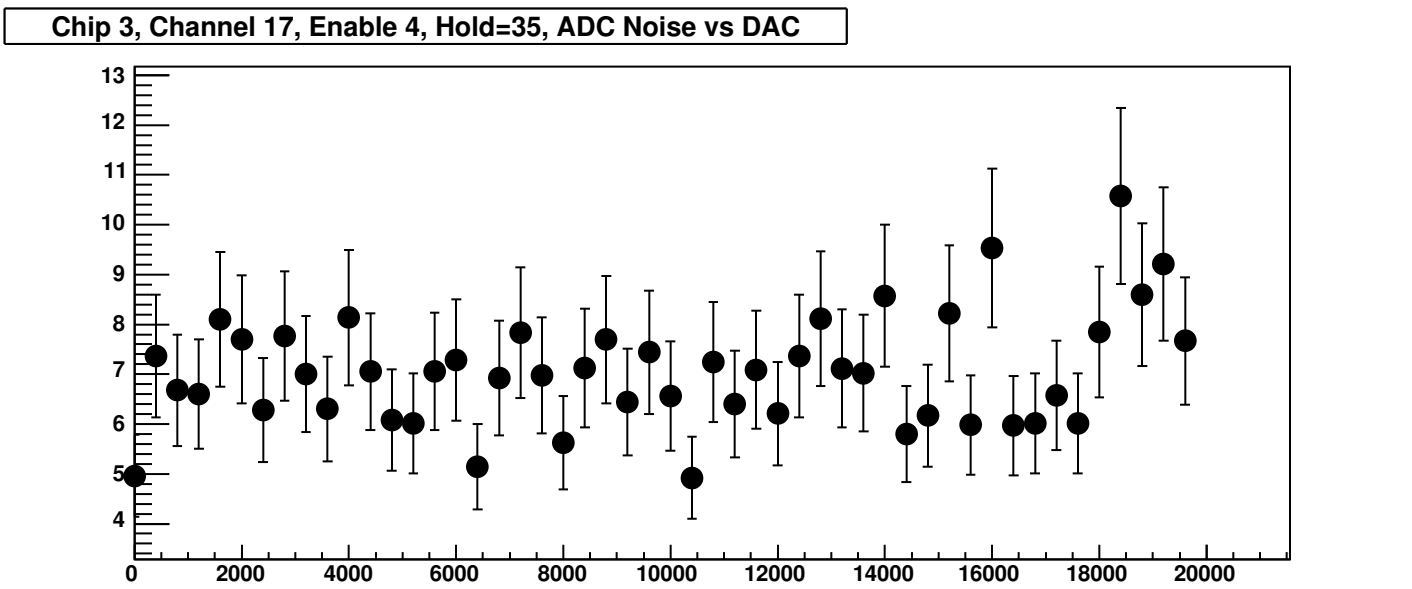

Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

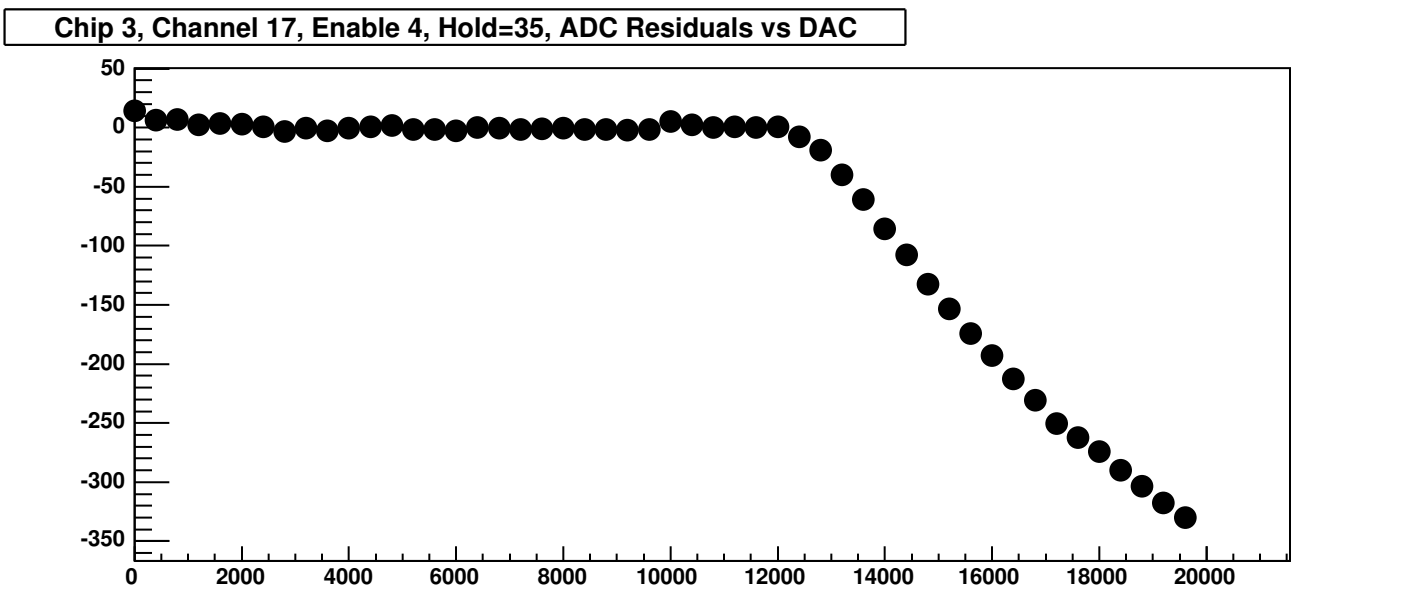

Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.09 / 23
p0 473.3 ± 0.7593
p1 0.01671 ± 0.0001123
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

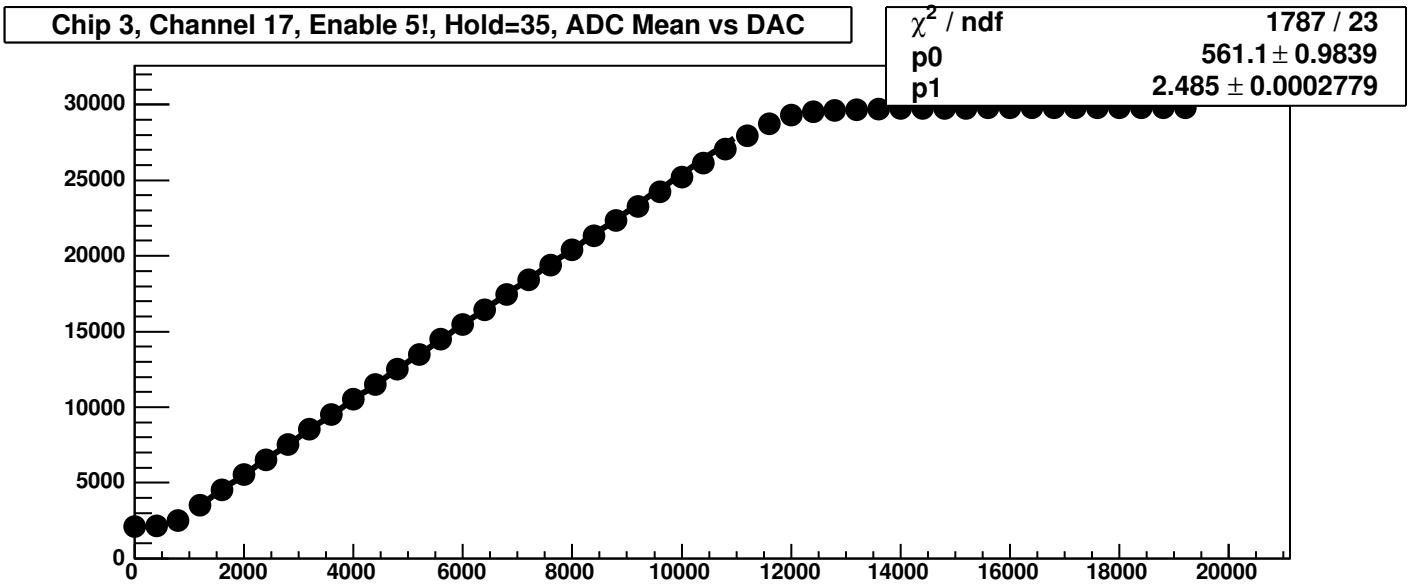

Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

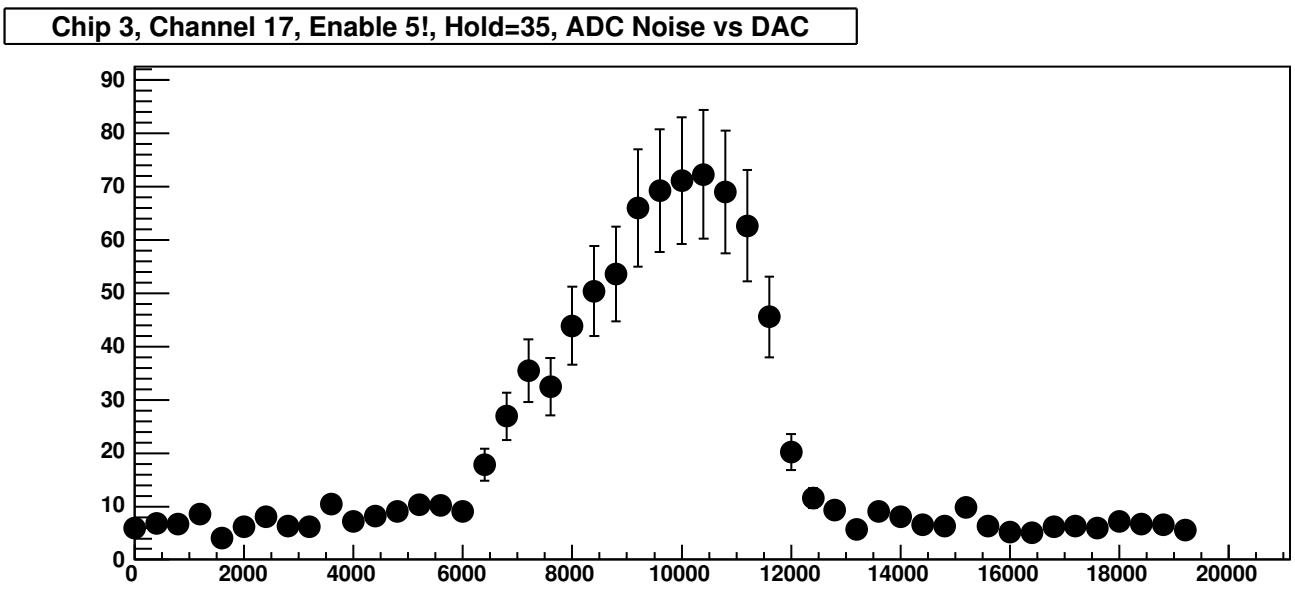

Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

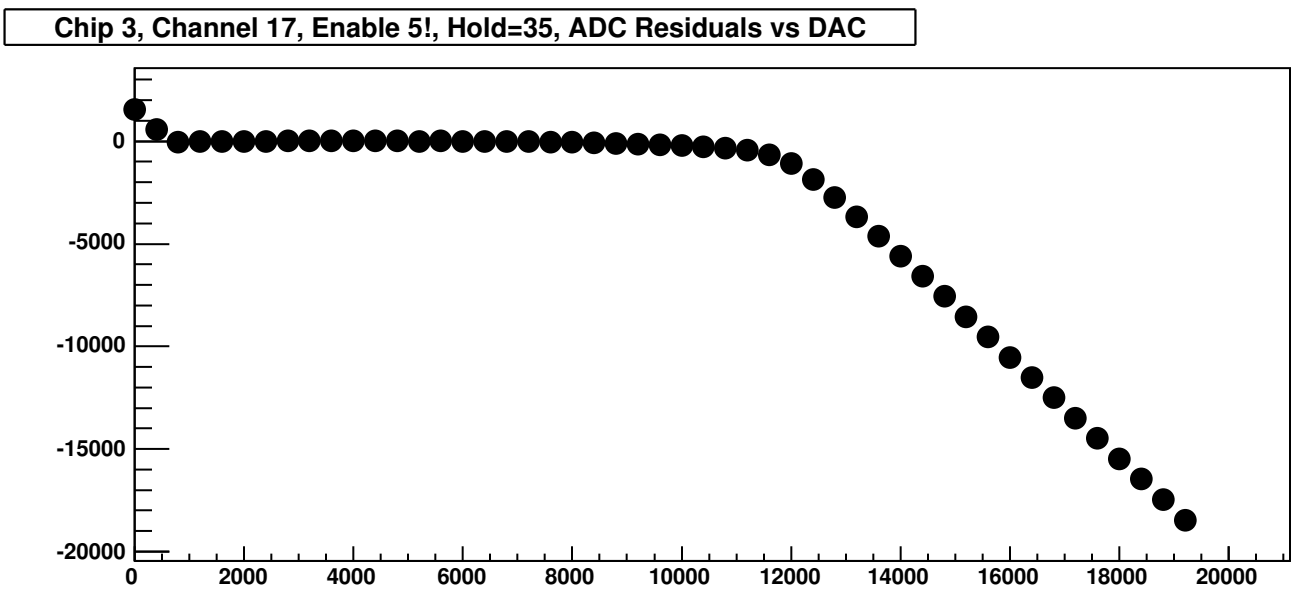

Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.25 / 23
p0 476.8 ± 0.7316
p1 0.01738 ± 0.000107
700
650
600
550
500
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

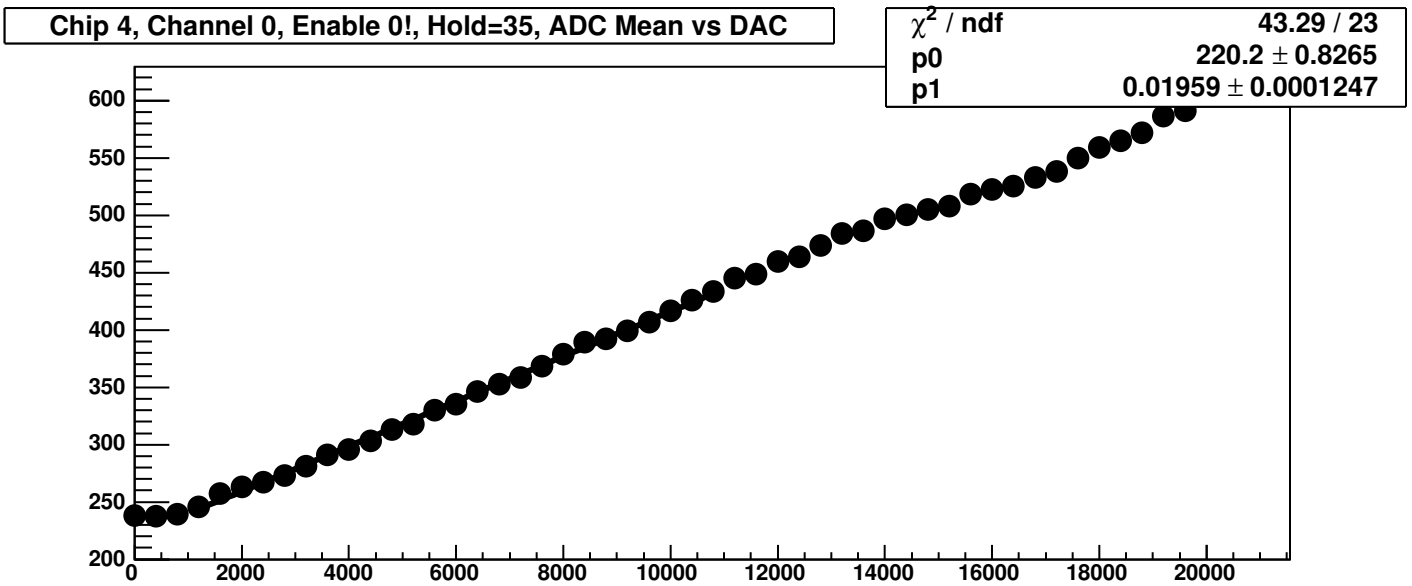

Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

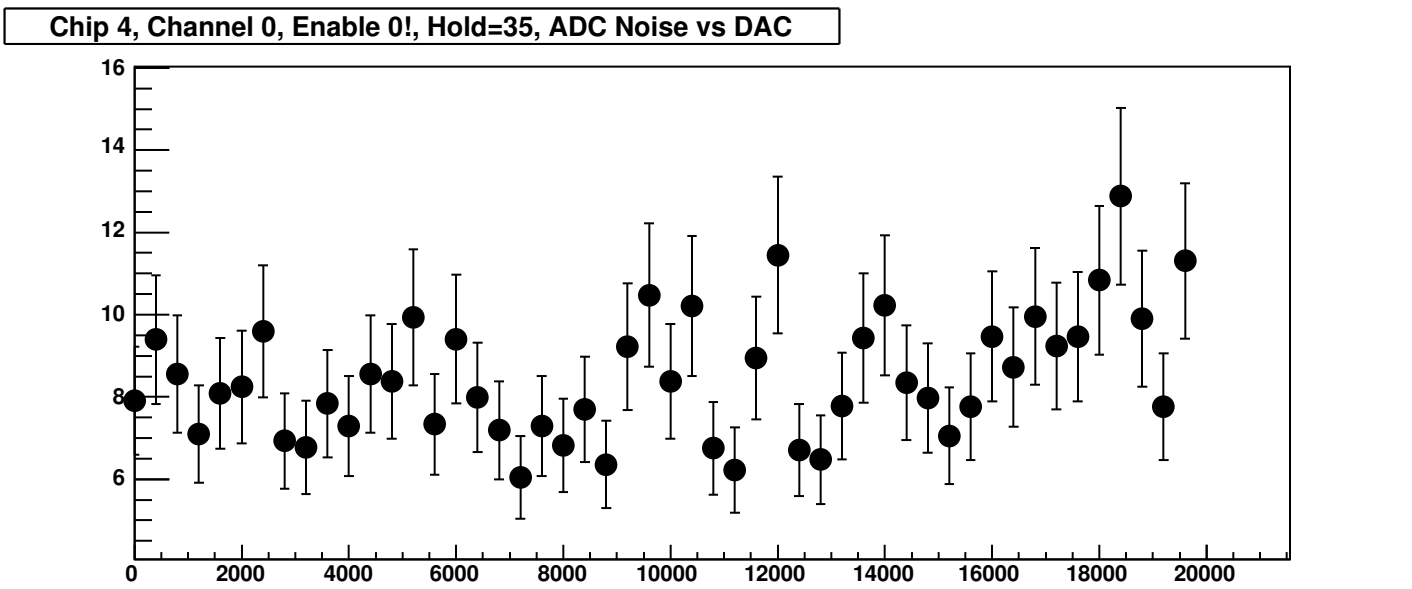

Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

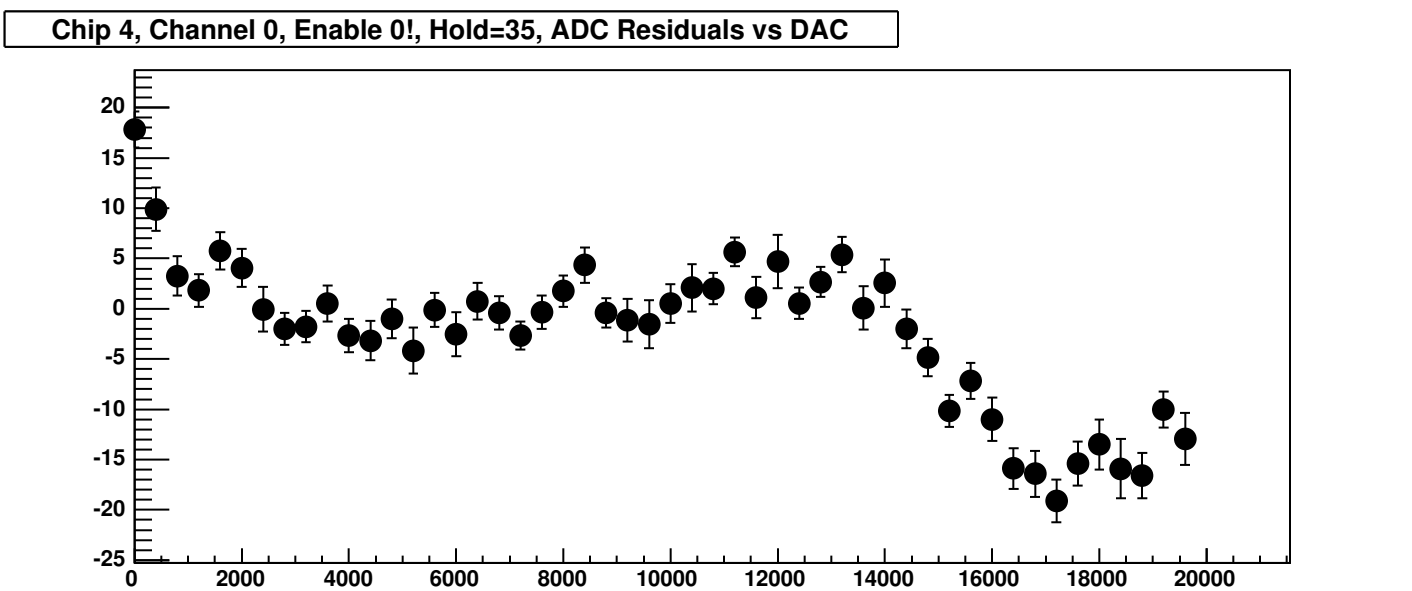

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1787 / 23
p0 561.1 ± 0.9839
p1 2.485 ± 0.0002779
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

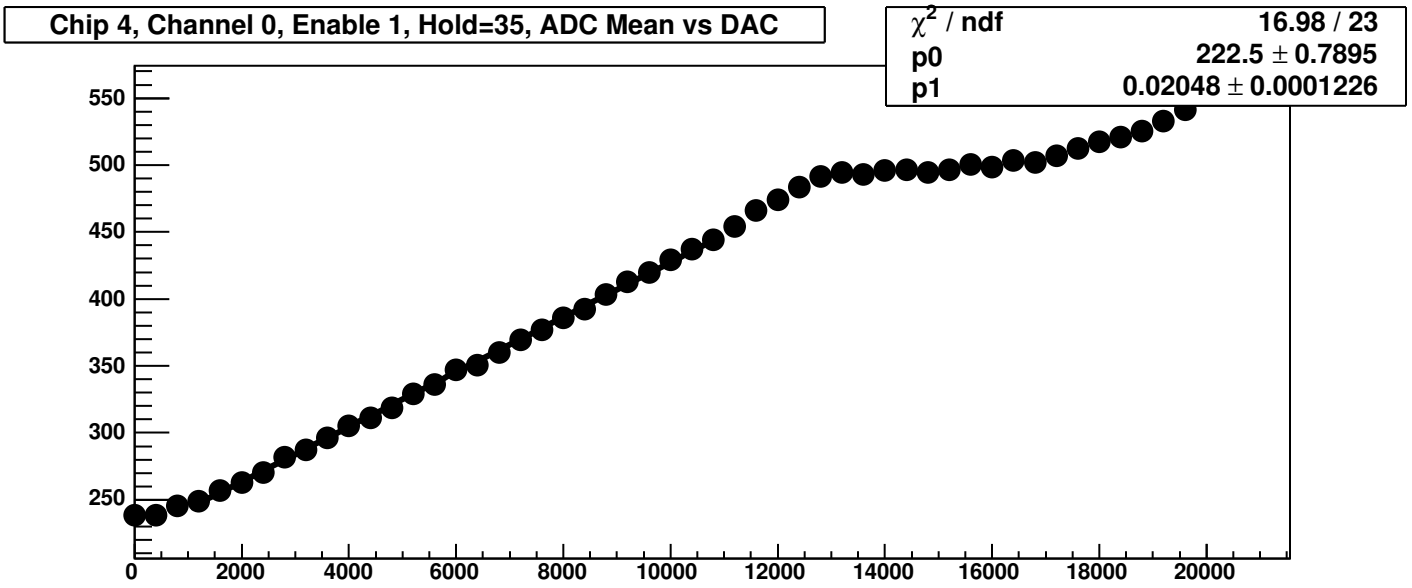

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

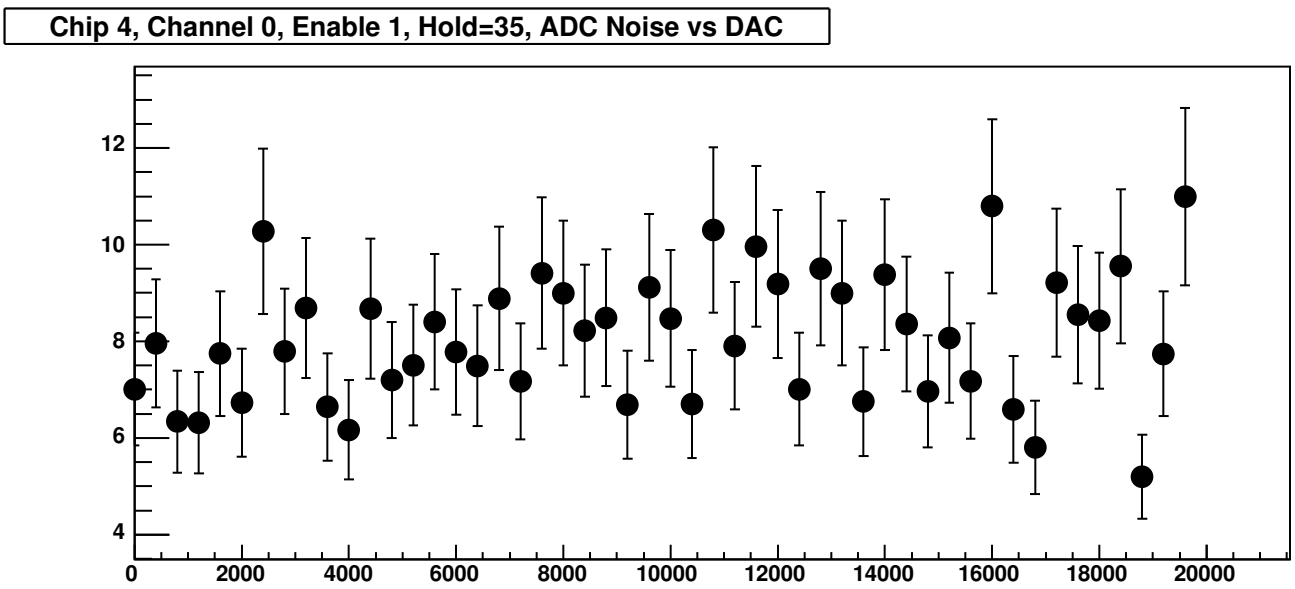

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

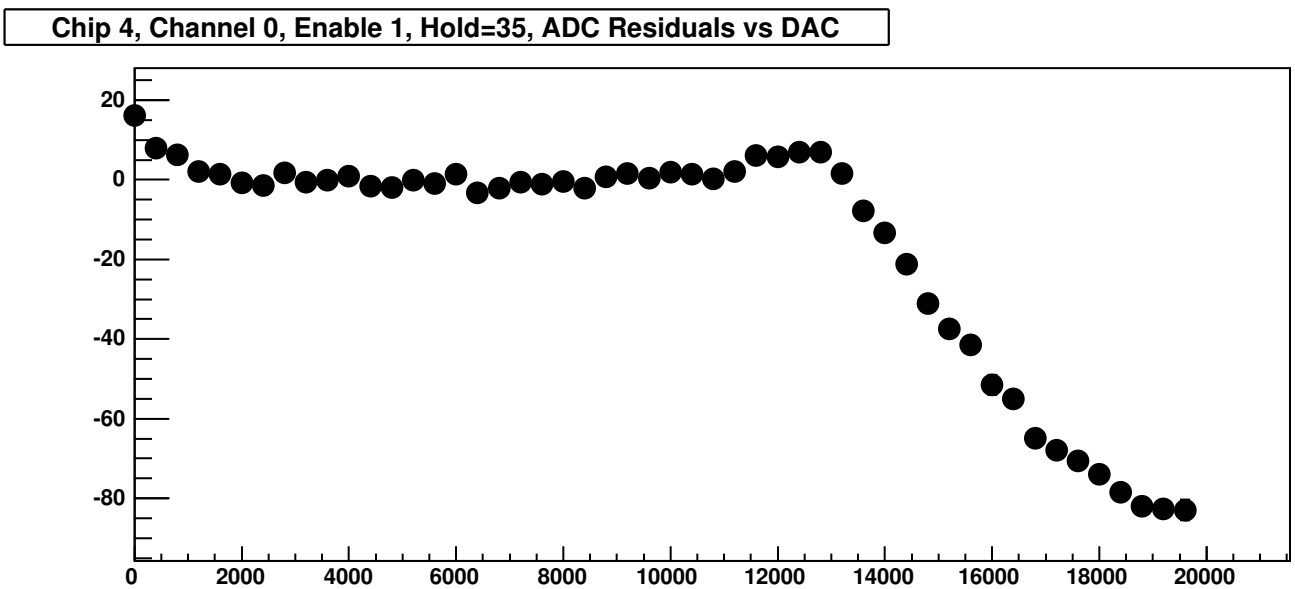

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 43.29 / 23
p0 220.2 ± 0.8265
p1 0.01959 ± 0.0001247
600
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

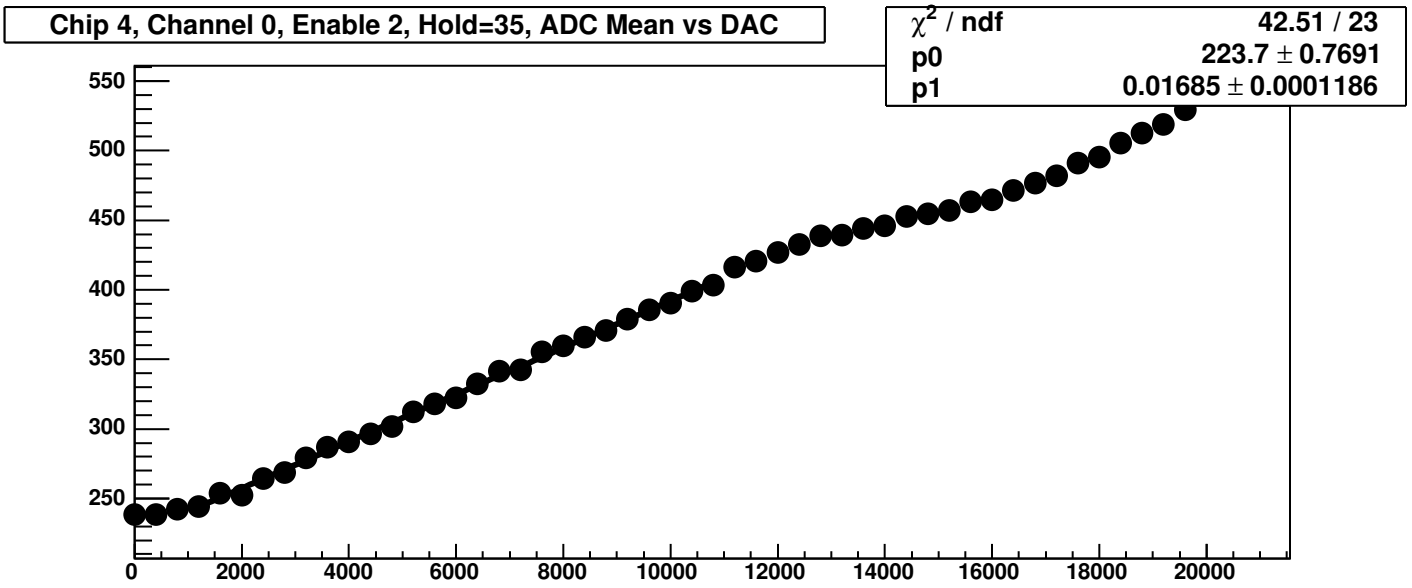

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

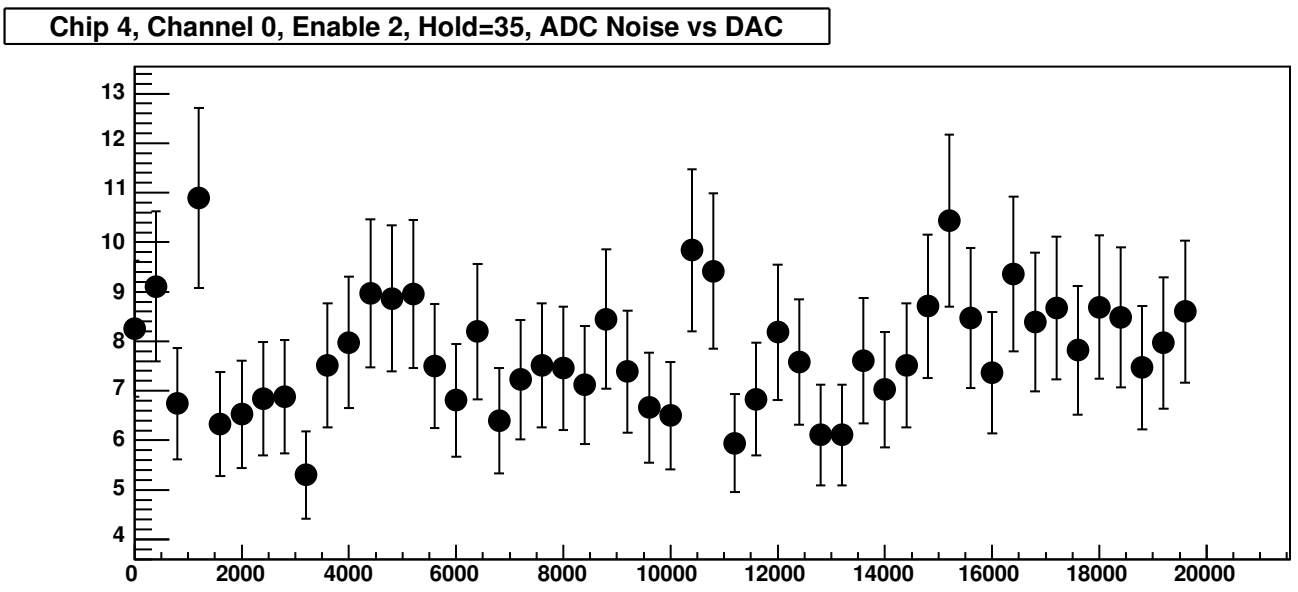

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

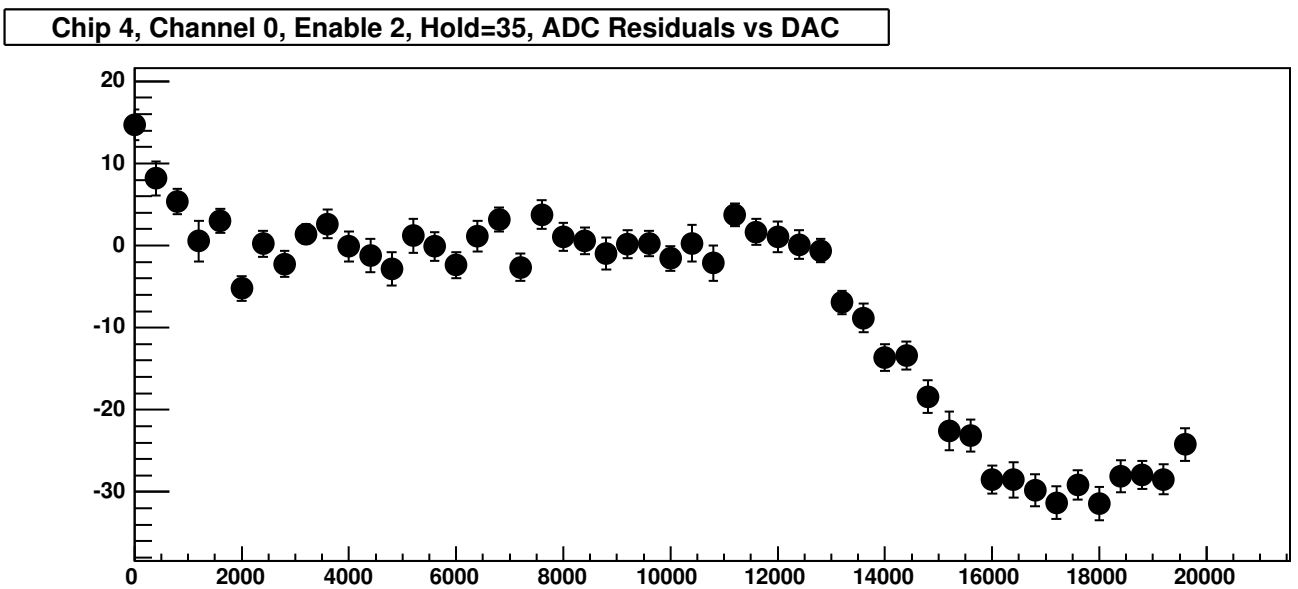

Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 16.98 / 23
p0 222.5 ± 0.7895
p1 0.02048 ± 0.0001226
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

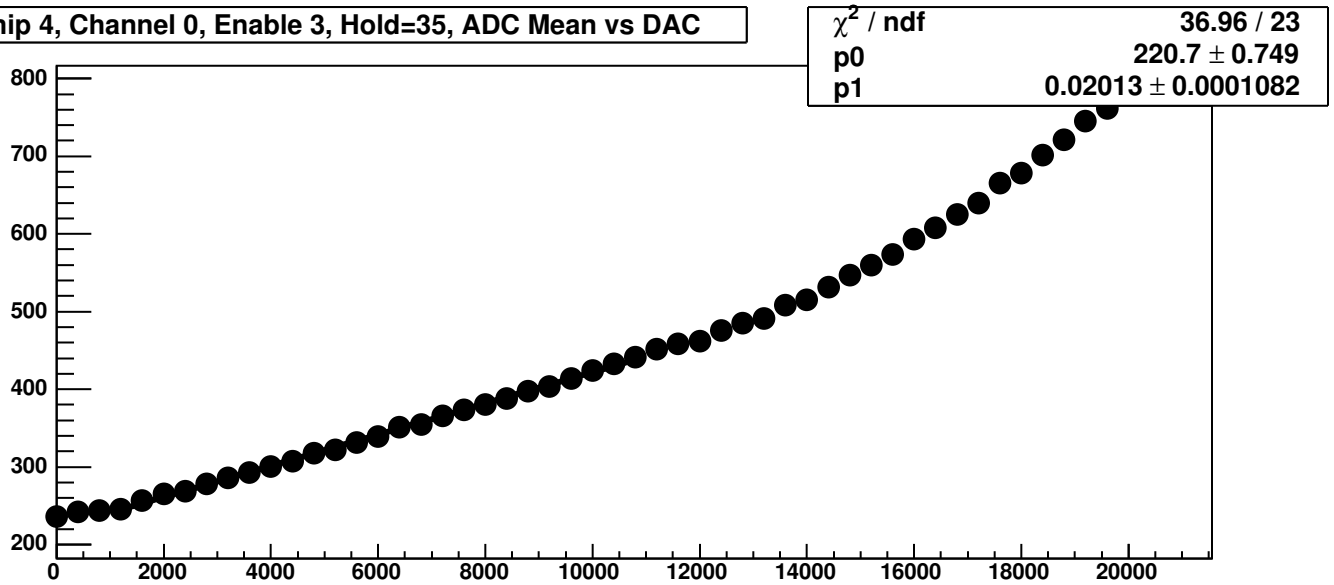

Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 42.51 / 23
p0 223.7 ± 0.7691
p1 0.01685 ± 0.0001186
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

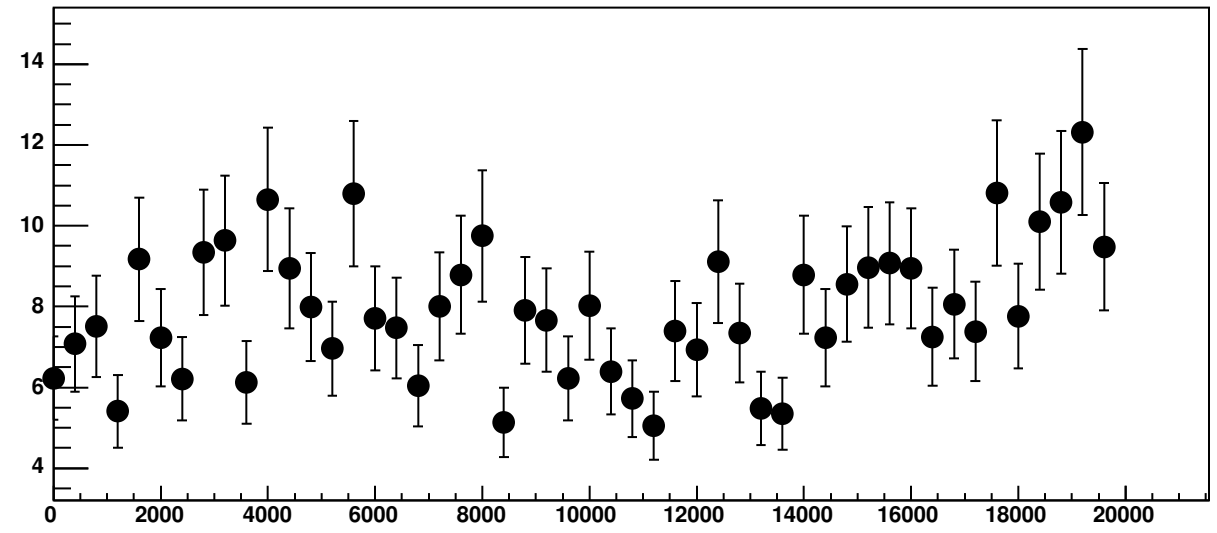

Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

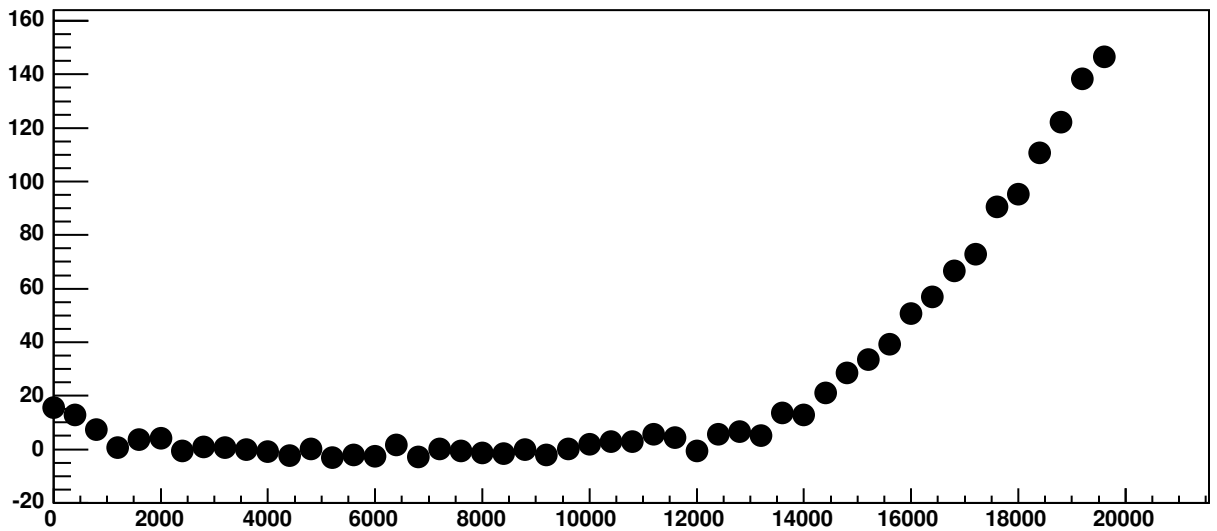
Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

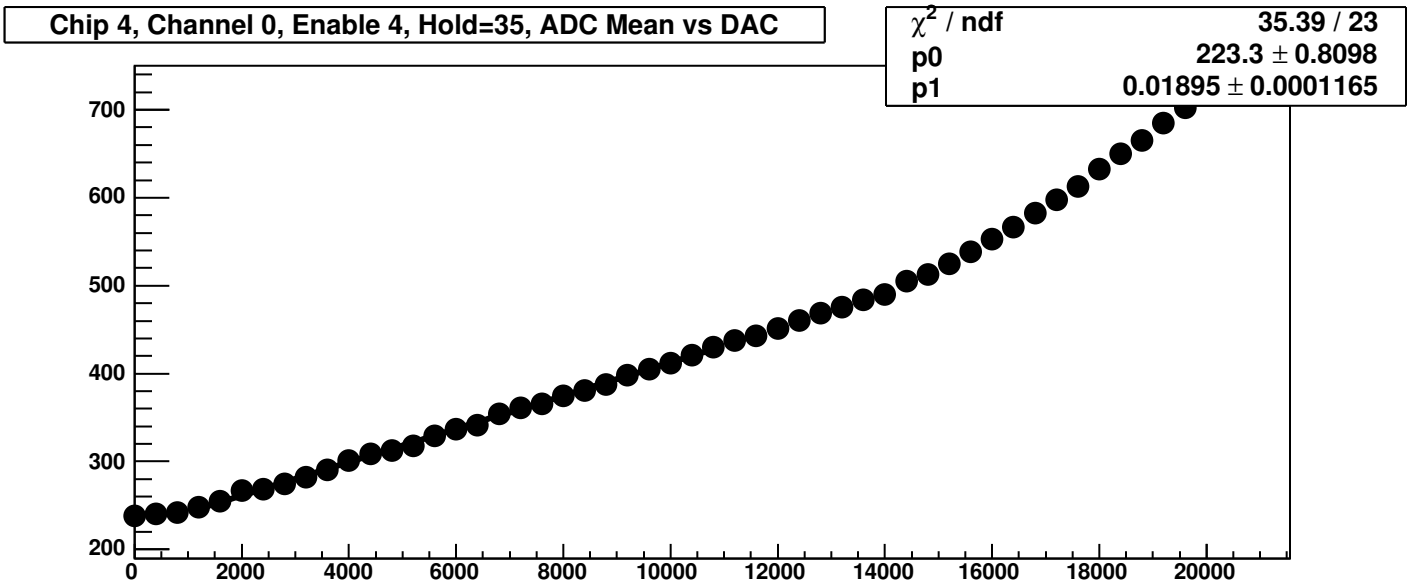


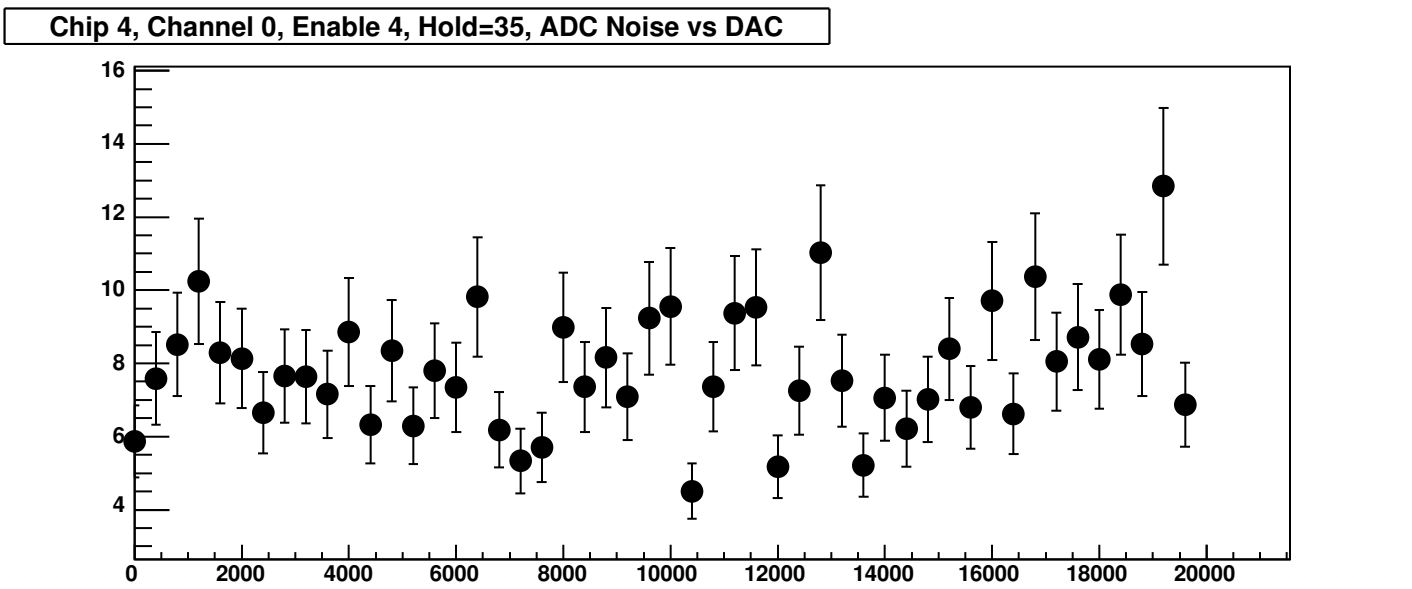
Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC

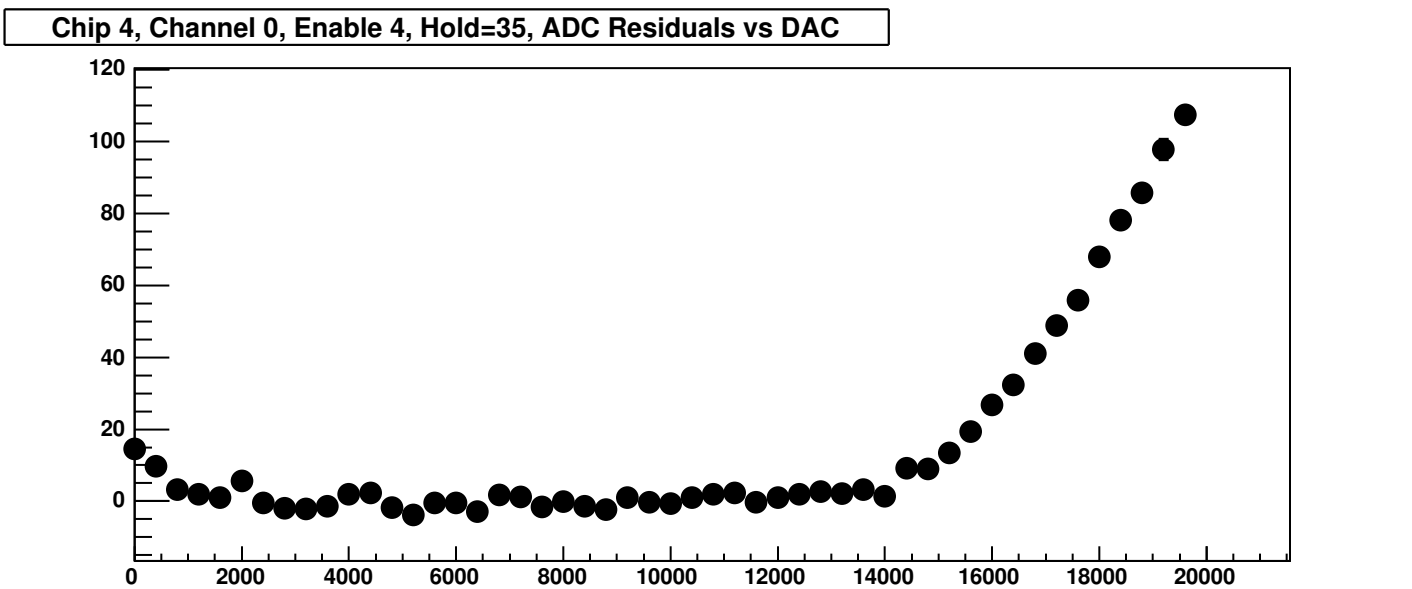


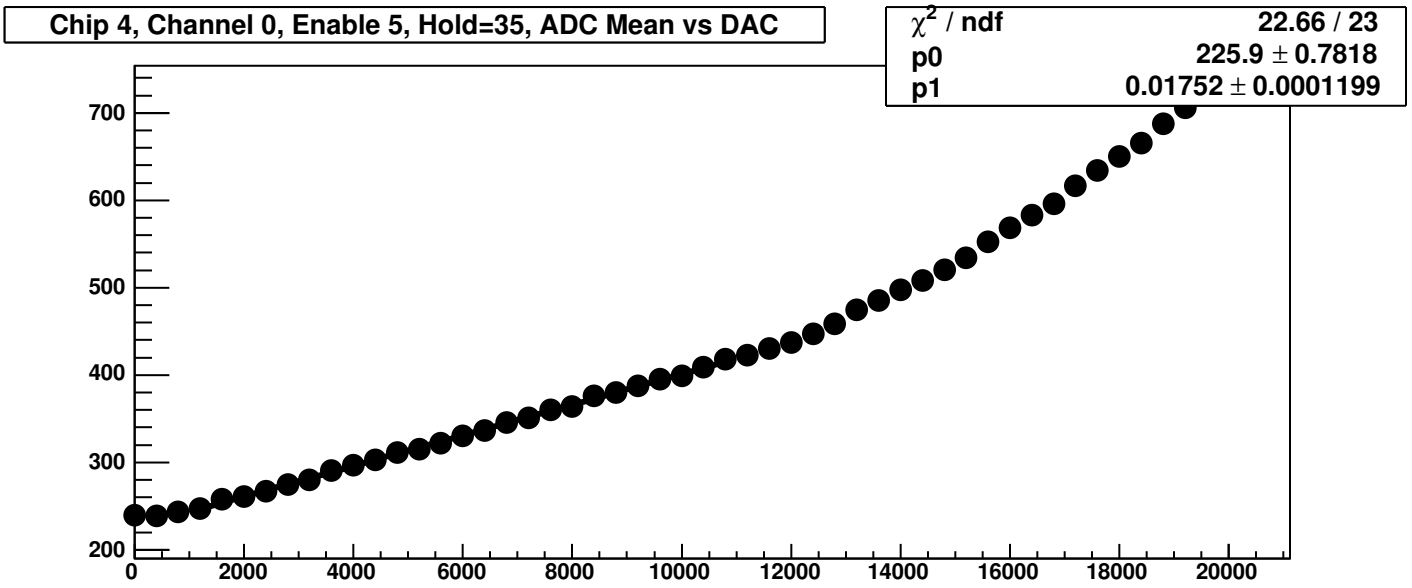
Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

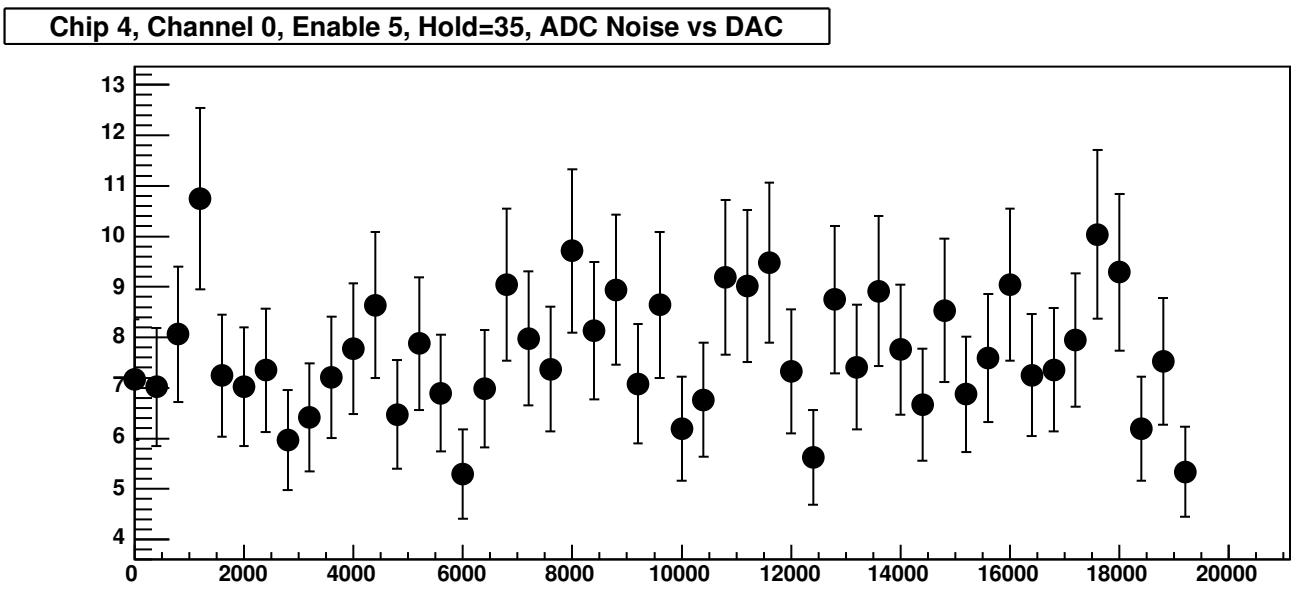


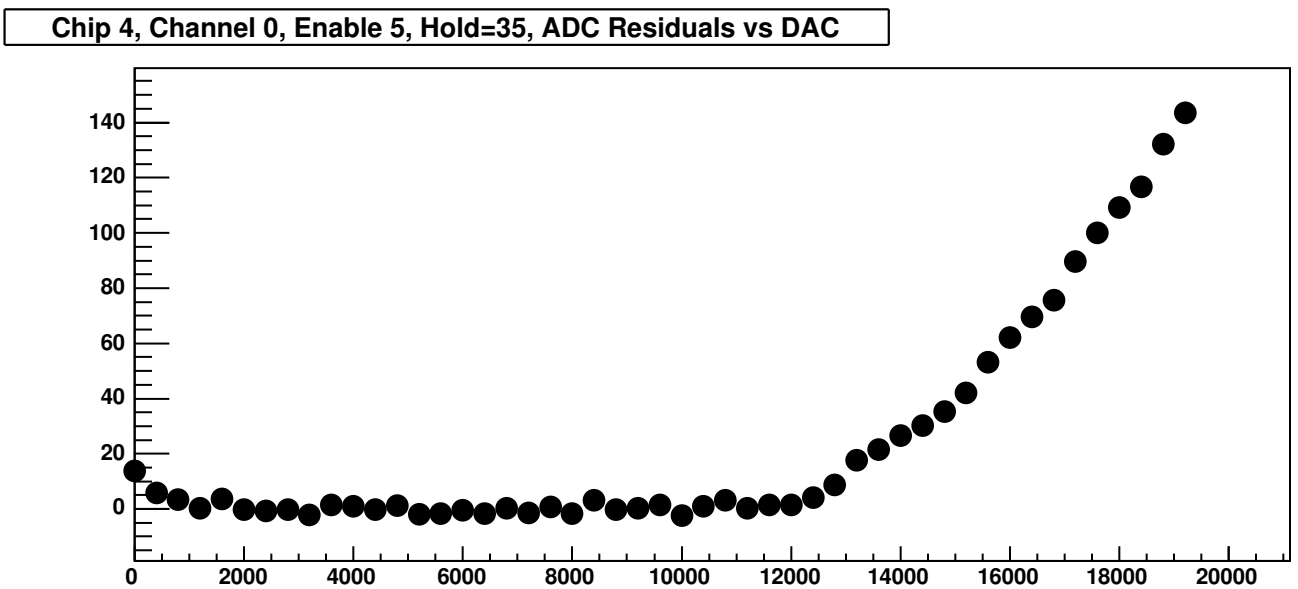

Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.39 / 23
p0 223.3 ± 0.8098
p1 0.01895 ± 0.0001165
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

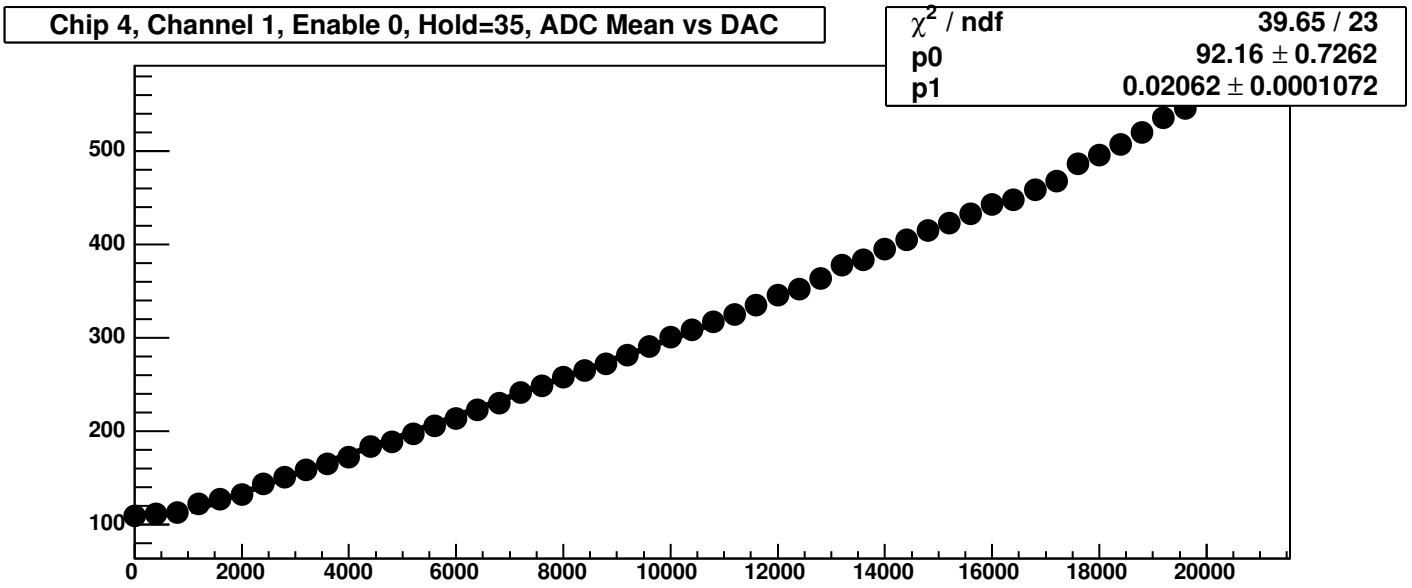

Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

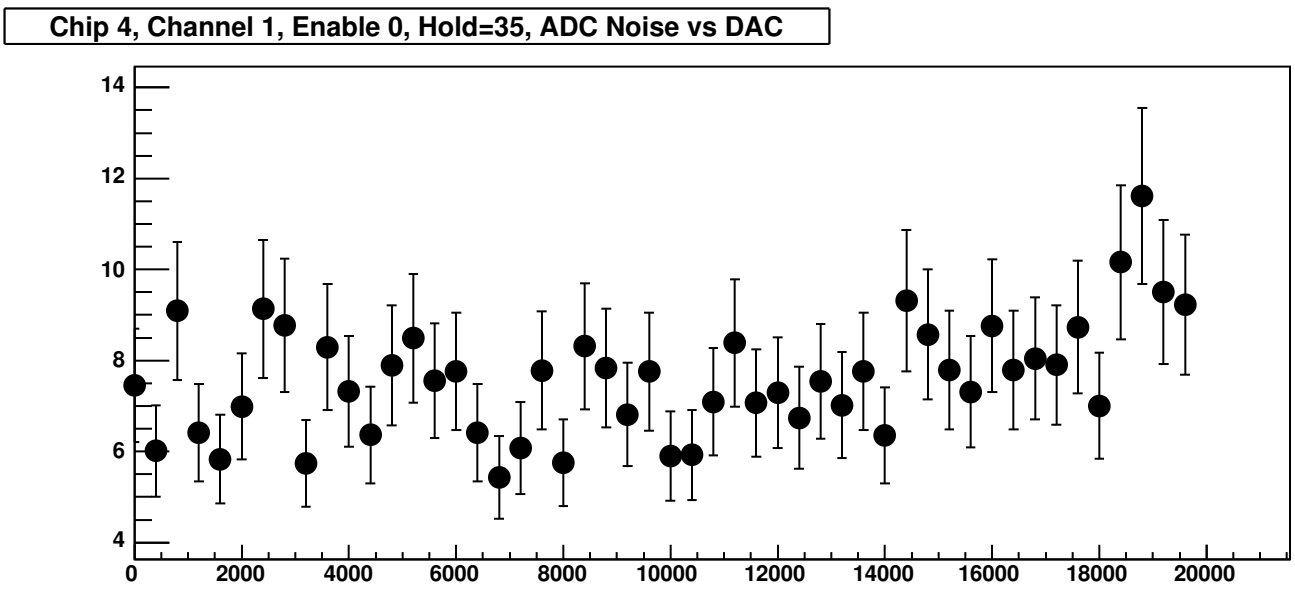

Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

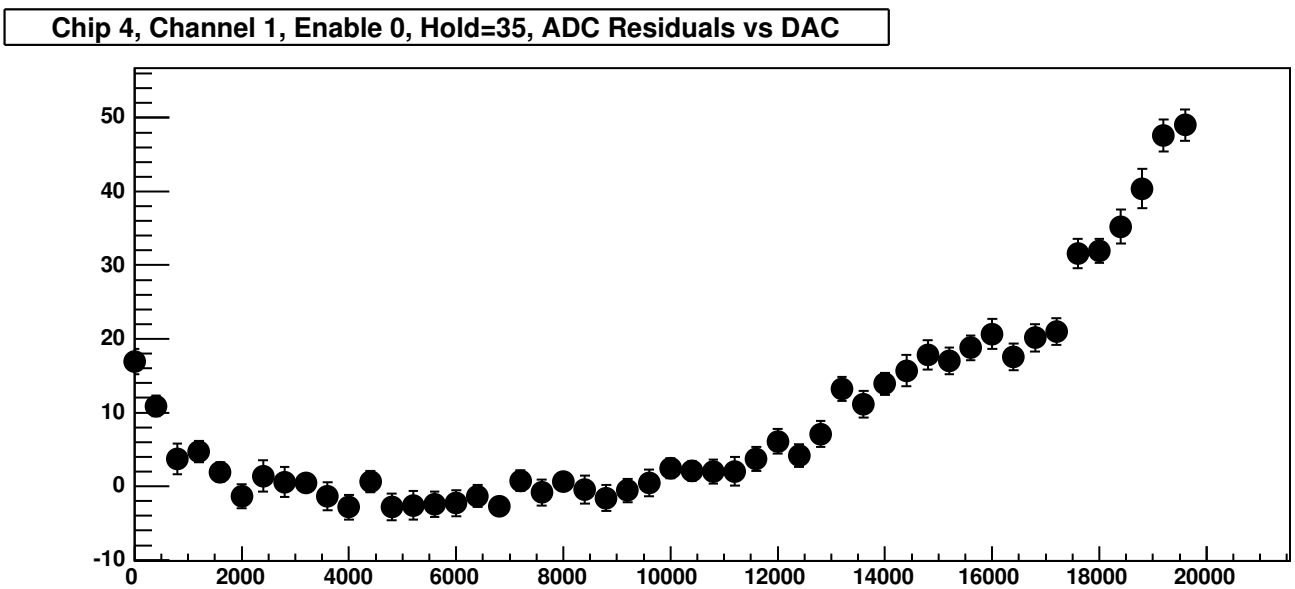

Chip 4, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 22.66 / 23
p0 225.9 ± 0.7818
p1 0.01752 ± 0.0001199
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

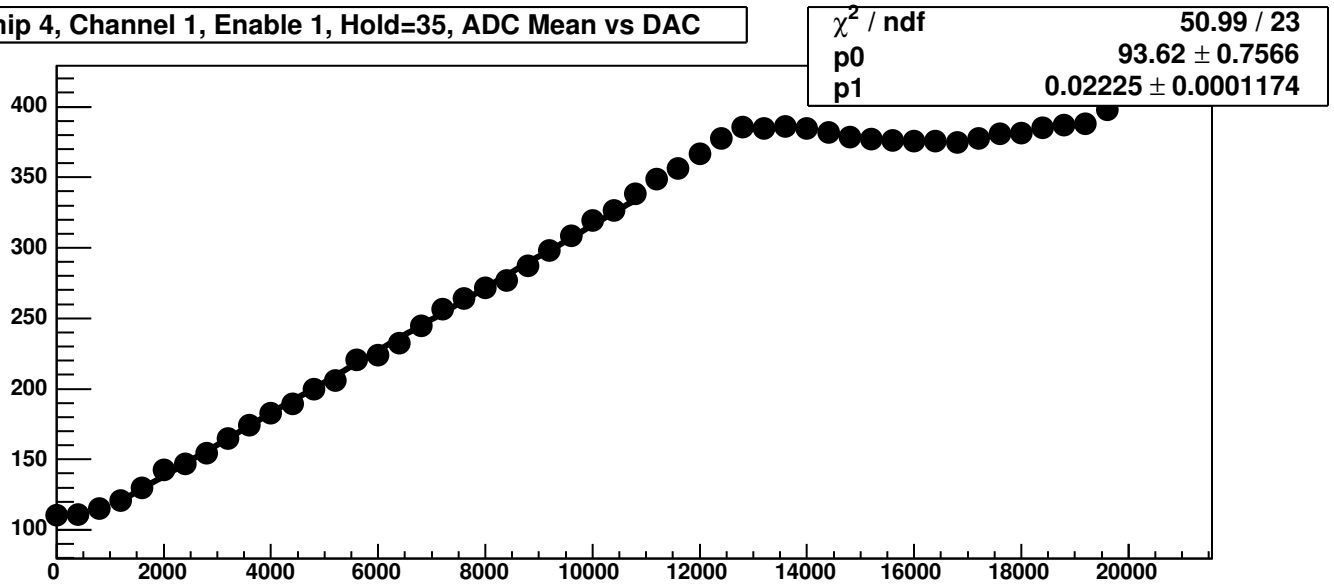

Chip 4, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 39.65 / 23
p0 92.16 ± 0.7262
p1 0.02062 ± 0.0001072
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

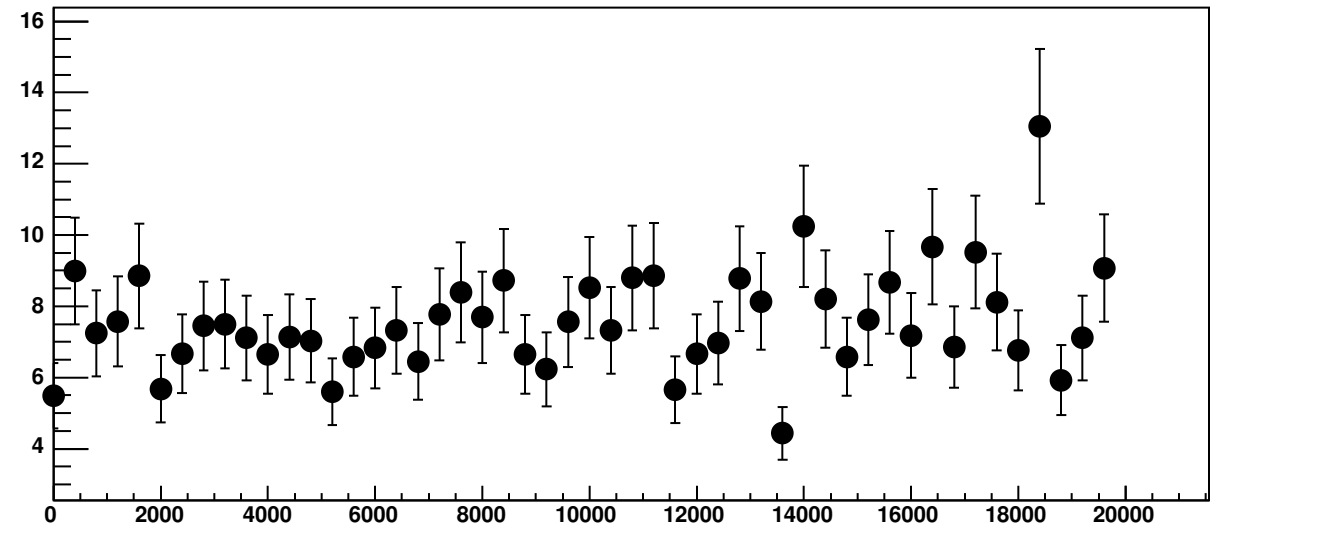

Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

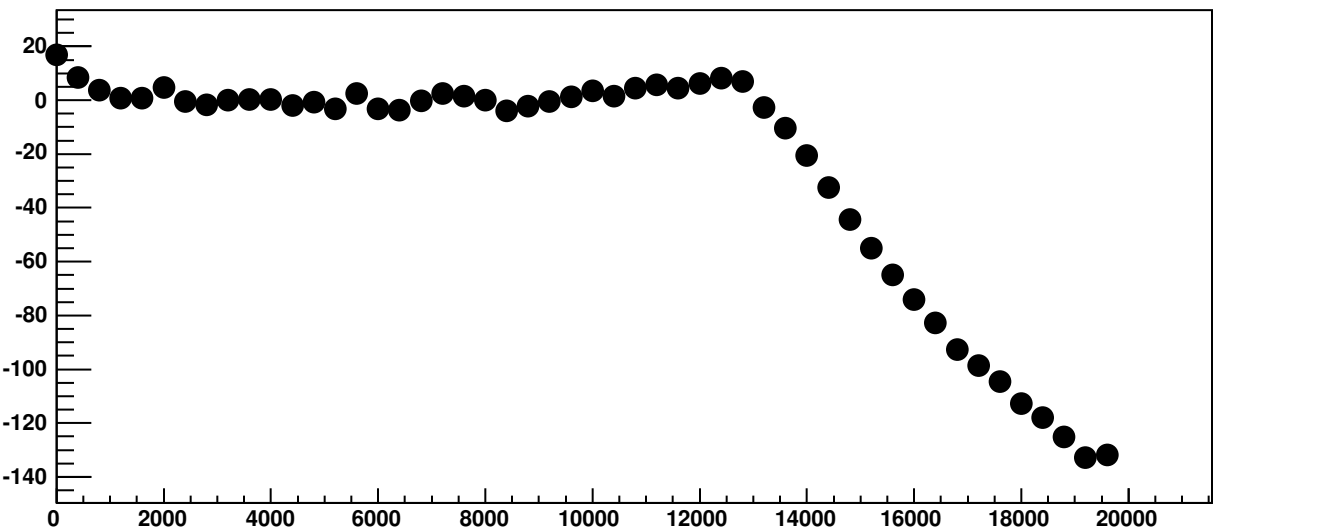
Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



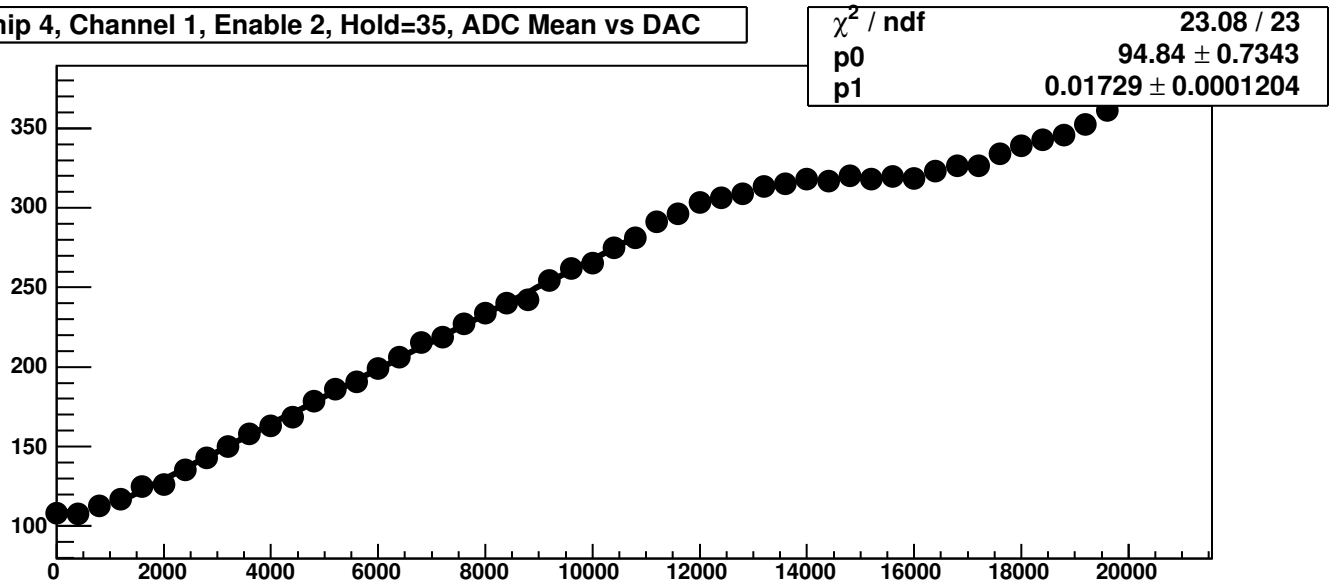
Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



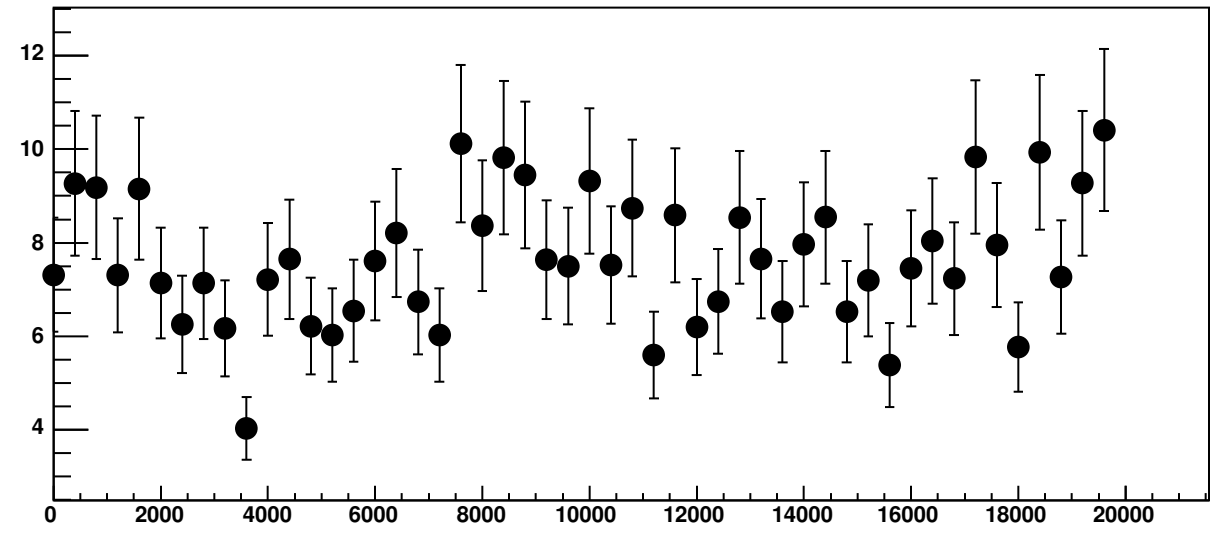
Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



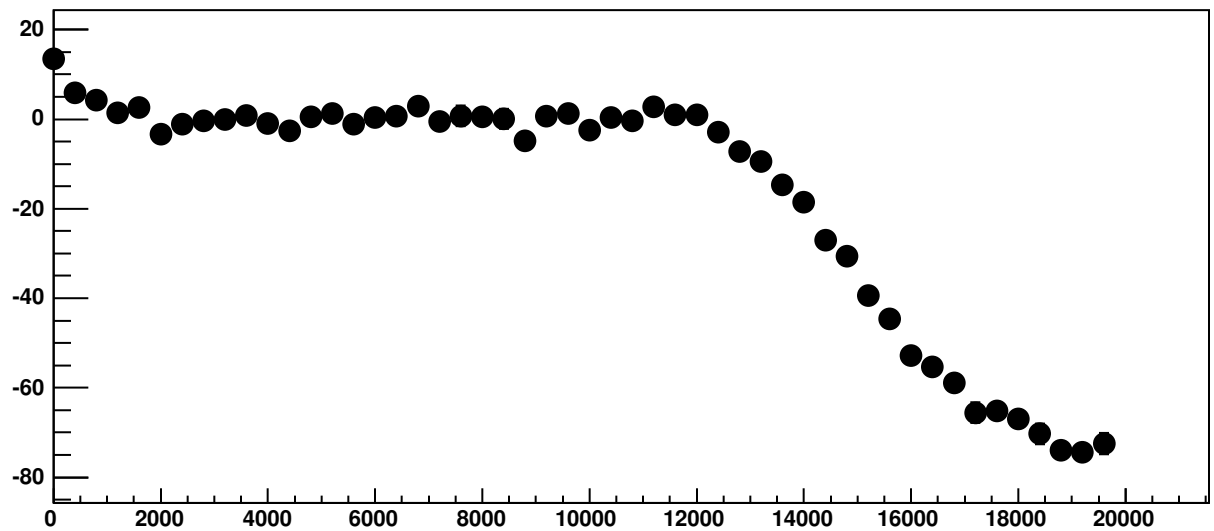
Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

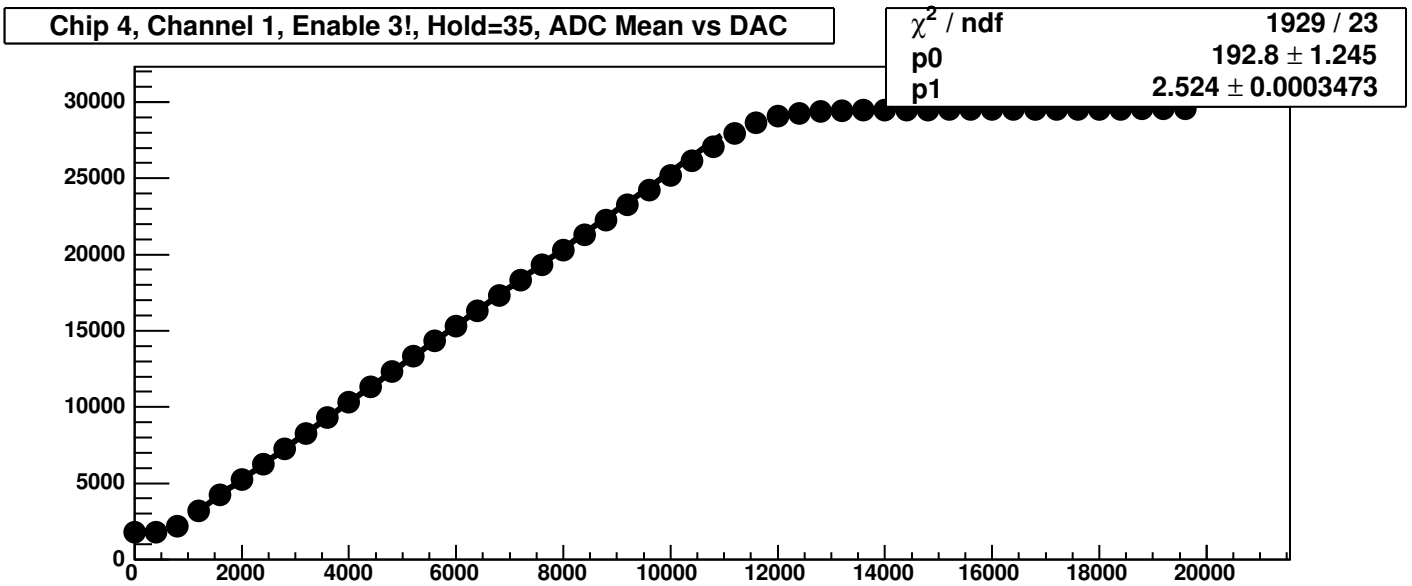


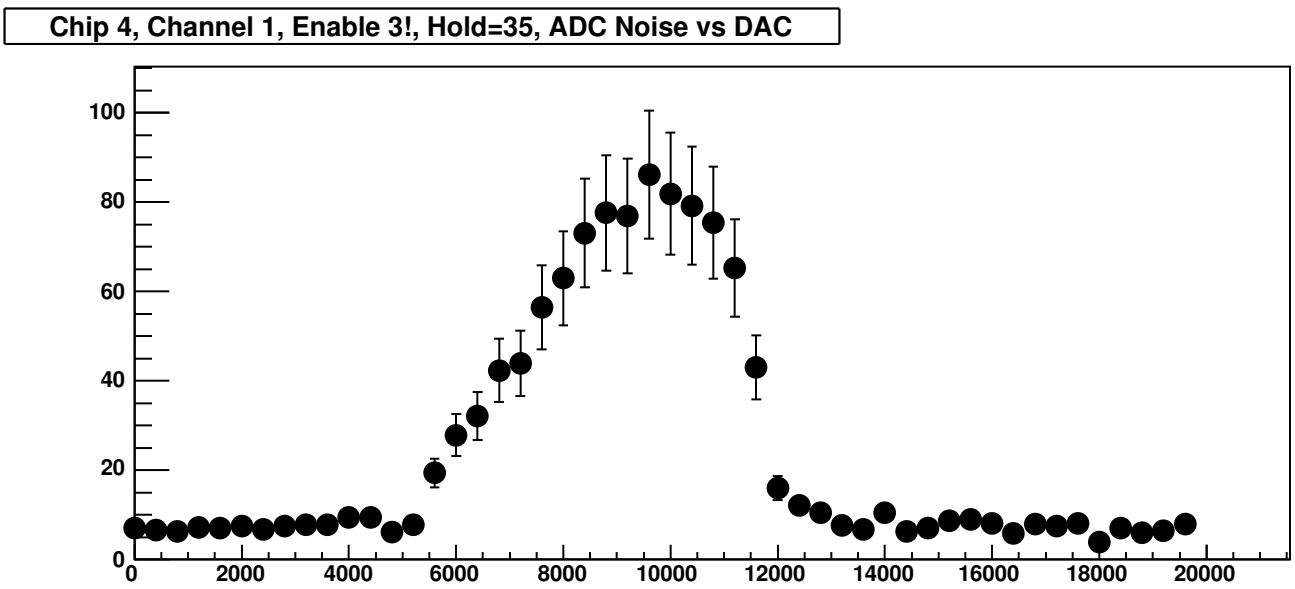
Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

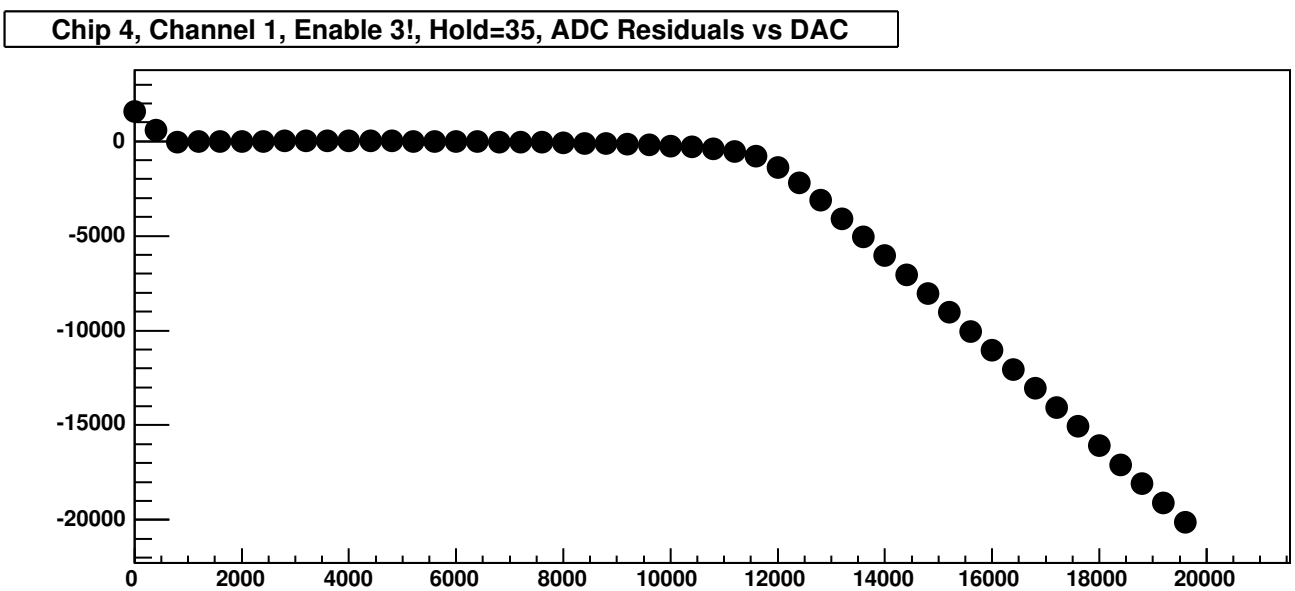


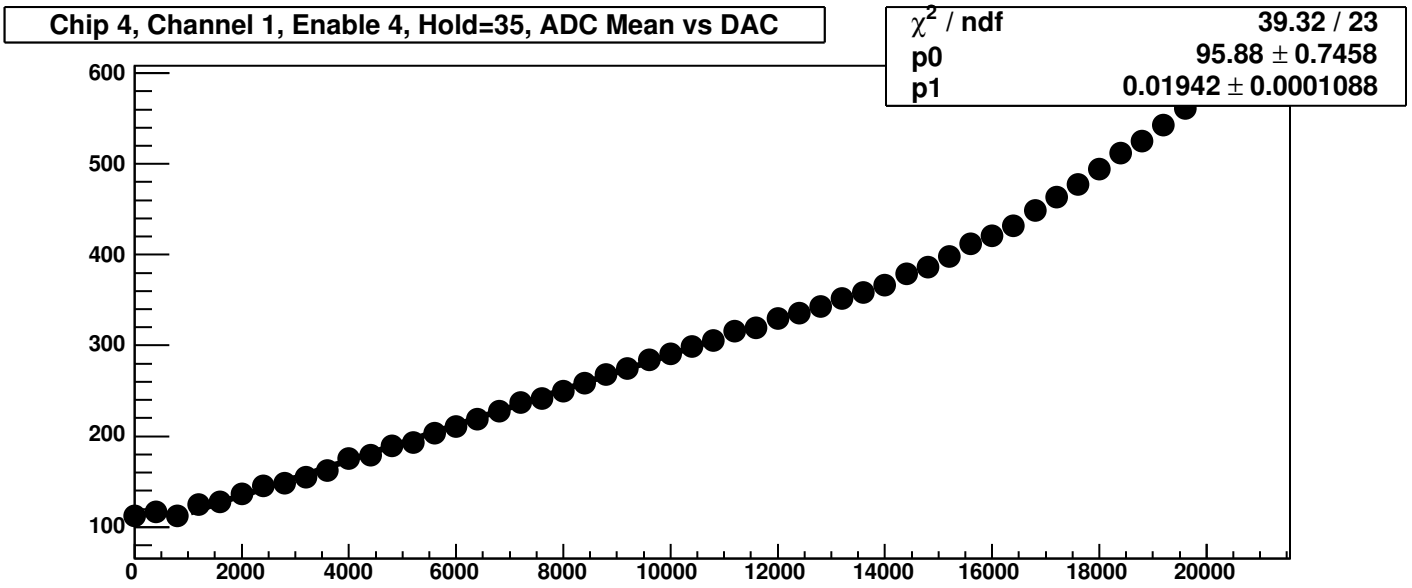
Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

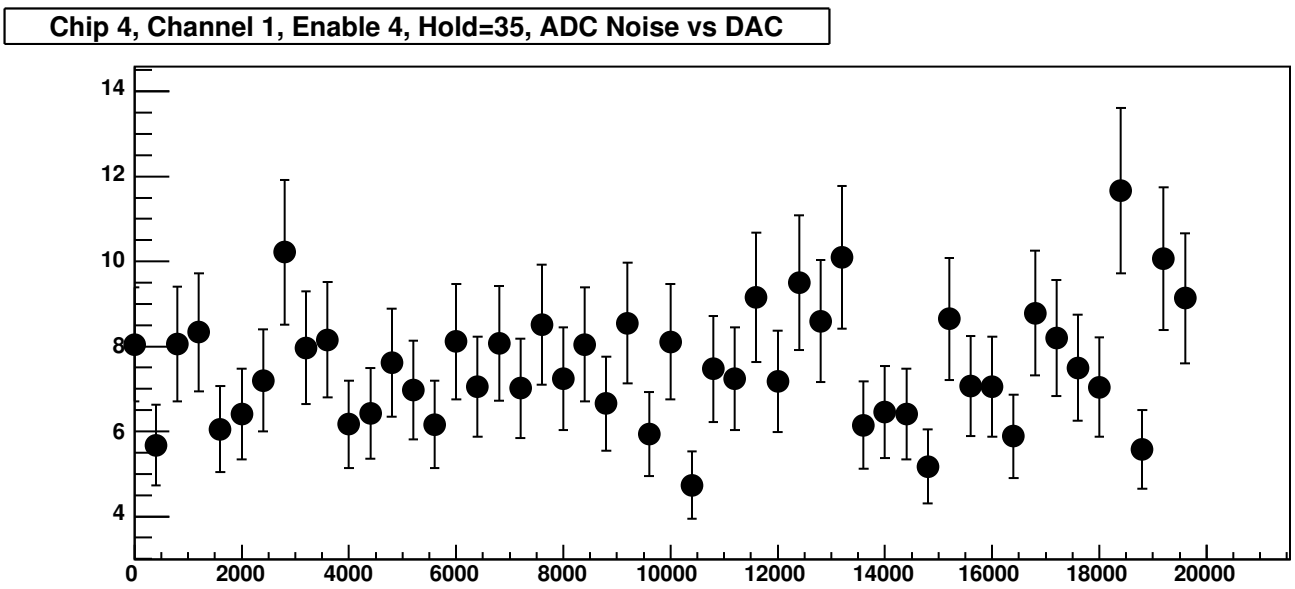


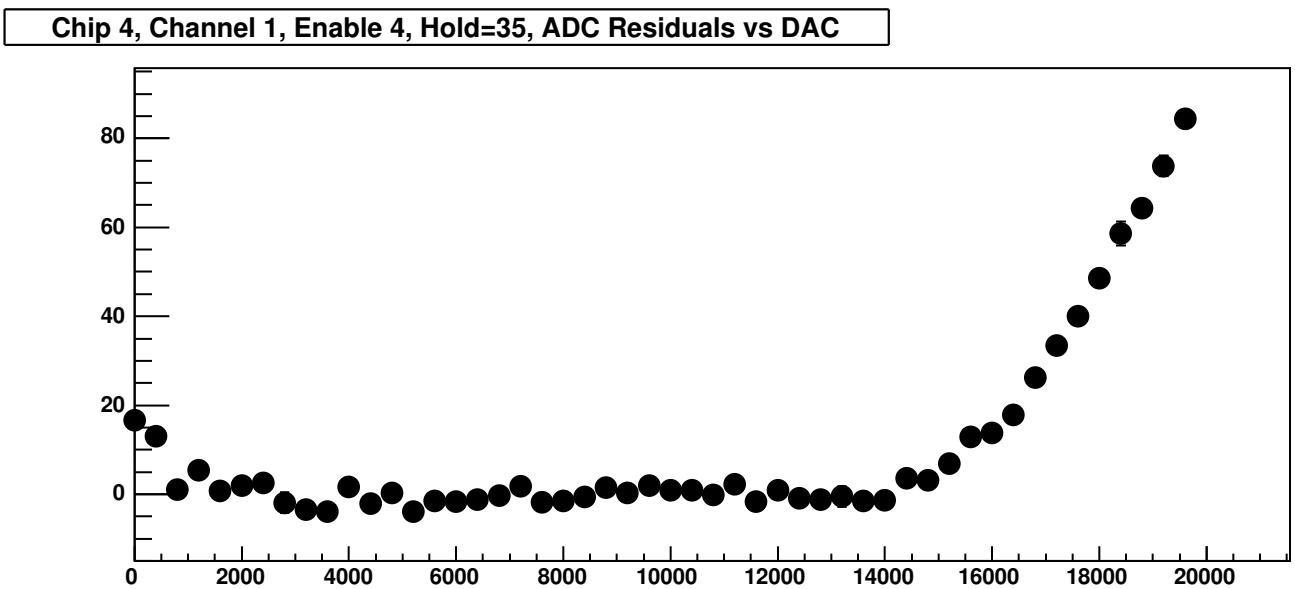

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1929 / 23
p0 192.8 ± 1.245
p1 2.524 ± 0.0003473
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

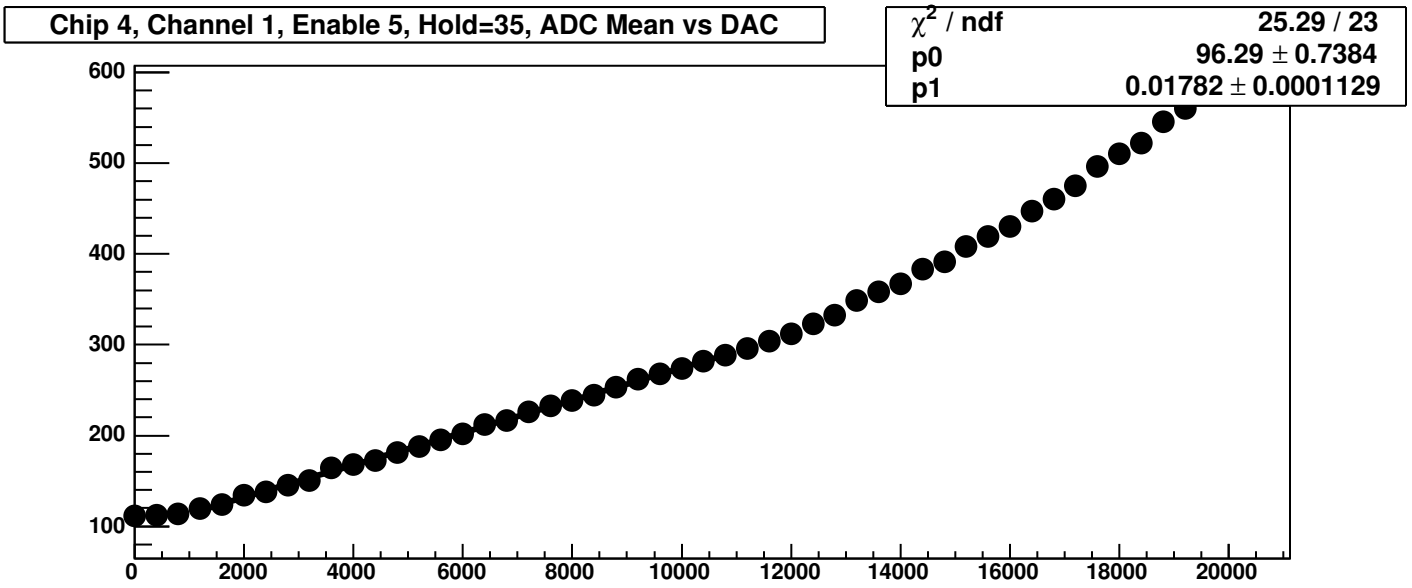

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

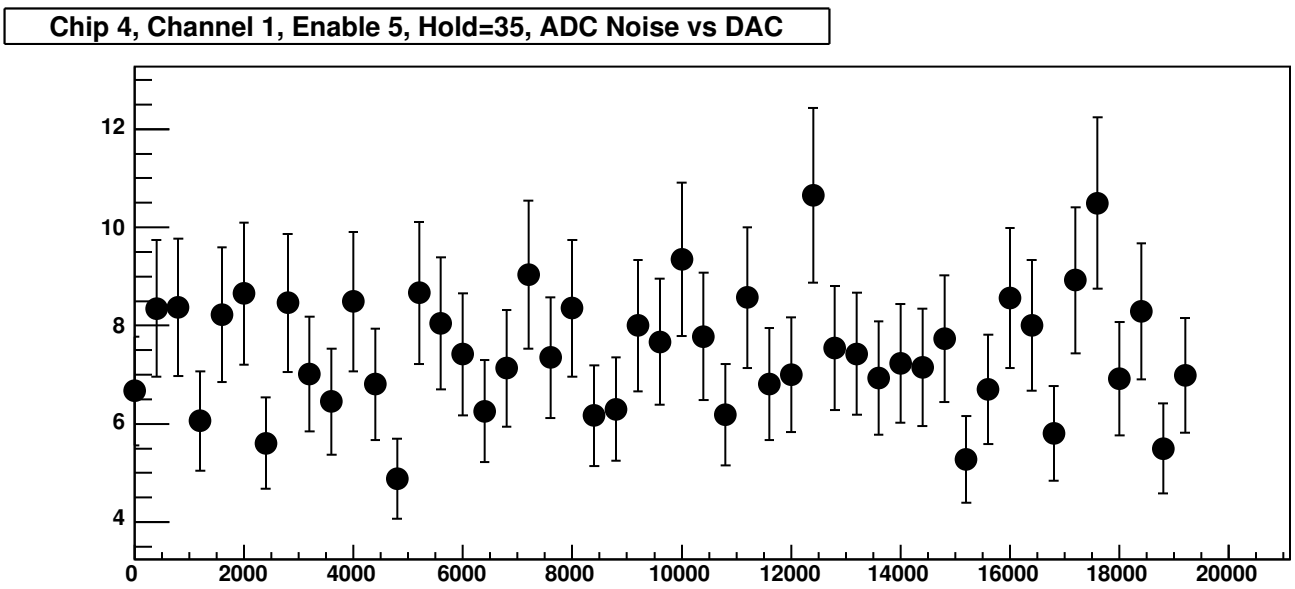

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

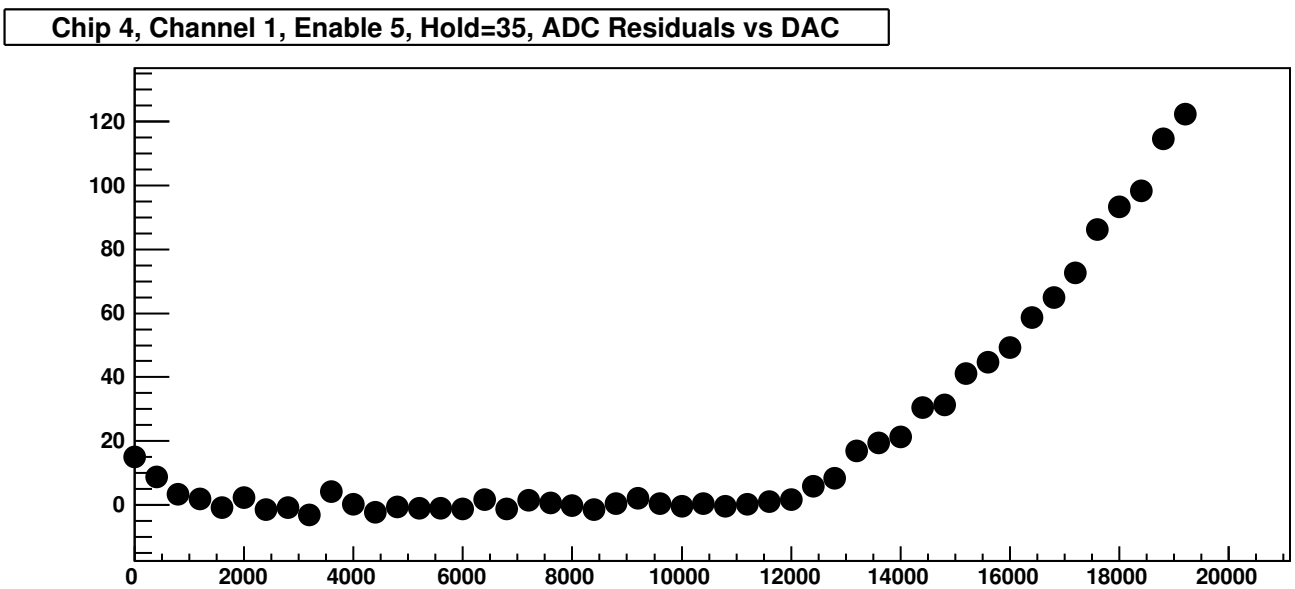

Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 39.32 / 23
p0 95.88 ± 0.7458
p1 0.01942 ± 0.0001088
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

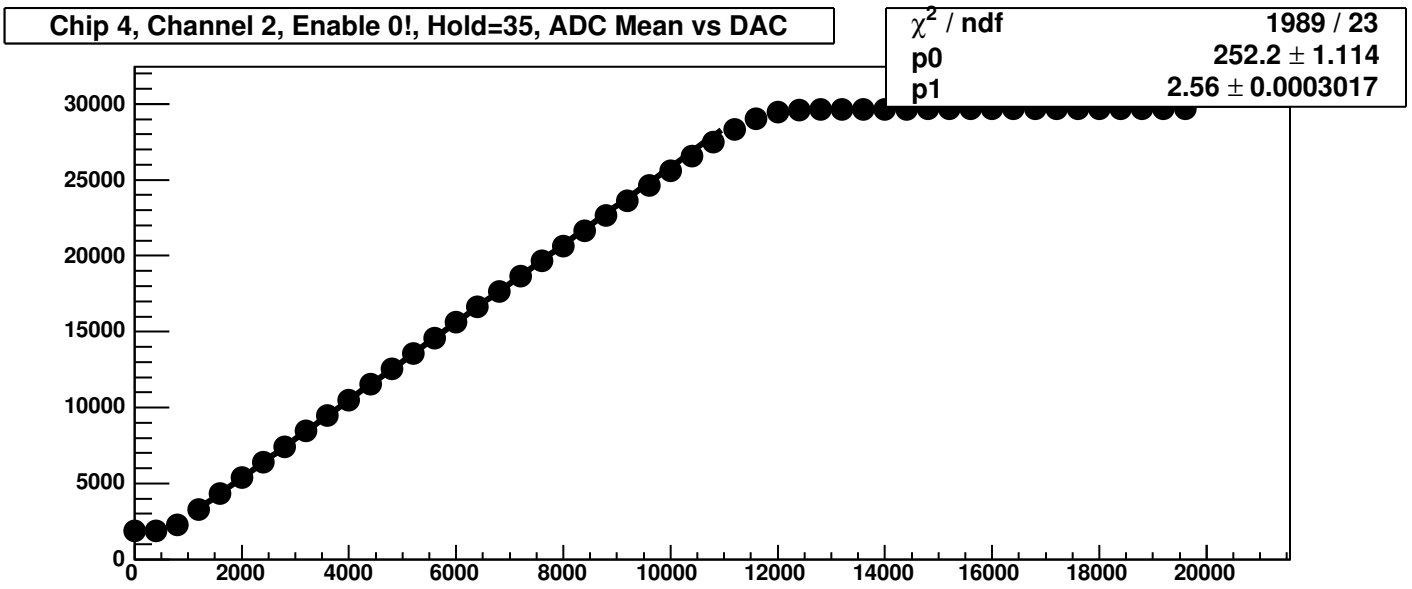

Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

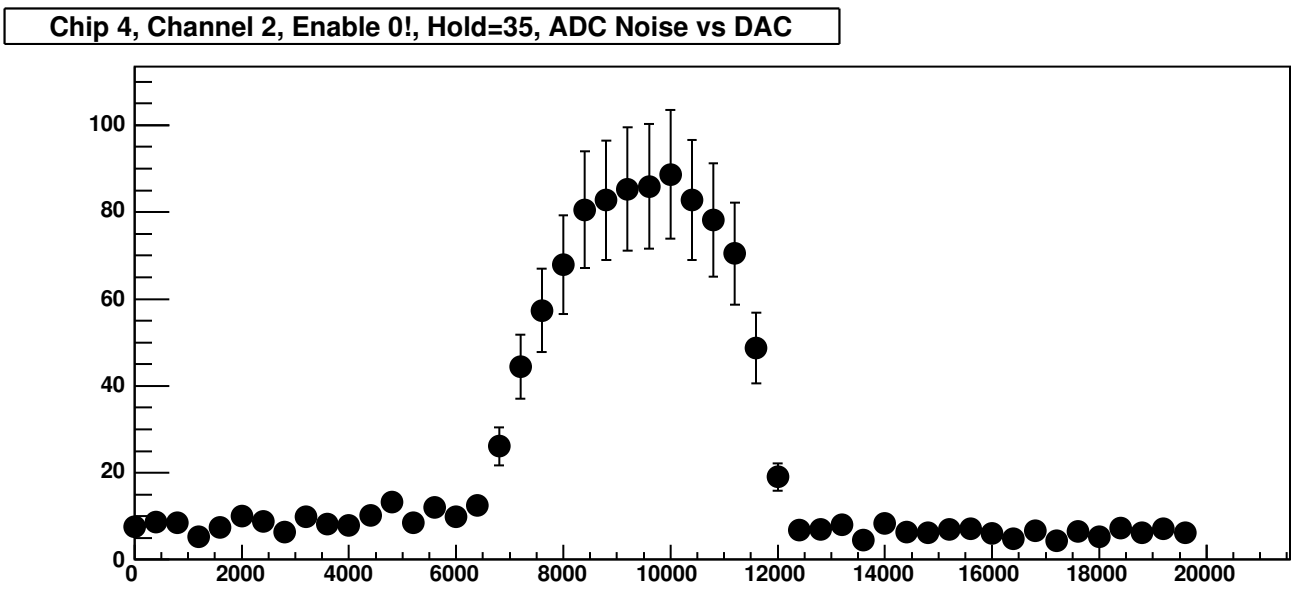

Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

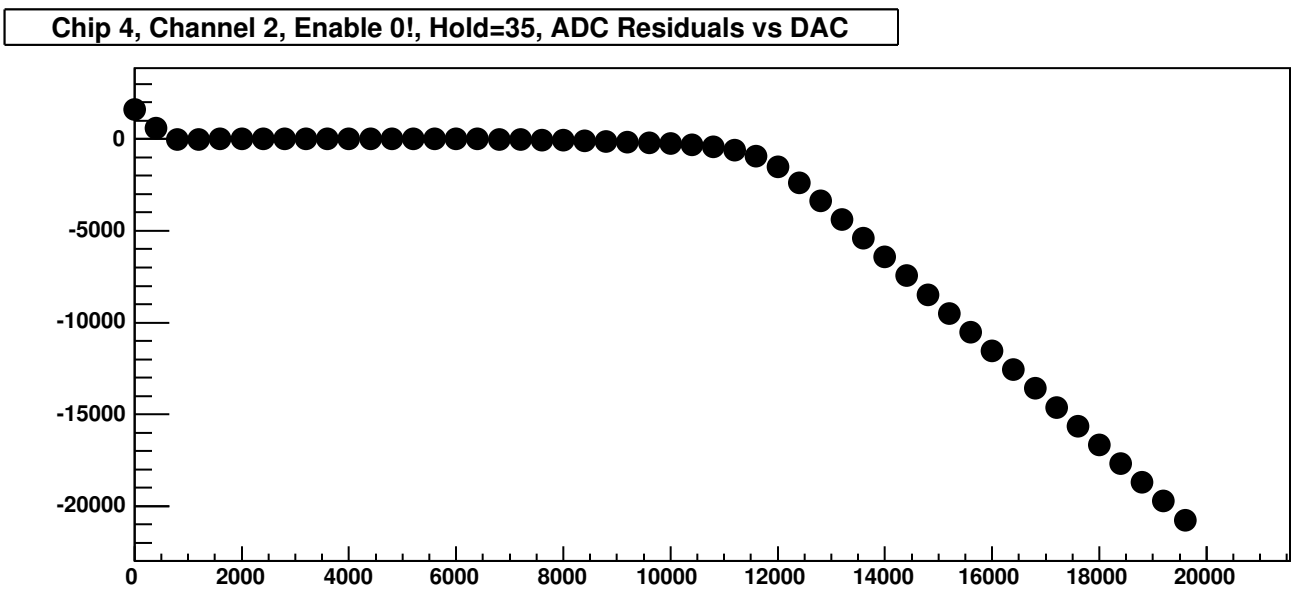

Chip 4, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 25.29 / 23
p0 96.29 ± 0.7384
p1 0.01782 ± 0.0001129
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

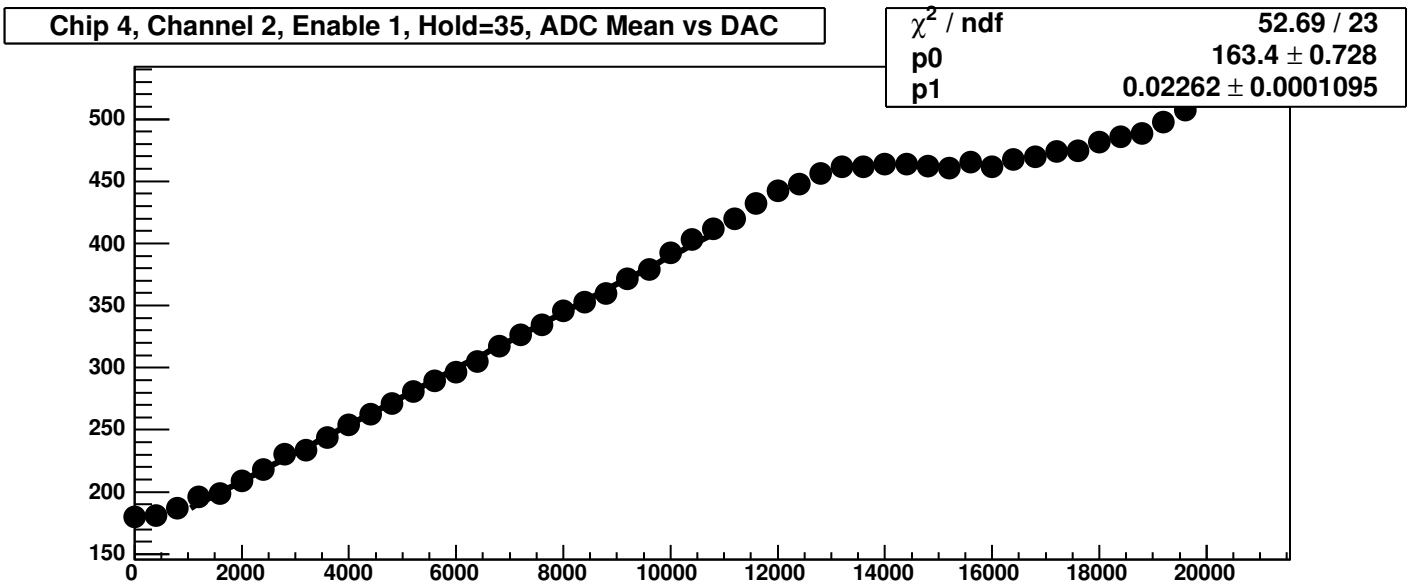

Chip 4, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

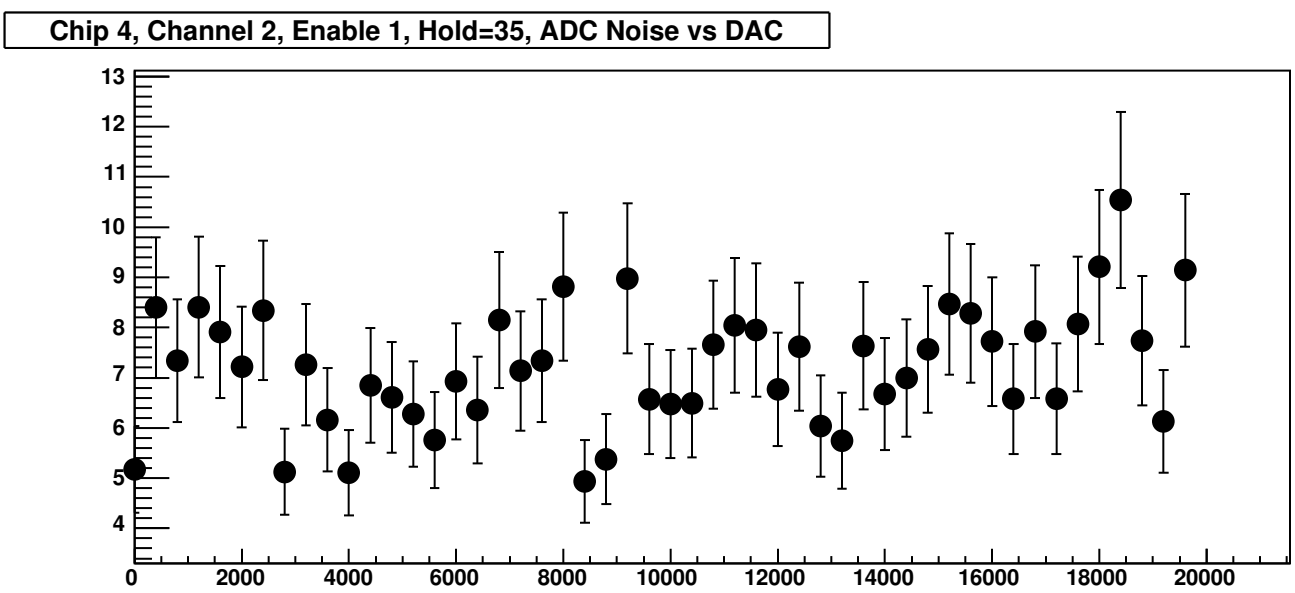

Chip 4, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

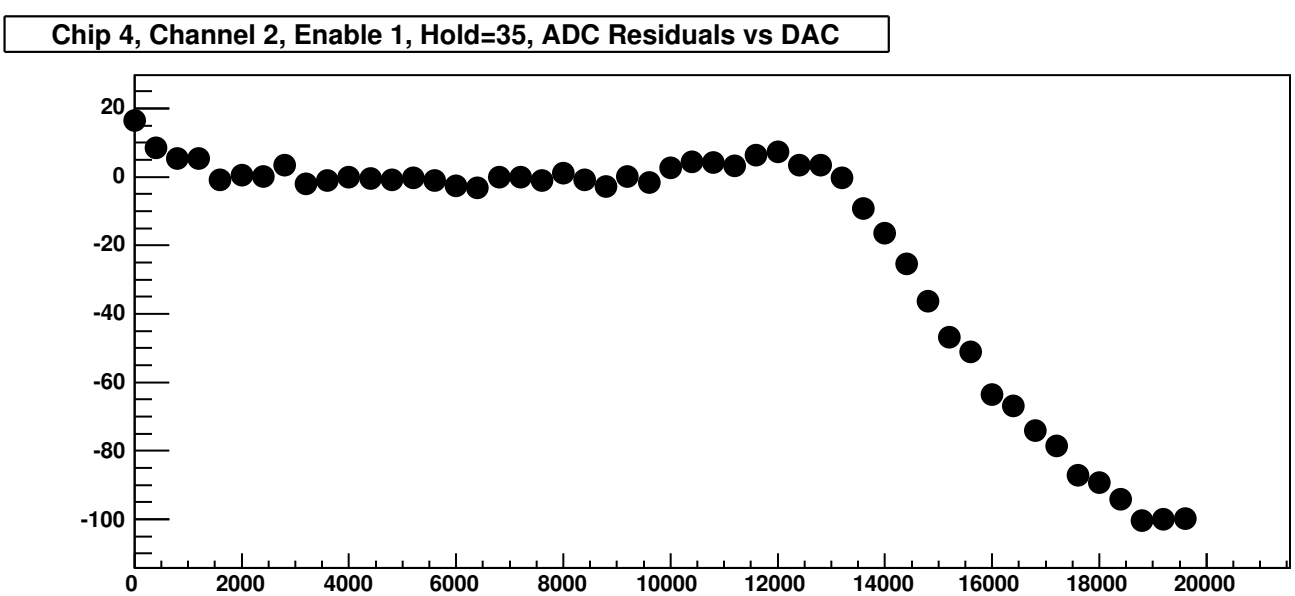

Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1989 / 23
p0 252.2 ± 1.114
p1 2.56 ± 0.0003017
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

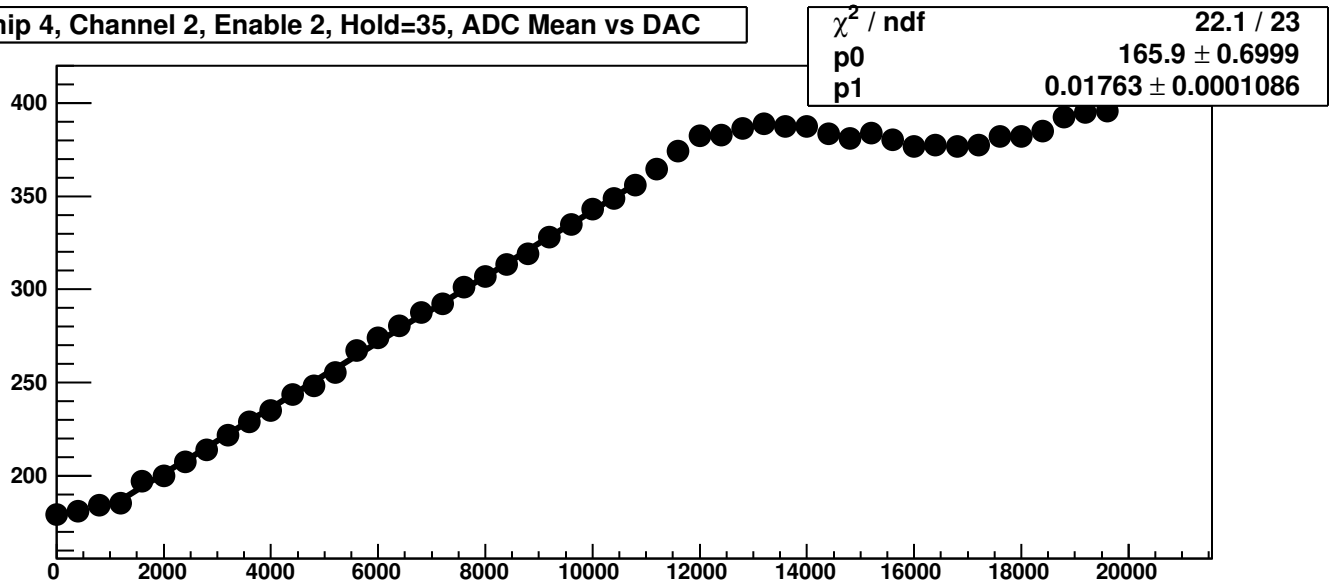

Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 52.69 / 23
p0 163.4 ± 0.728
p1 0.02262 ± 0.0001095
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

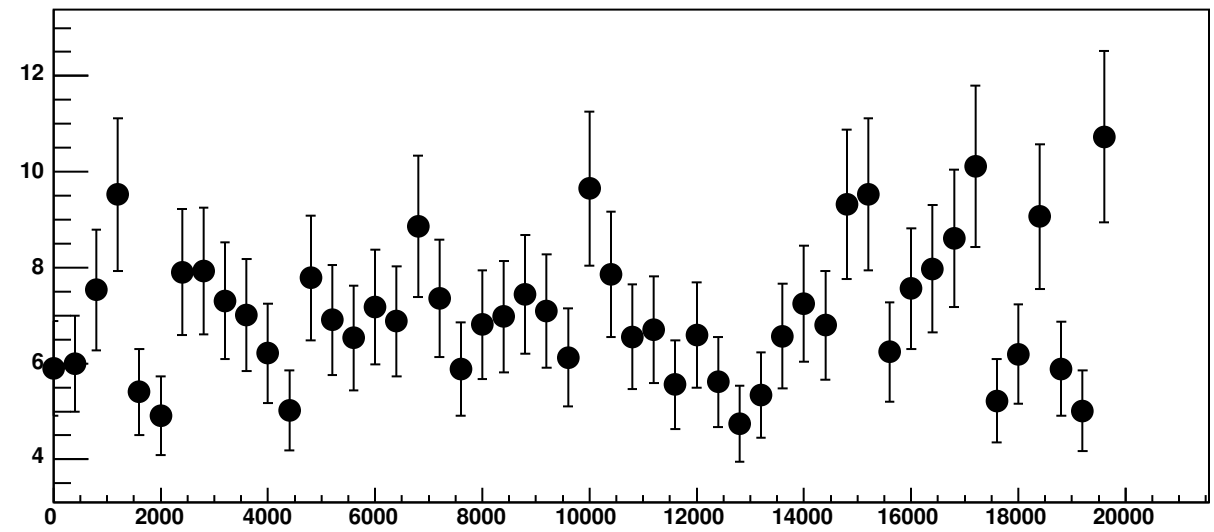

Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

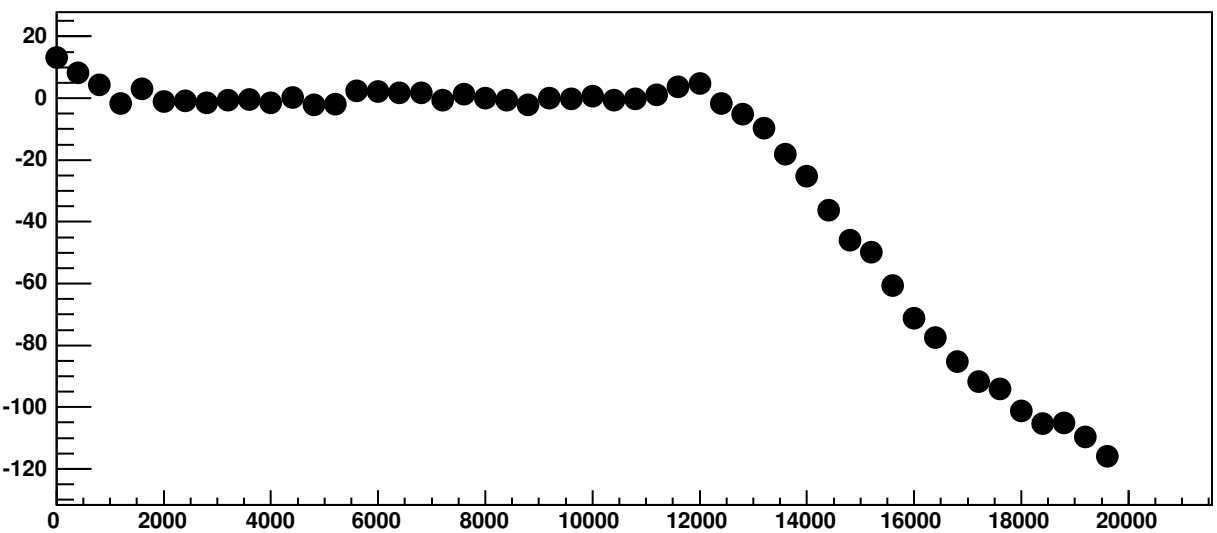
Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



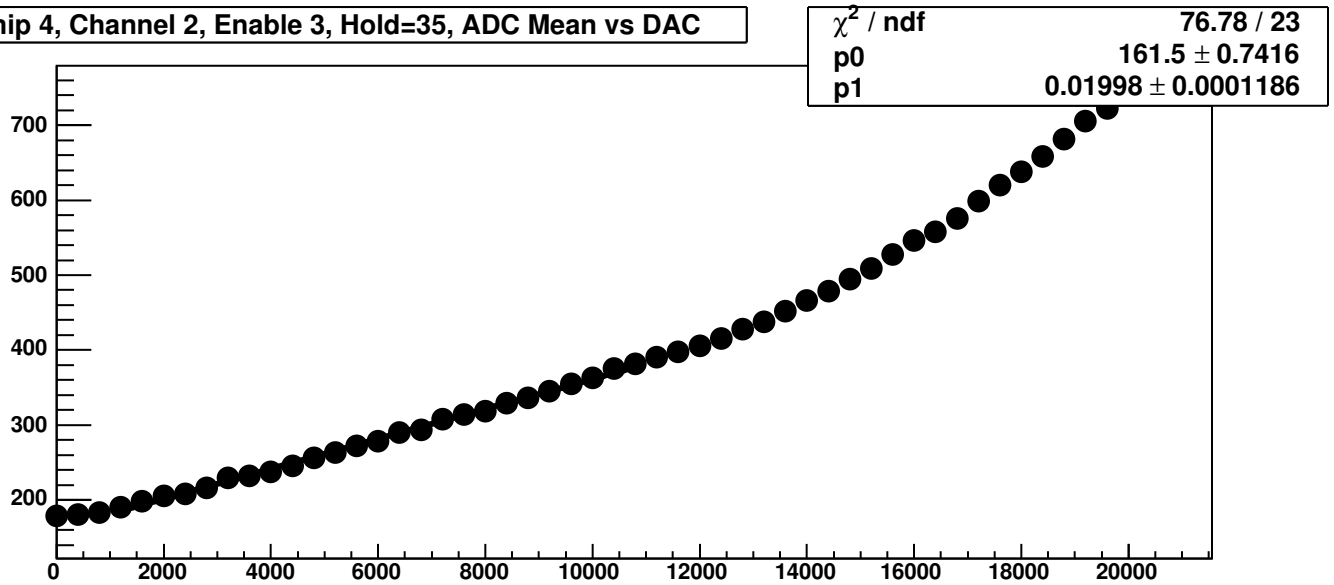
Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



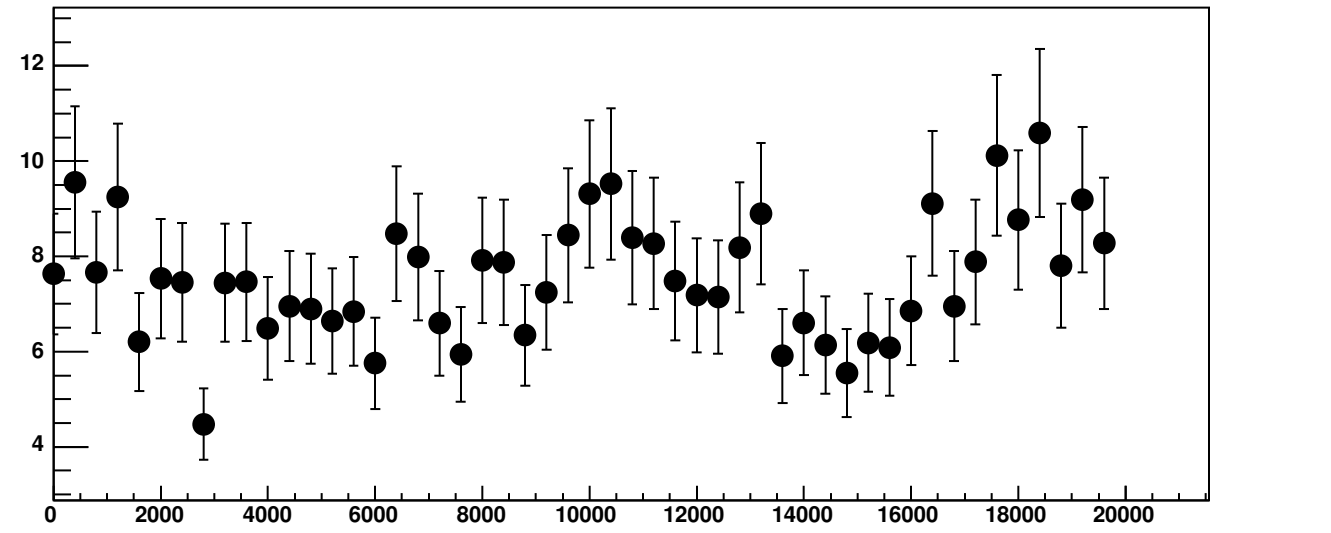
Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



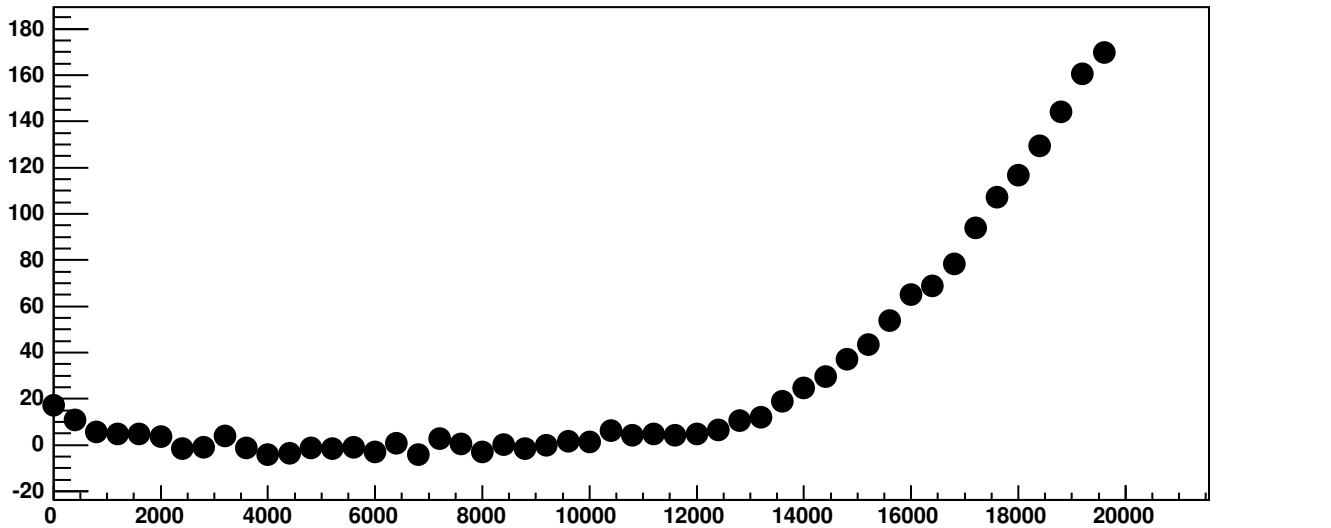
Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC

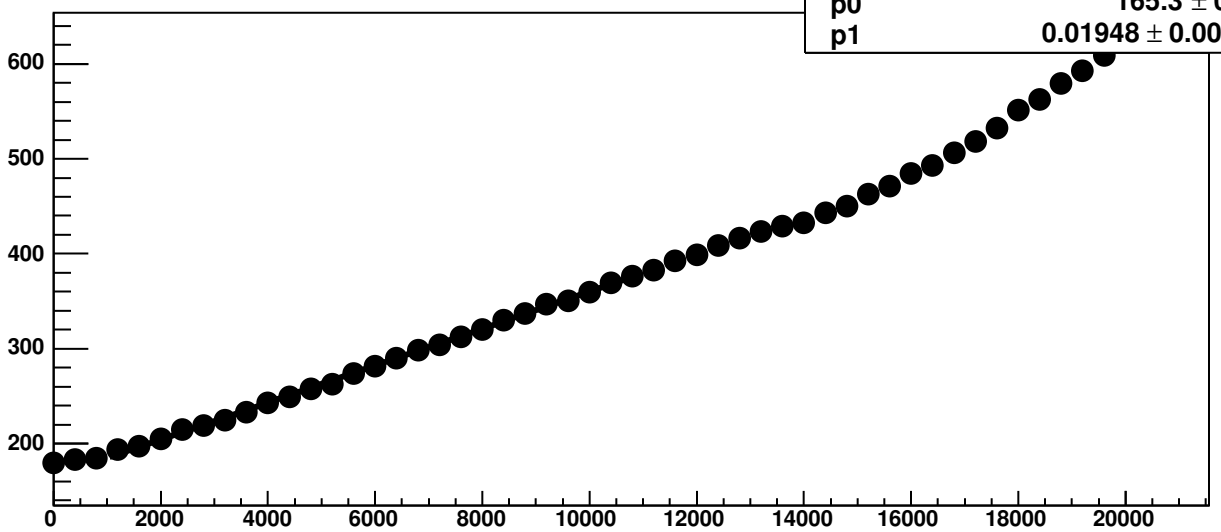


Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

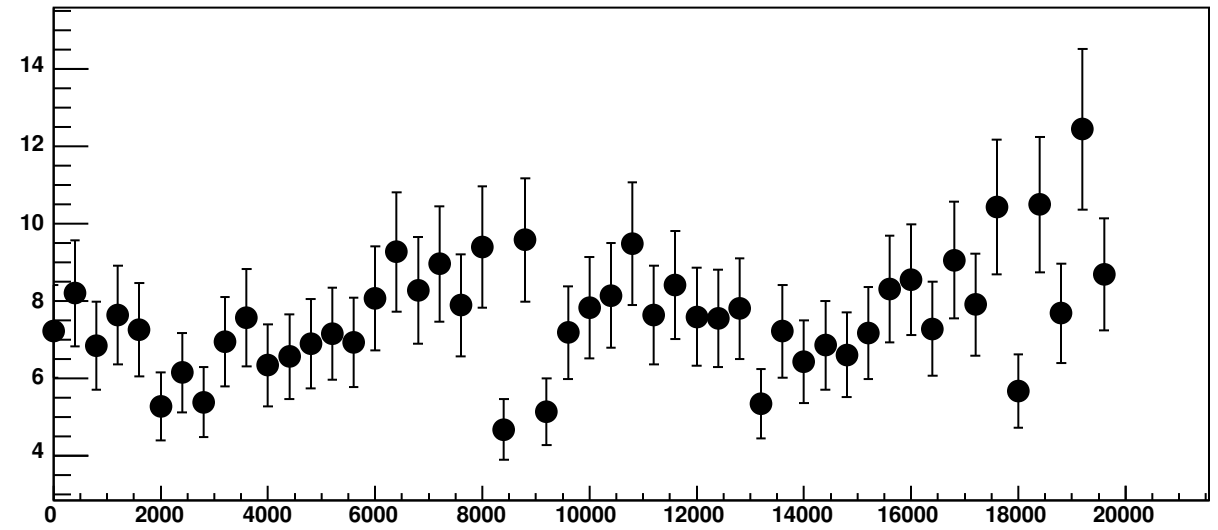


Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

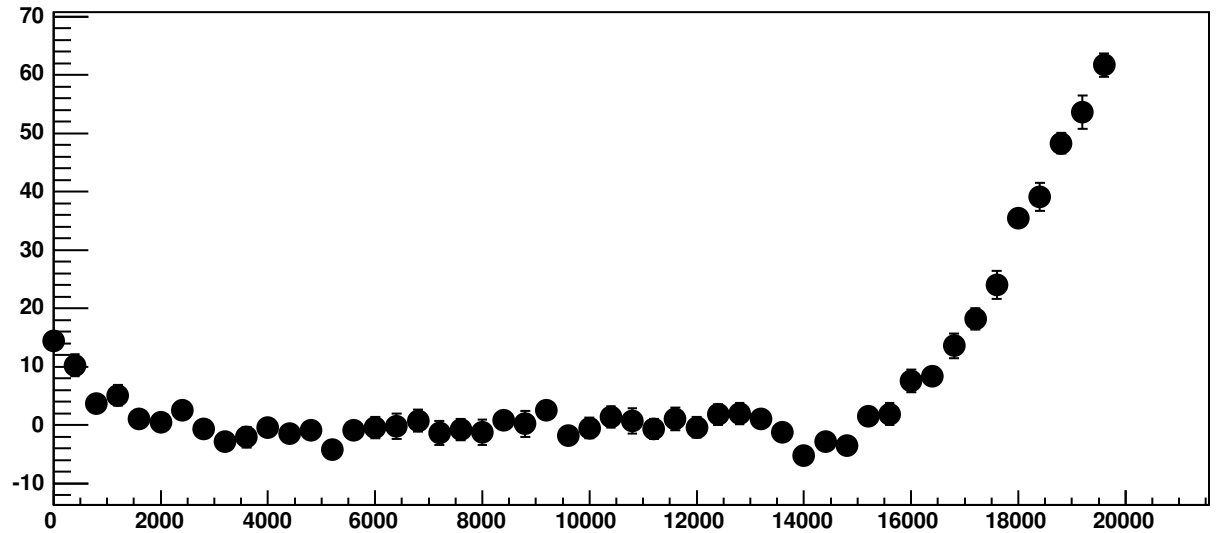
χ^2 / ndf	33.55 / 23
p0	165.3 ± 0.7023
p1	0.01948 ± 0.0001098



Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

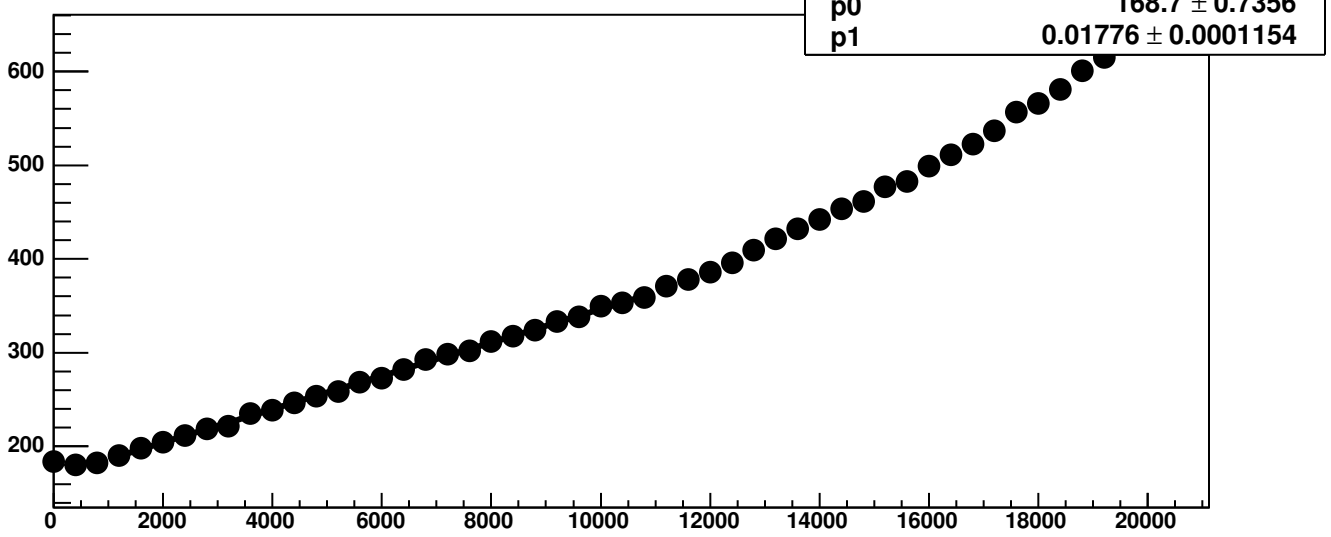


Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

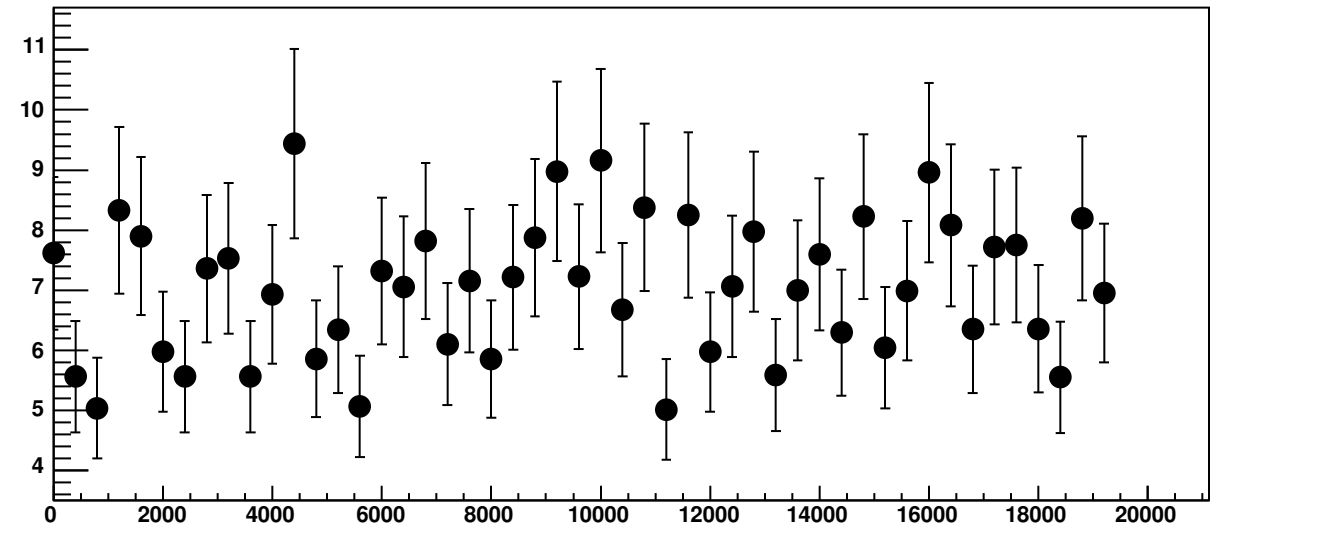


Chip 4, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

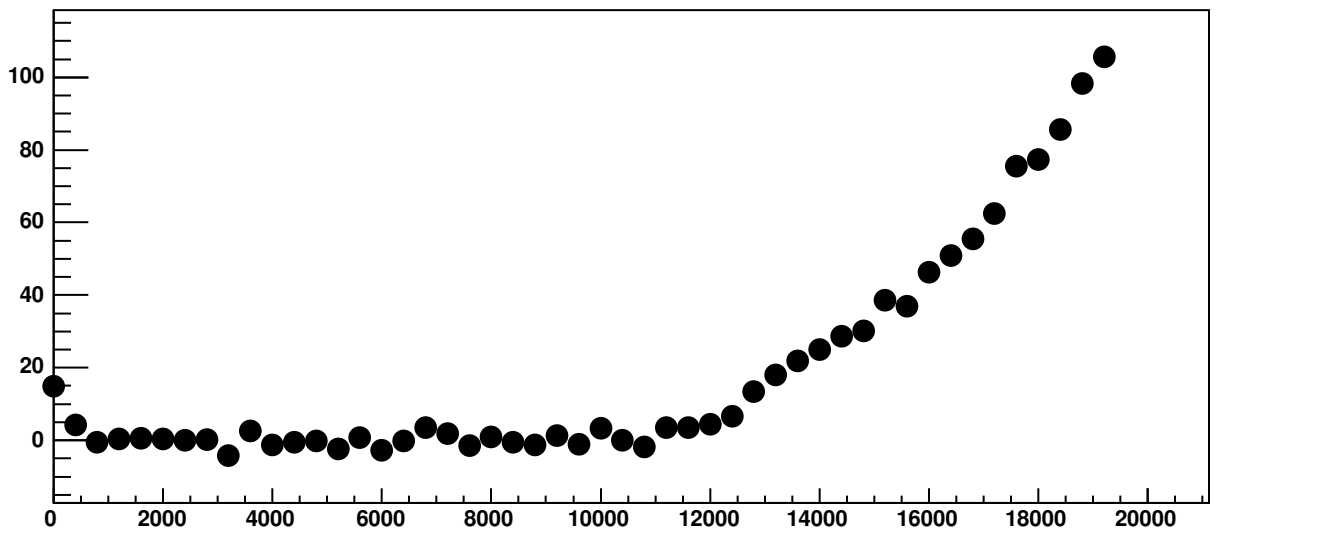
χ^2 / ndf	27.91 / 23
p0	168.7 ± 0.7356
p1	0.01776 ± 0.0001154



Chip 4, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

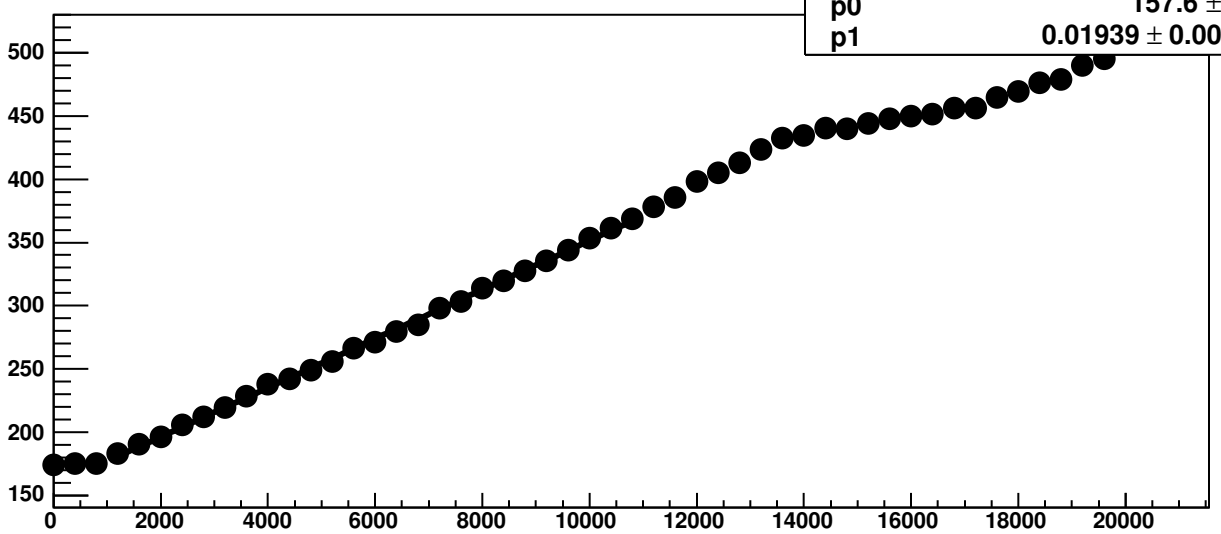


Chip 4, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

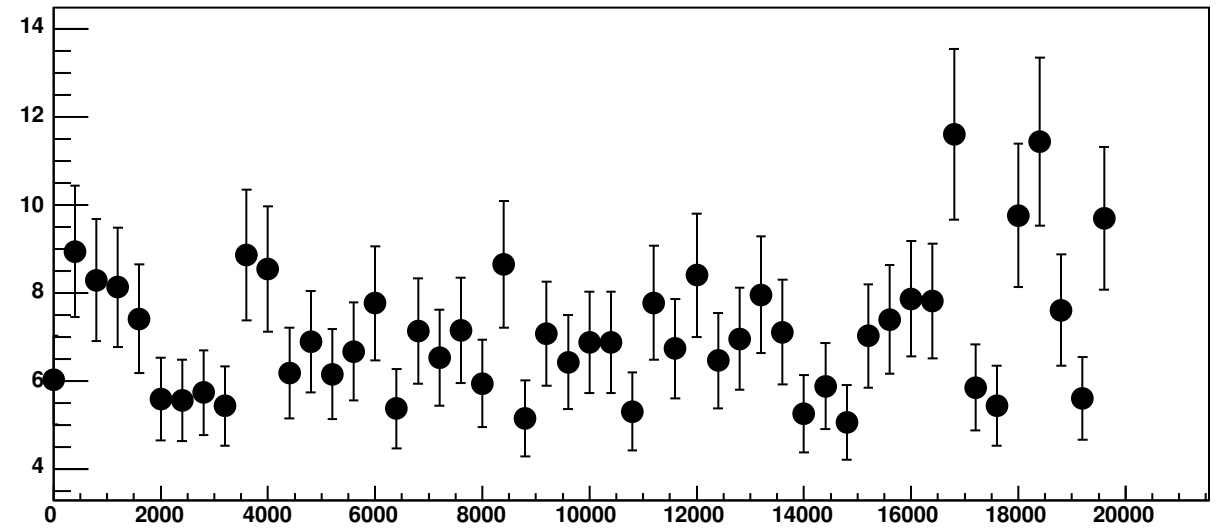


Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

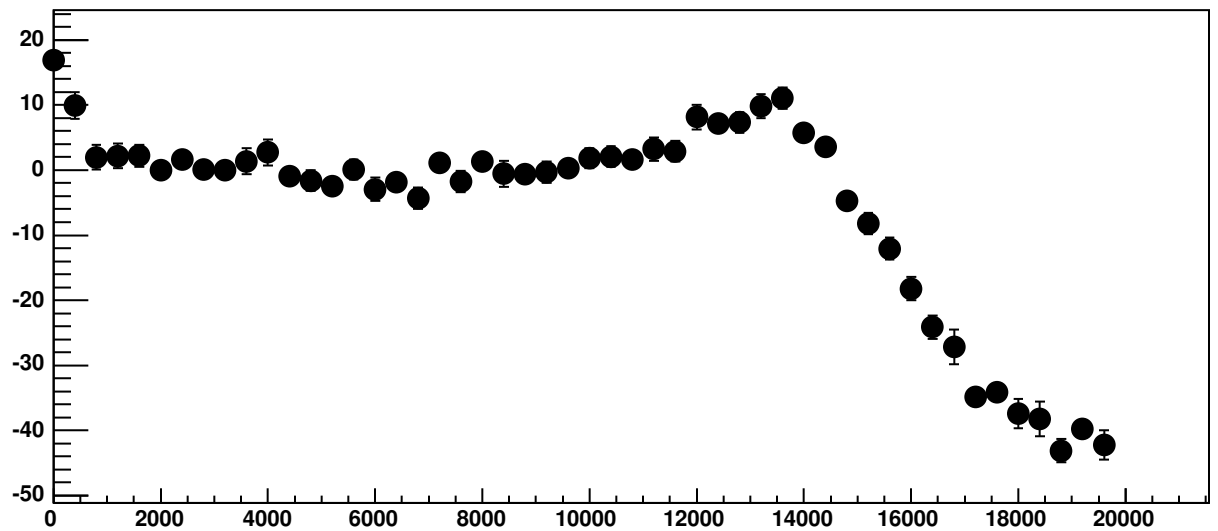
χ^2 / ndf	31.56 / 23
p0	157.6 ± 0.683
p1	0.01939 ± 0.0001015

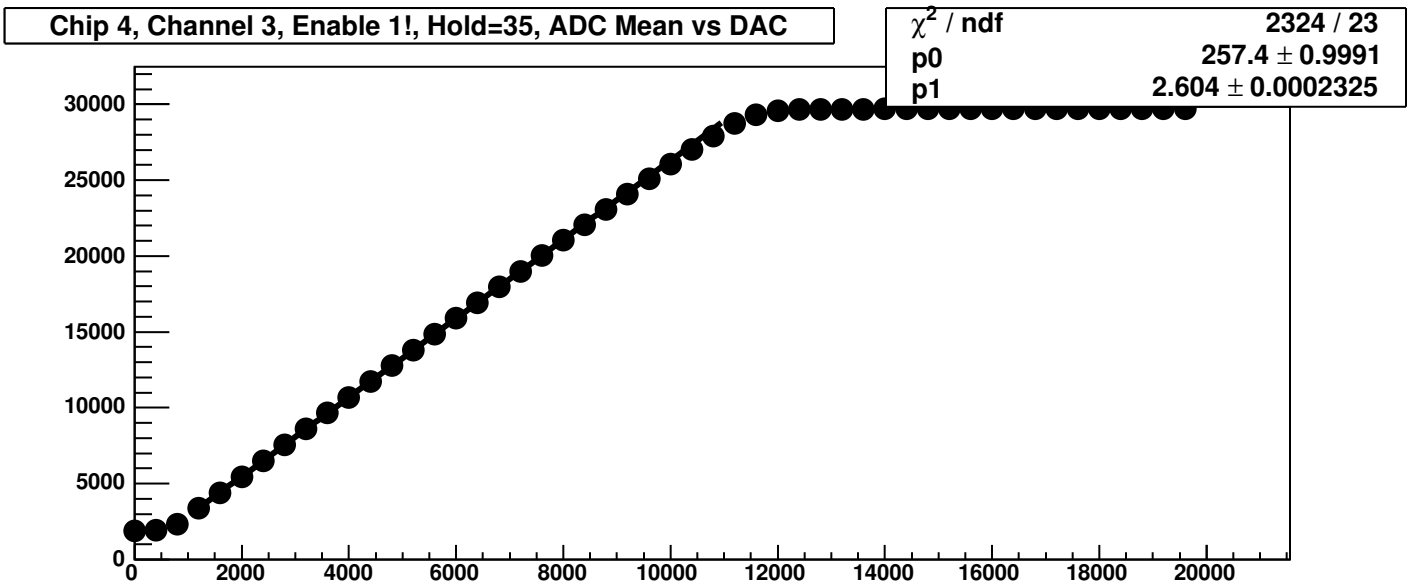


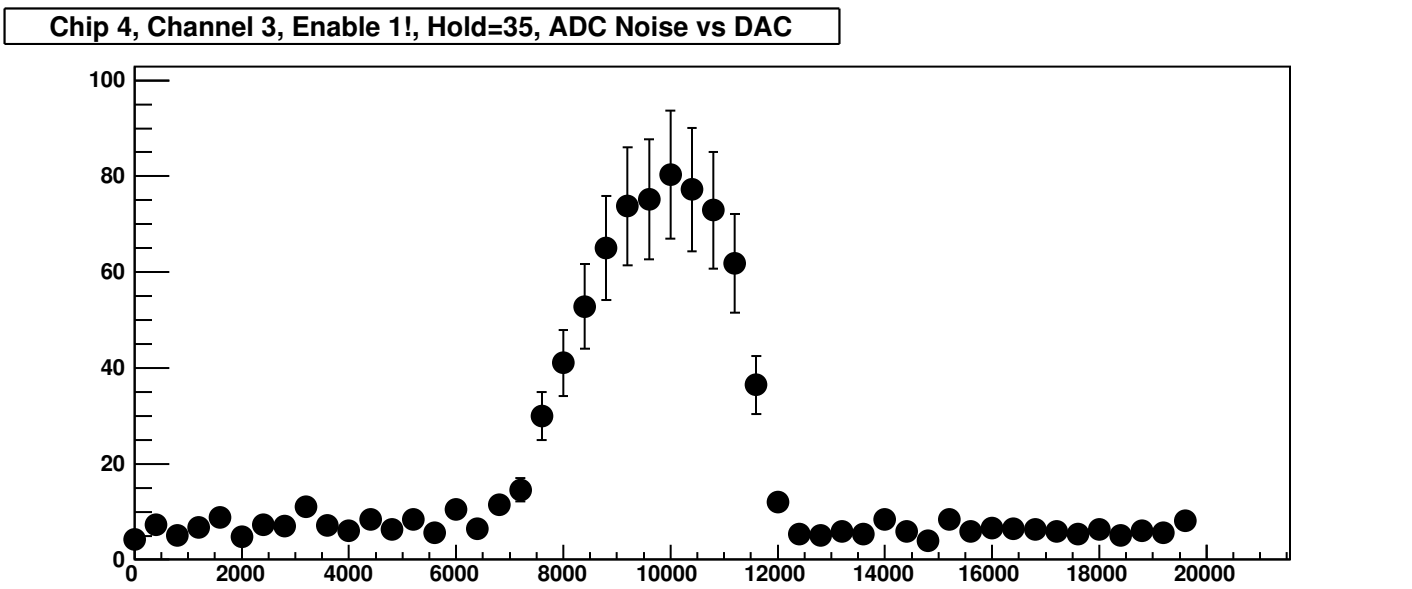
Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

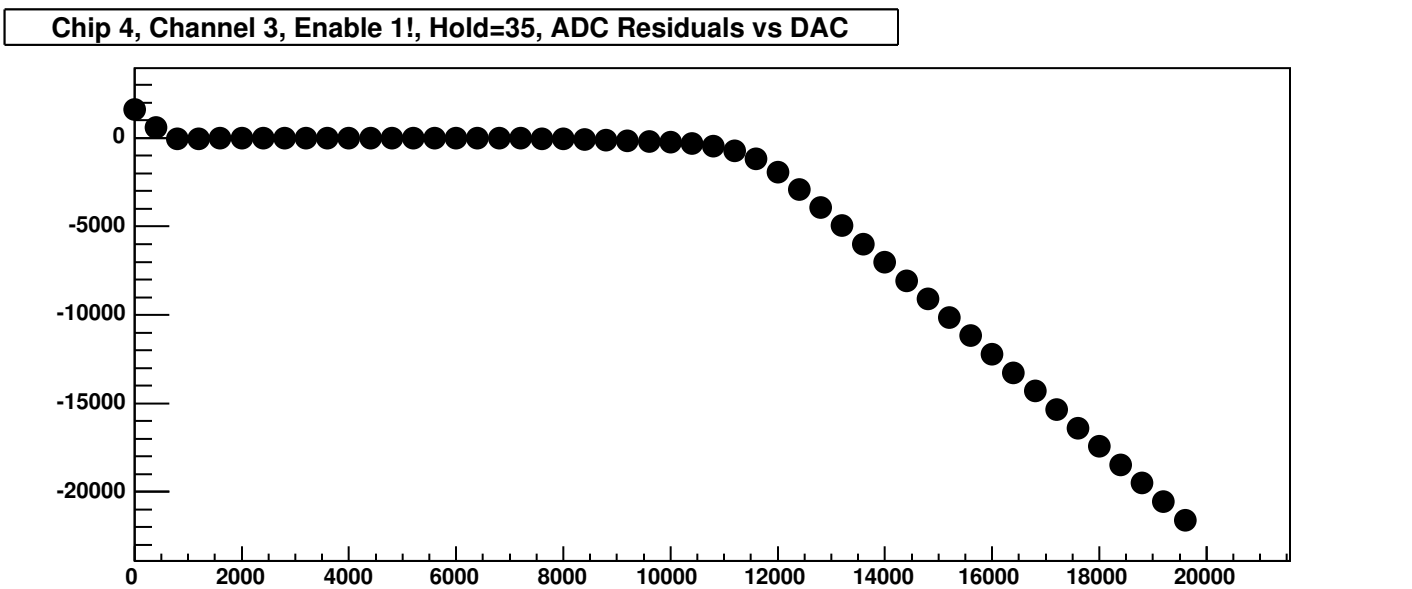


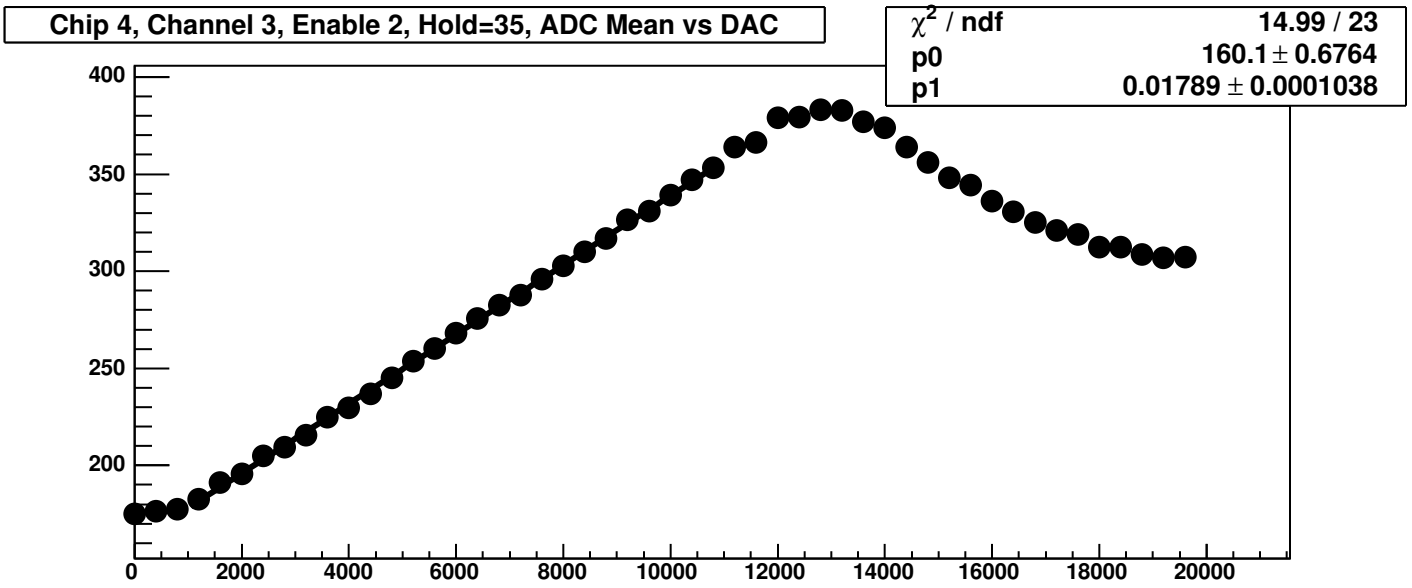
Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

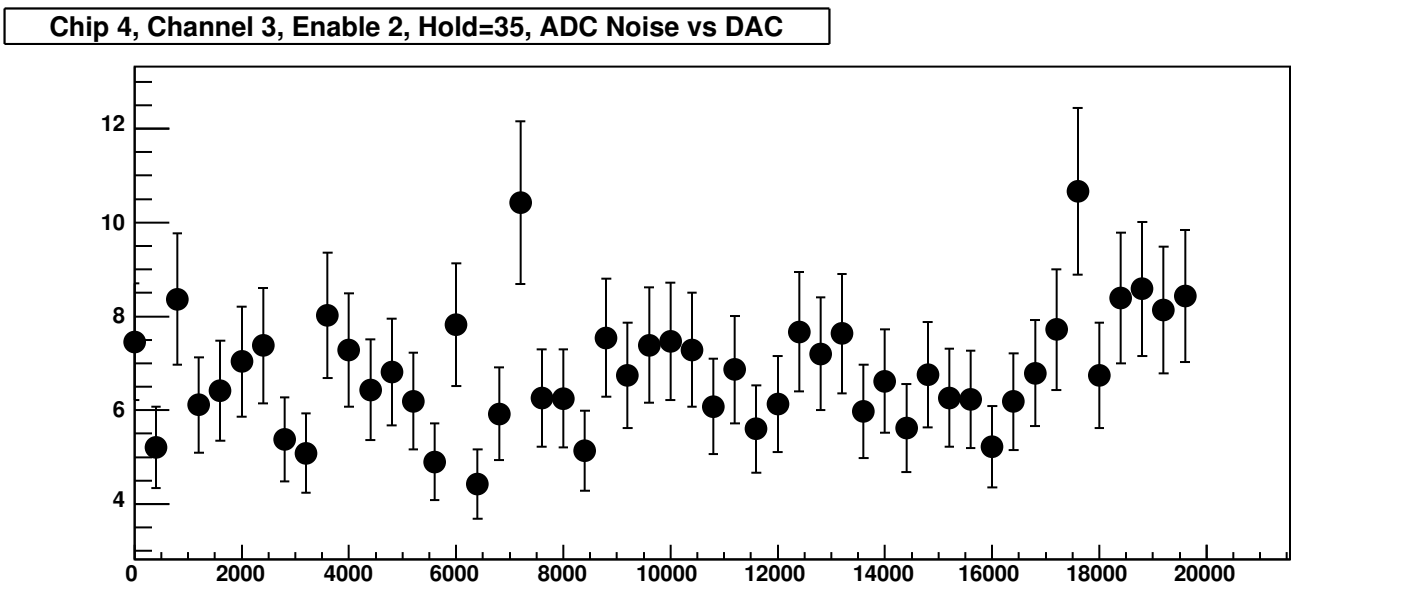


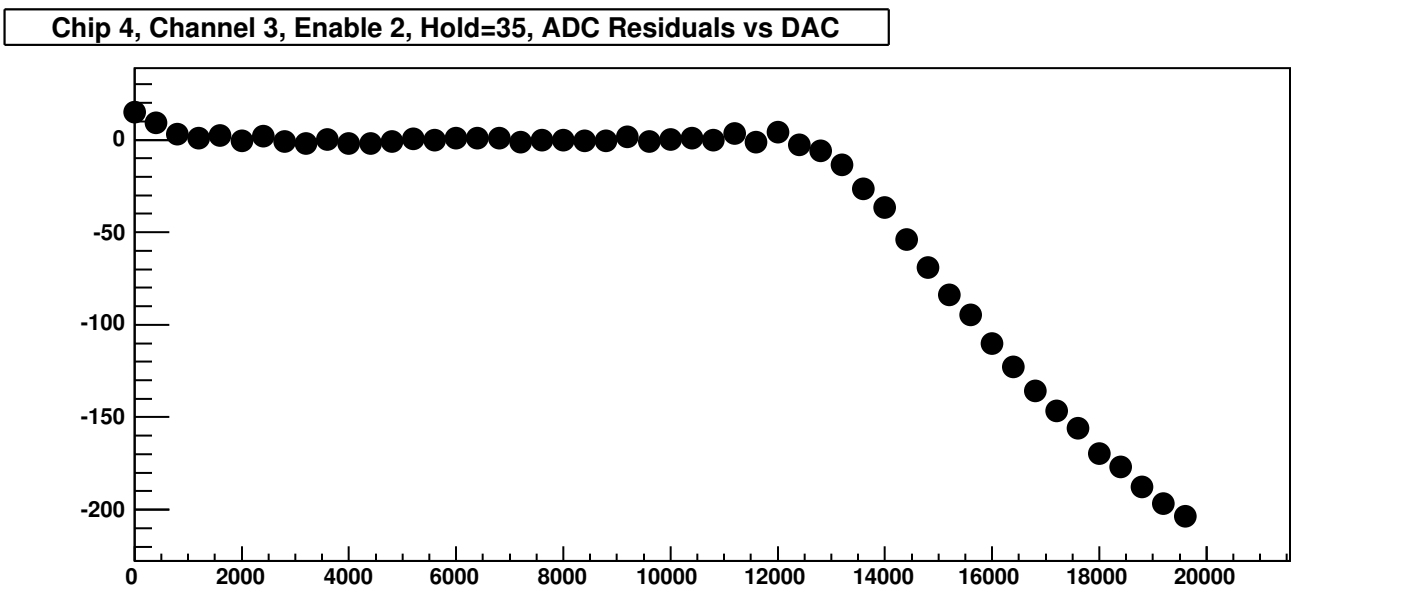

Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 2324 / 23
p0 257.4 ± 0.9991
p1 2.604 ± 0.0002325
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

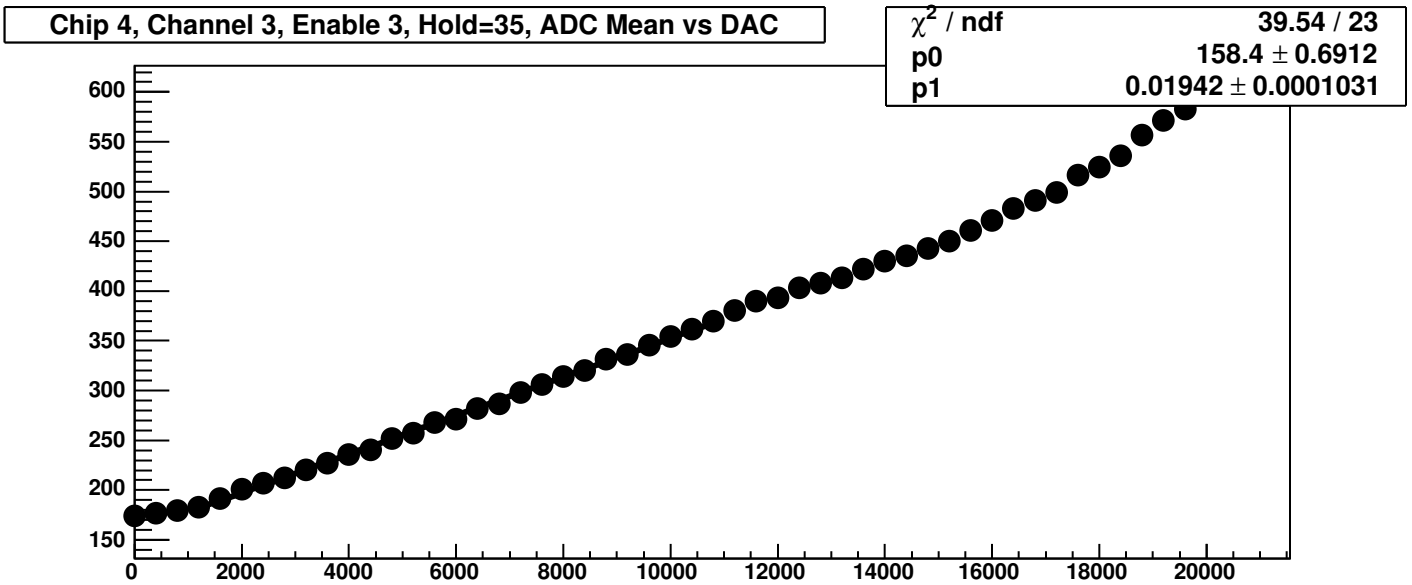

Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

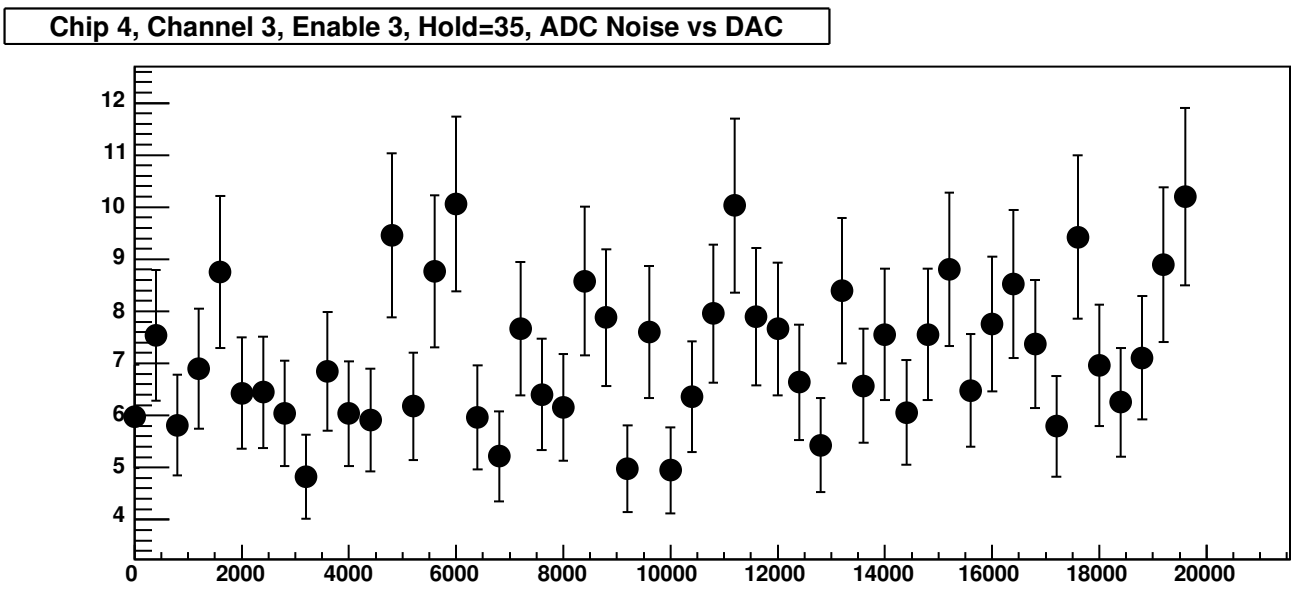

Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

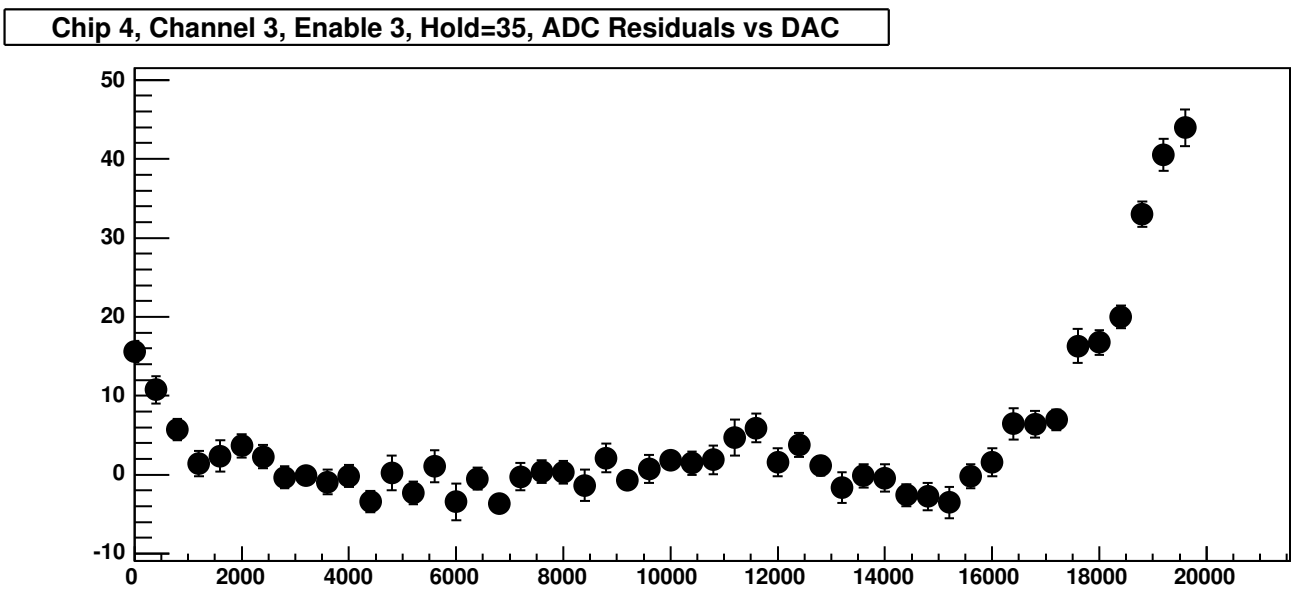

Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 14.99 / 23
p0 160.1 ± 0.6764
p1 0.01789 ± 0.0001038
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

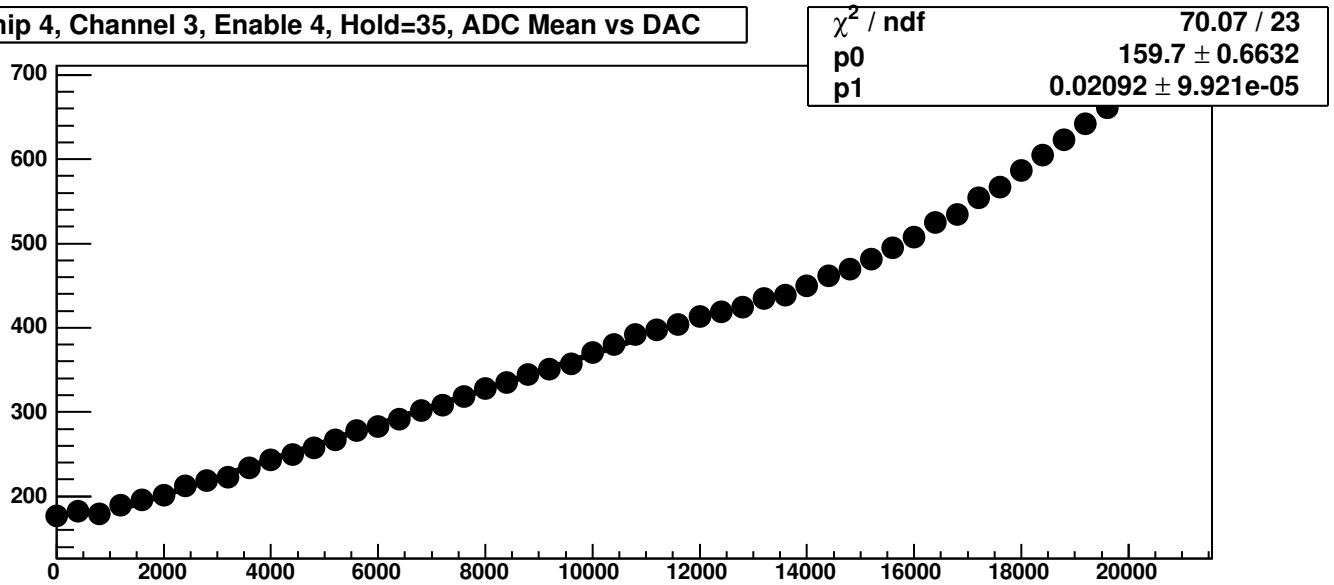

Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.54 / 23
p0 158.4 ± 0.6912
p1 0.01942 ± 0.0001031
600
550
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

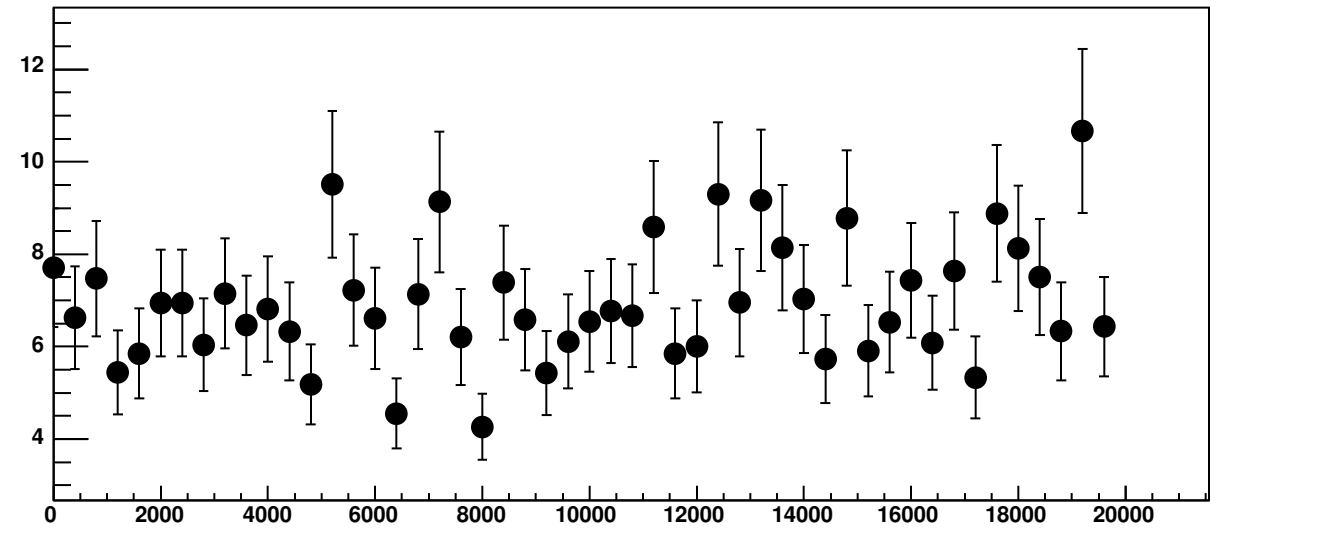

Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

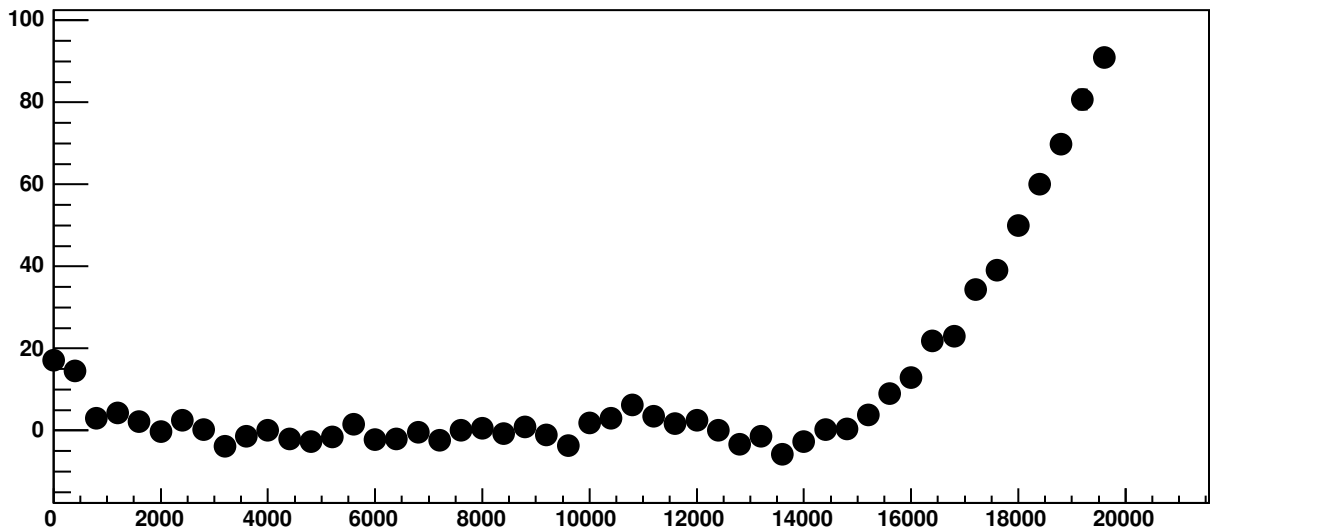
Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC



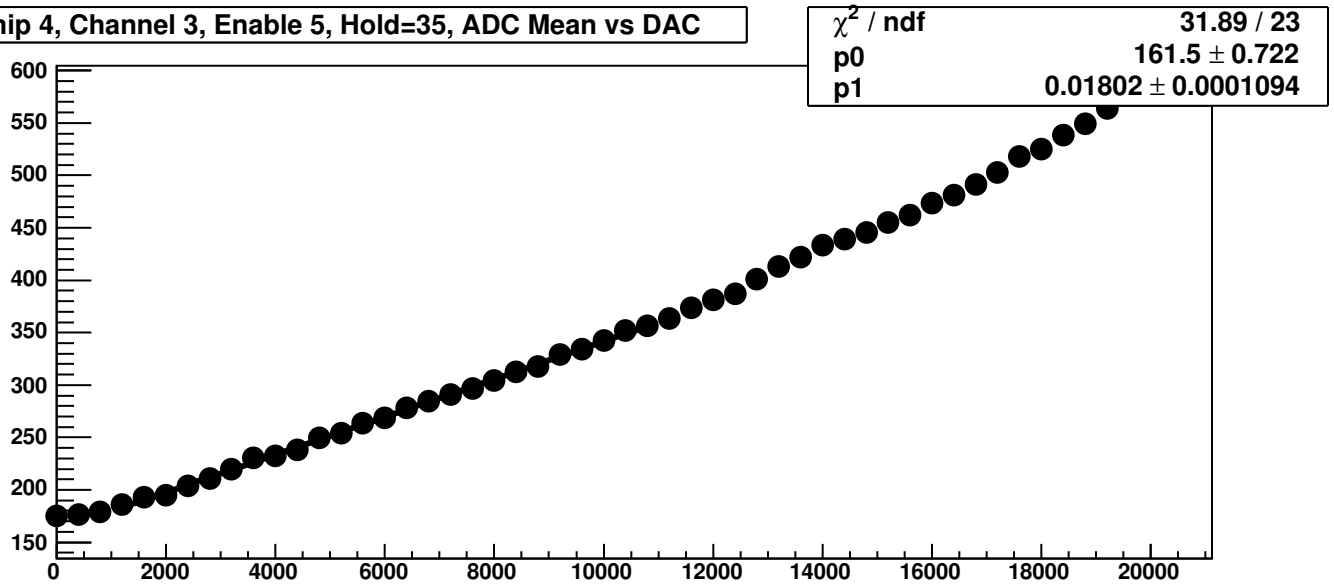
Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



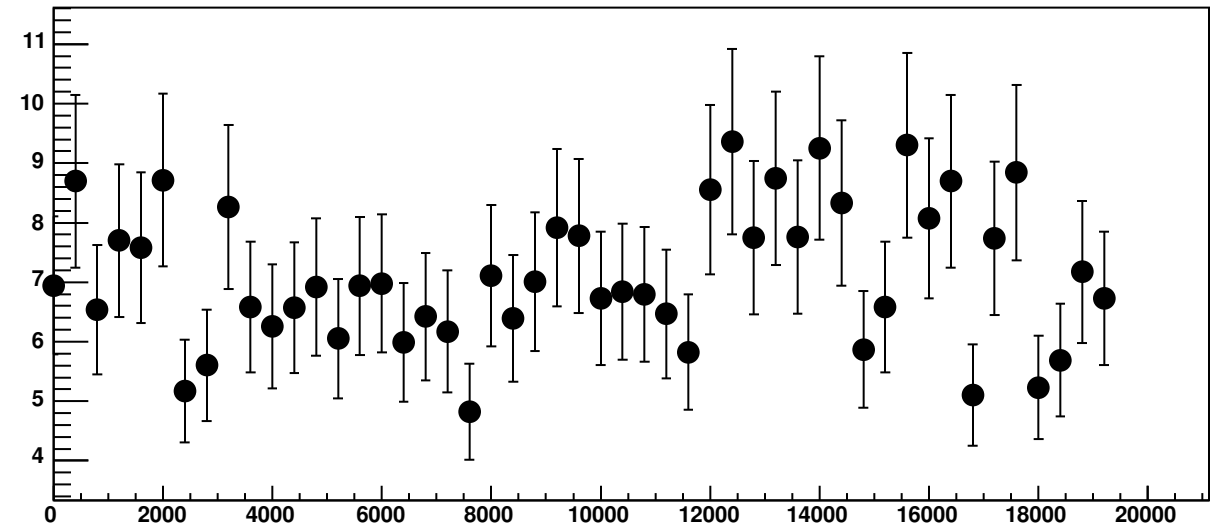
Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



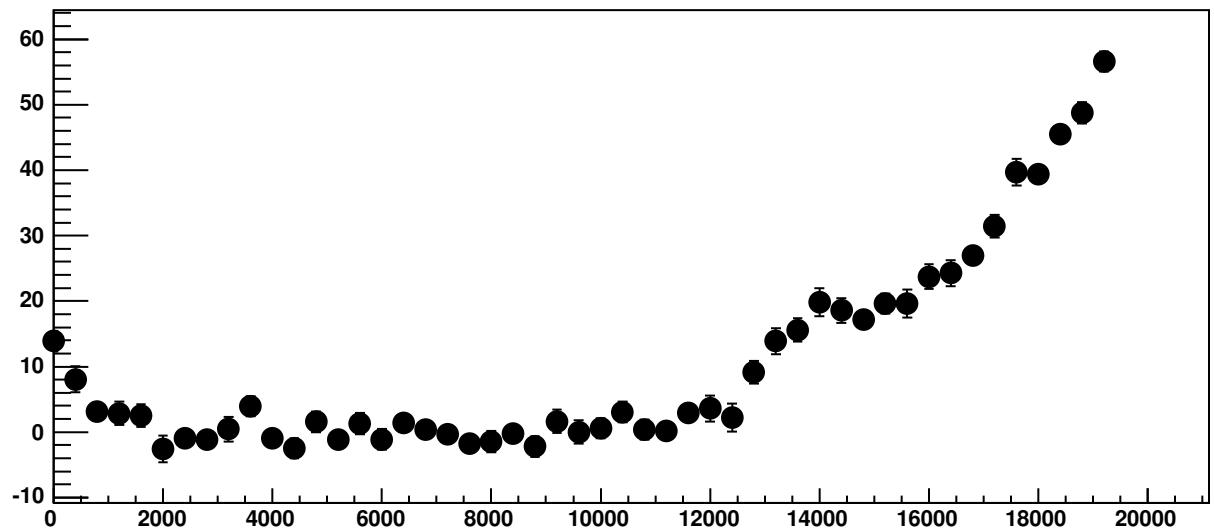
Chip 4, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



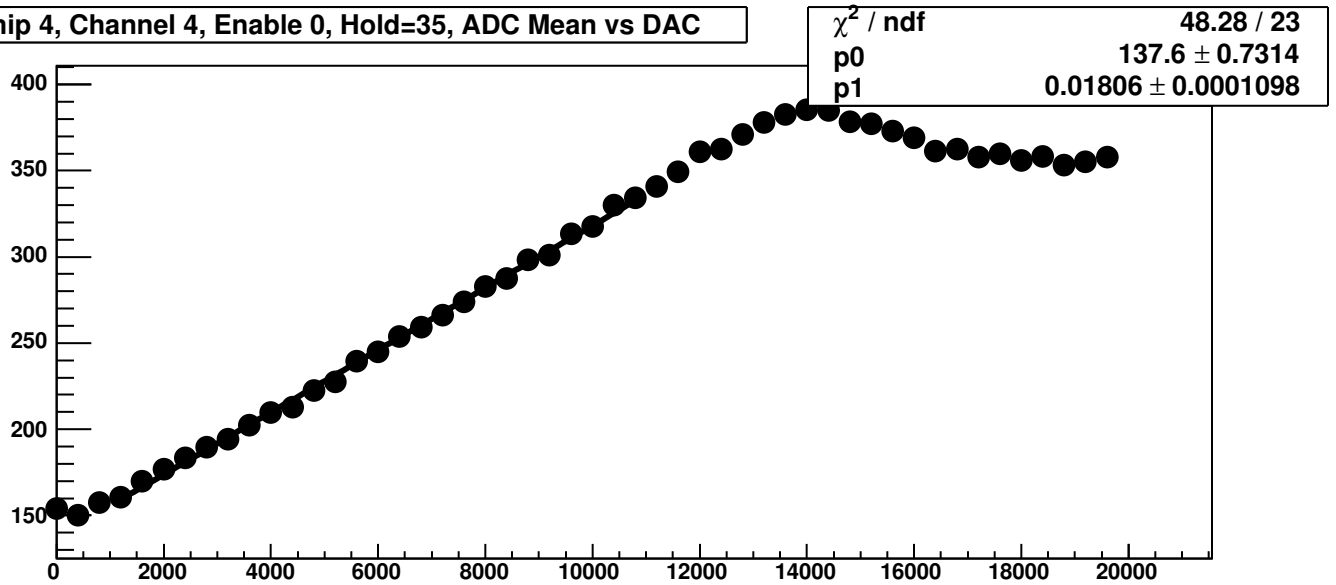
Chip 4, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



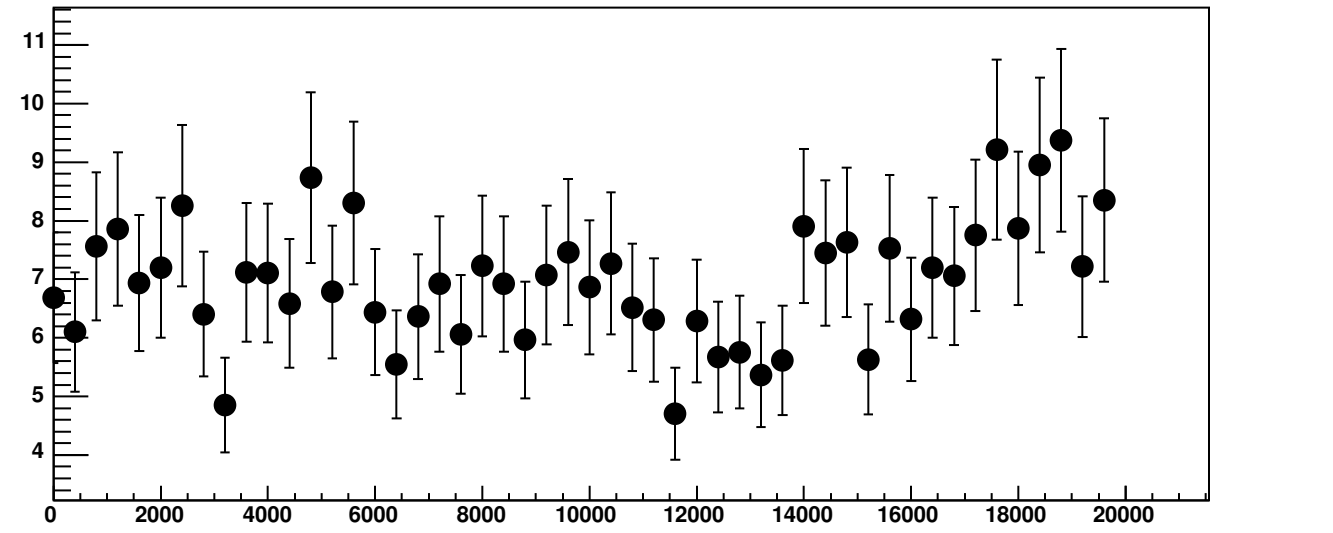
Chip 4, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



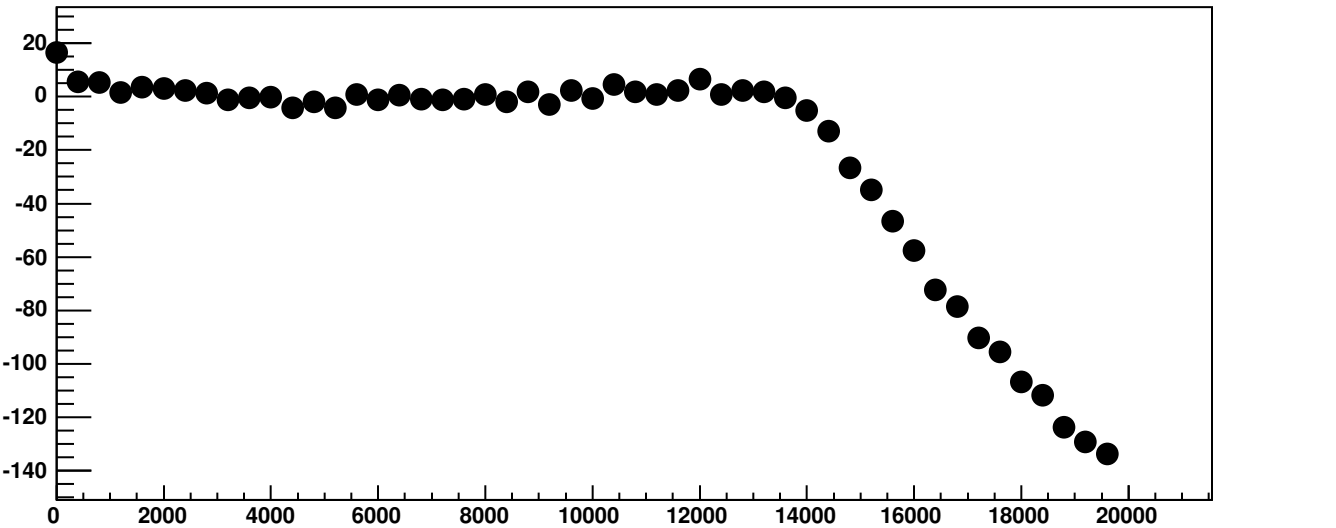
Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

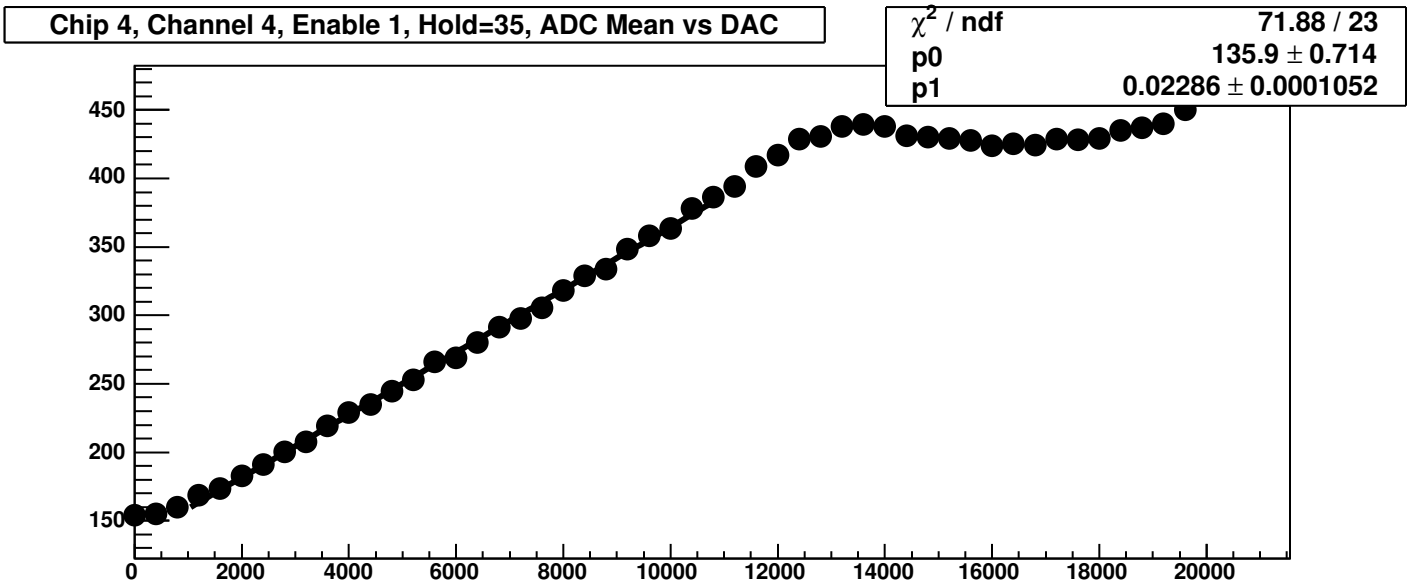


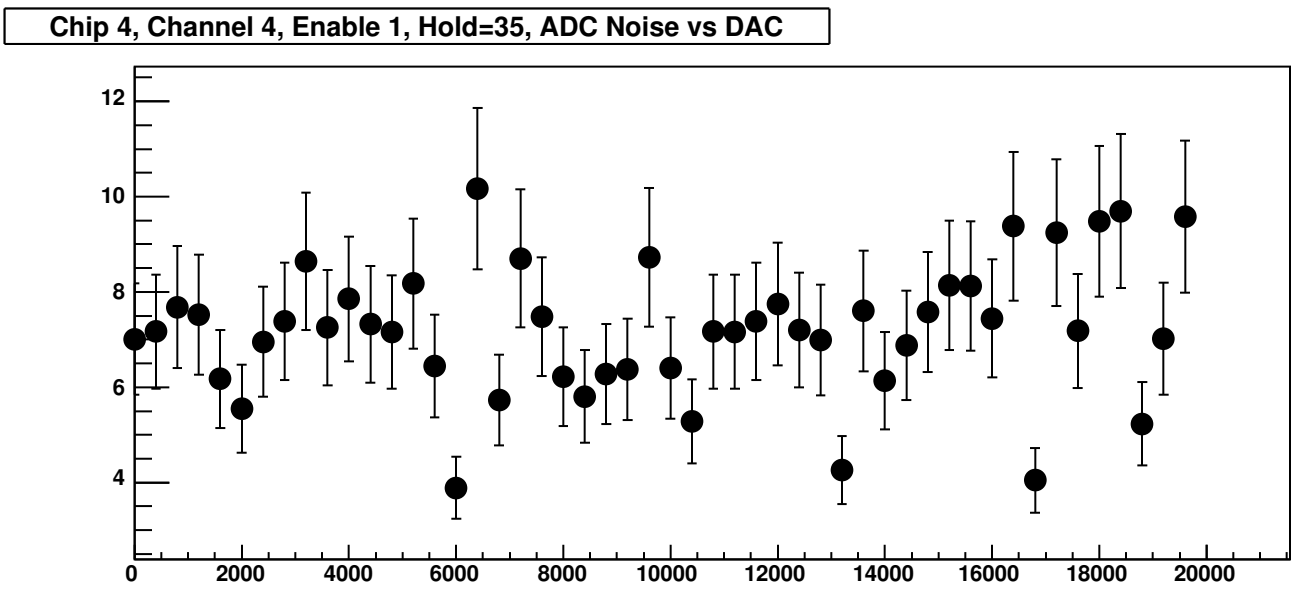
Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

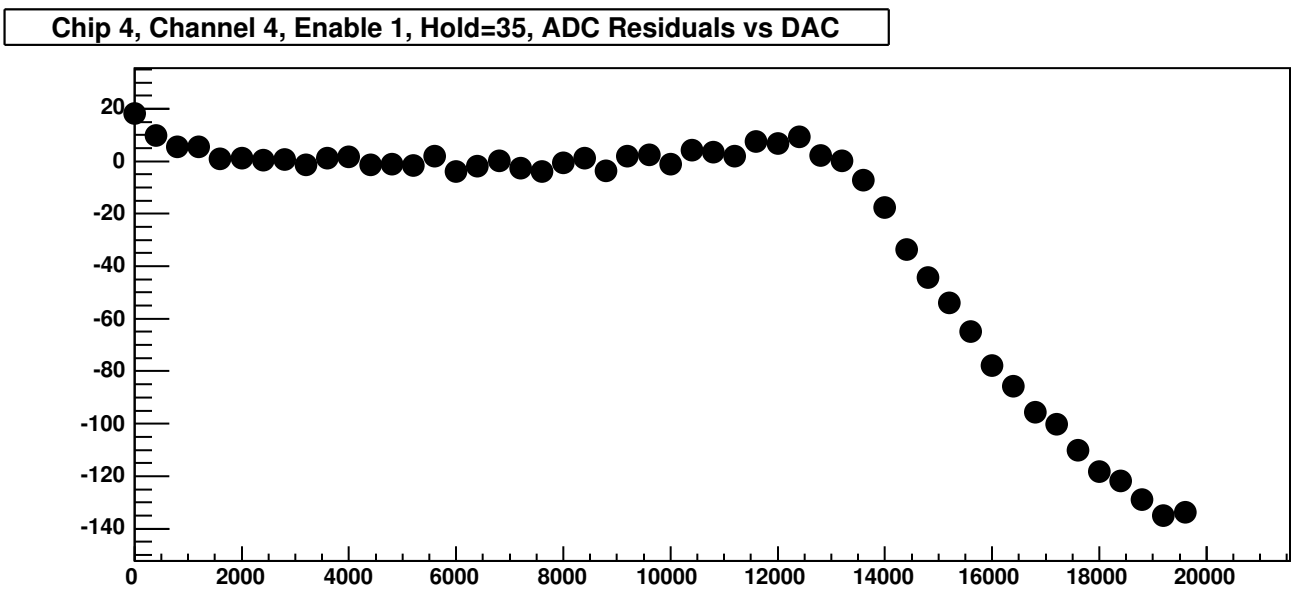


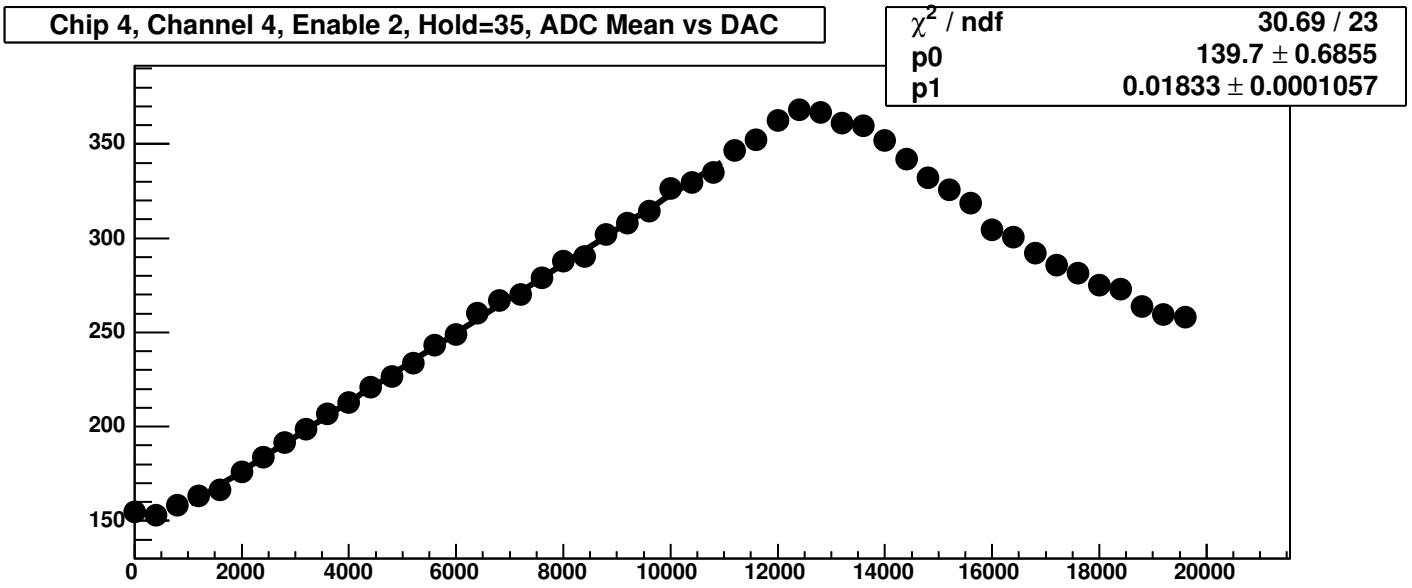
Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

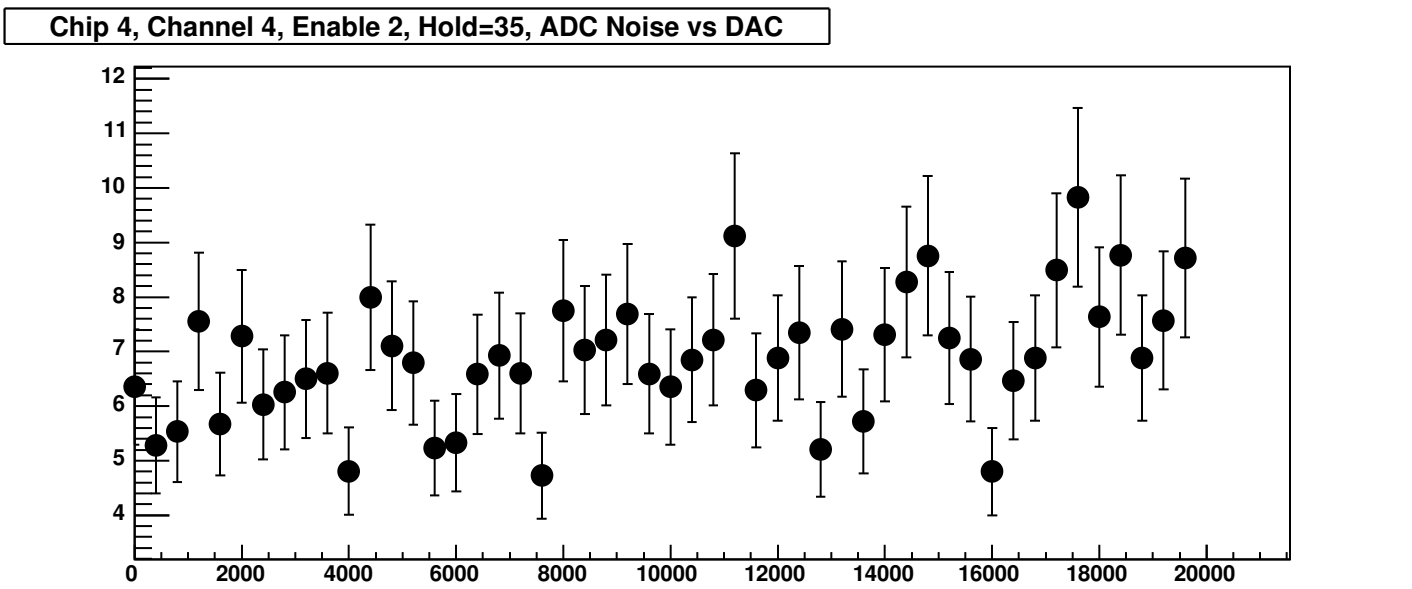


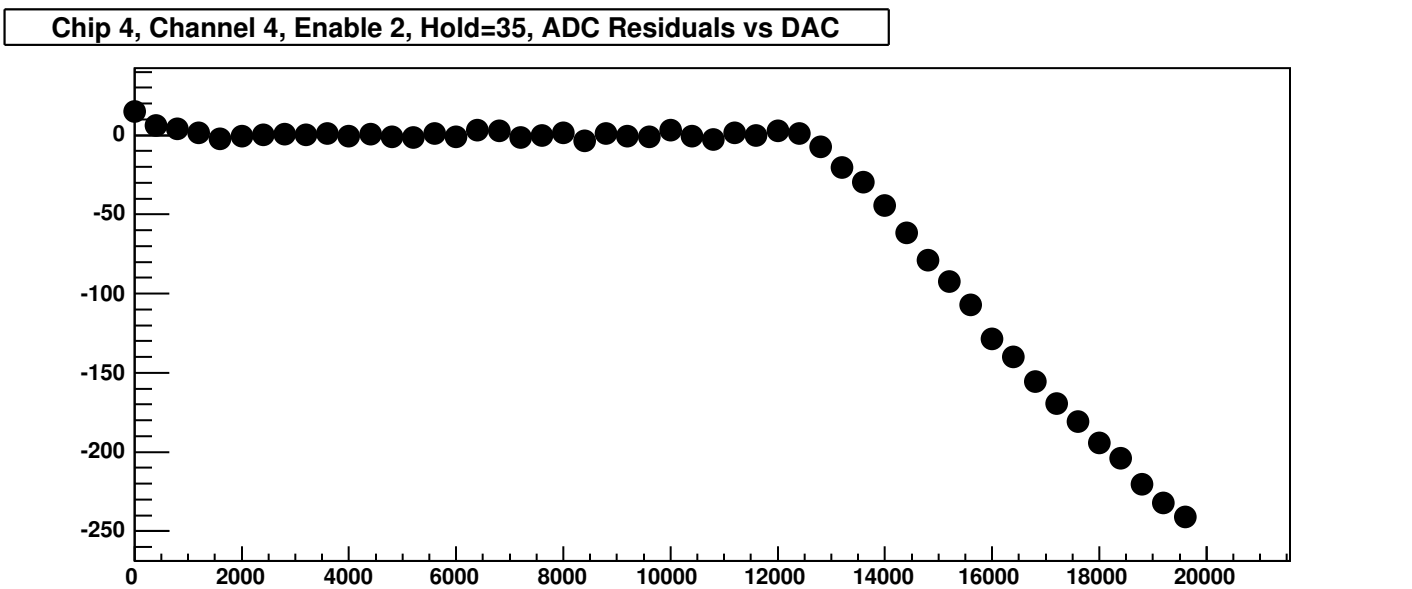

Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 71.88 / 23
p0 135.9 ± 0.714
p1 0.02286 ± 0.0001052
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

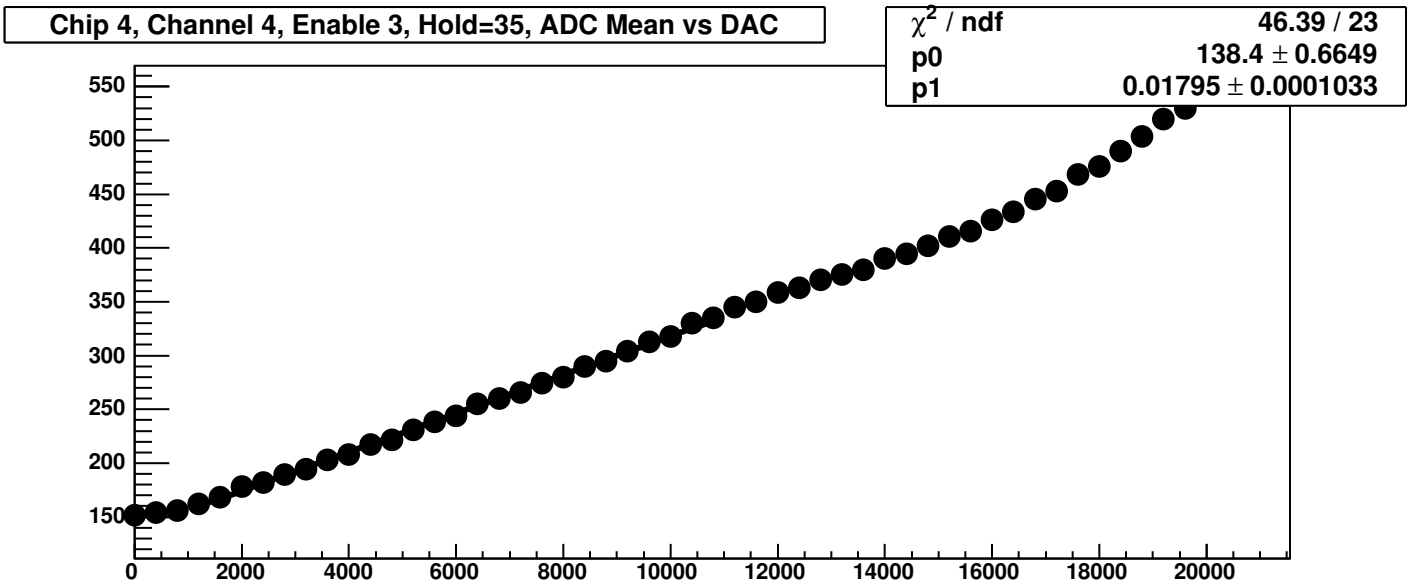

Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

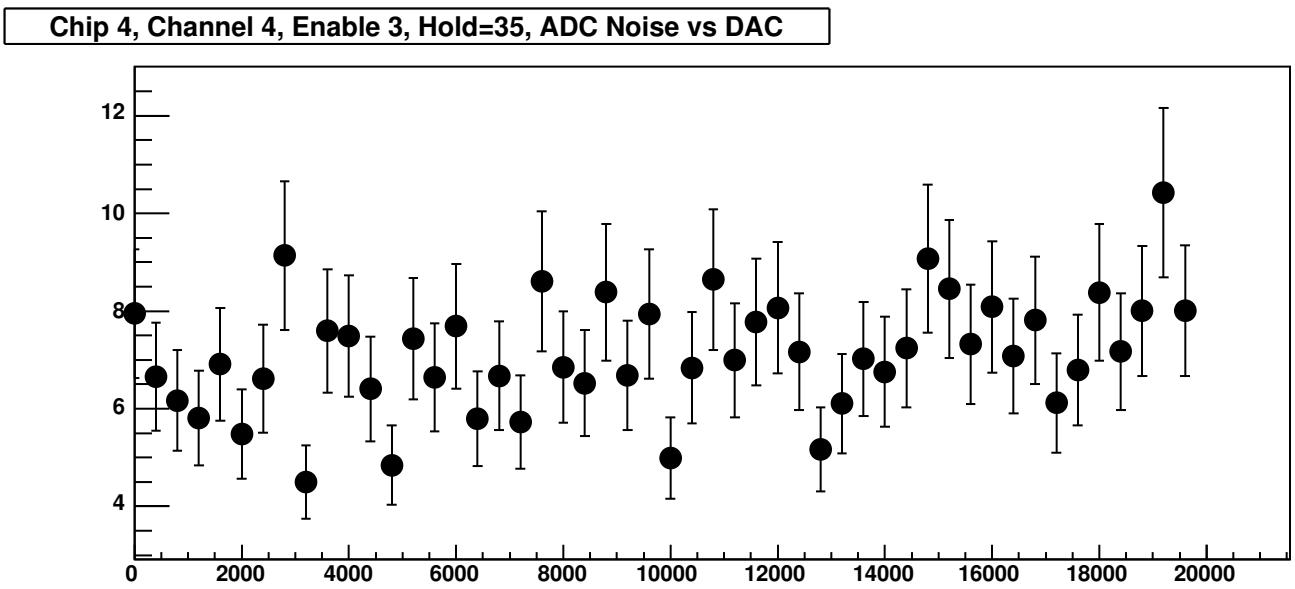

Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

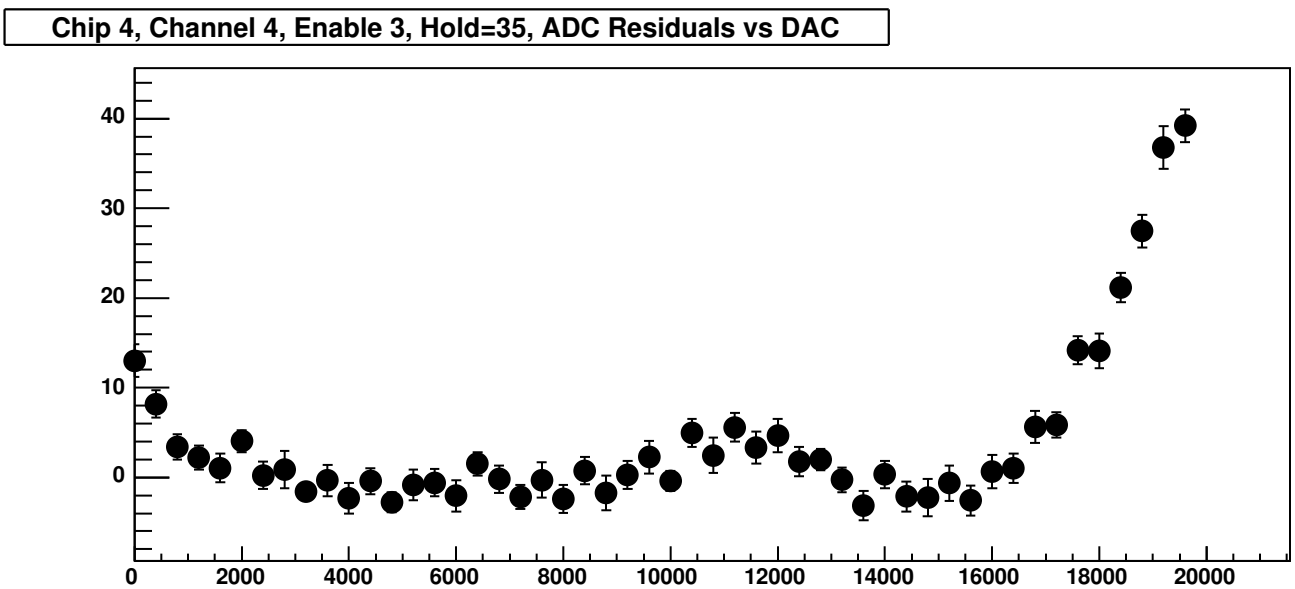

Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.69 / 23
p0 139.7 ± 0.6855
p1 0.01833 ± 0.0001057
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

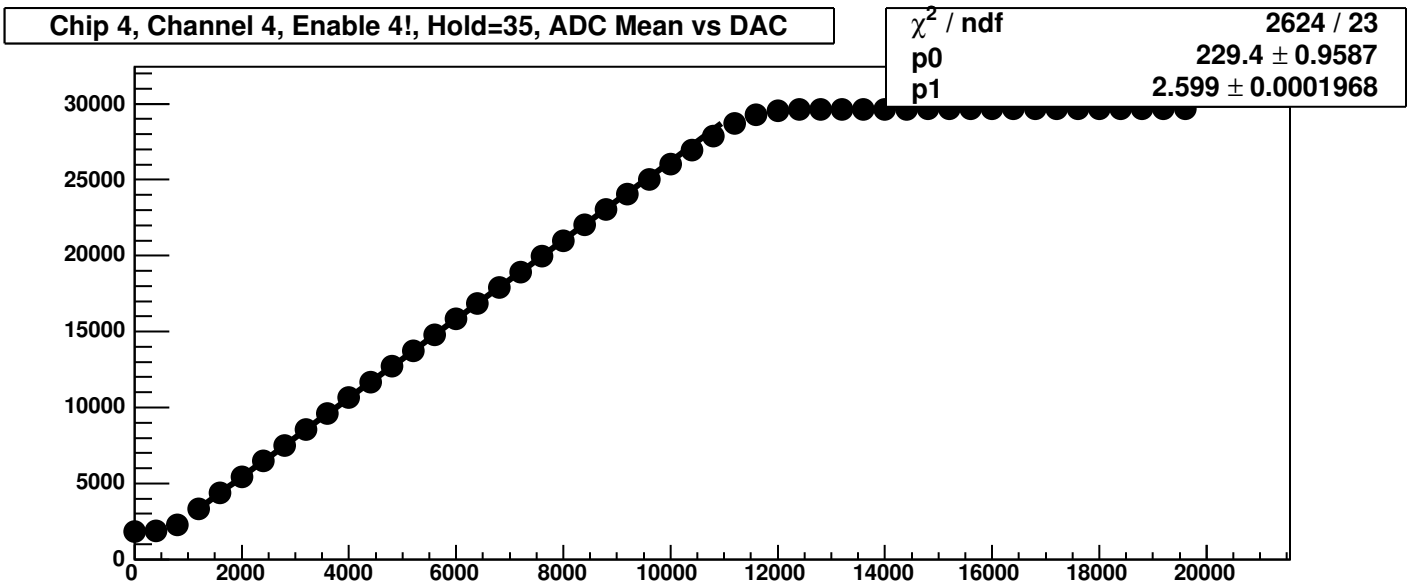

Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

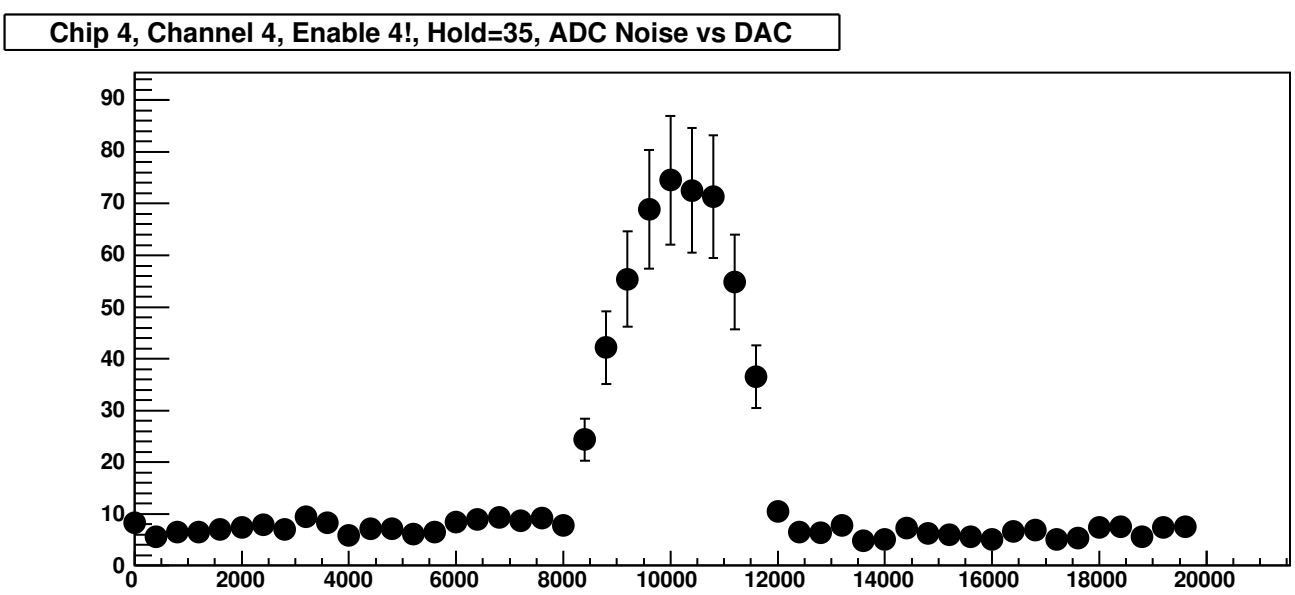

Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

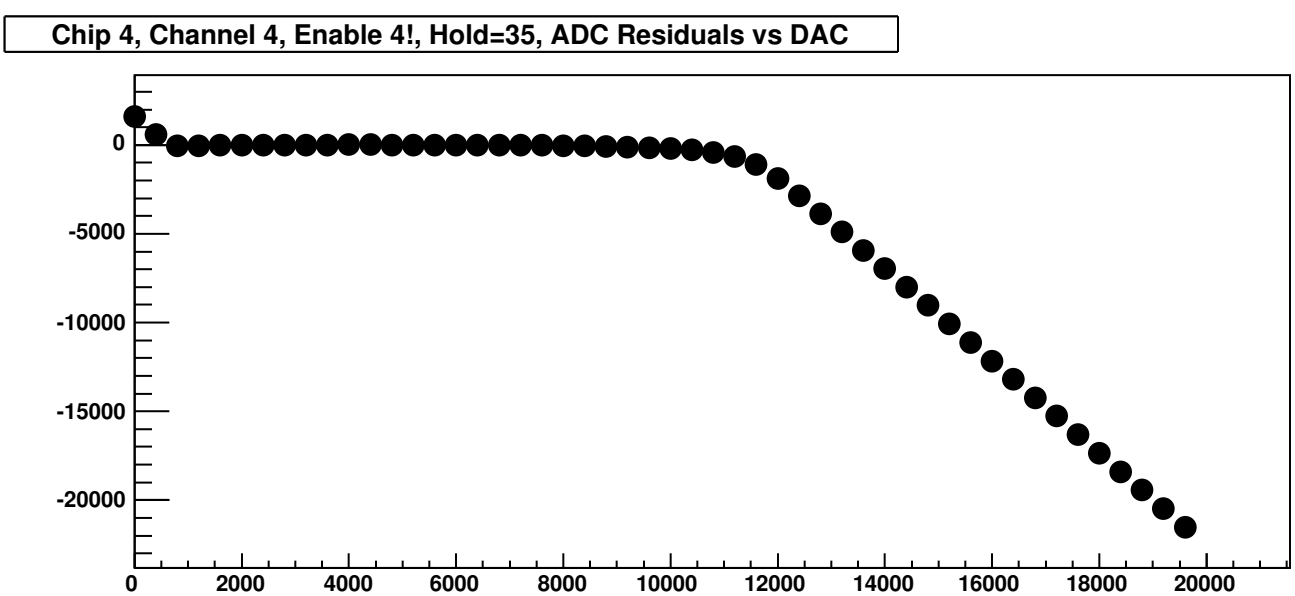

Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 46.39 / 23
p0 138.4 ± 0.6649
p1 0.01795 ± 0.0001033
550
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

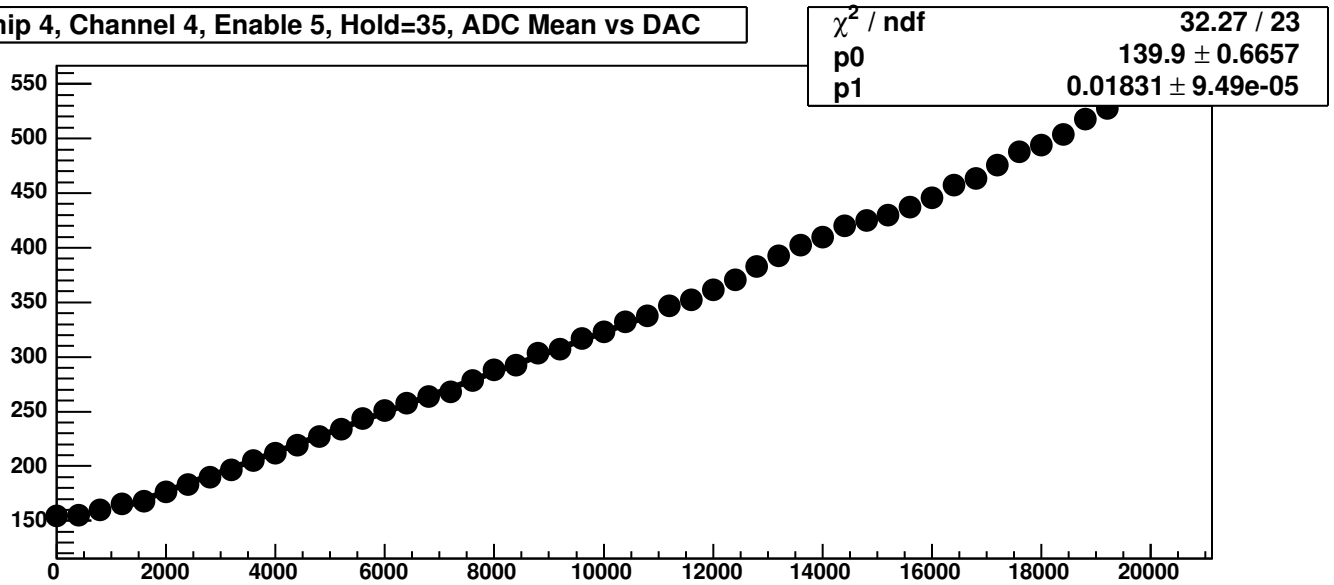

Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 2624 / 23
p0 229.4 ± 0.9587
p1 2.599 ± 0.0001968
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

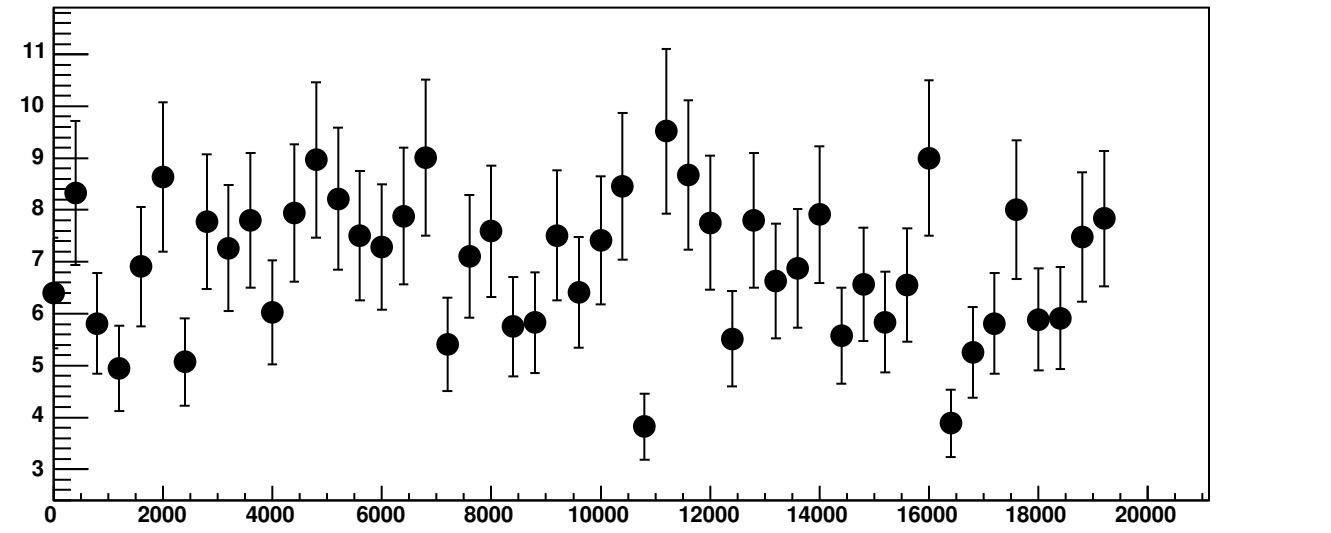

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

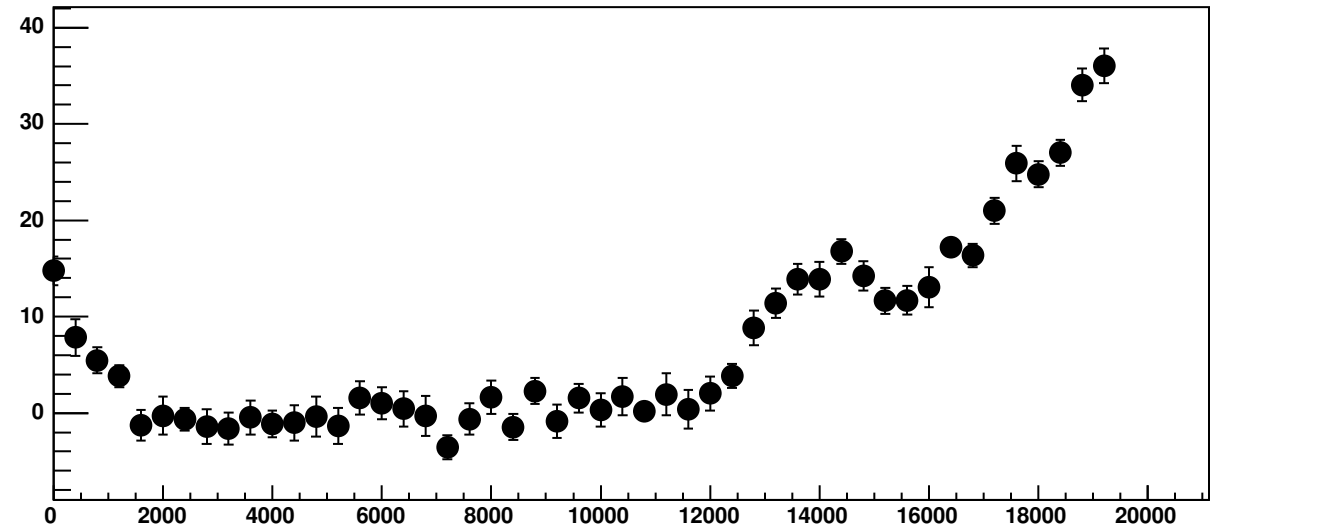
Chip 4, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

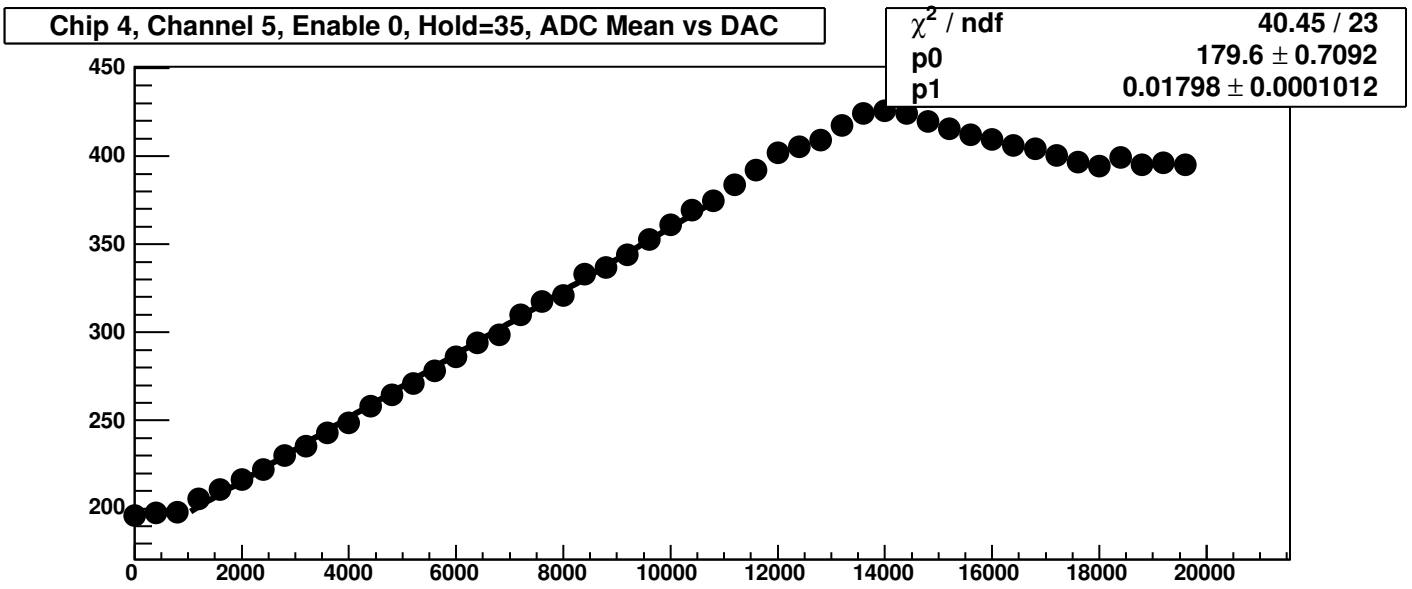


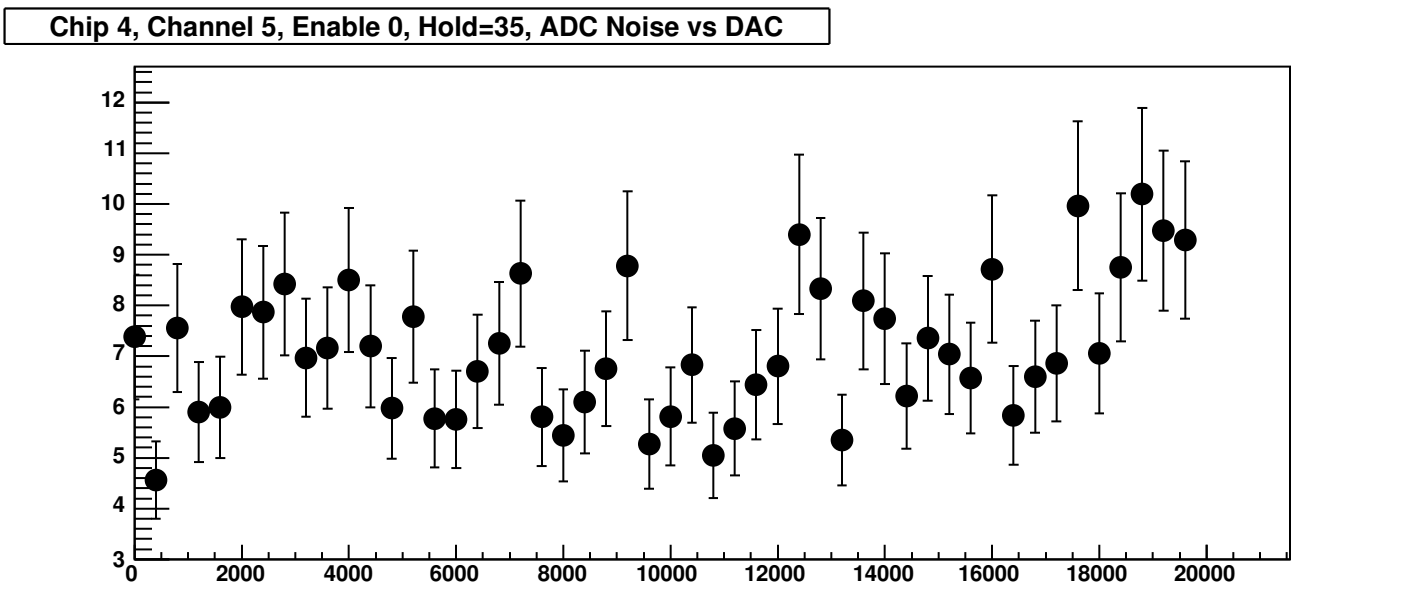
Chip 4, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

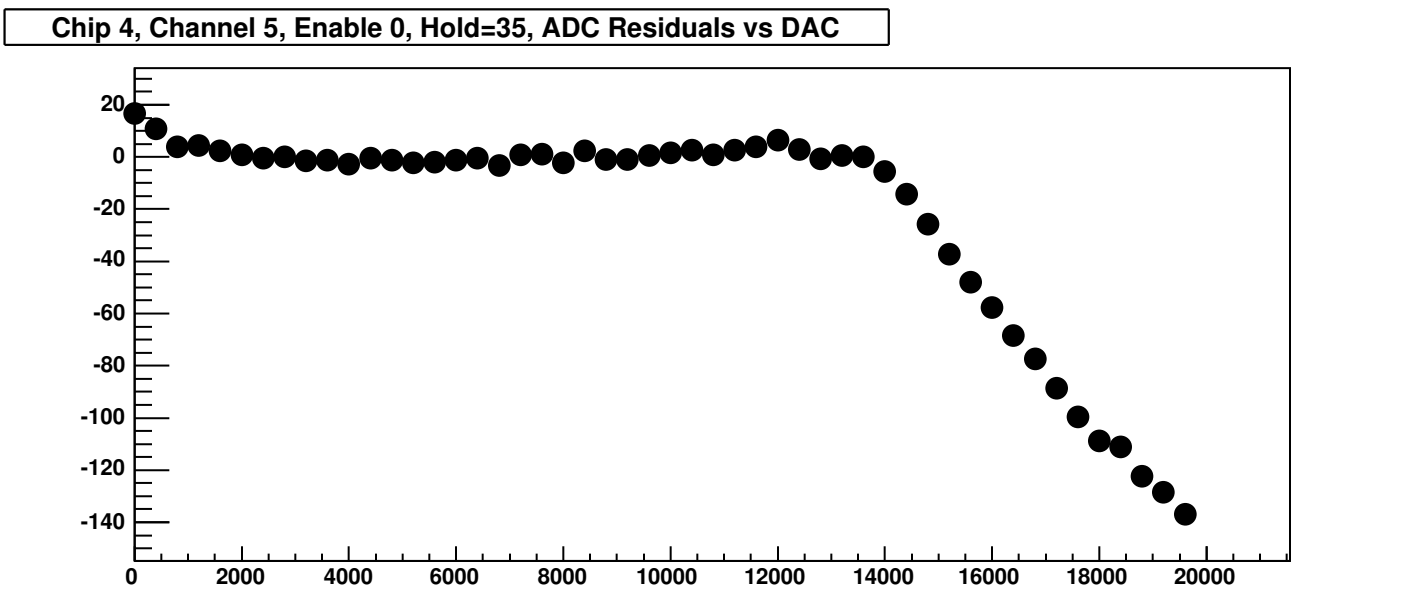


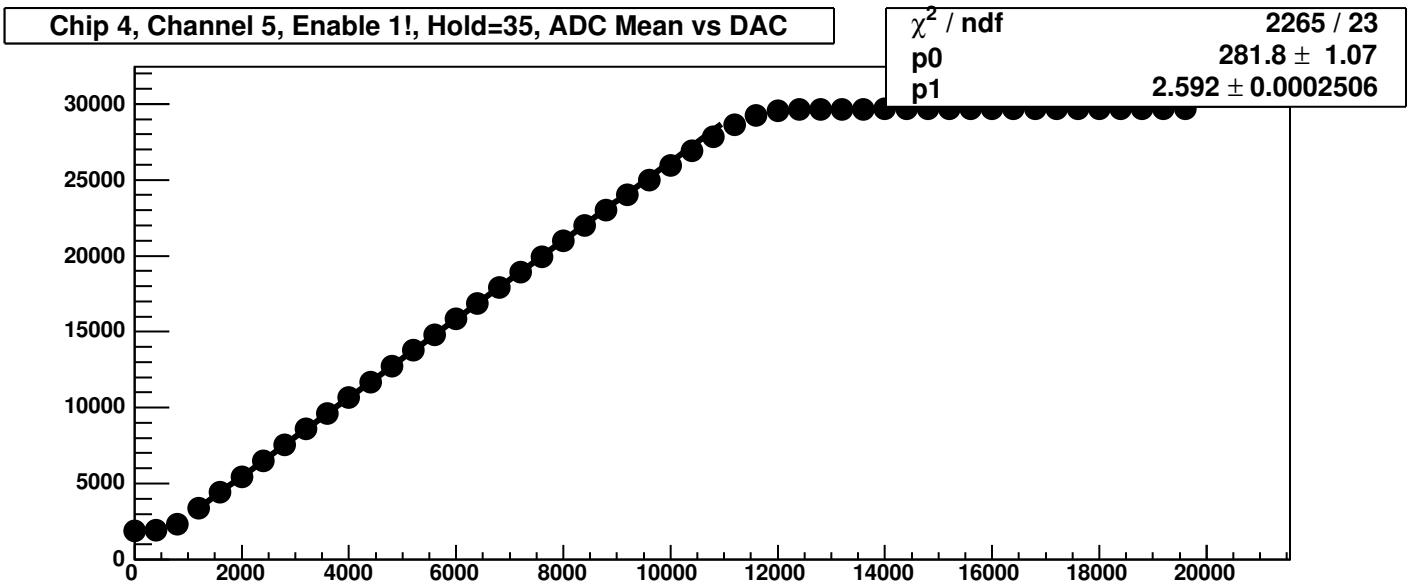
Chip 4, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

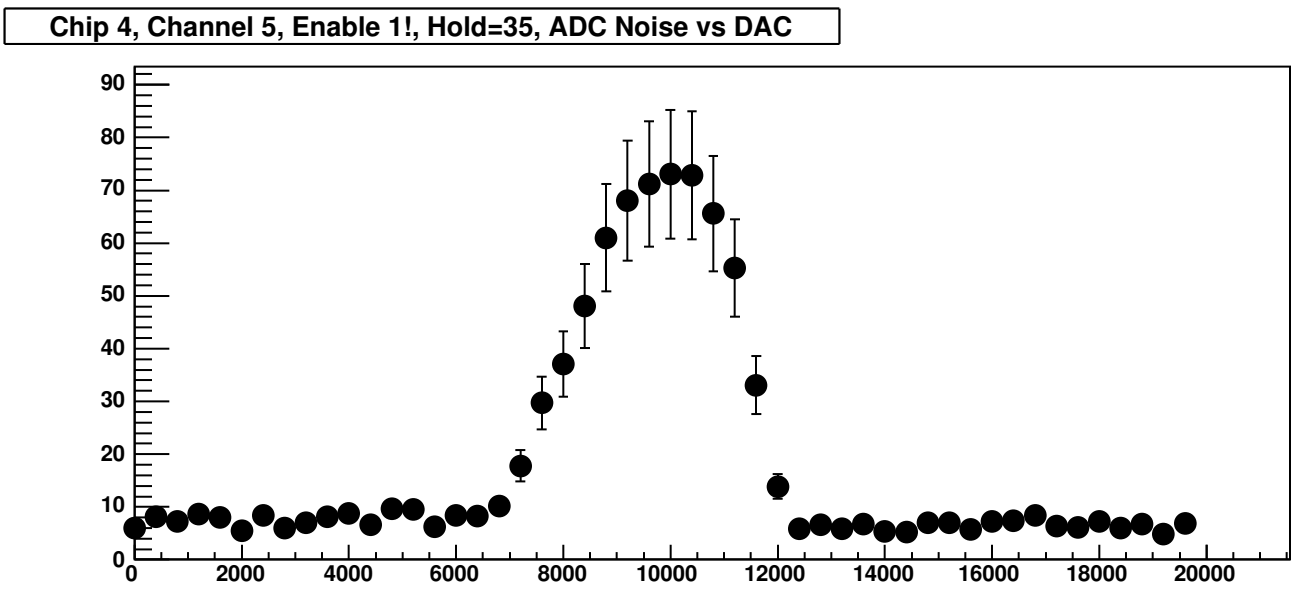


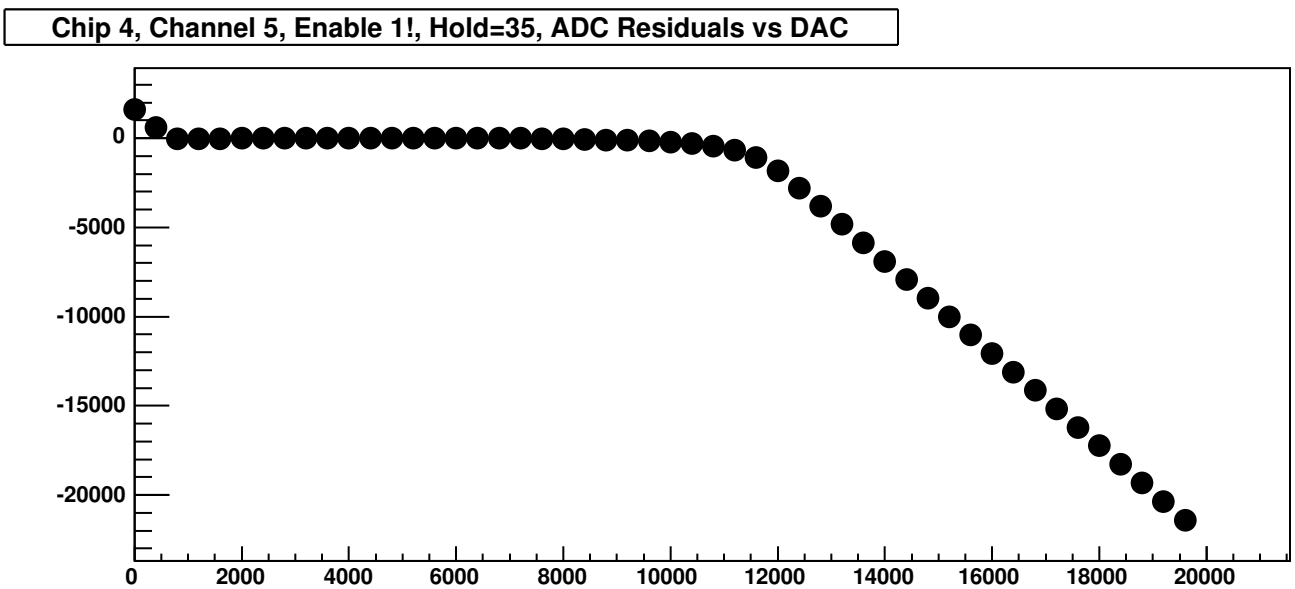

Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 40.45 / 23
p0 179.6 ± 0.7092
p1 0.01798 ± 0.0001012
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

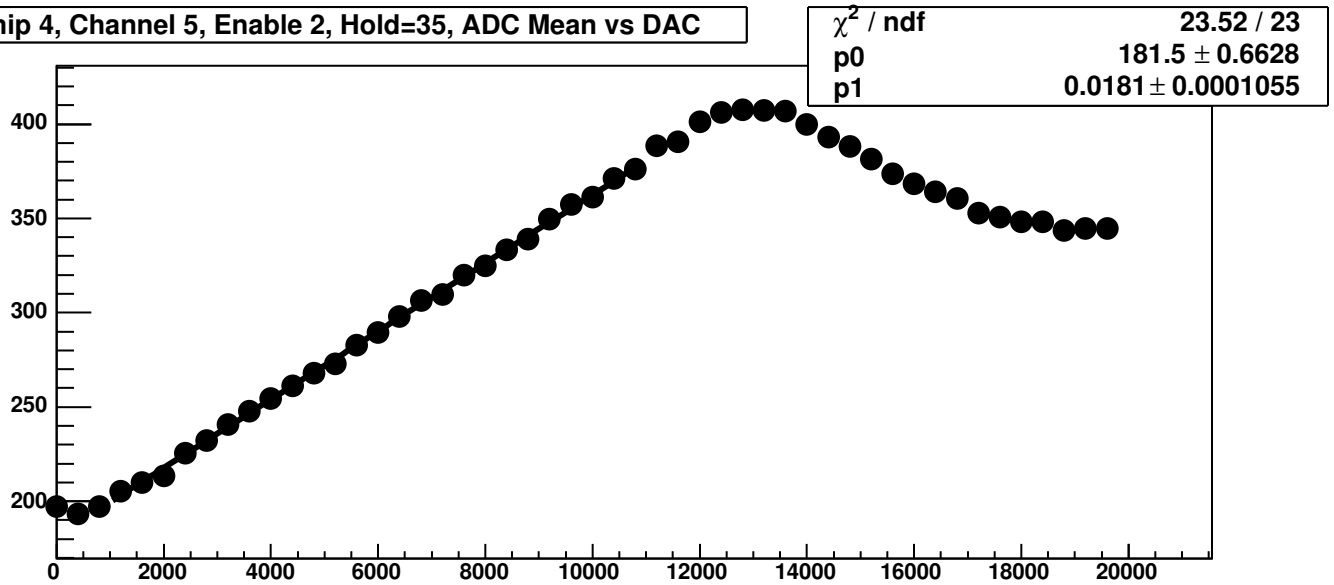

Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 2265 / 23
p0 281.8 ± 1.07
p1 2.592 ± 0.0002506
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

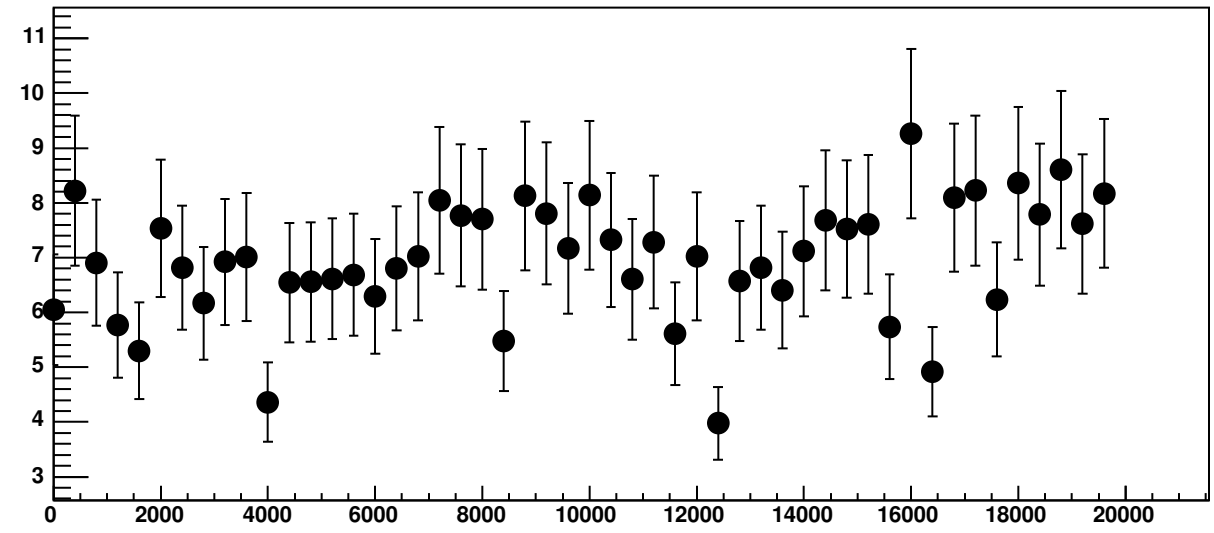

Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

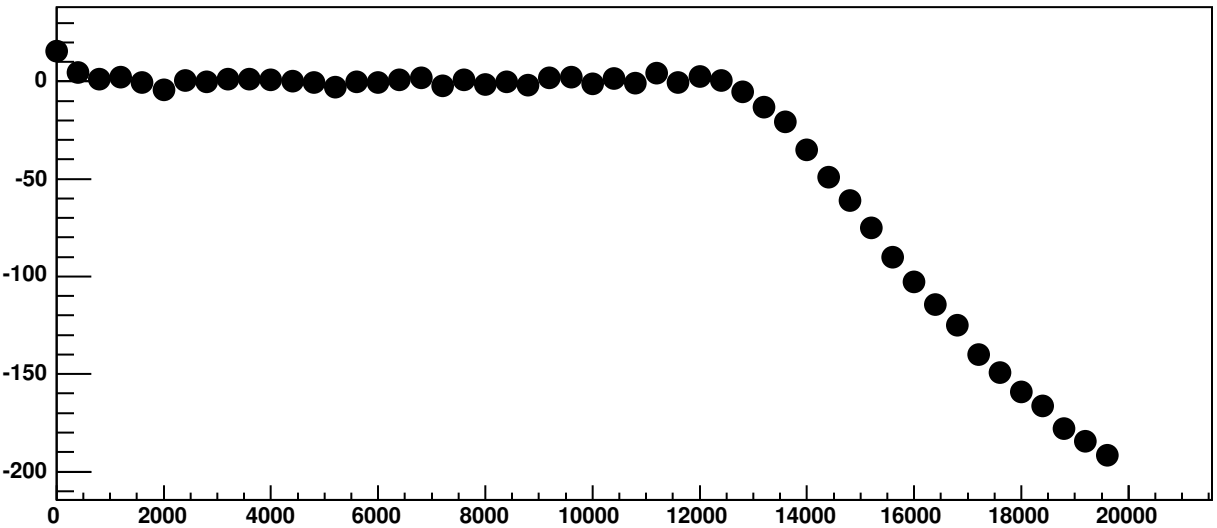
Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



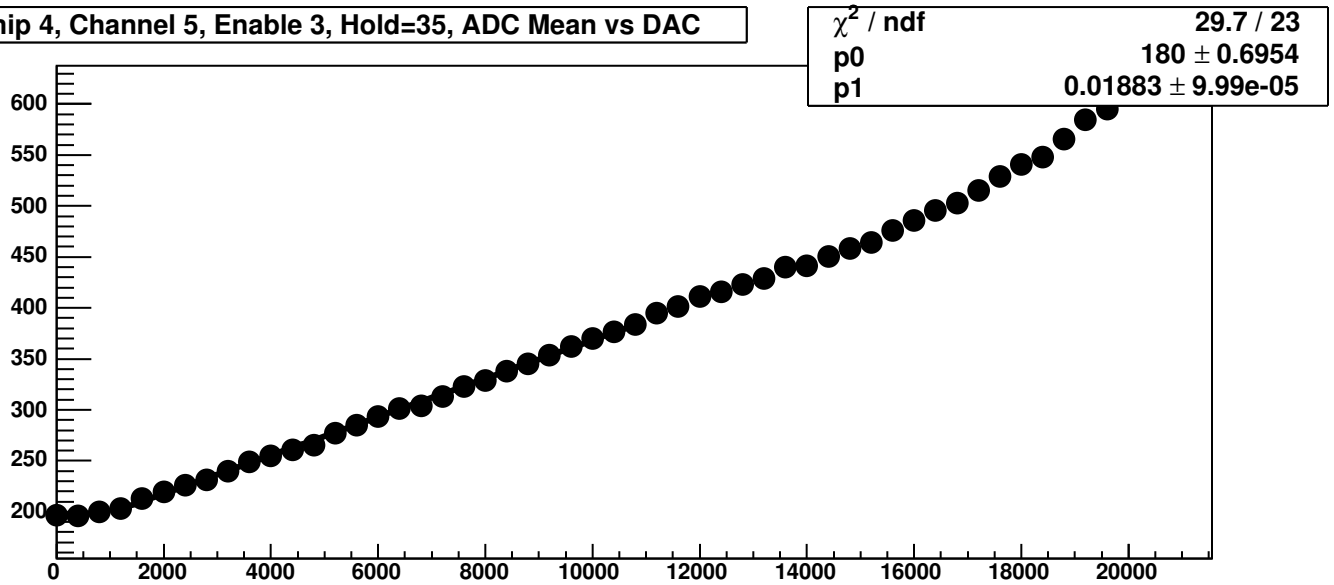
Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



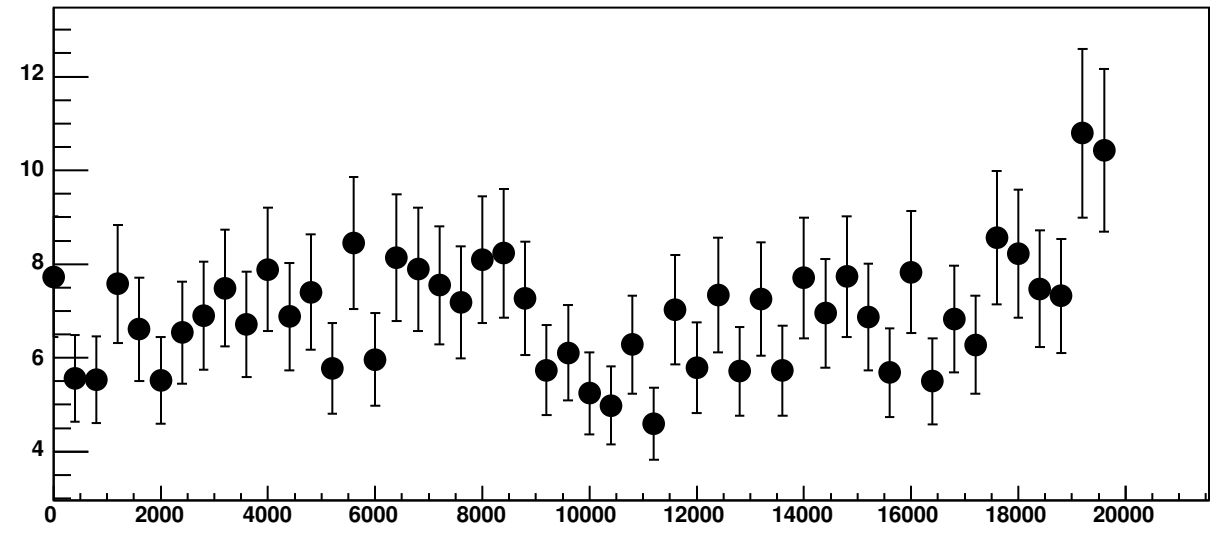
Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



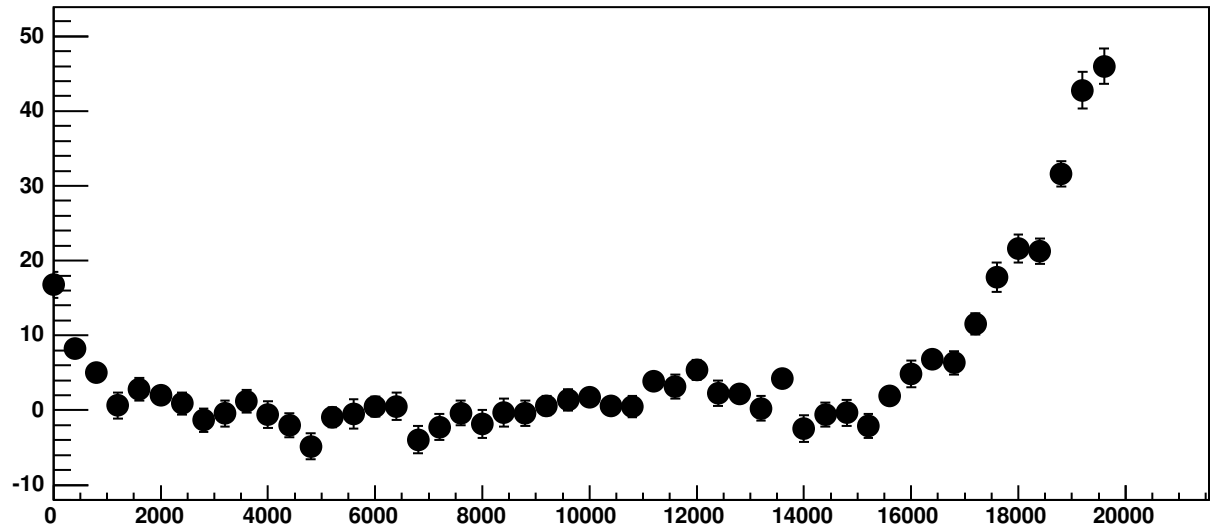
Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



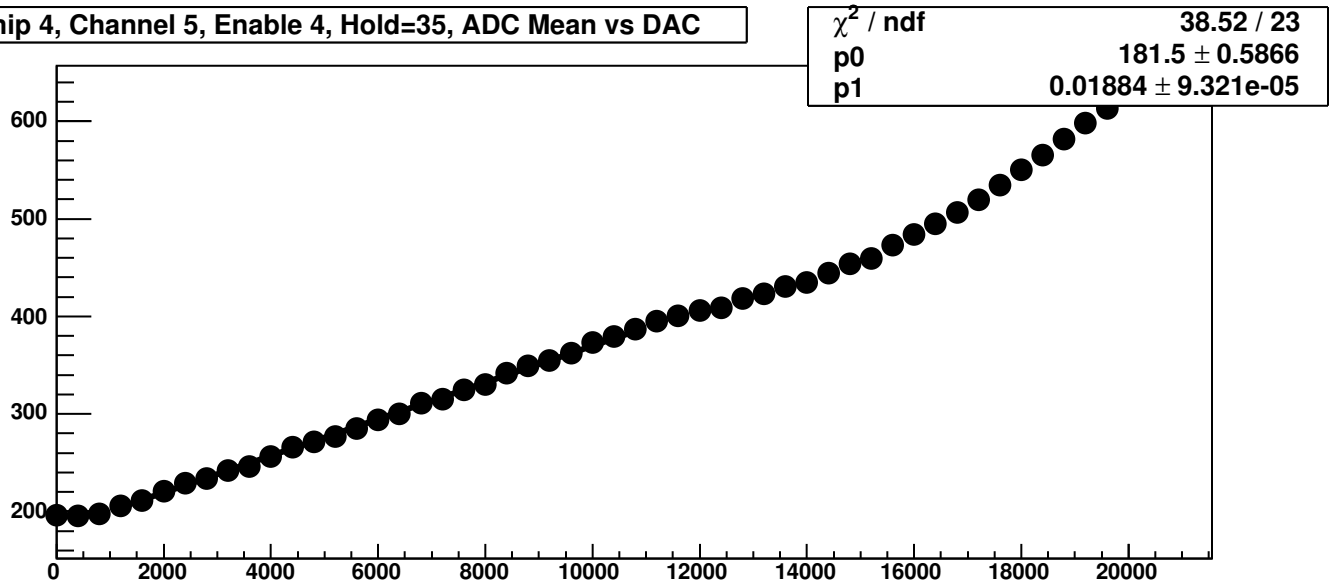
Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



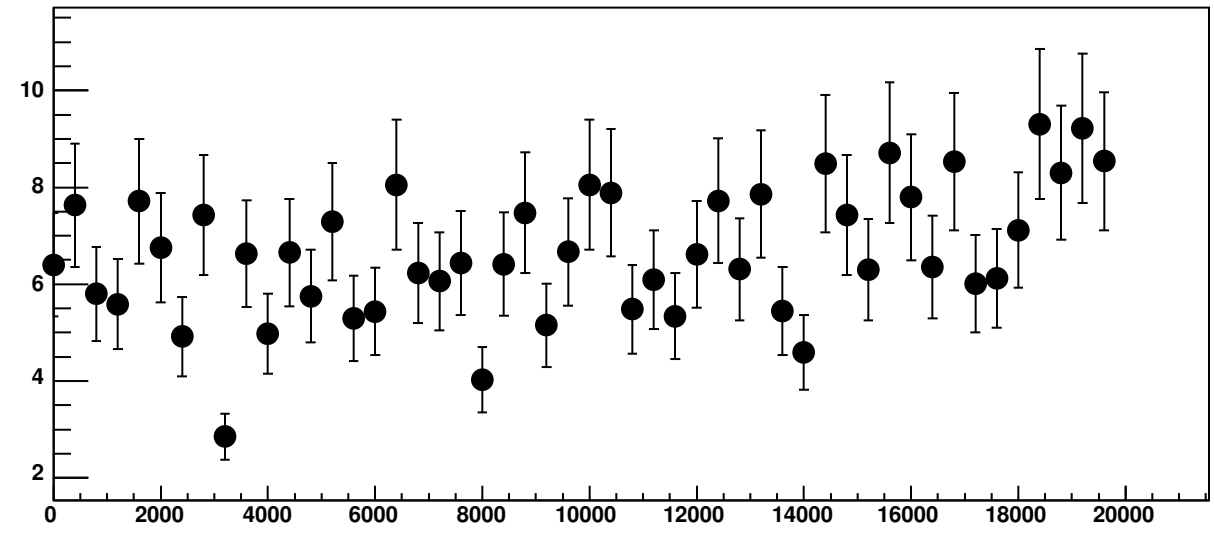
Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



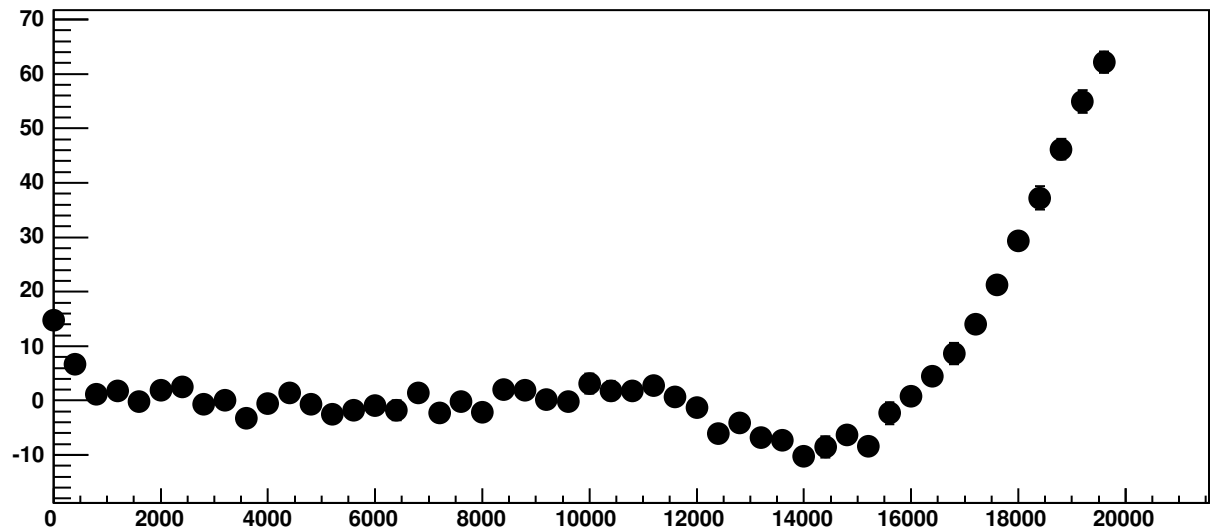
Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



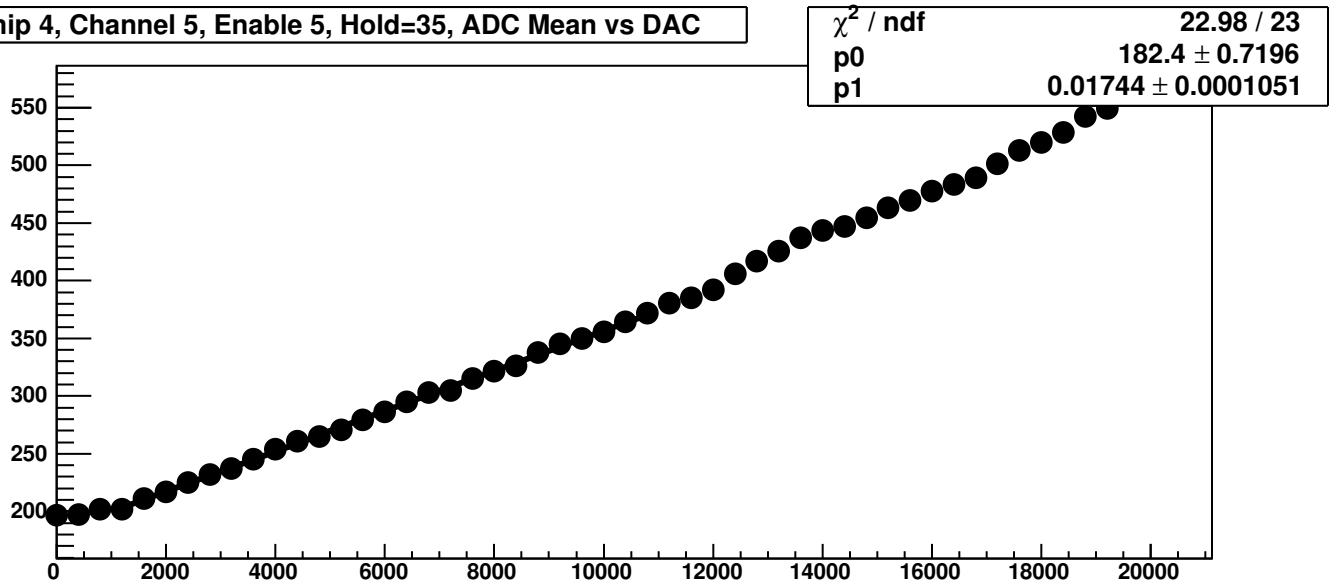
Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



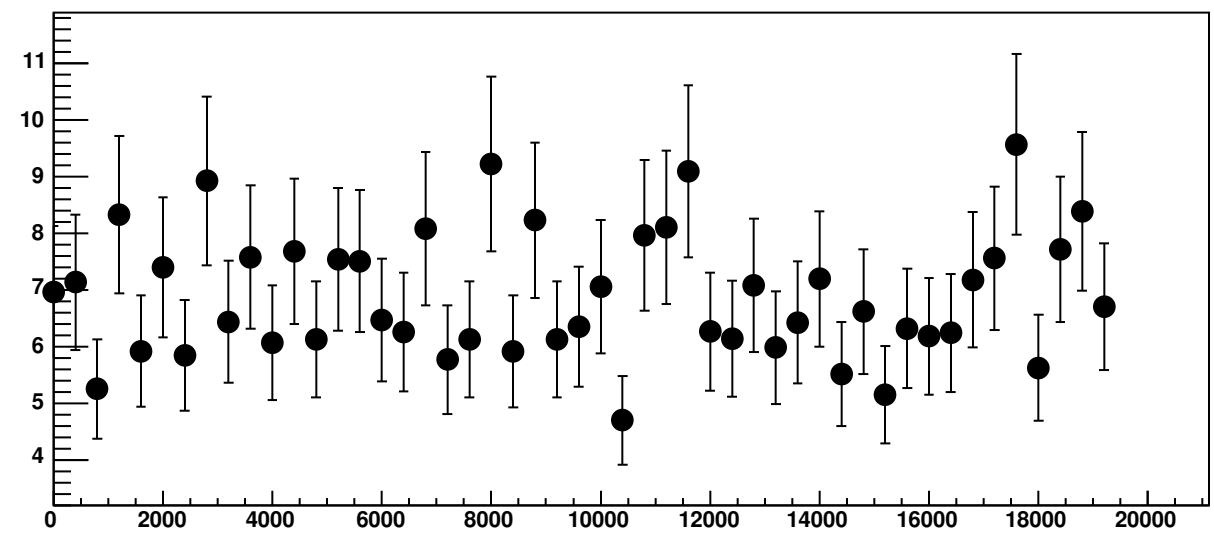
Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



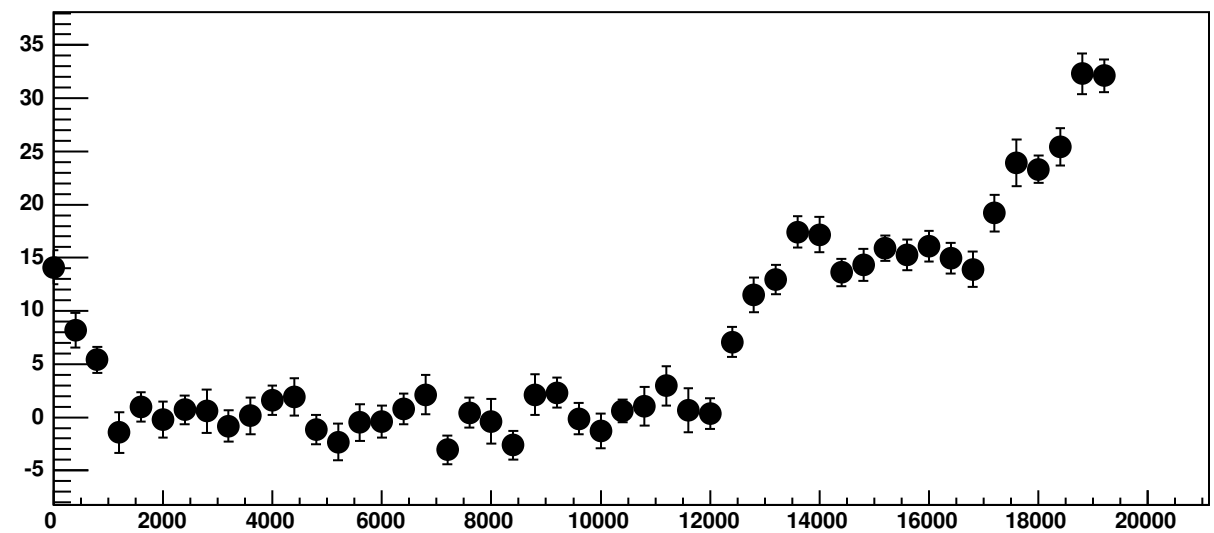
Chip 4, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

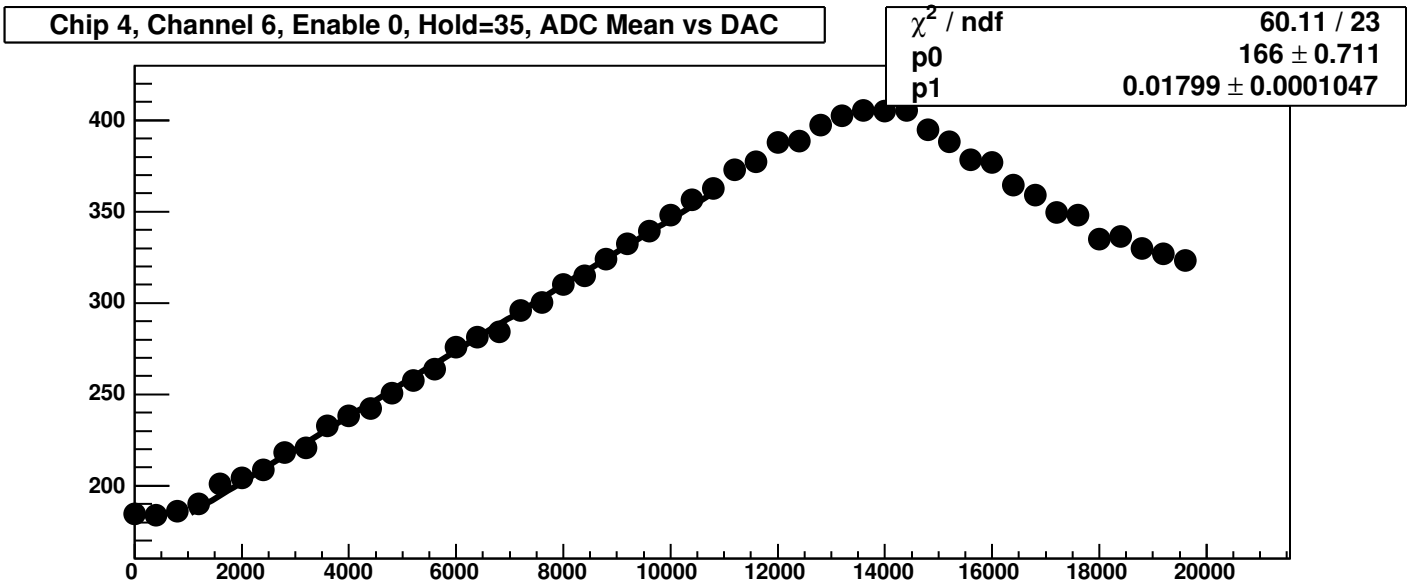


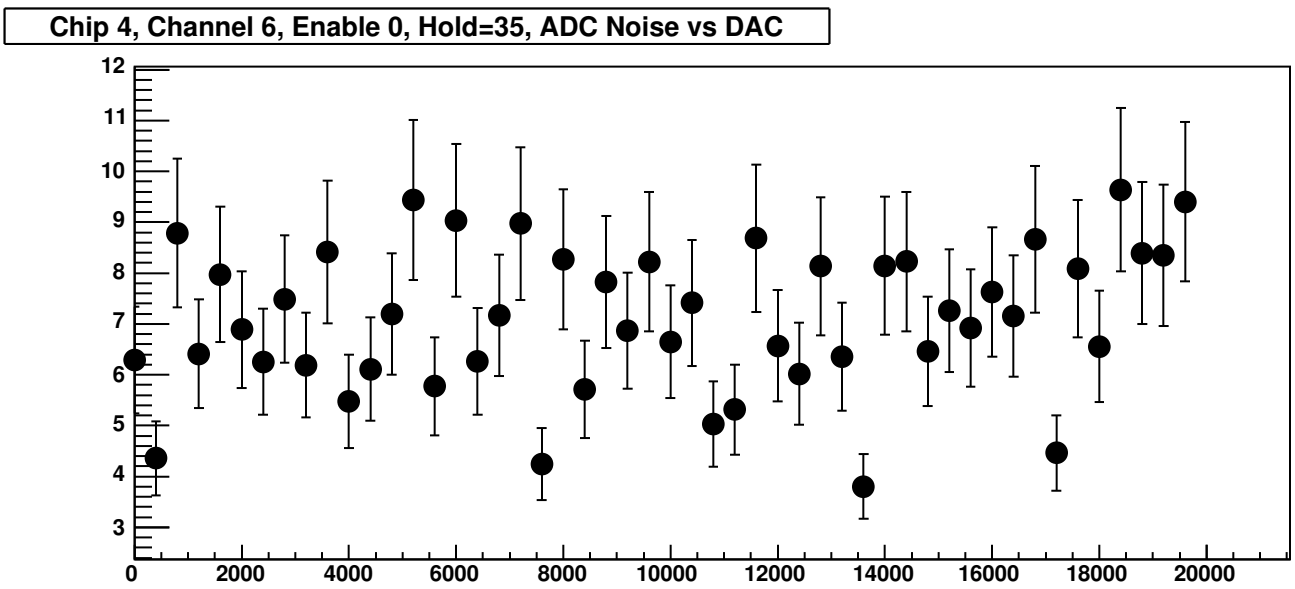
Chip 4, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

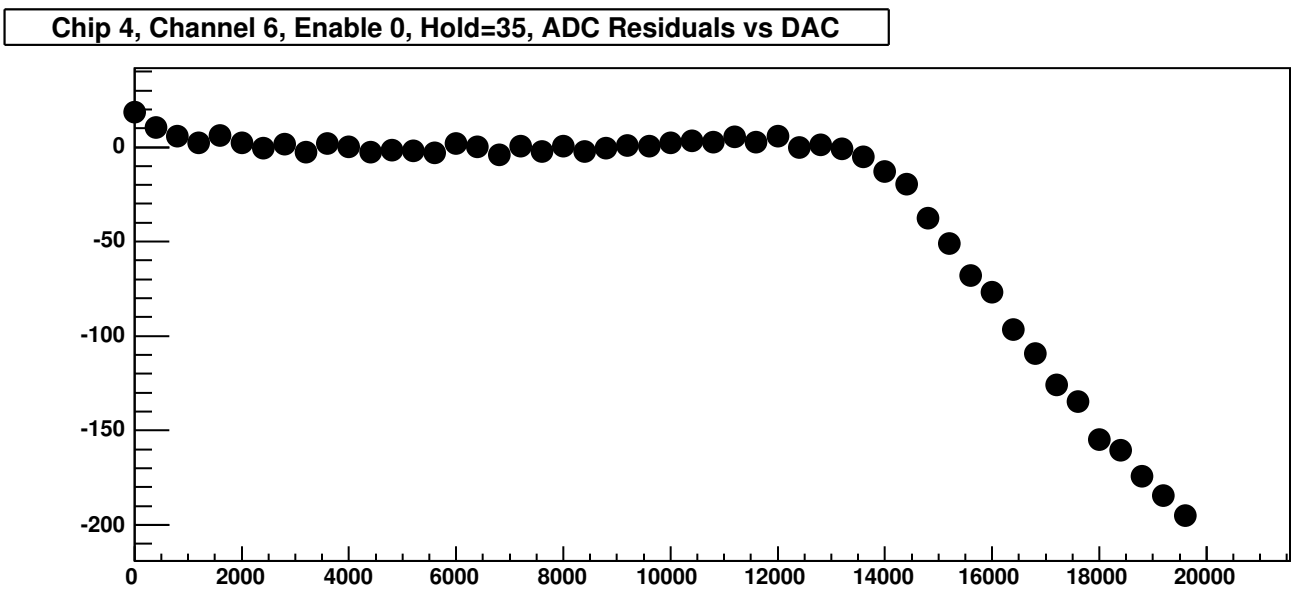


Chip 4, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

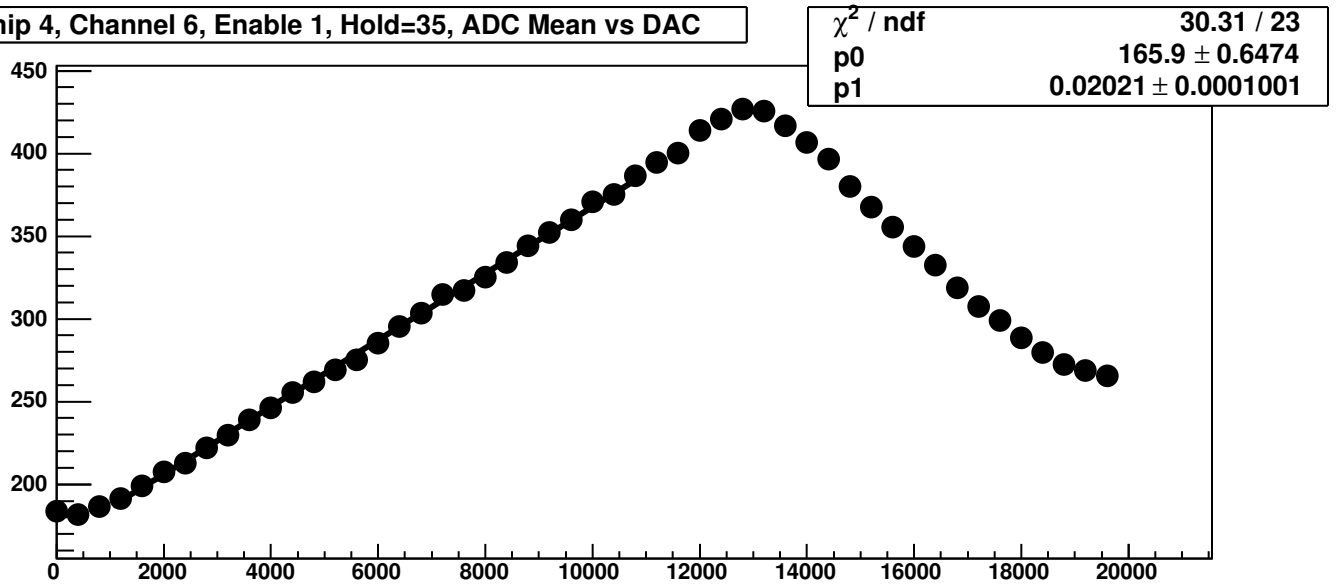



Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 60.11 / 23
p0 166 ± 0.711
p1 0.01799 ± 0.0001047
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

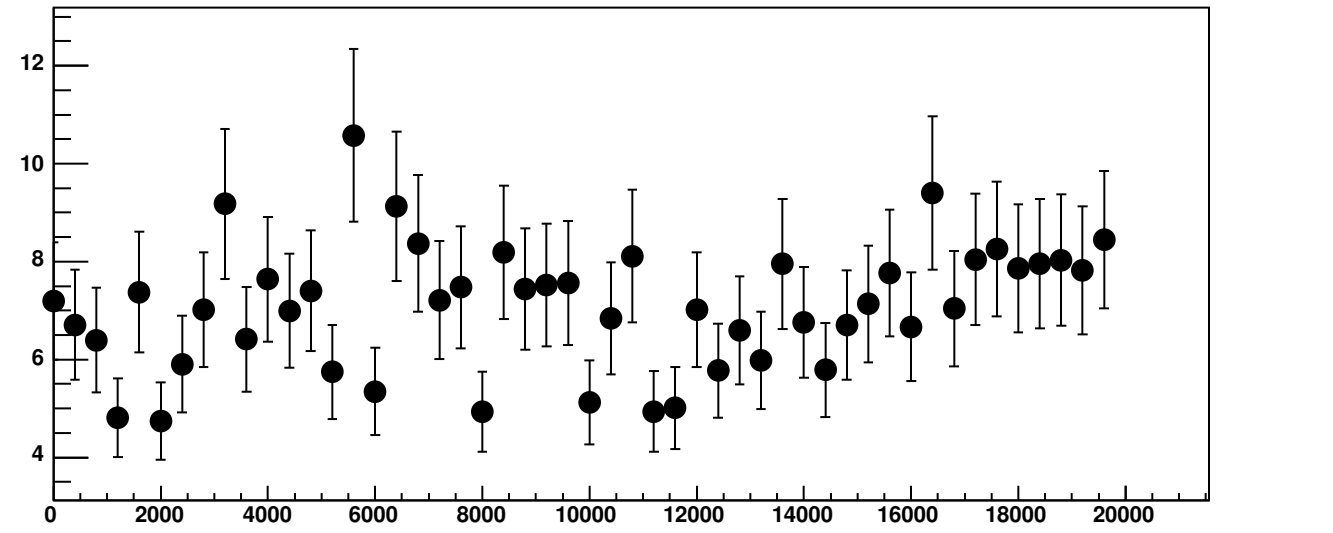

Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

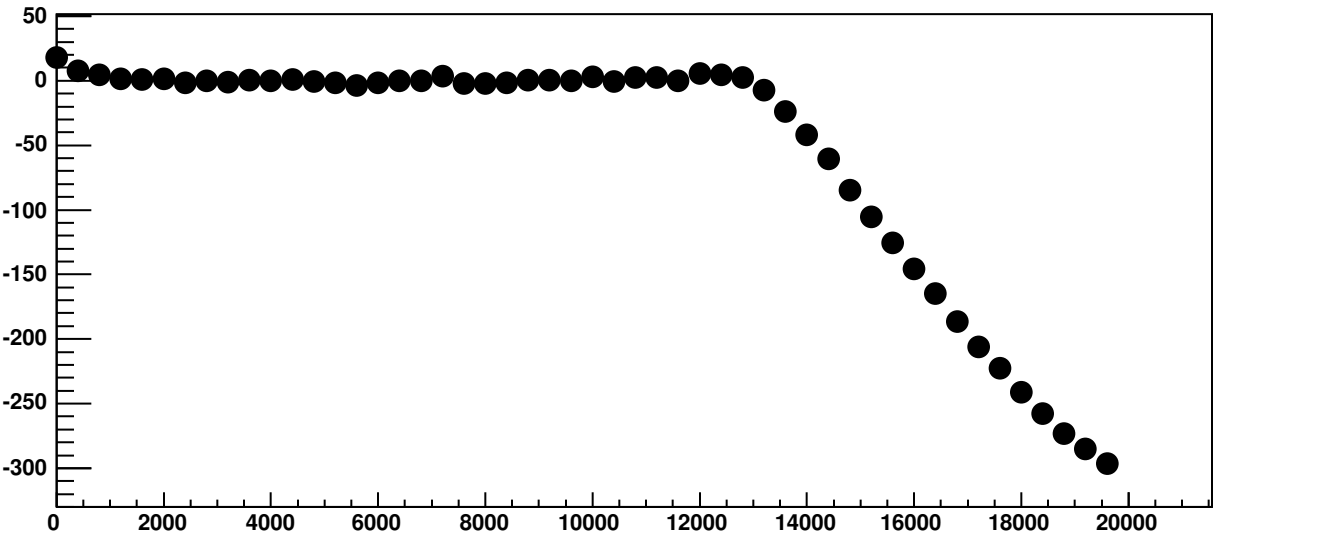
Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

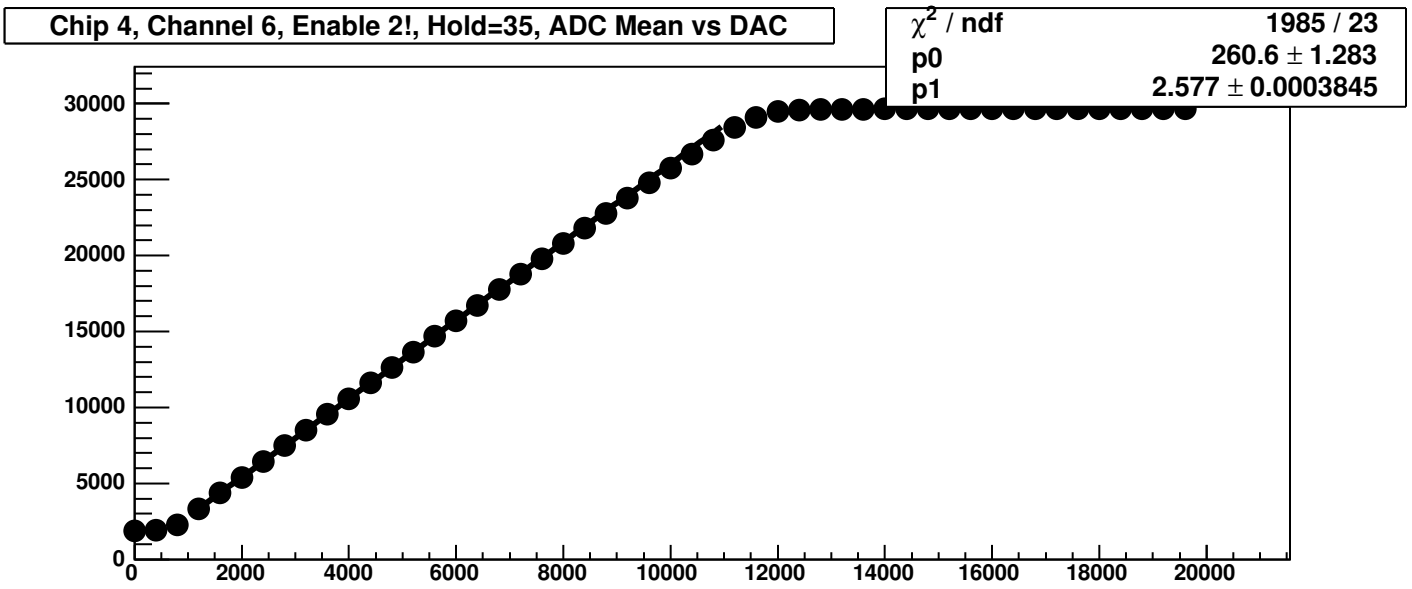


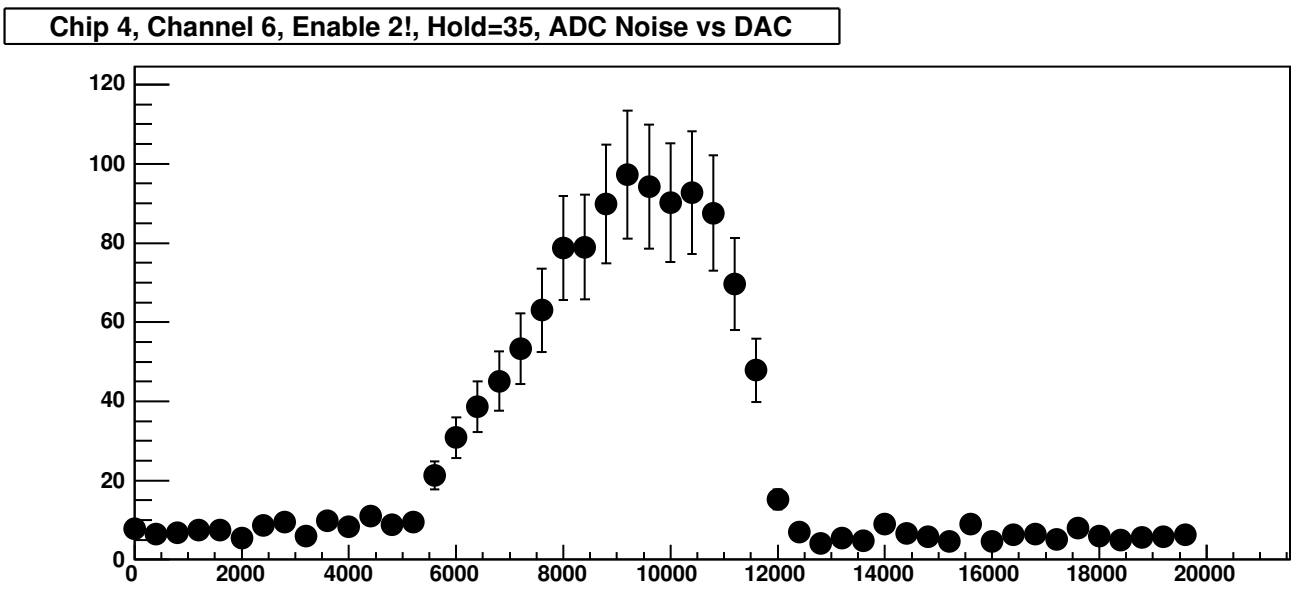
Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

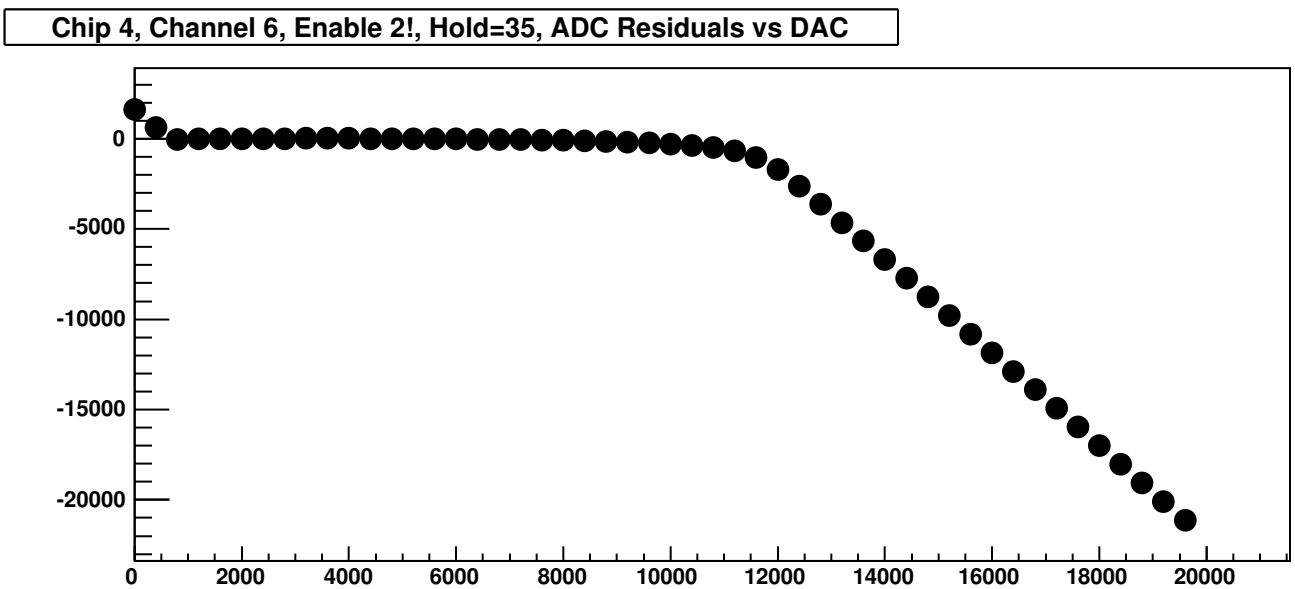


Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

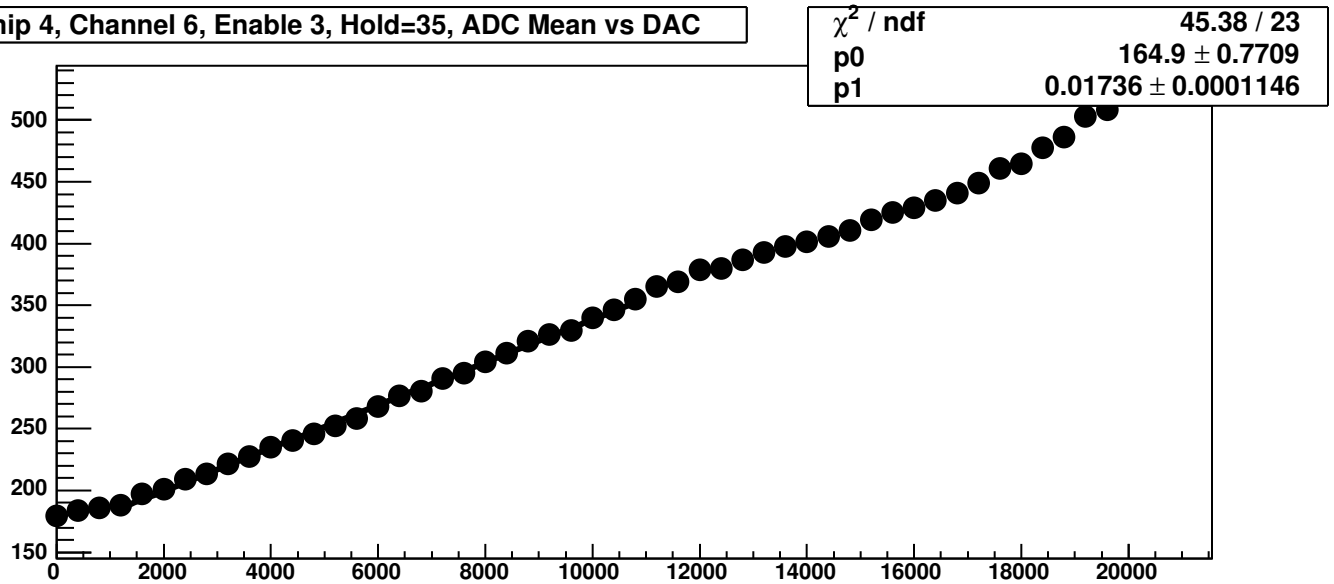



Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1985 / 23
p0 260.6 ± 1.283
p1 2.577 ± 0.0003845
30000
25000
20000
15000
10000
5000
0
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

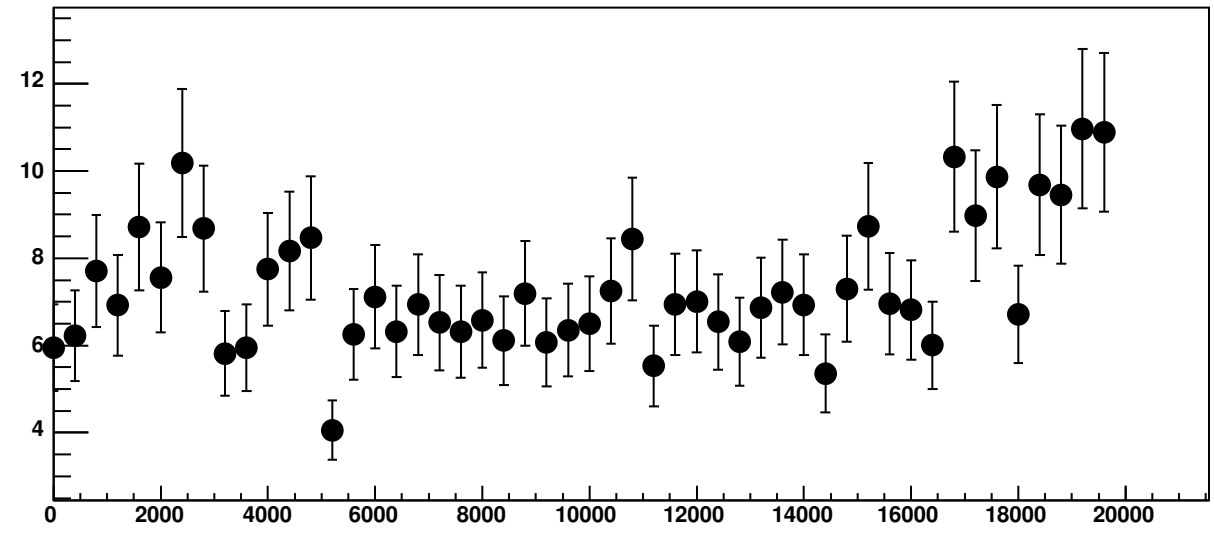

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000


Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

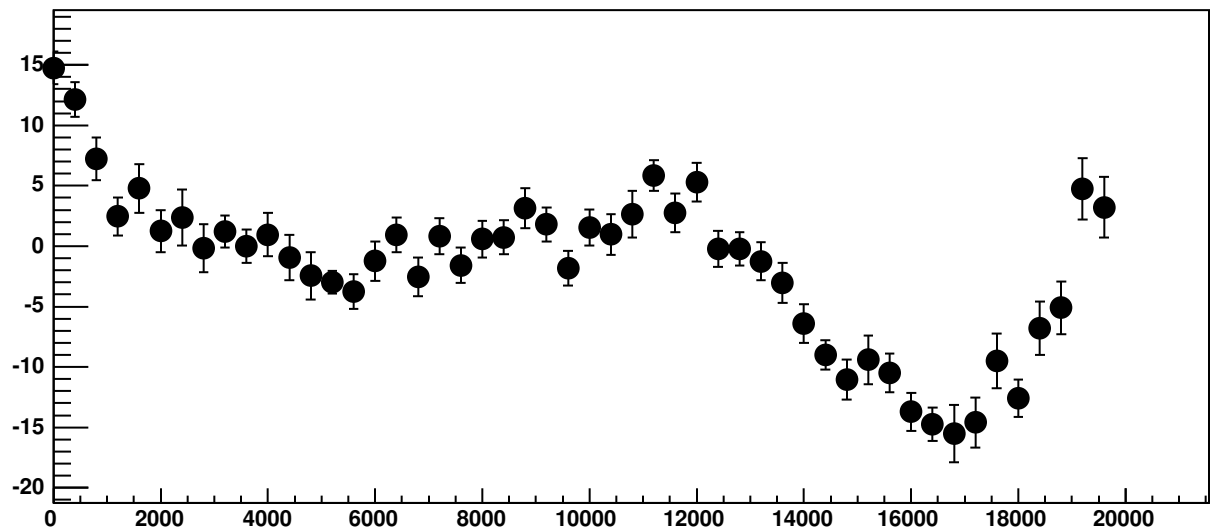
Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



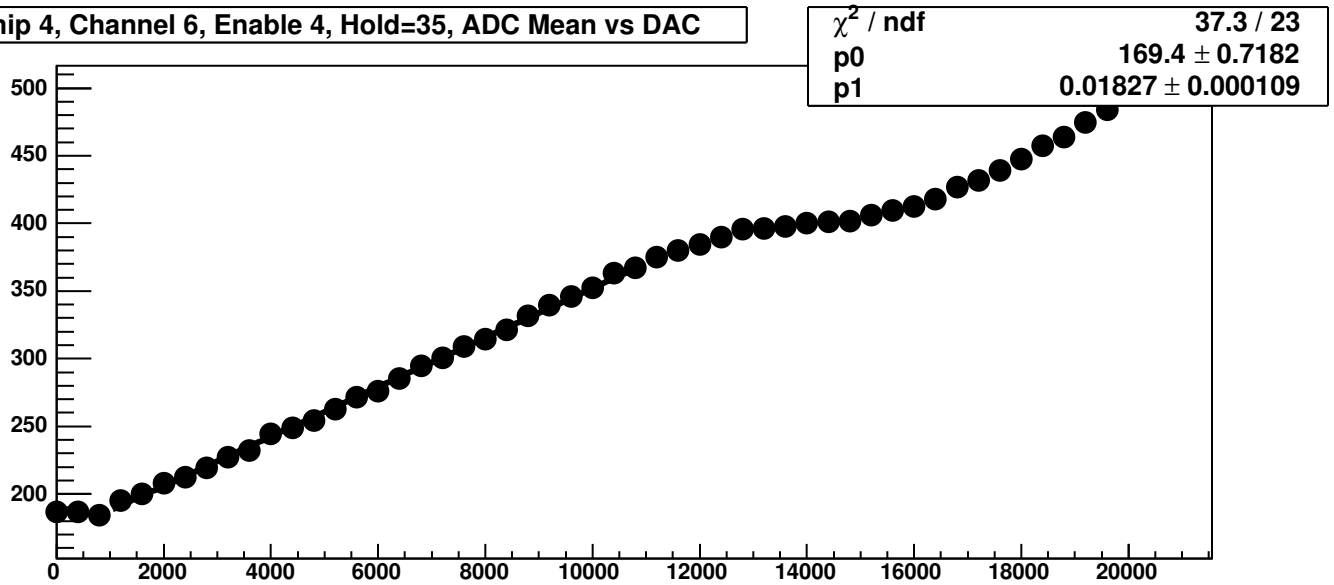
Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



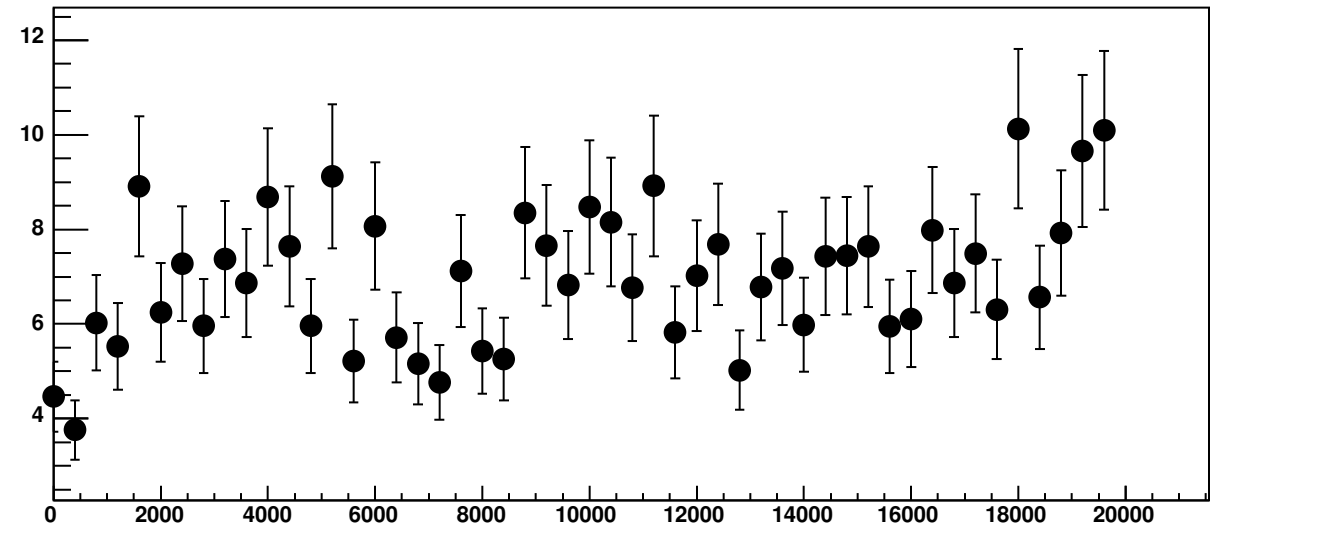
Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



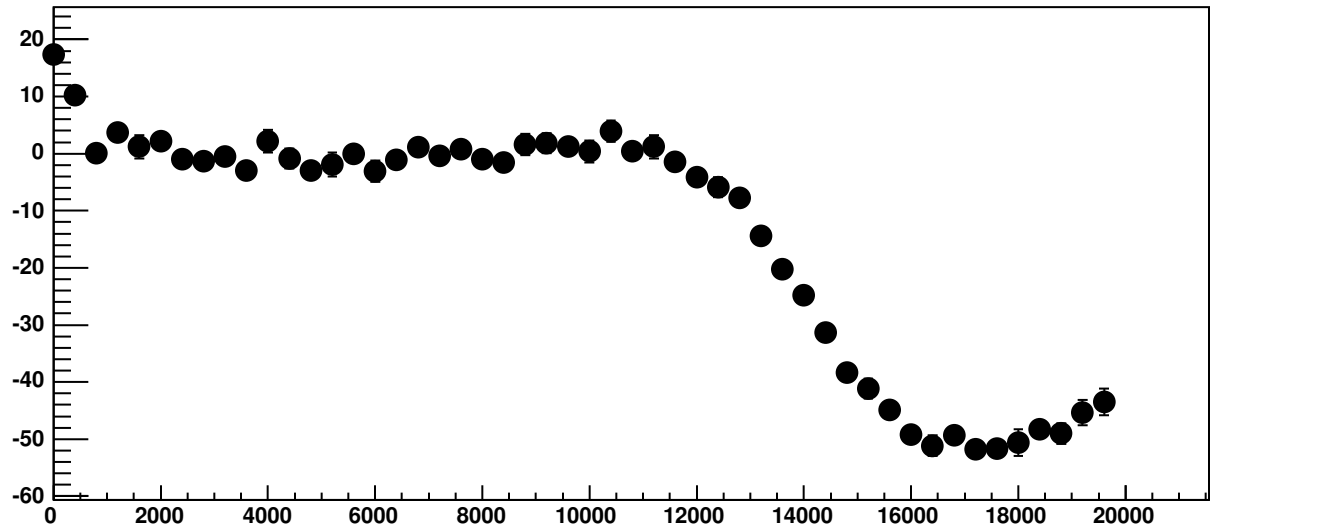
Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



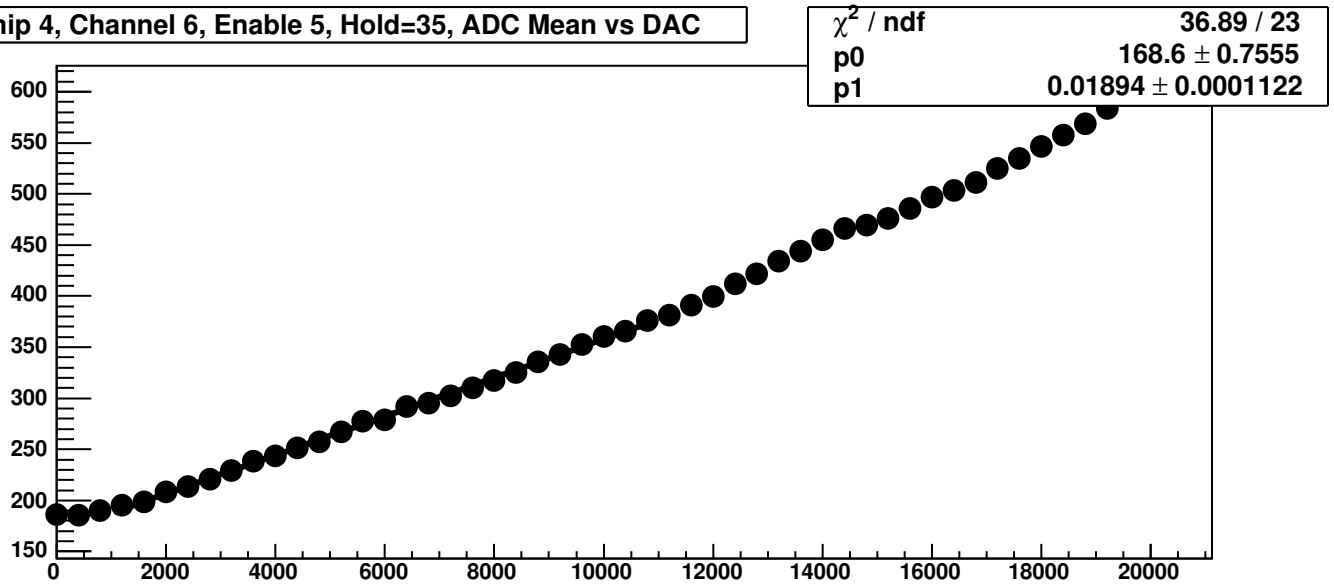
Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



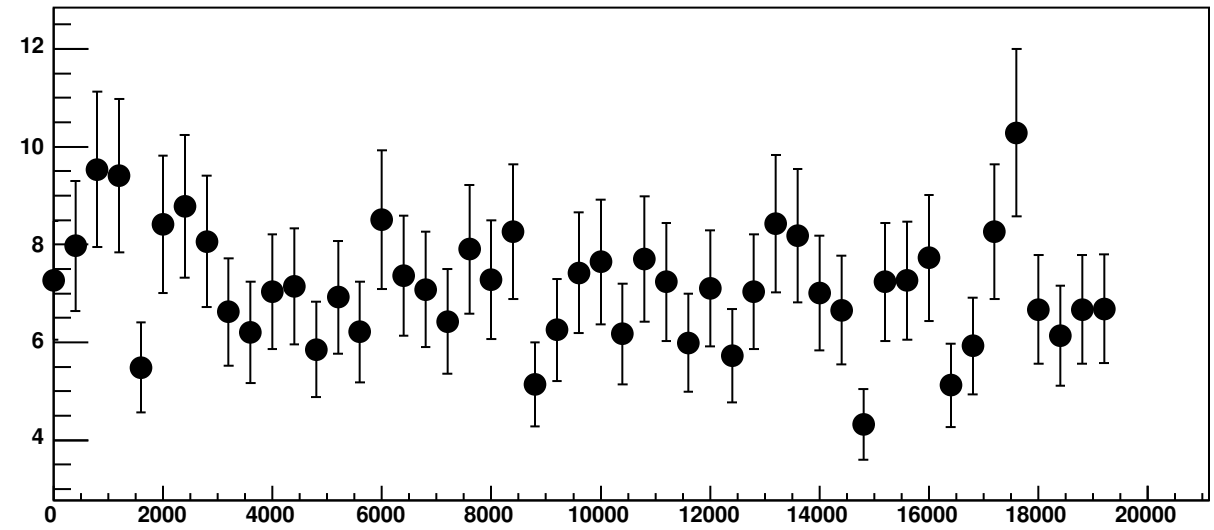
Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



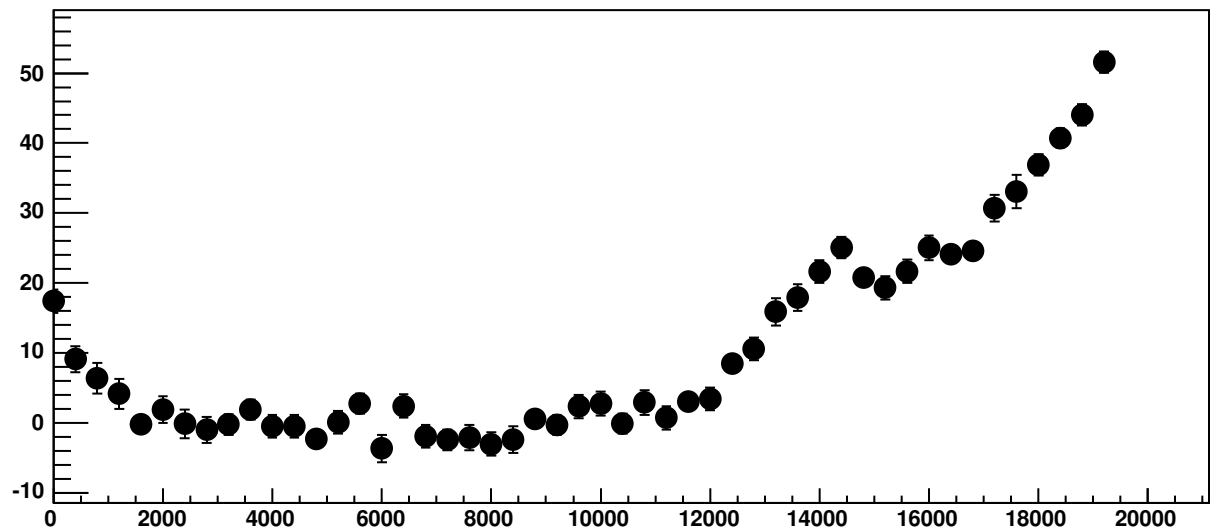
Chip 4, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

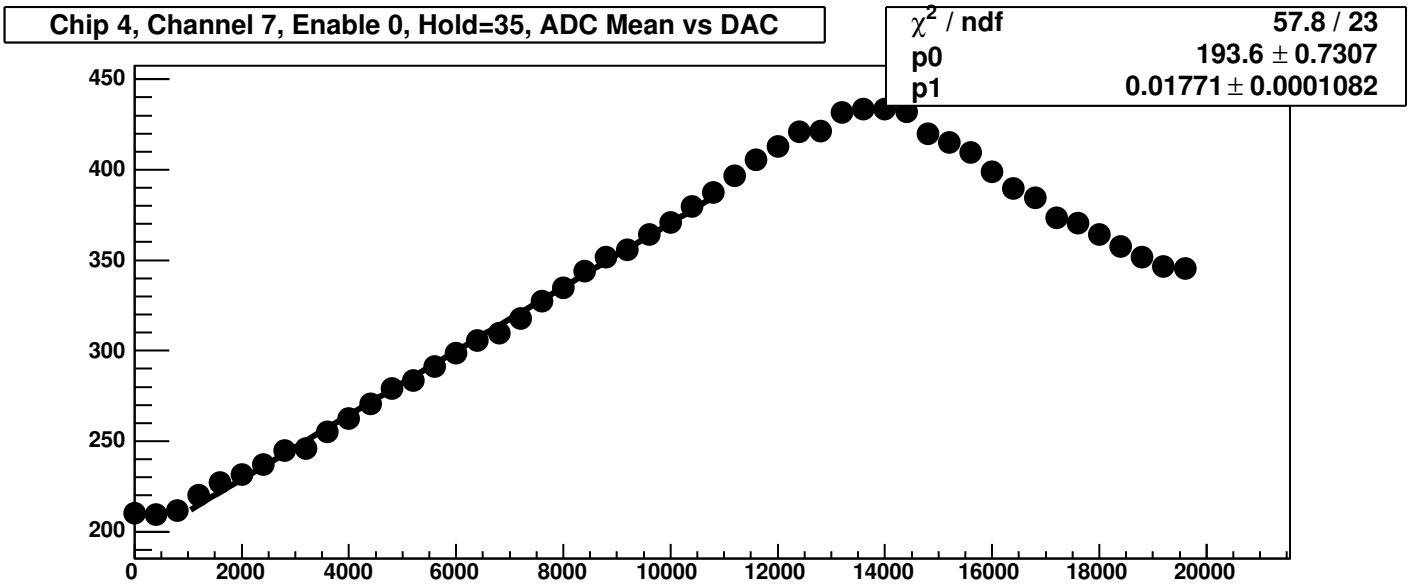


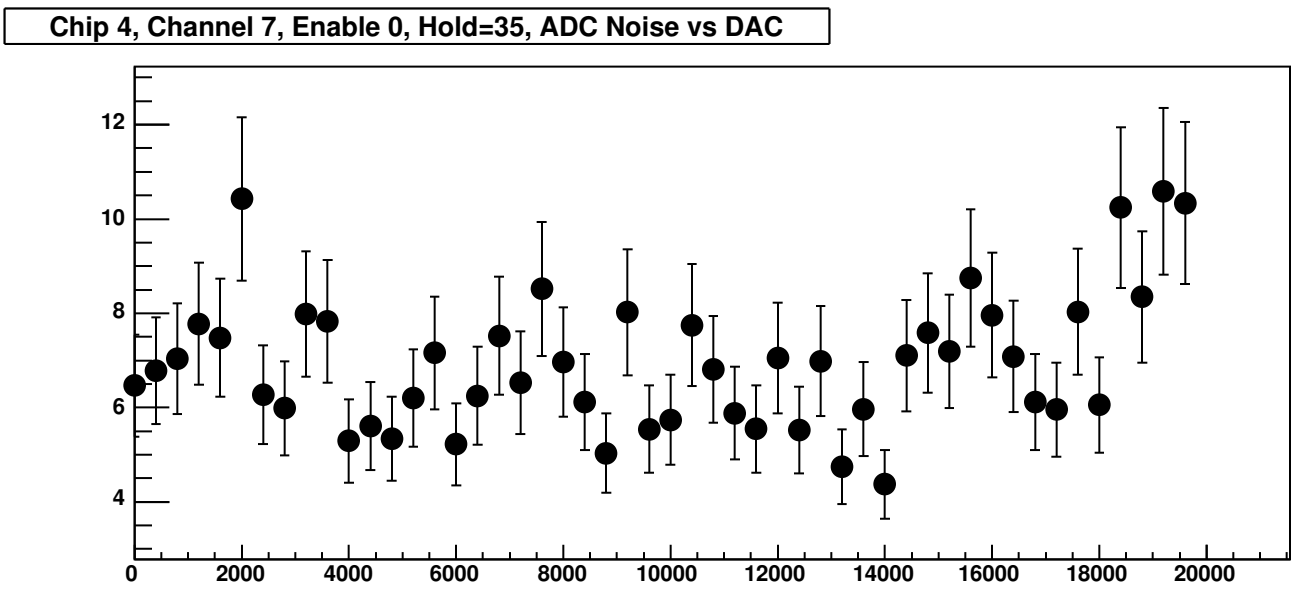
Chip 4, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC

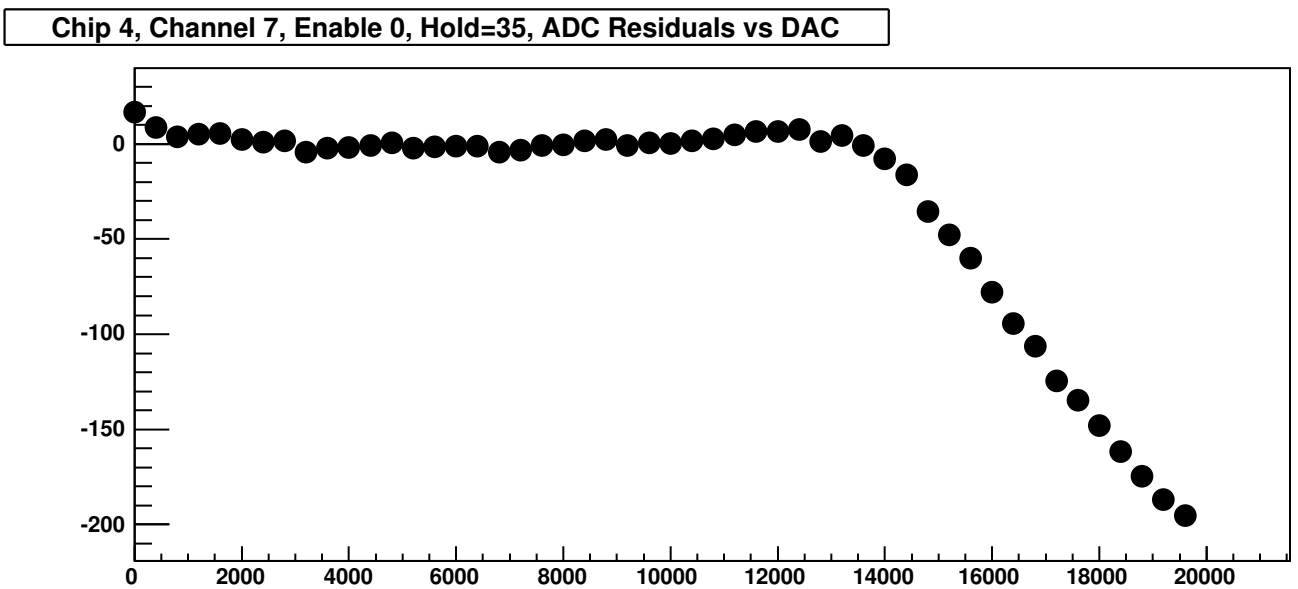


Chip 4, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

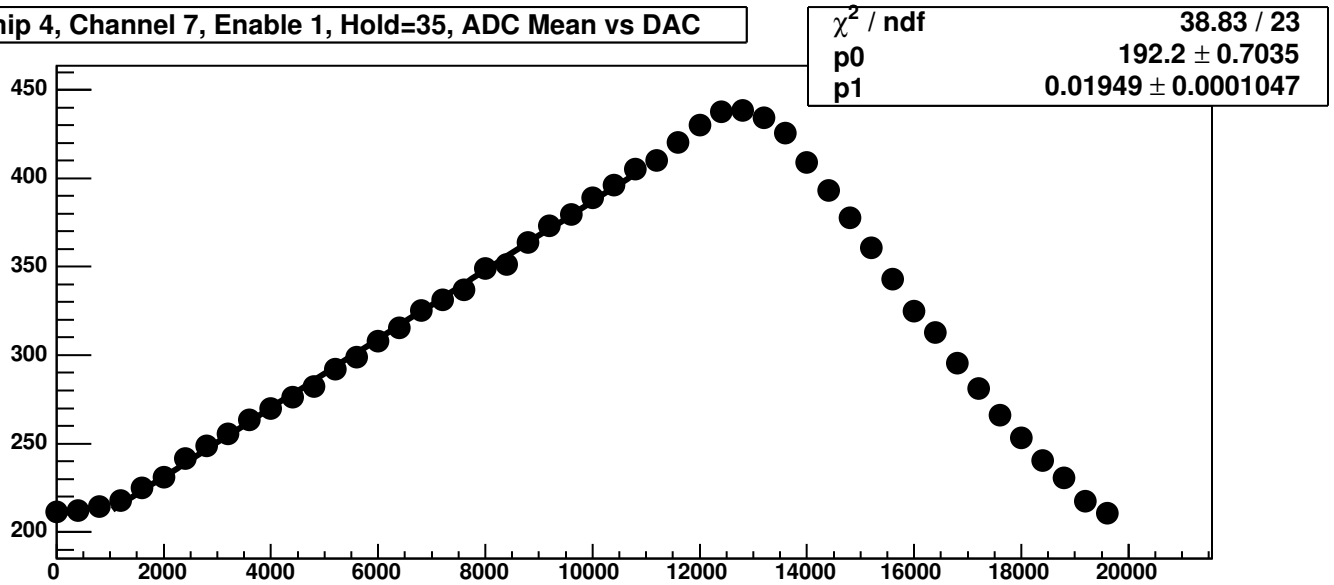



Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 57.8 / 23
p0 193.6 ± 0.7307
p1 0.01771 ± 0.0001082
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

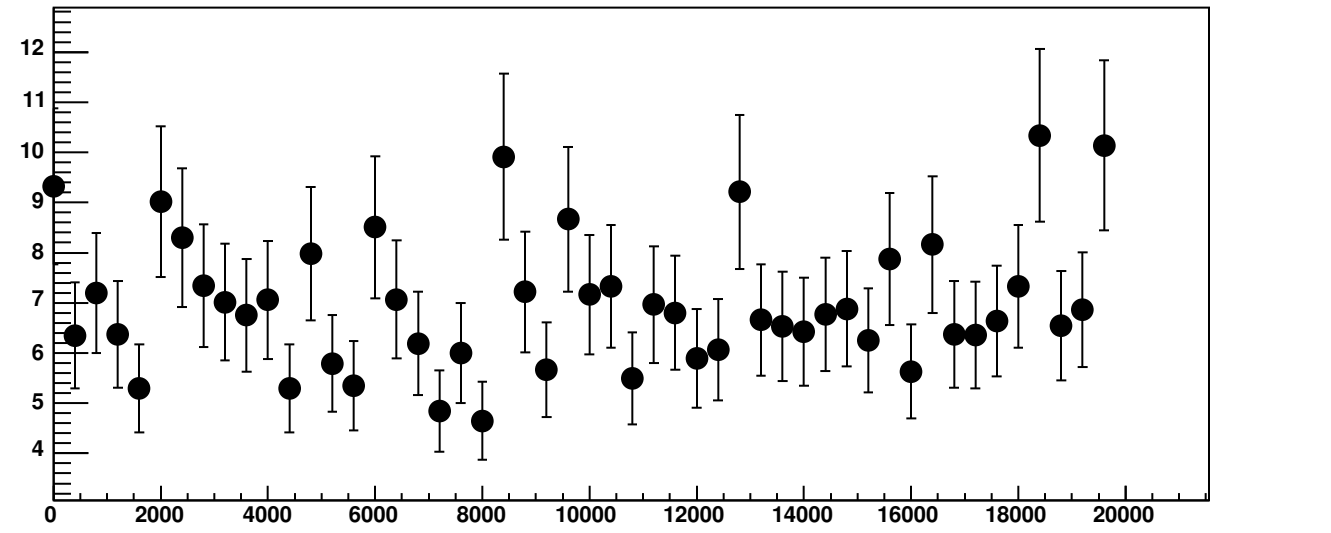

Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

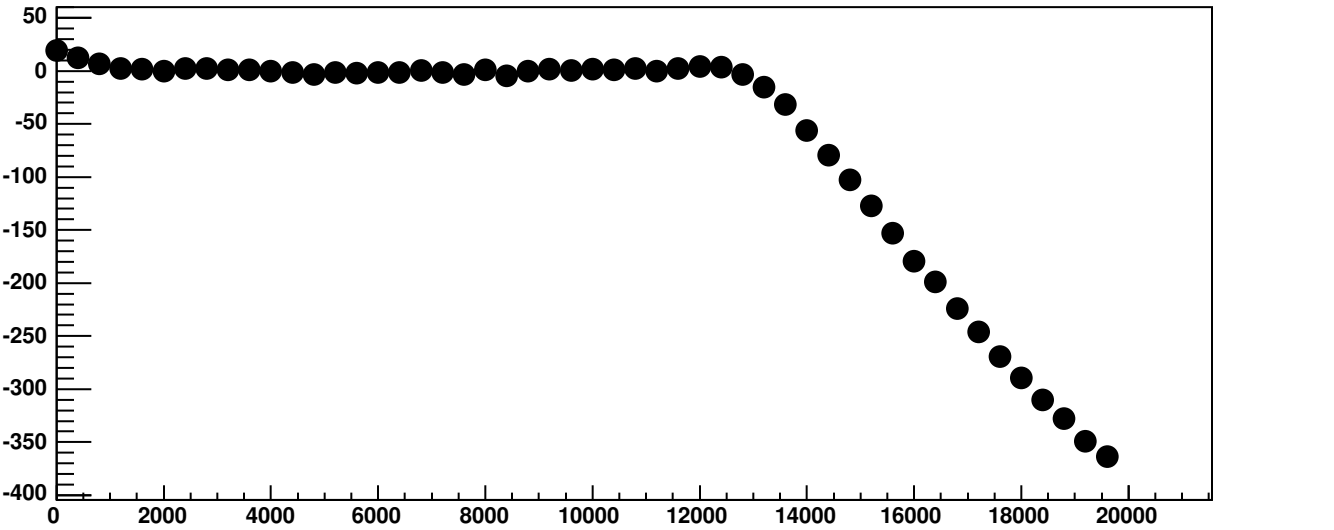
Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



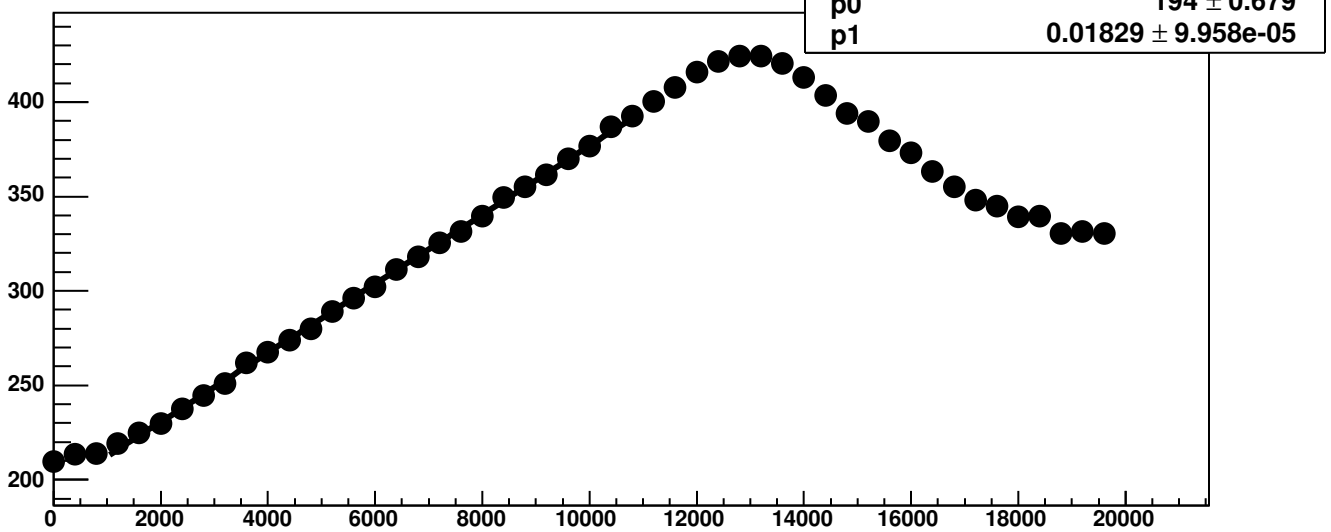
Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



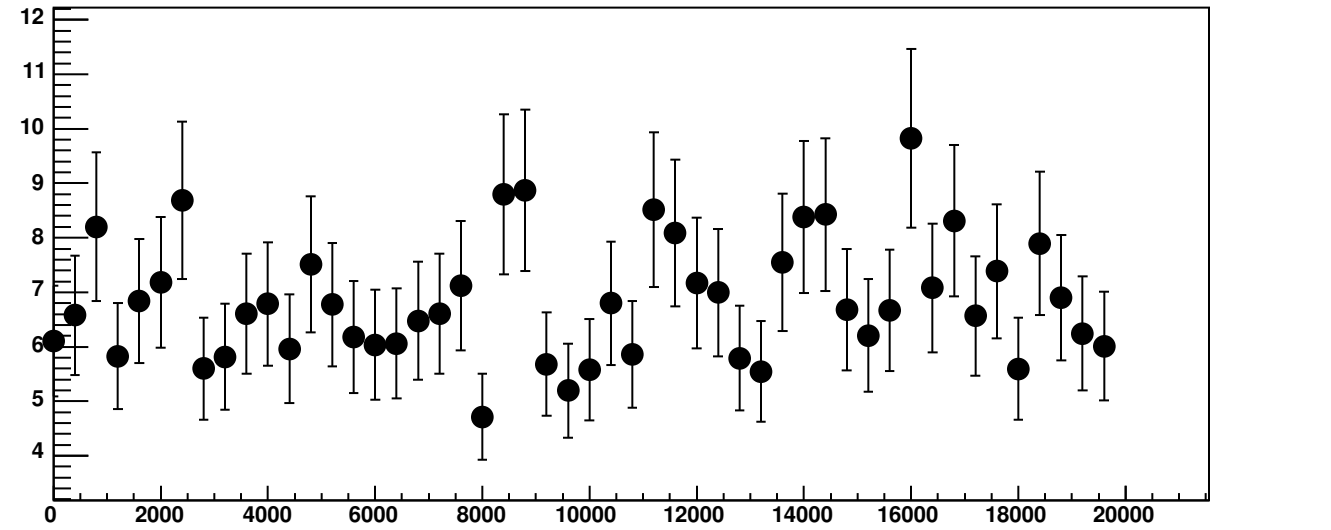
Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



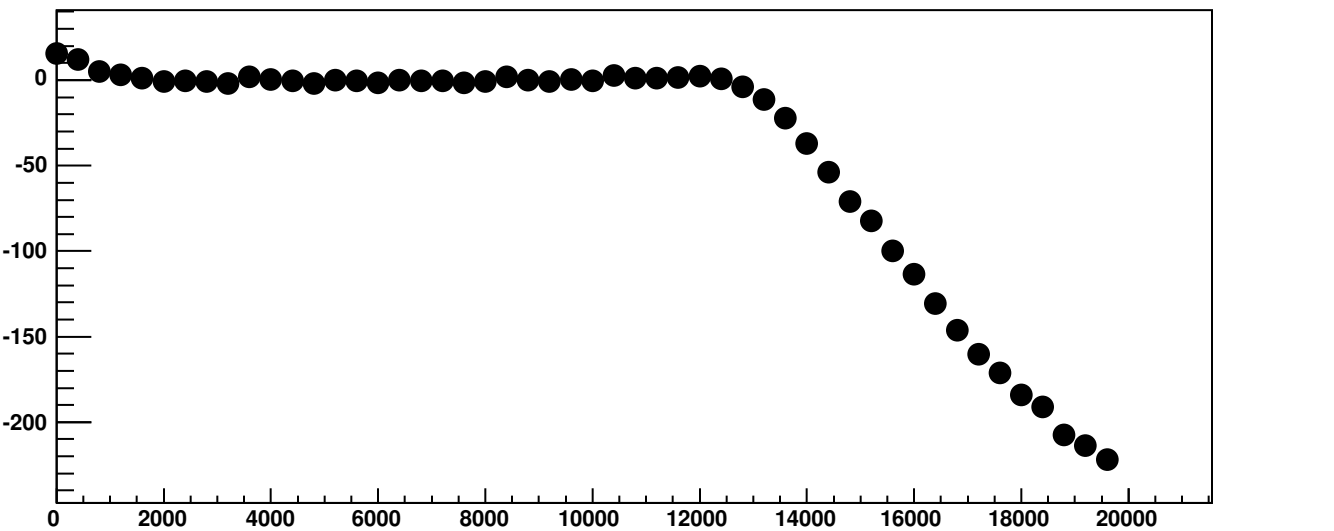
Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

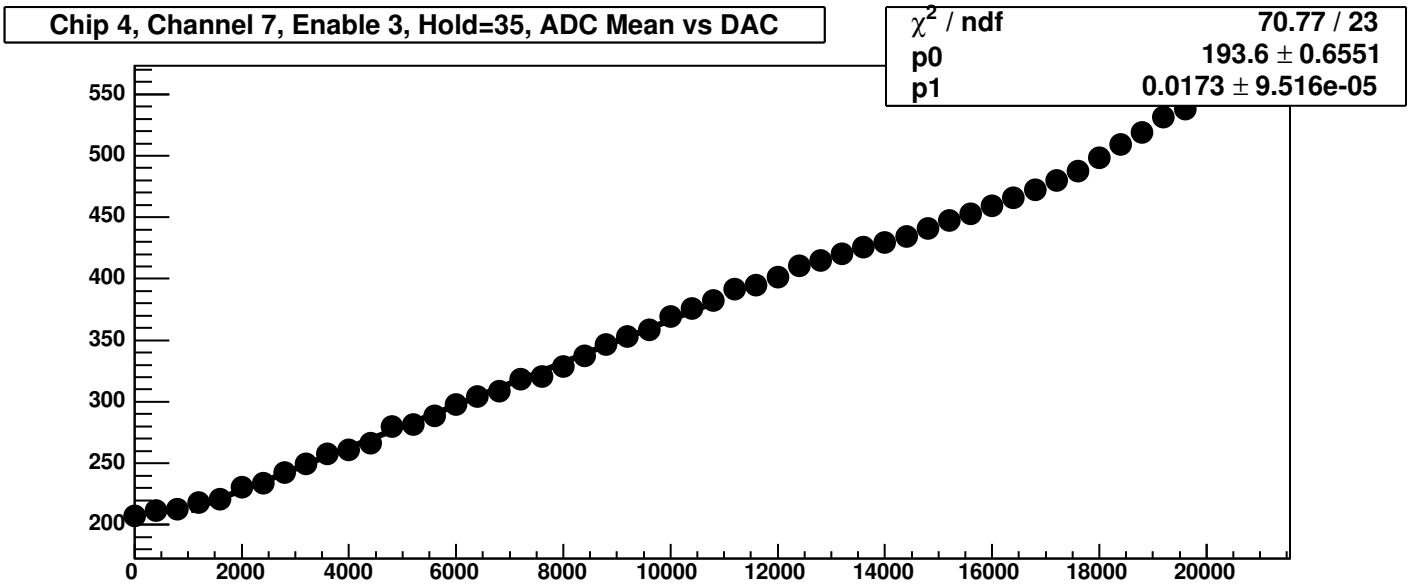


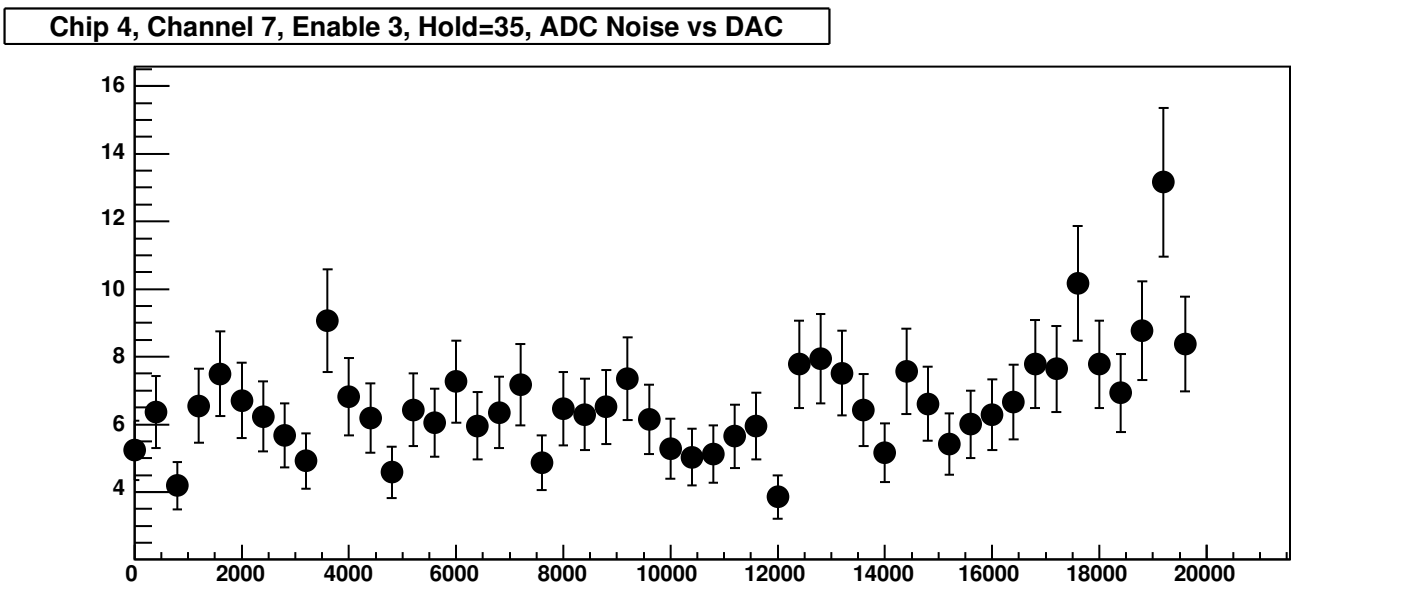
Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC

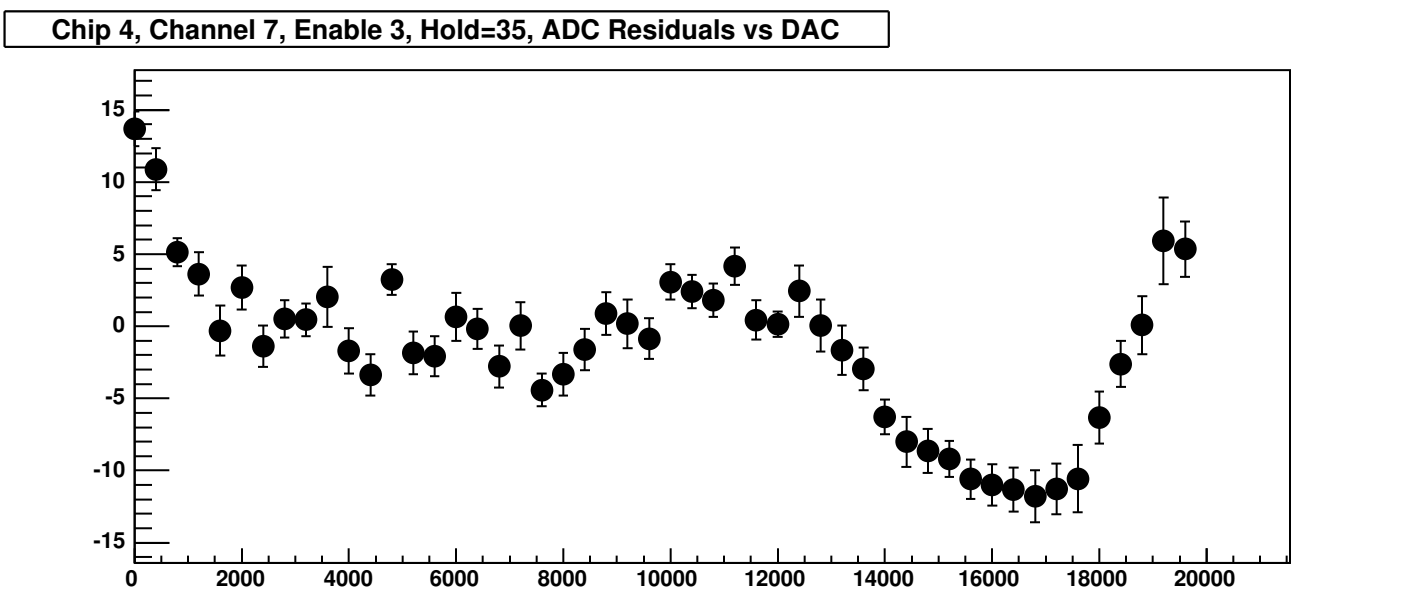


Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

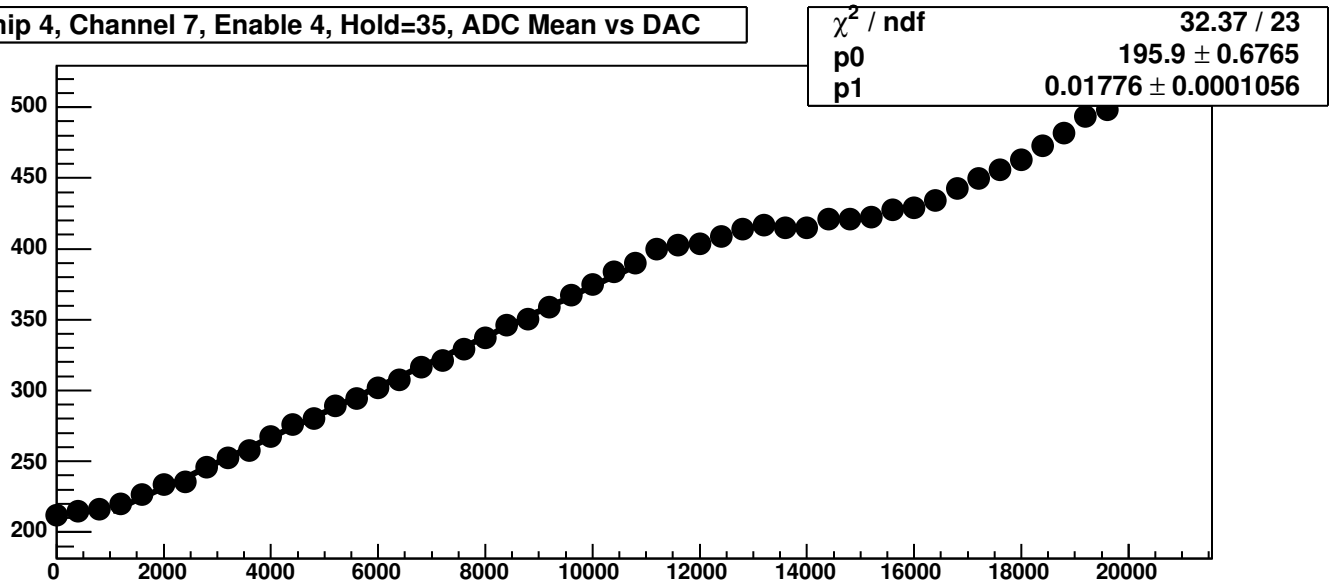



Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 70.77 / 23
p0 193.6 ± 0.6551
p1 0.0173 ± 9.516e-05
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

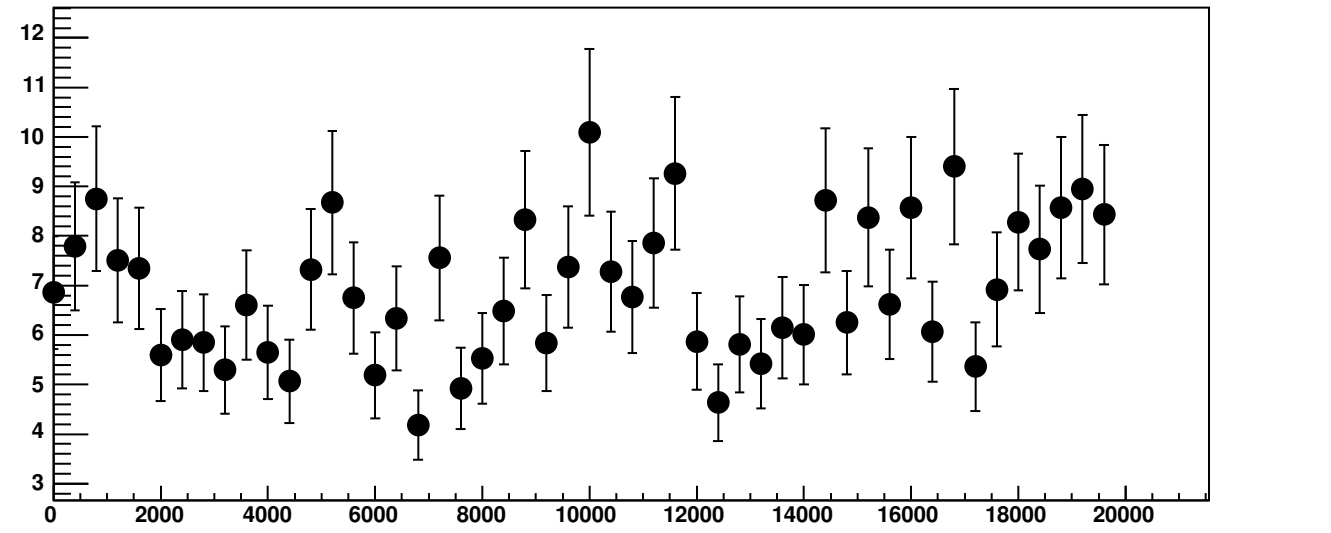

Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

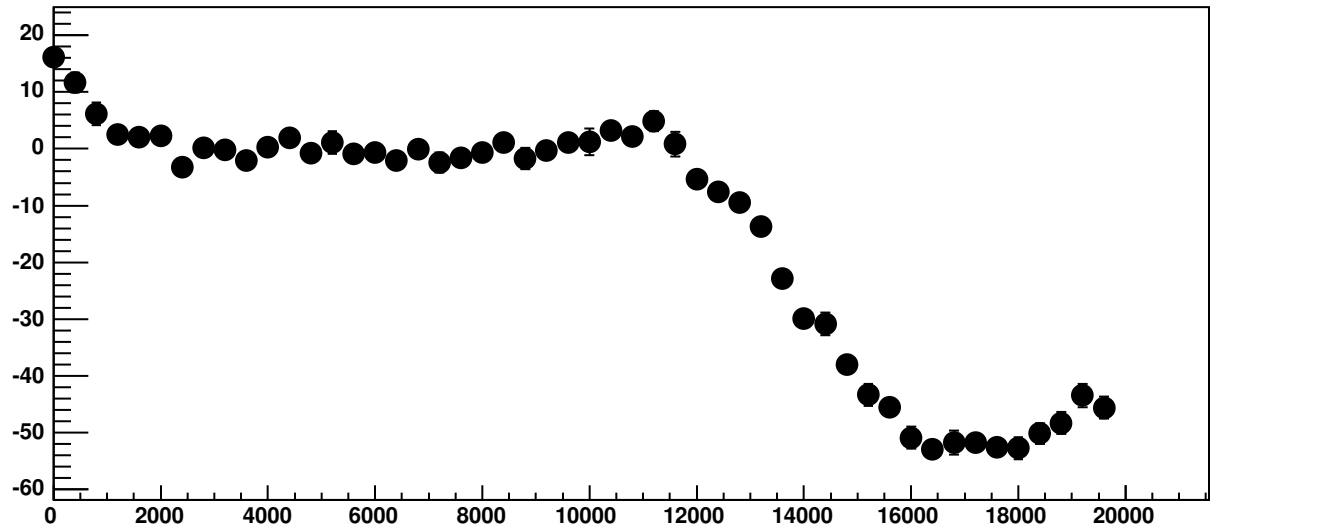
Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

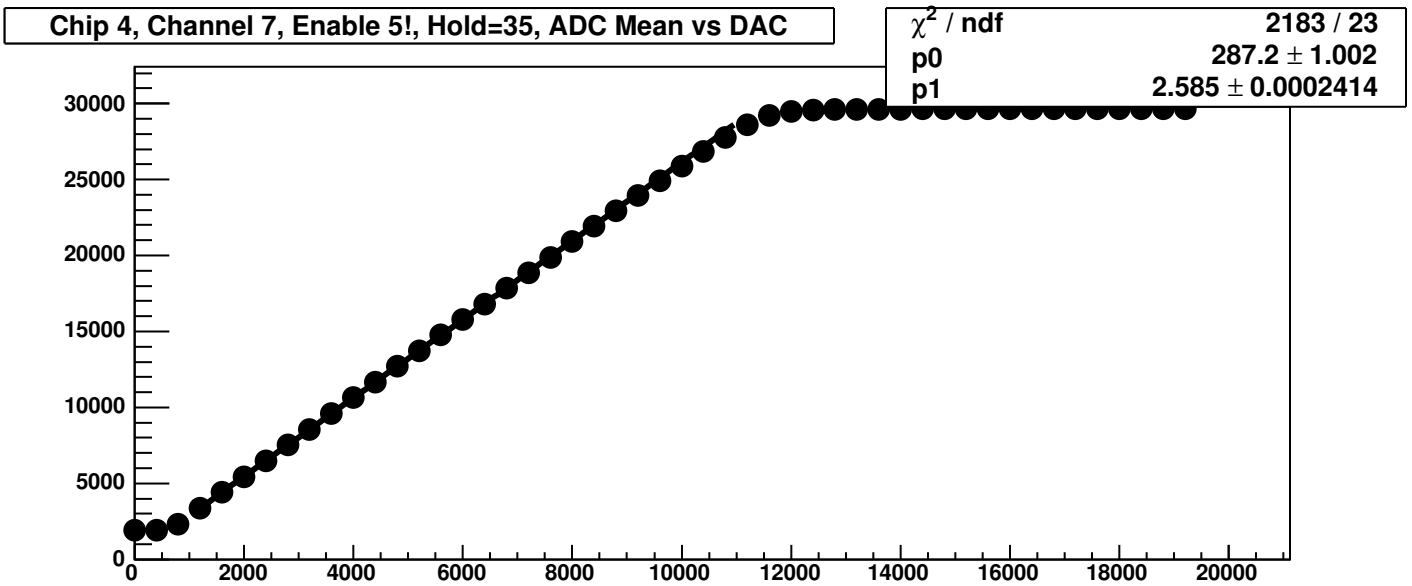


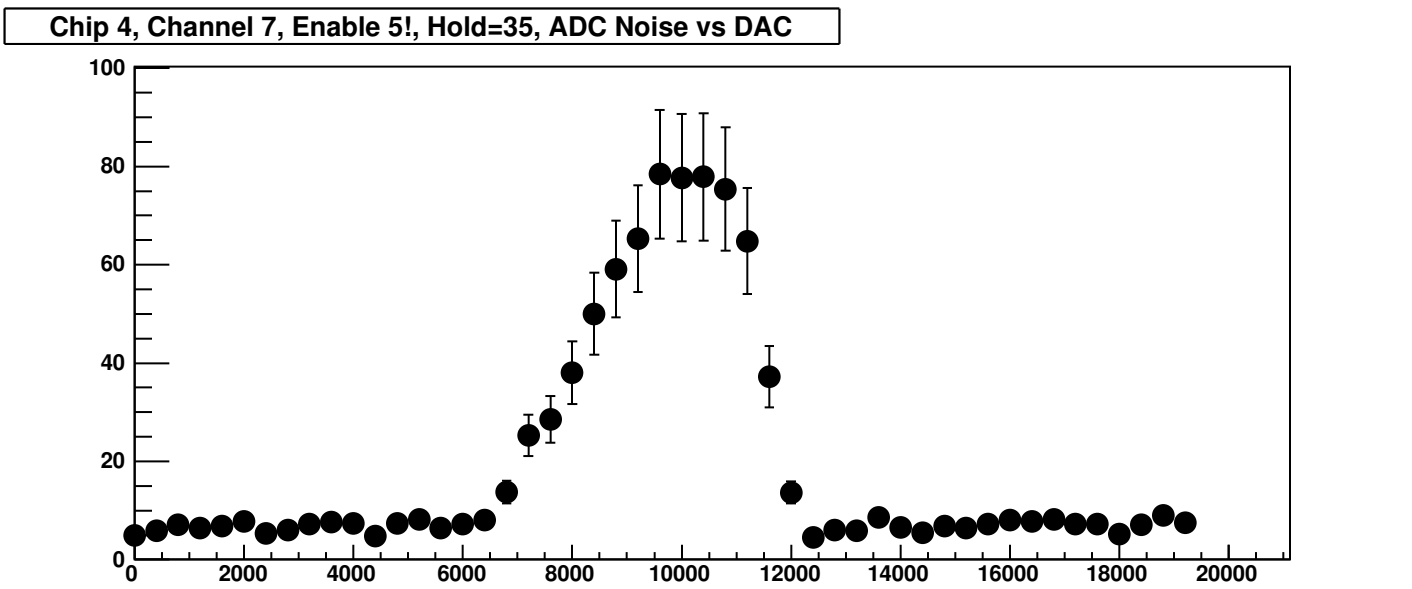
Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

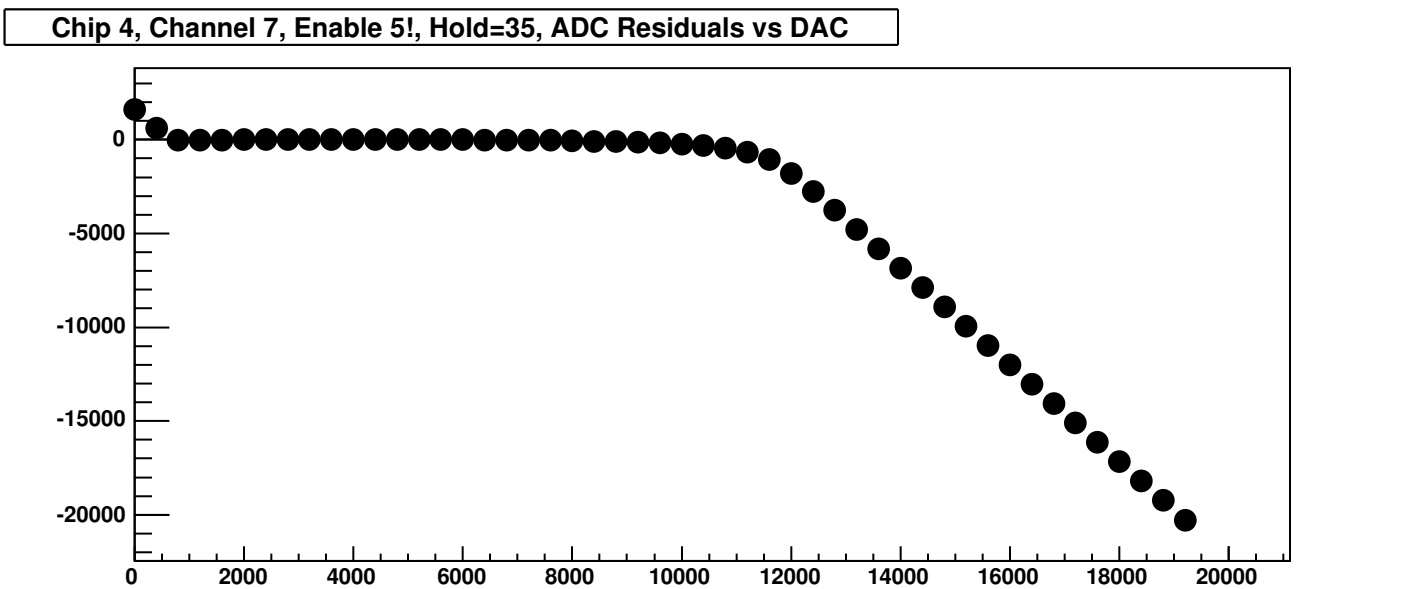


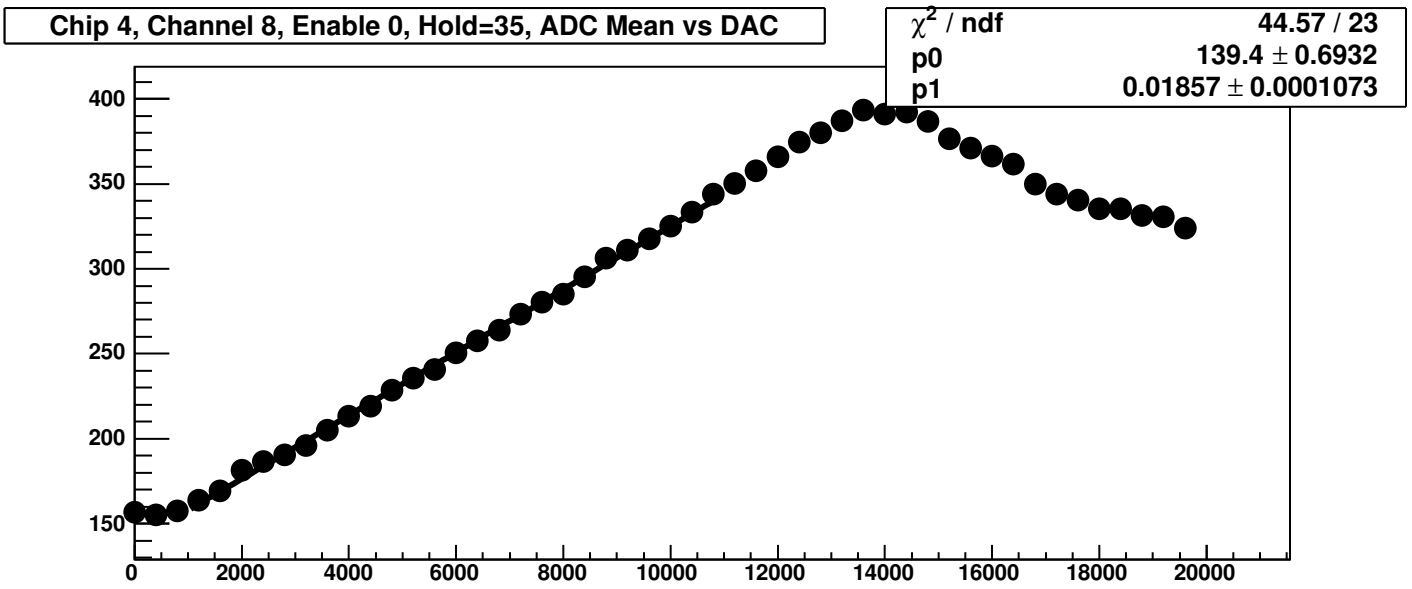
Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

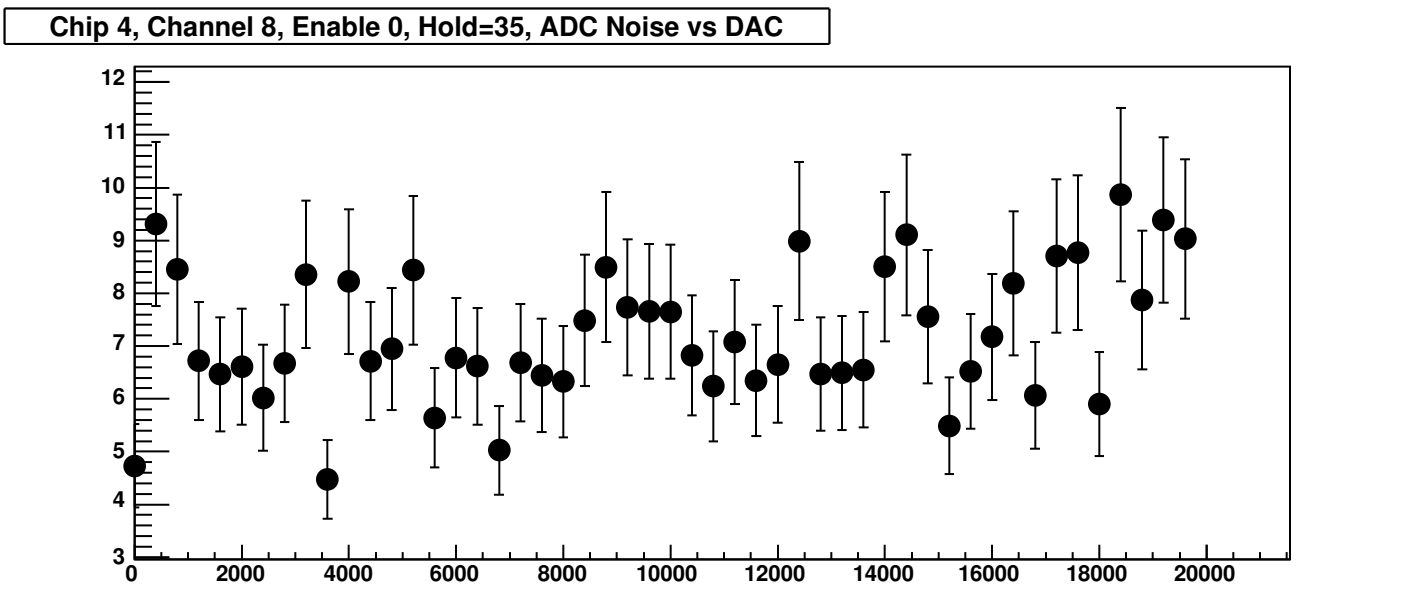


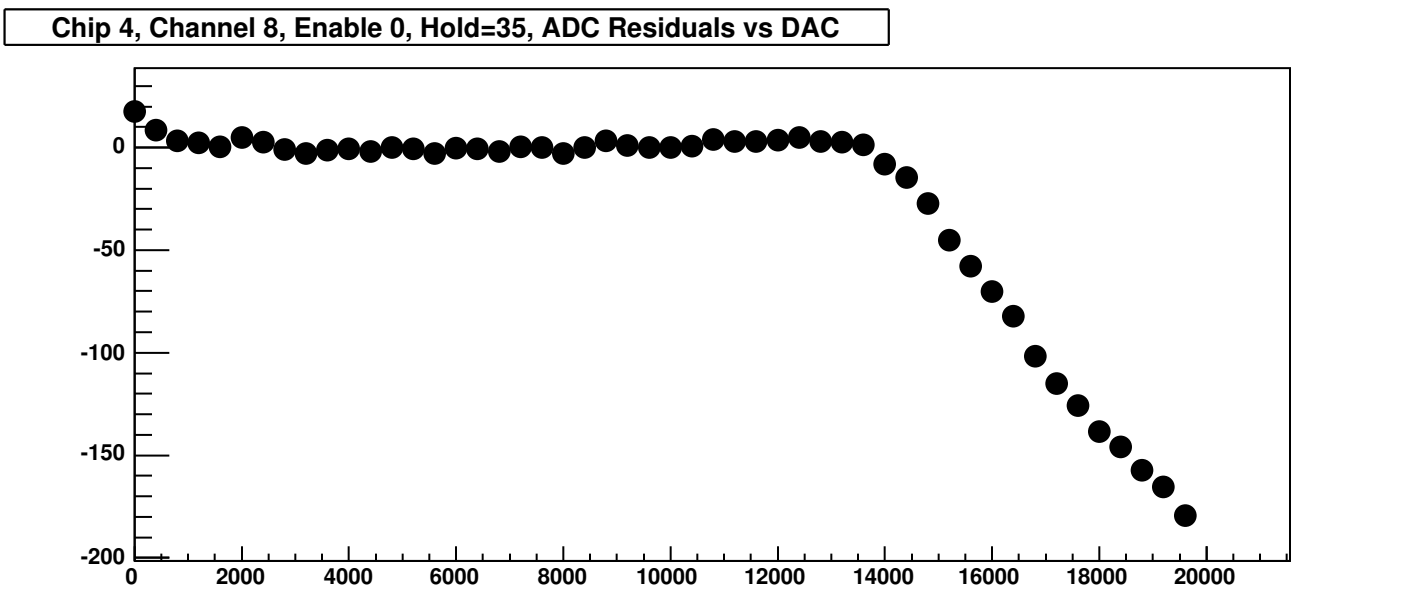

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2183 / 23
p0 287.2 ± 1.002
p1 2.585 ± 0.0002414
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

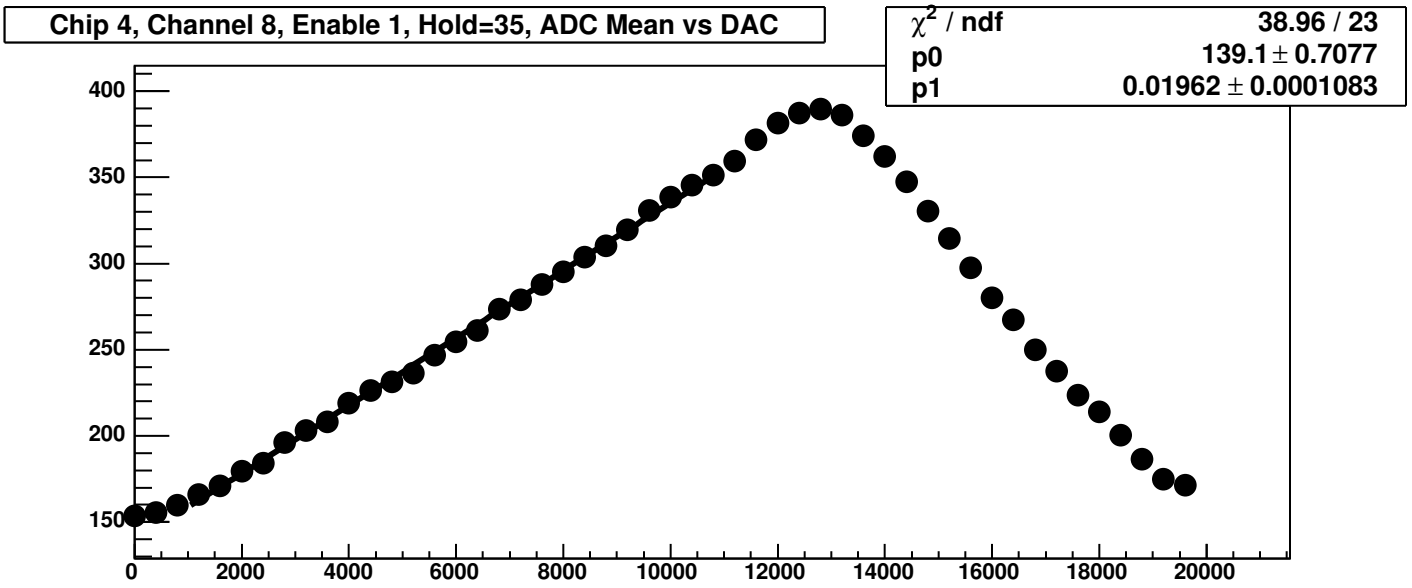

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

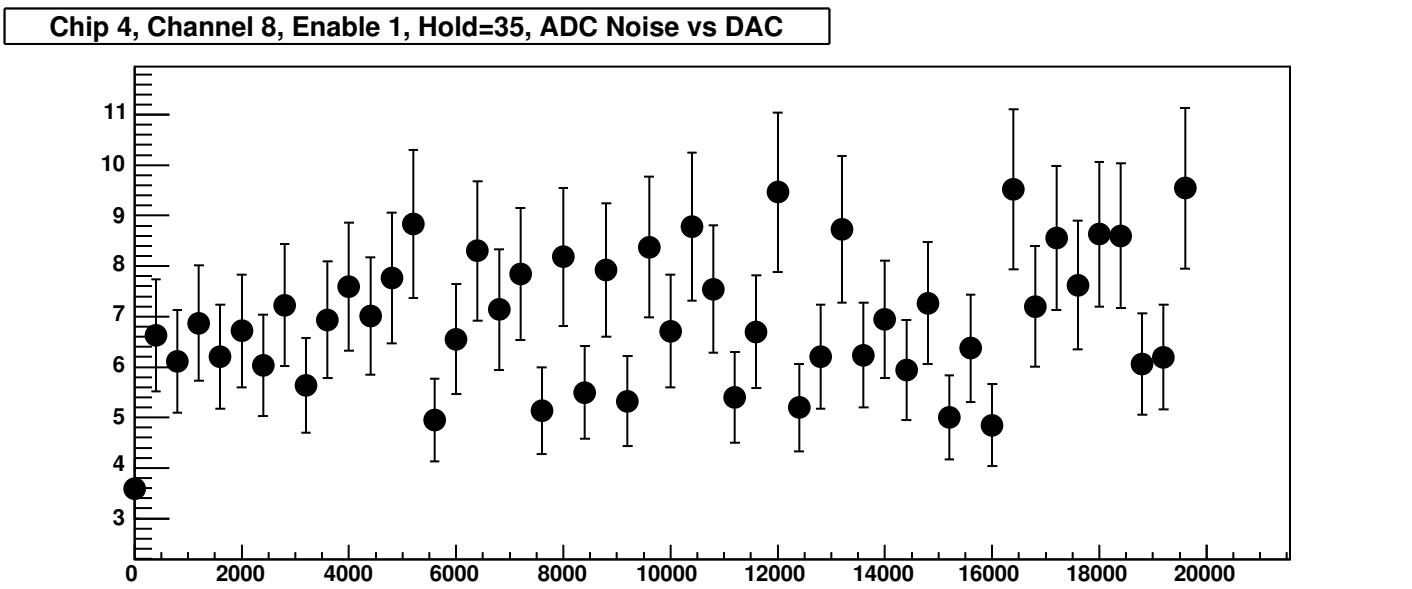

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

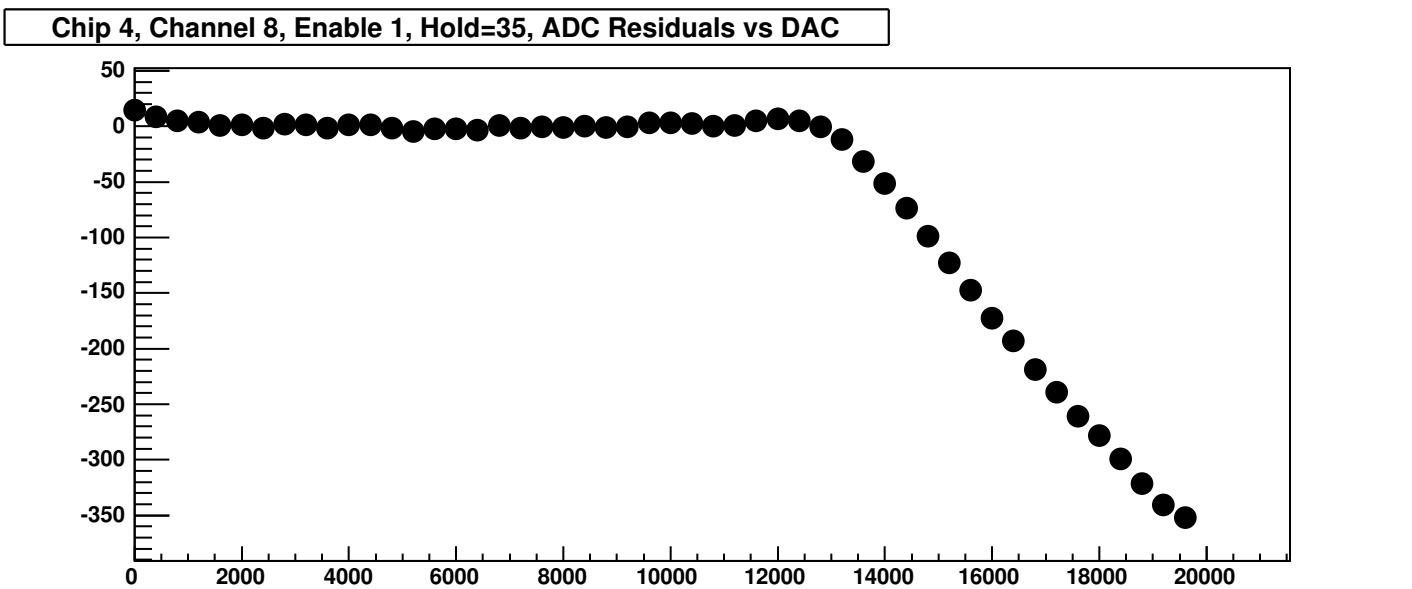

Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 44.57 / 23
p0 139.4 ± 0.6932
p1 0.01857 ± 0.0001073
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

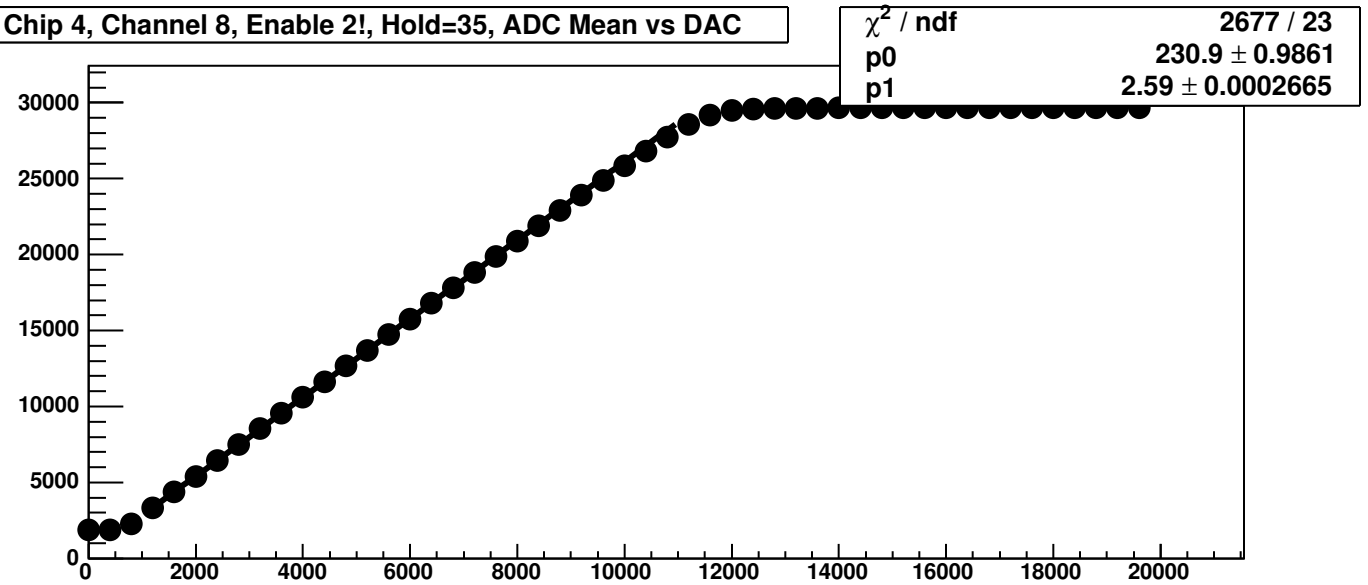

Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.96 / 23
p0 139.1 ± 0.7077
p1 0.01962 ± 0.0001083
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

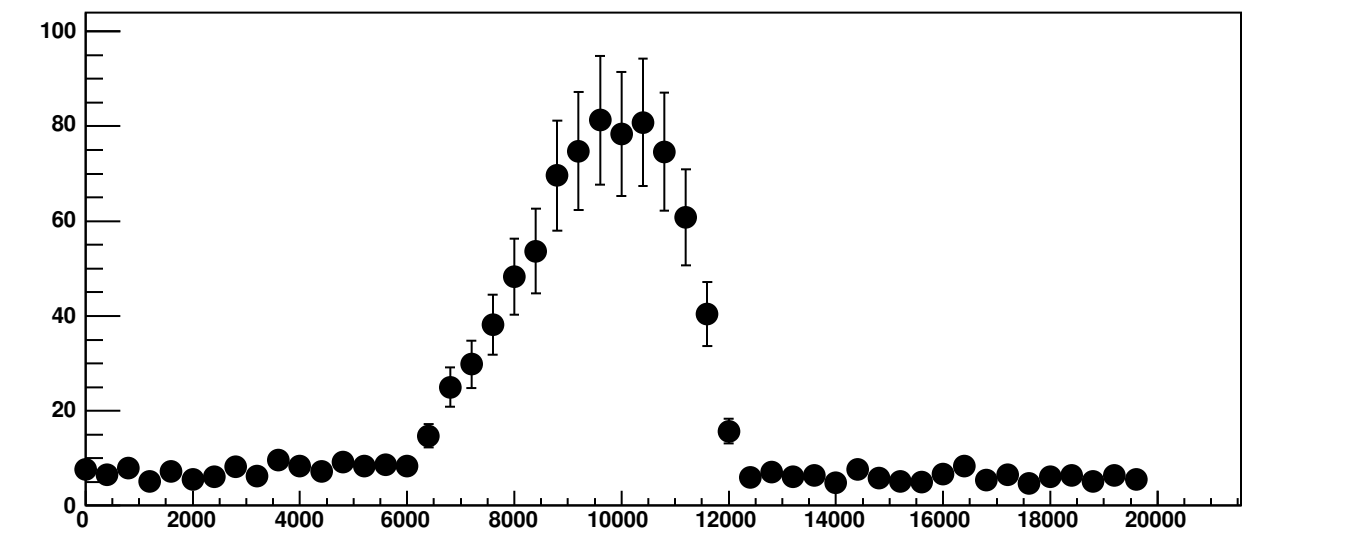

Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

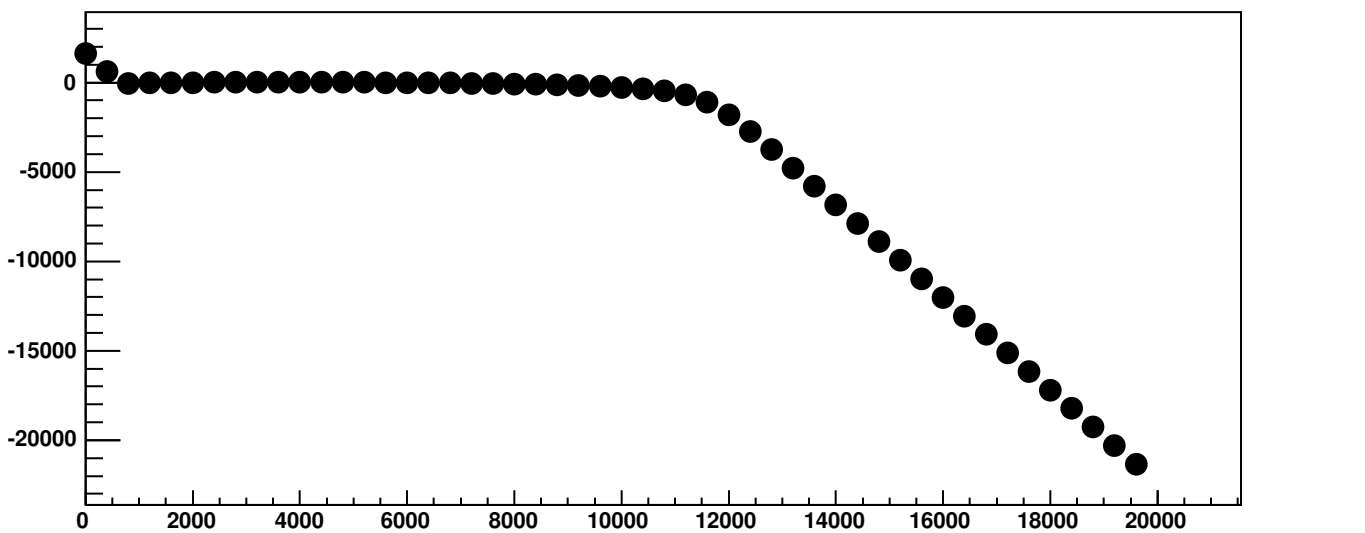
Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



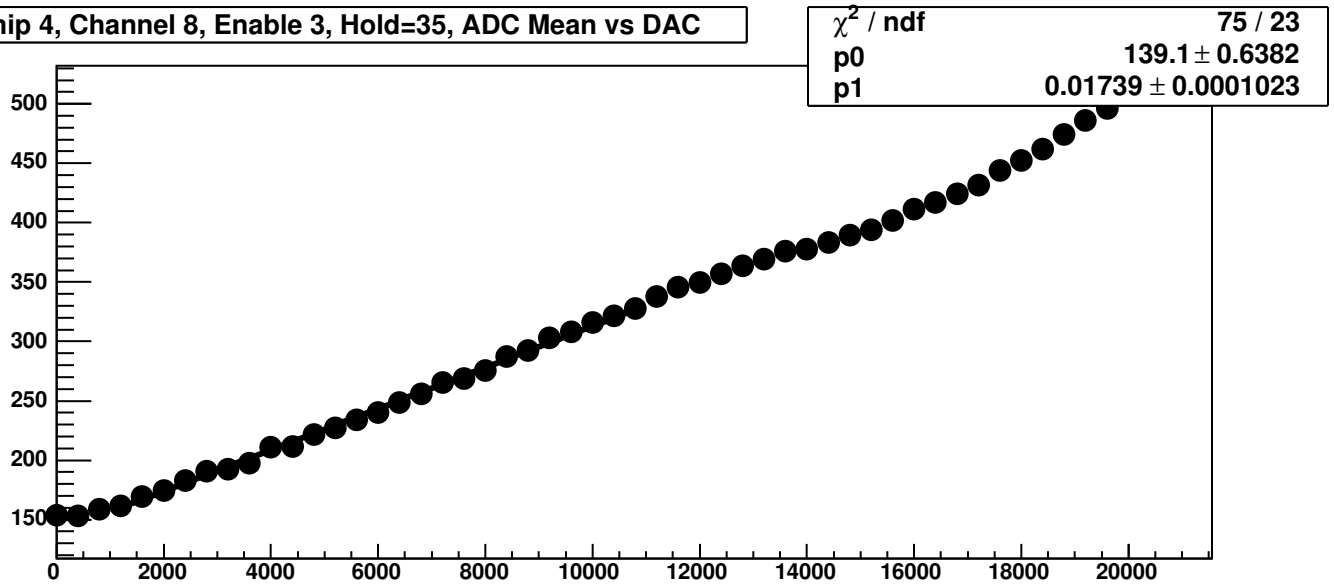
Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



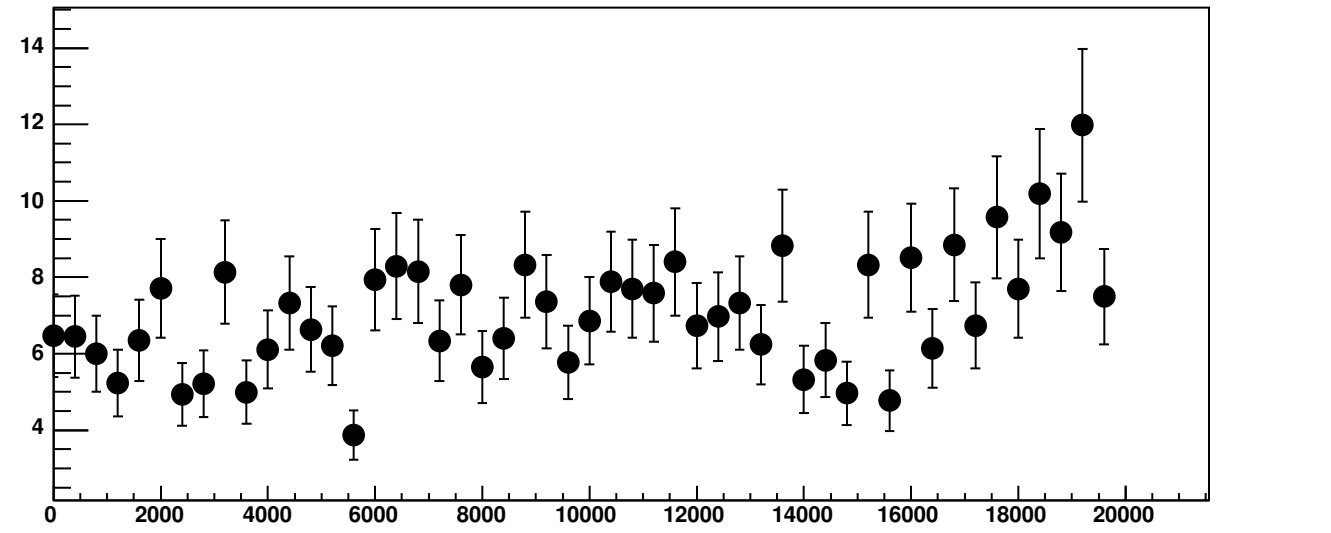
Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC



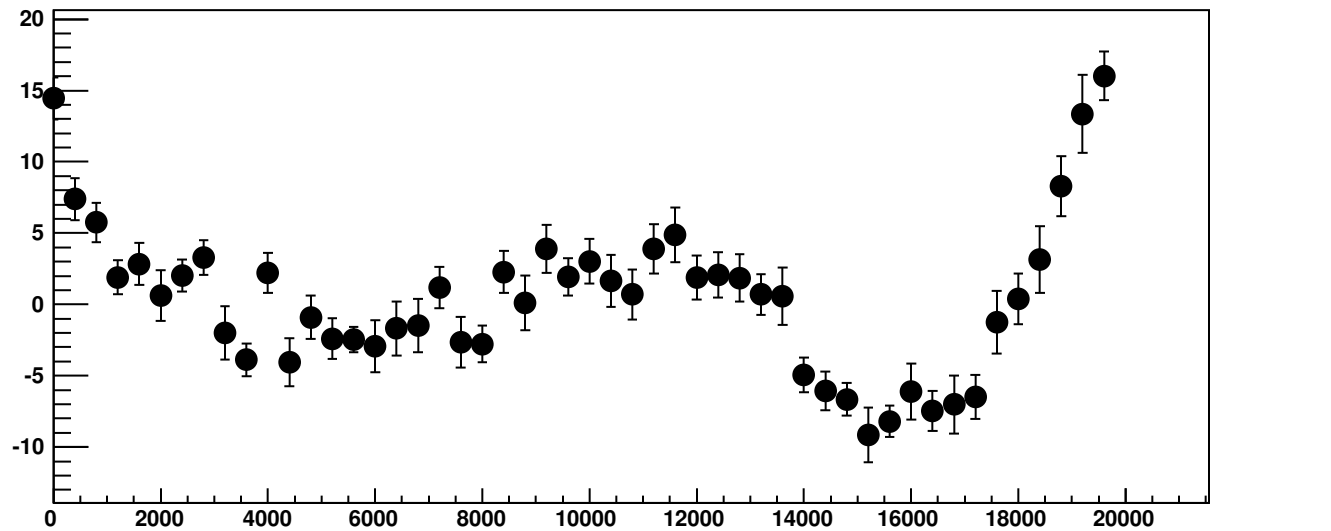
Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



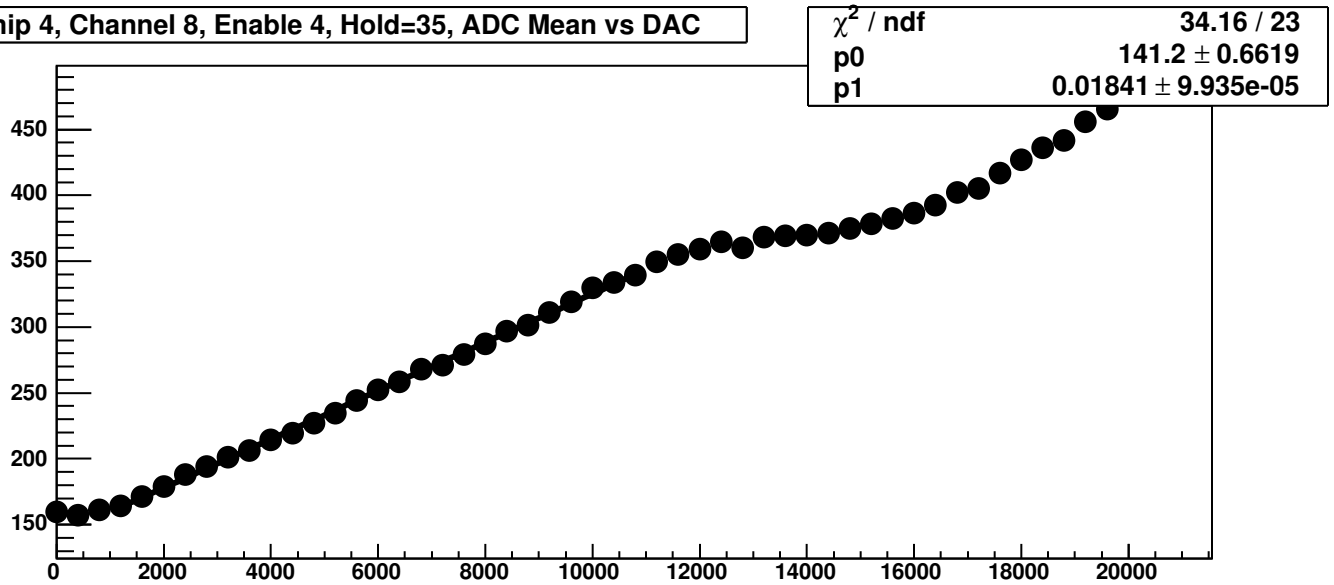
Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



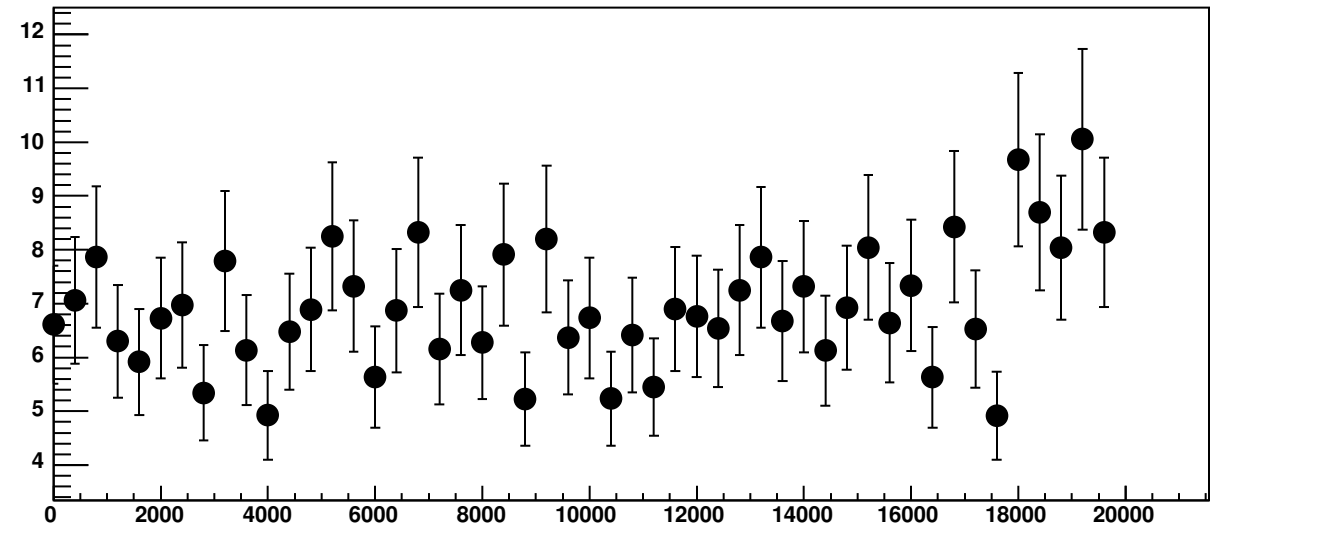
Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



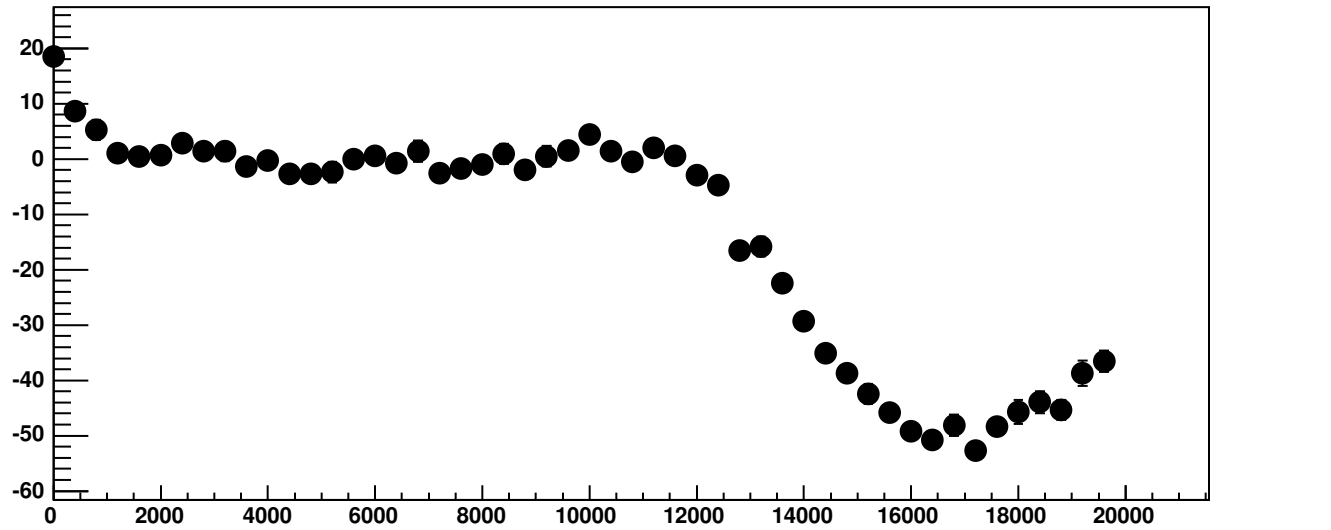
Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



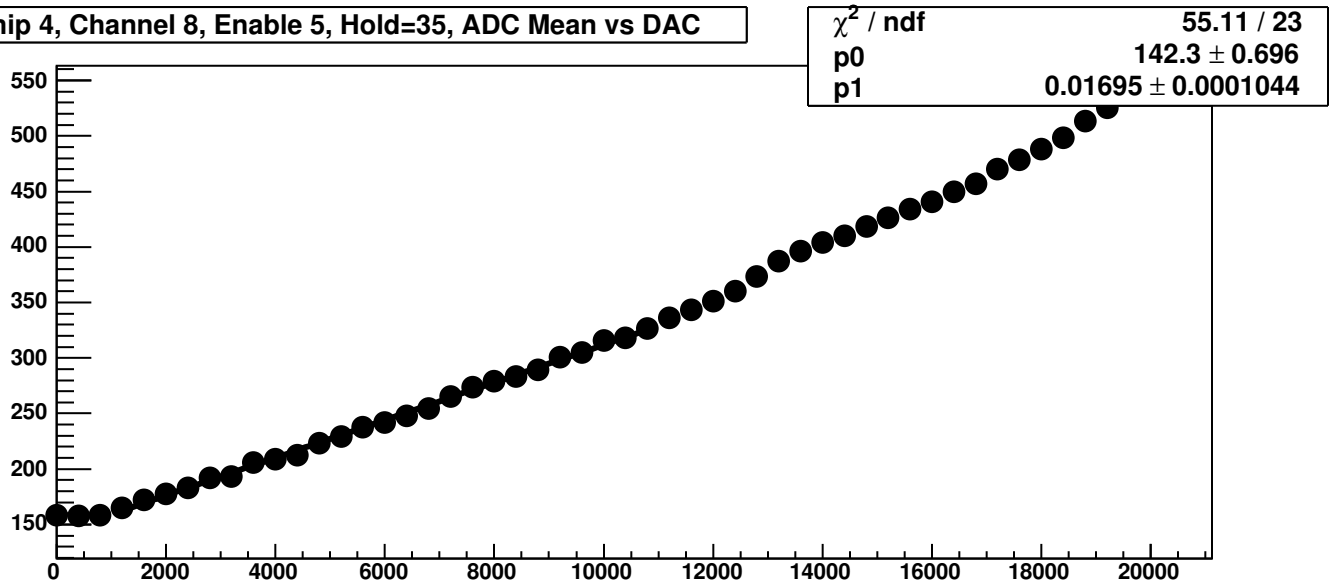
Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



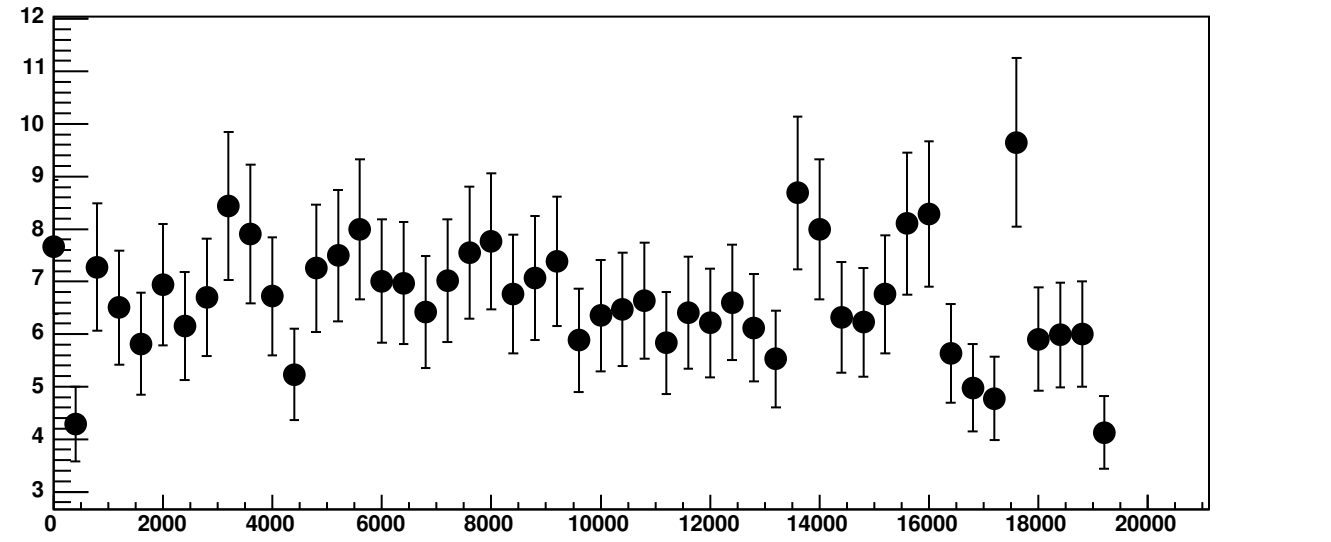
Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



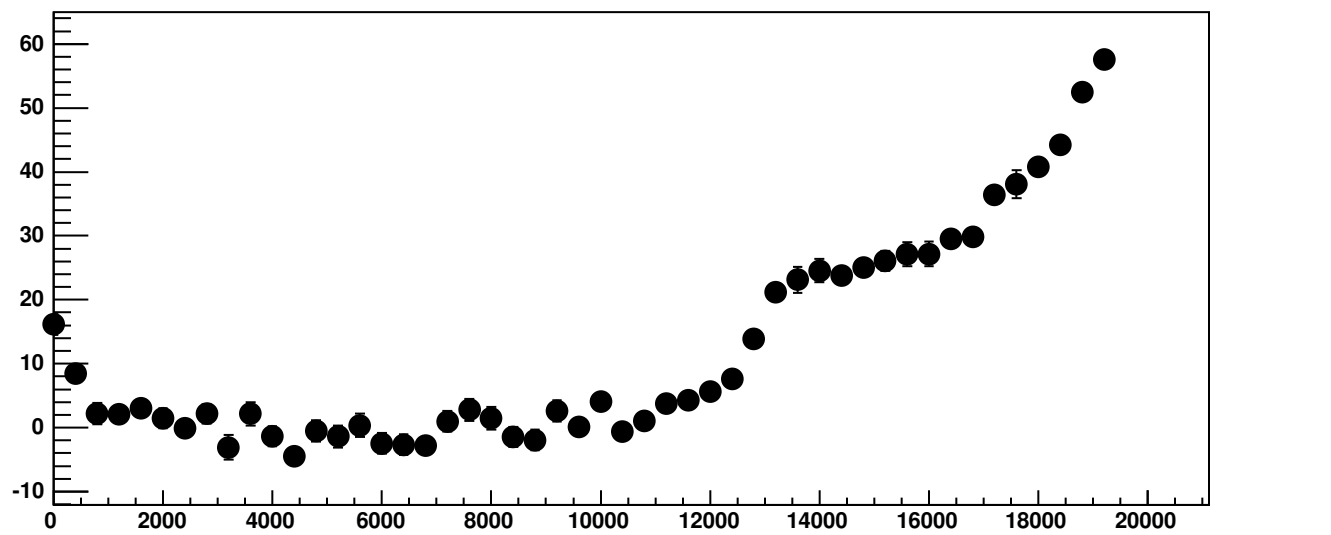
Chip 4, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

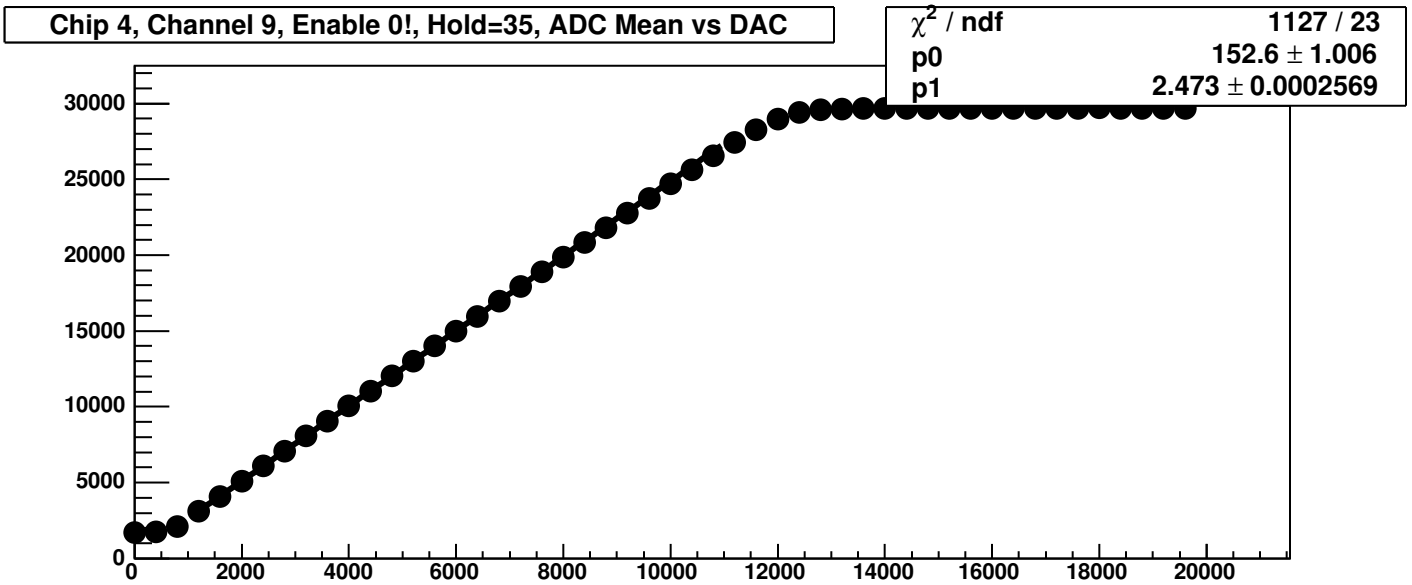


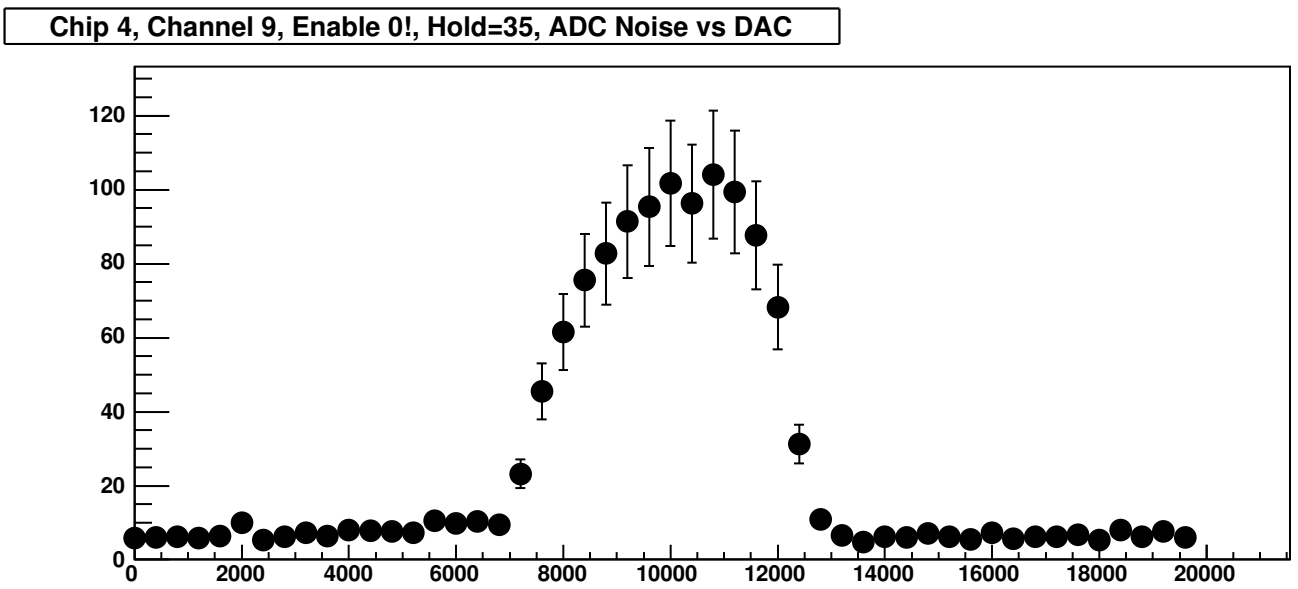
Chip 4, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC

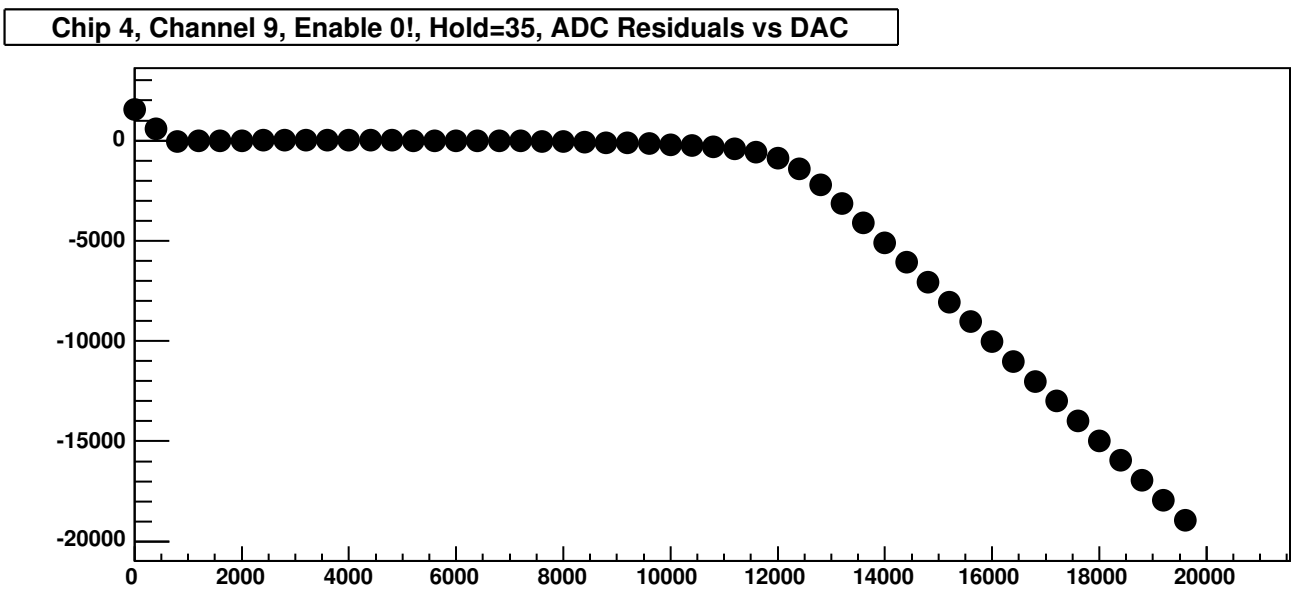


Chip 4, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



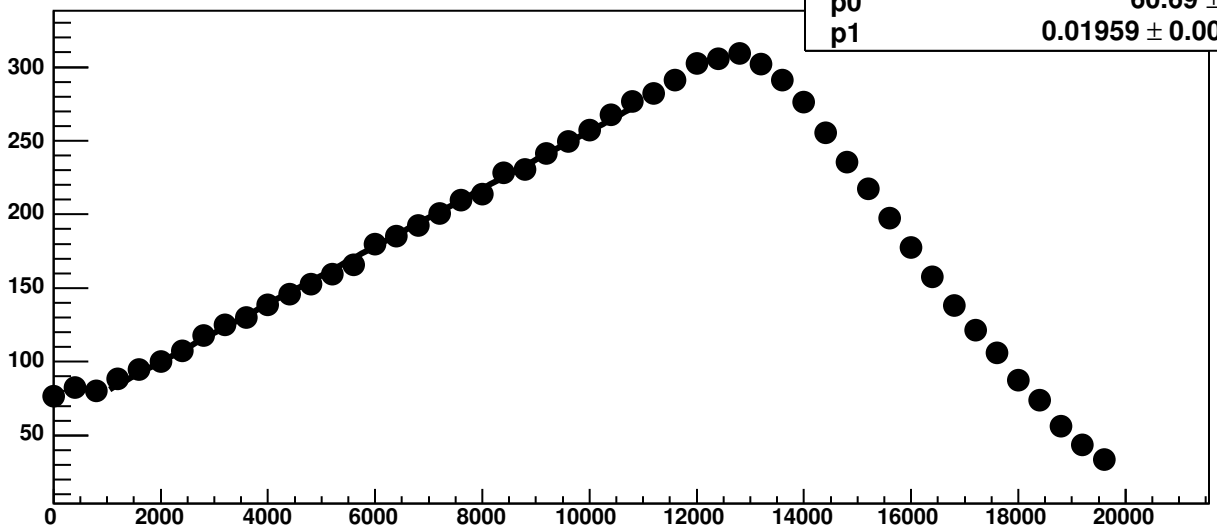

Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1127 / 23
p0 152.6 ± 1.006
p1 2.473 ± 0.0002569
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

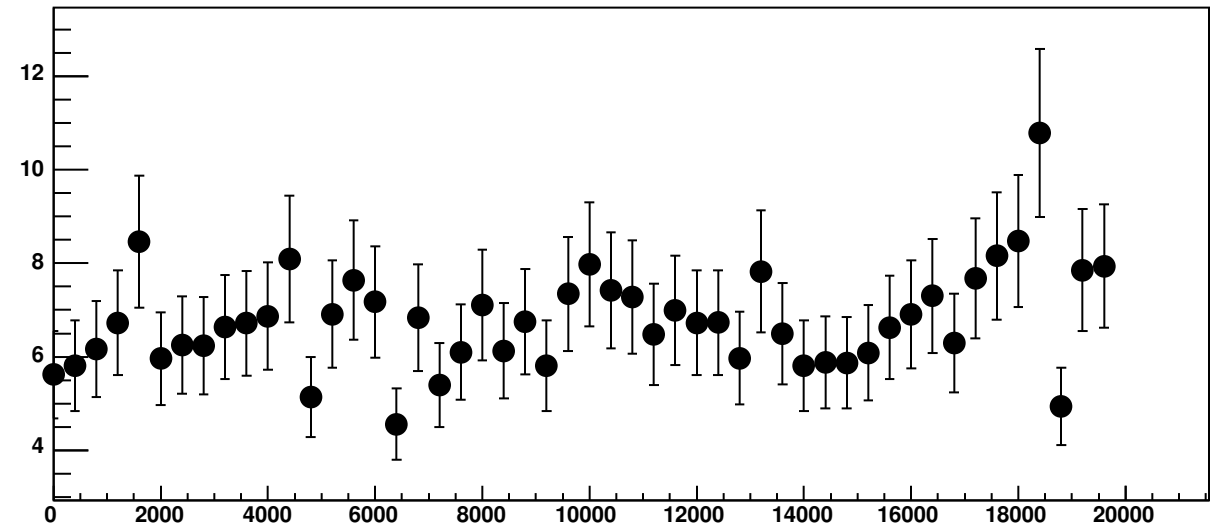

Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

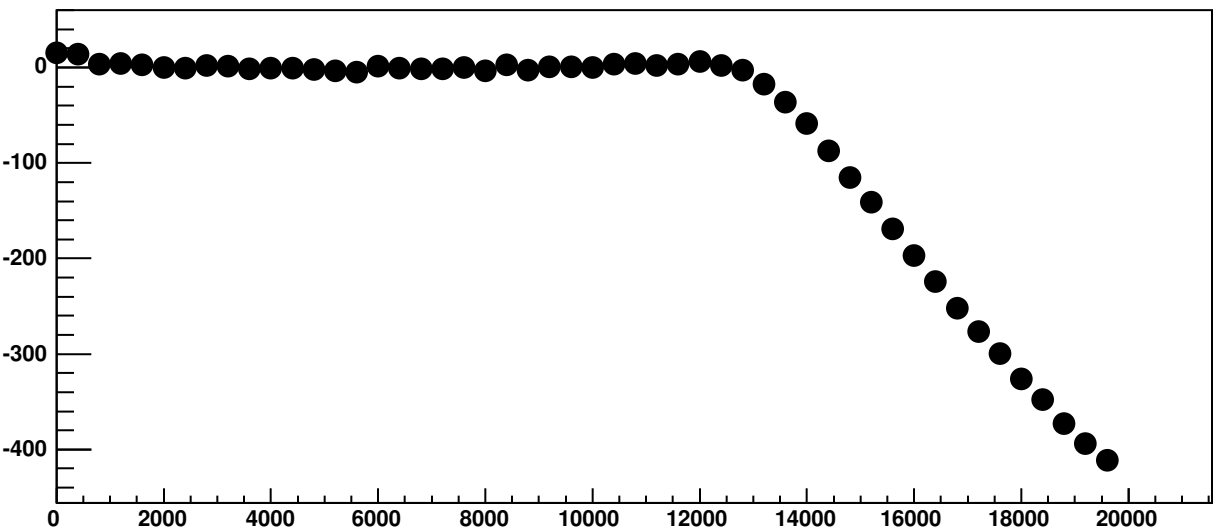
χ^2 / ndf	53.64 / 23
p0	60.69 ± 0.713
p1	0.01959 ± 0.0001087



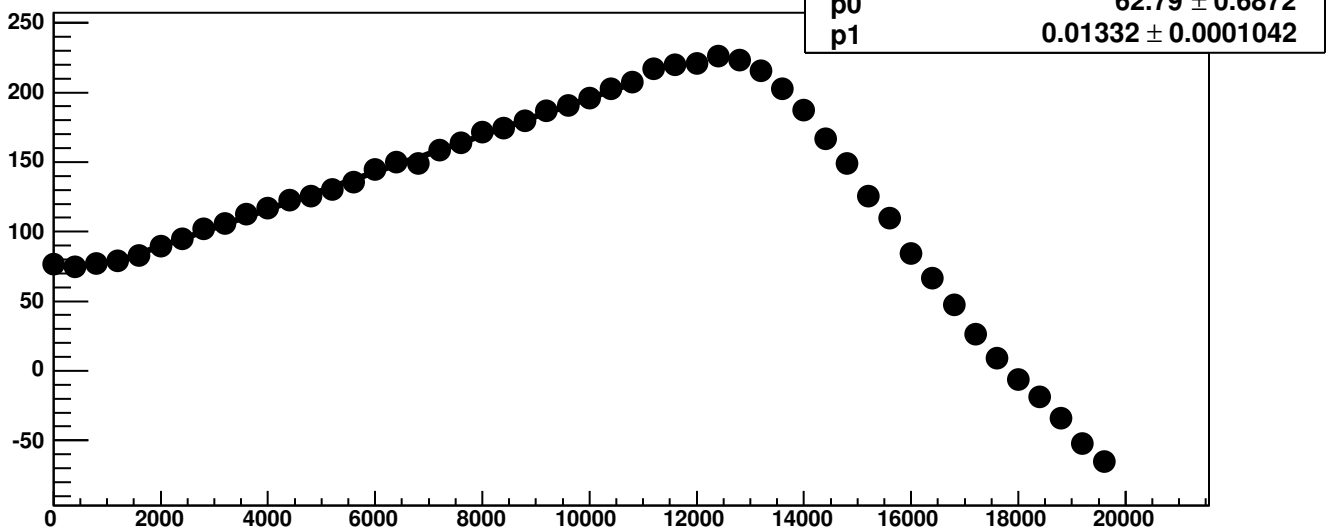
Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



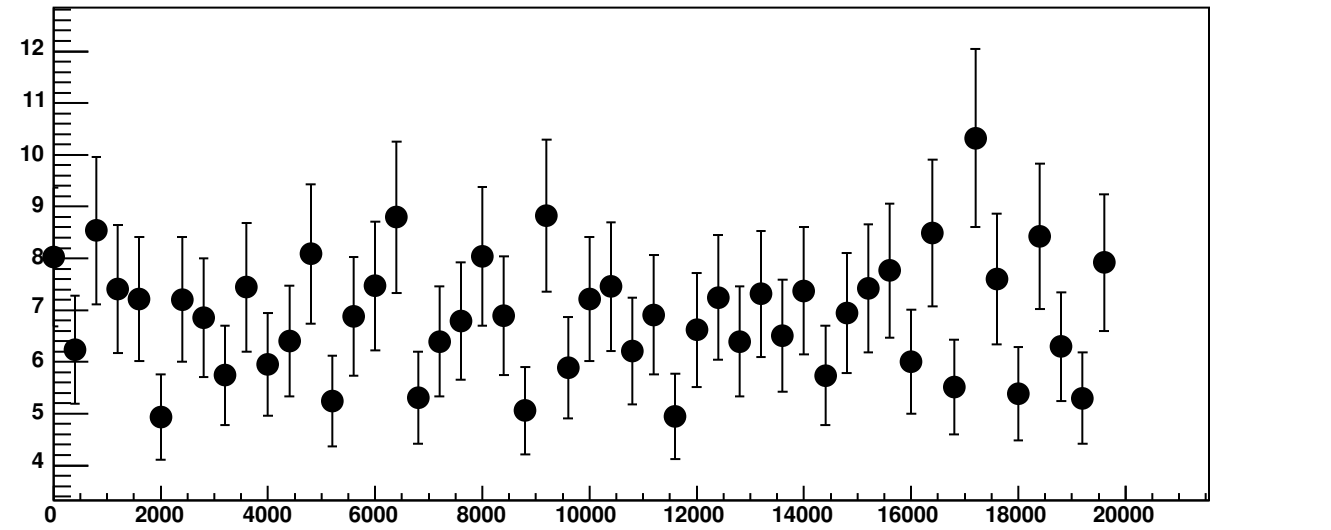
Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



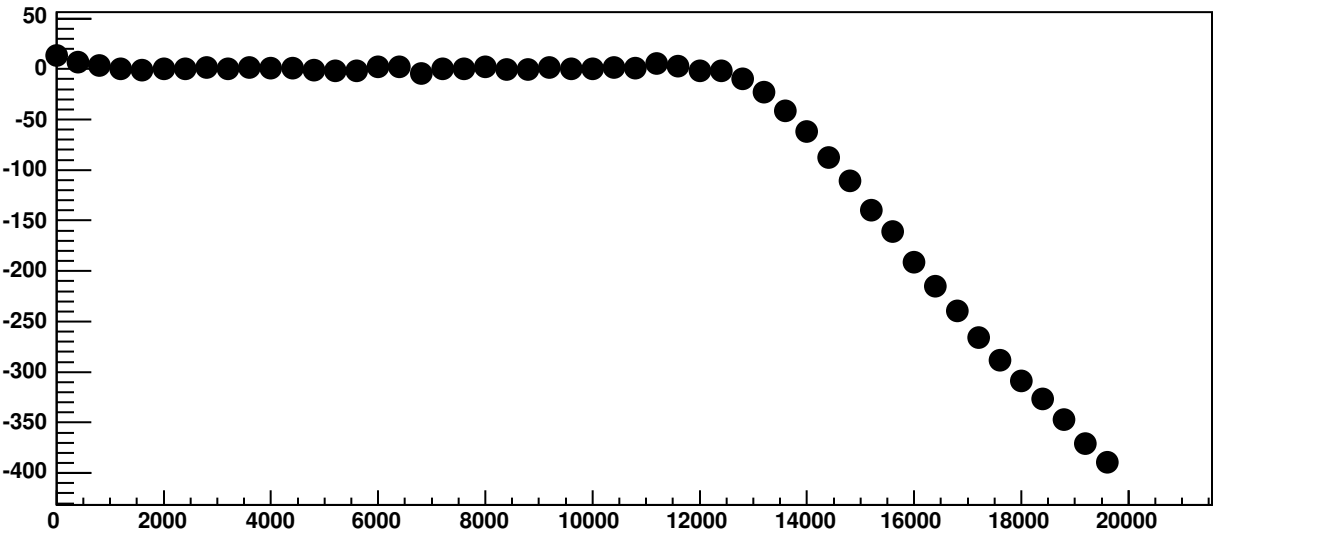
Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



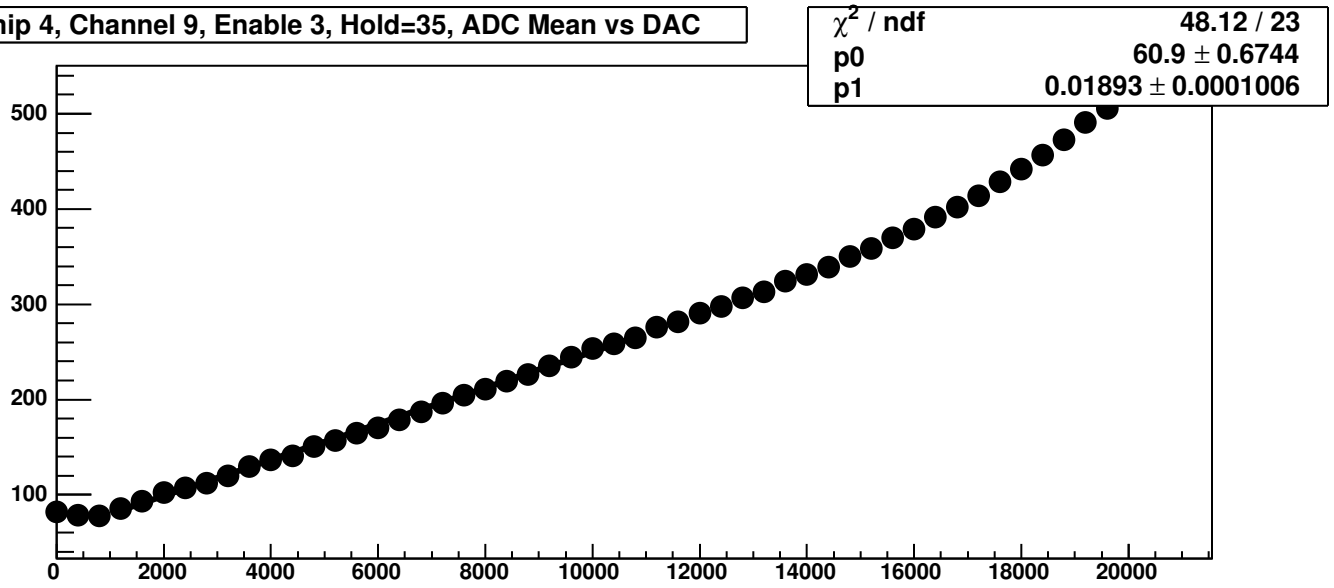
Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



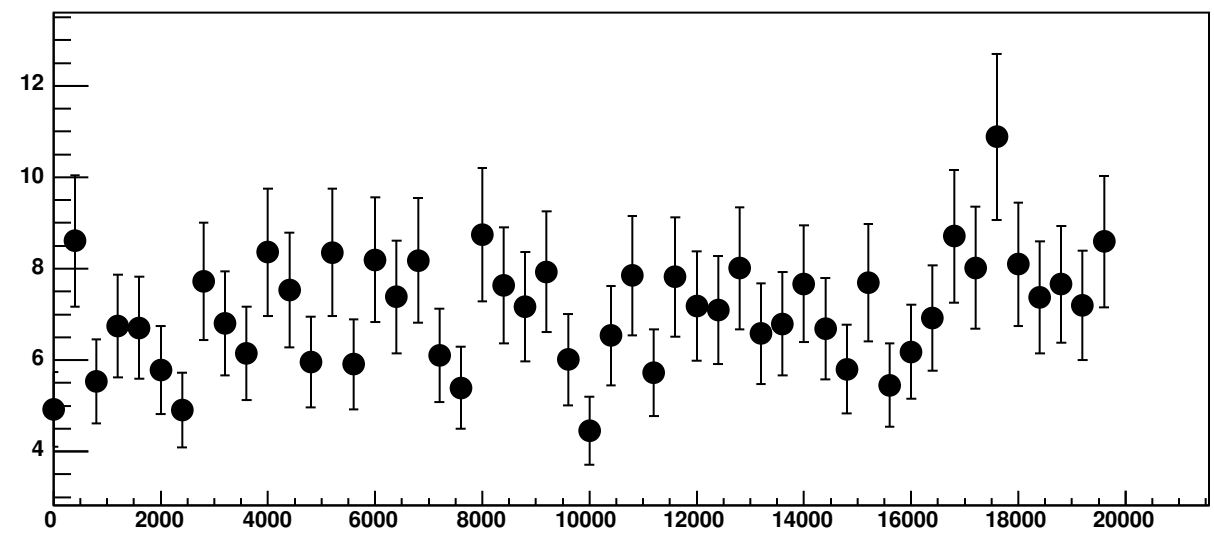
Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



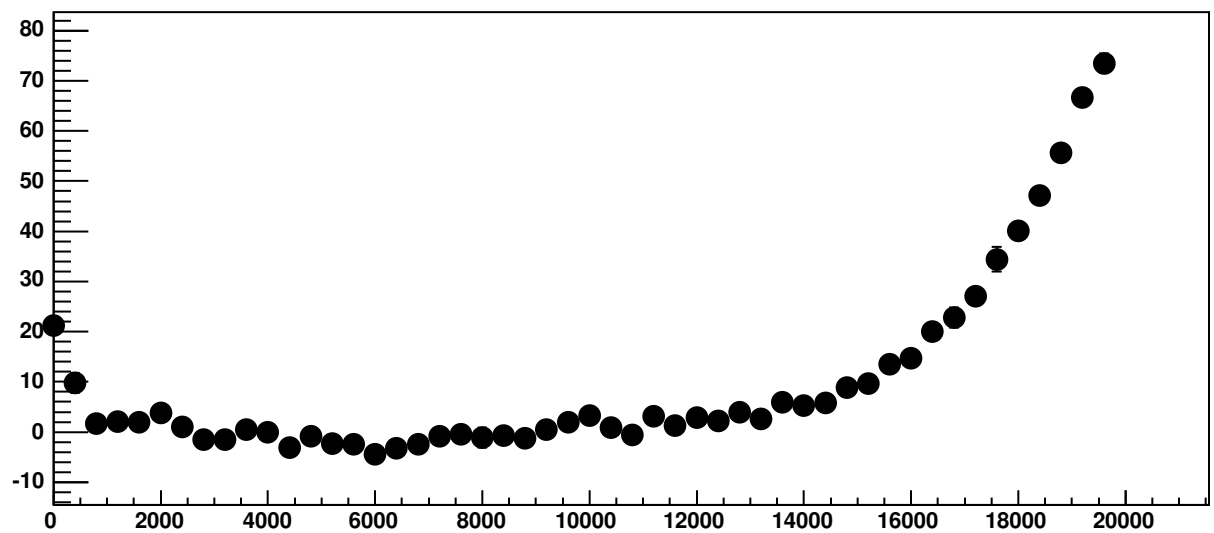
Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC



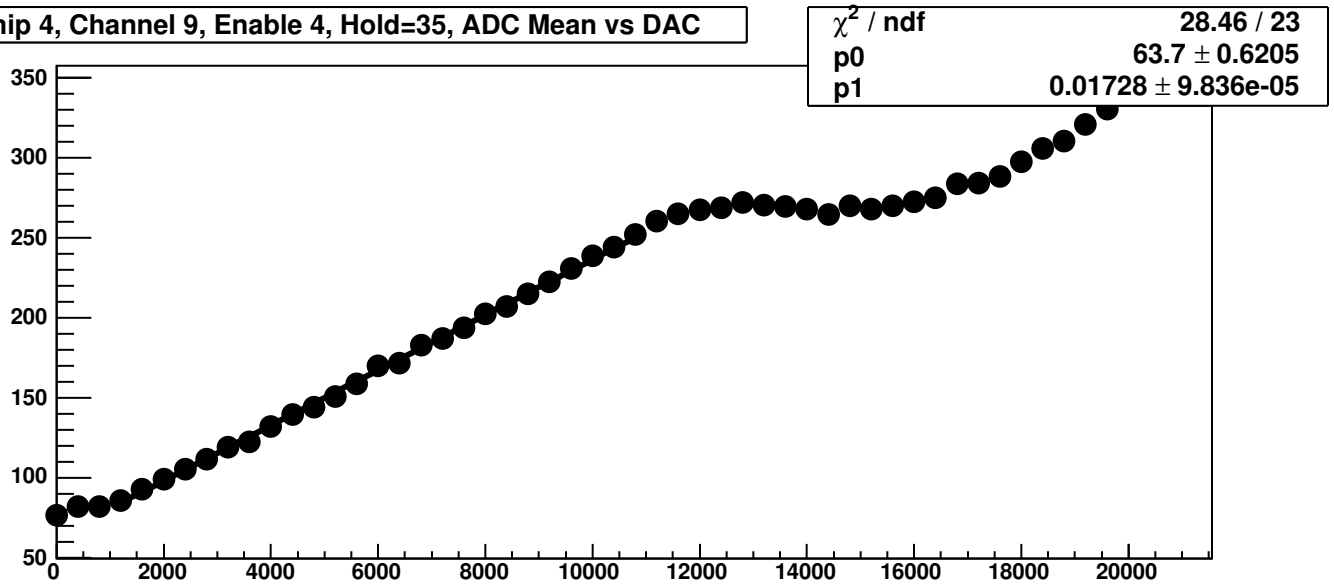
Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



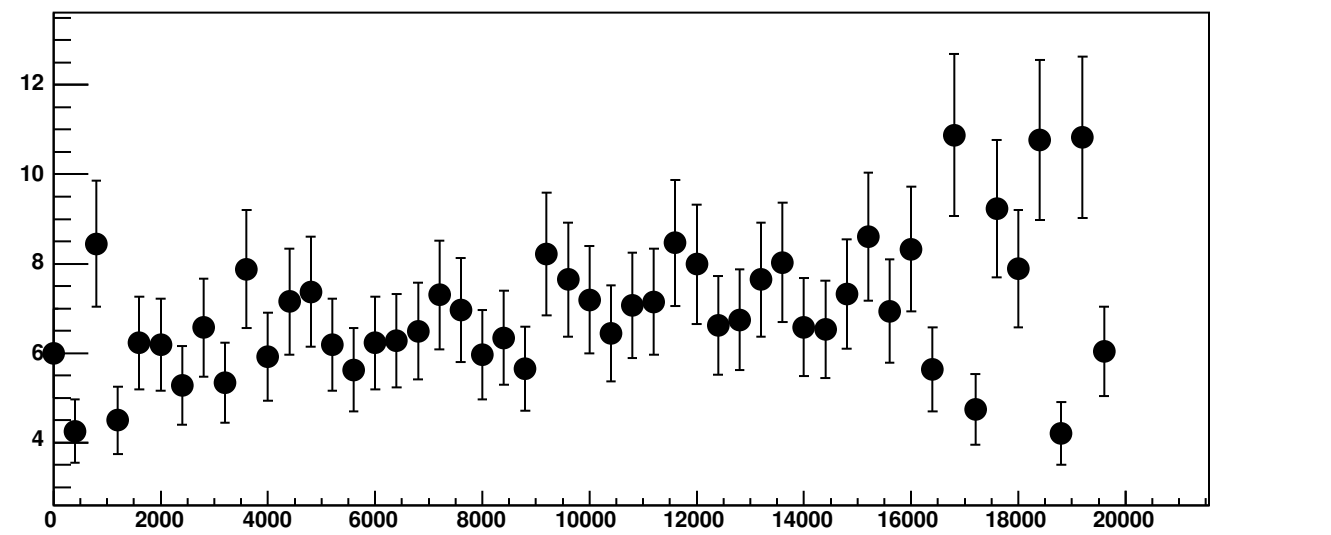
Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC



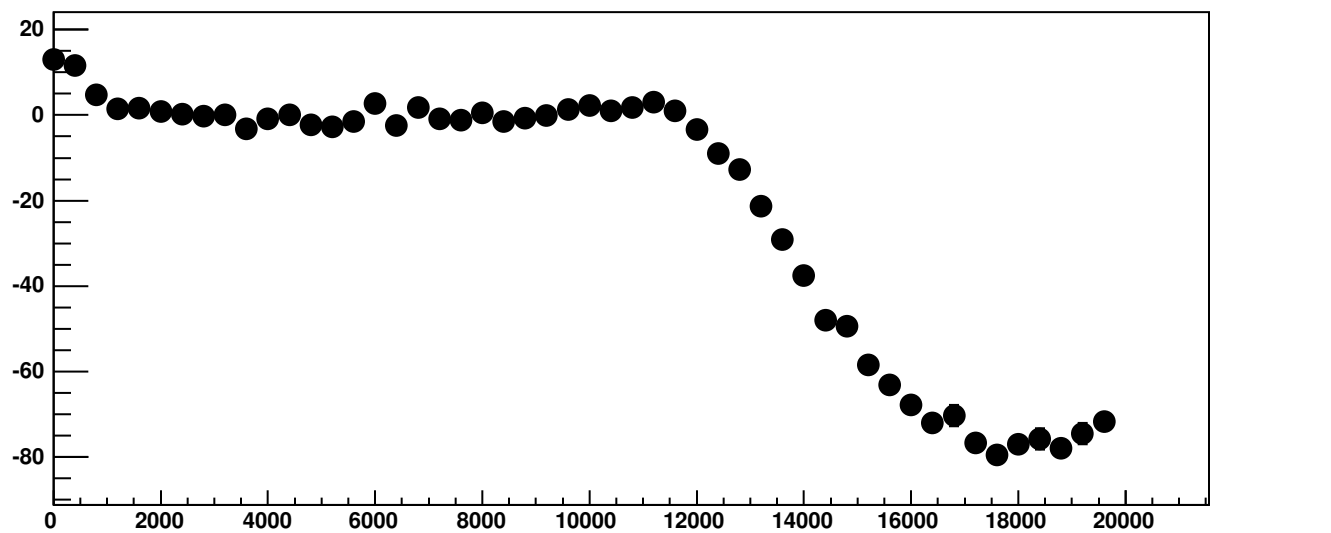
Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



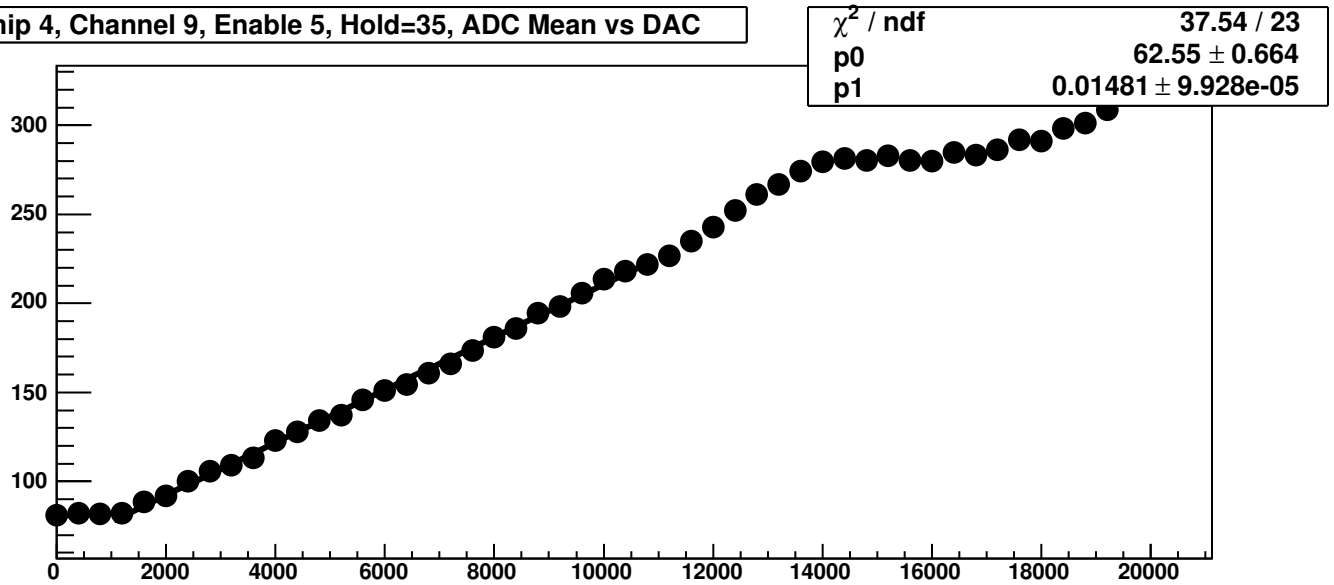
Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



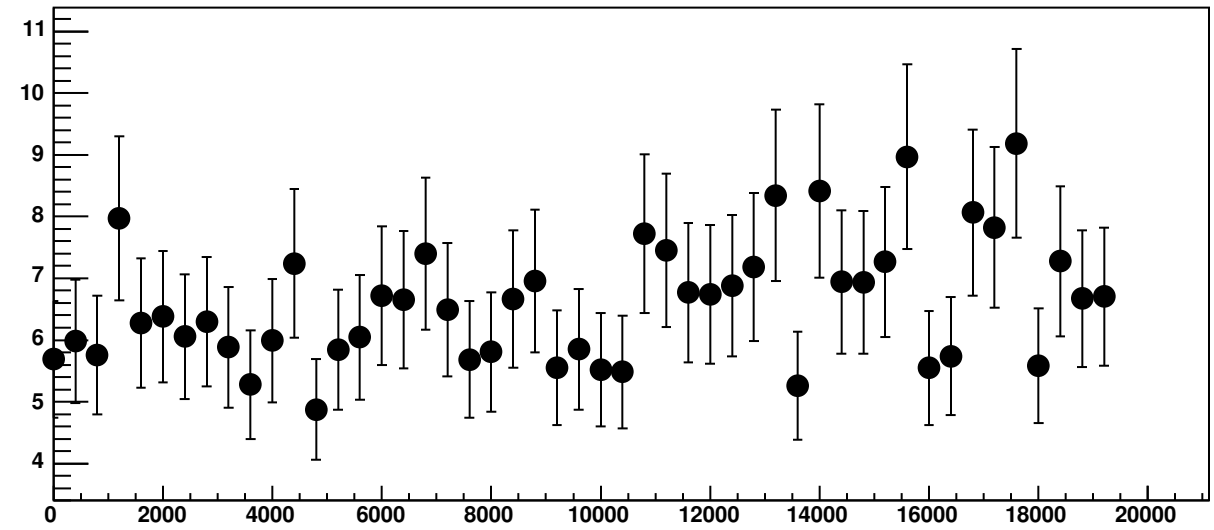
Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



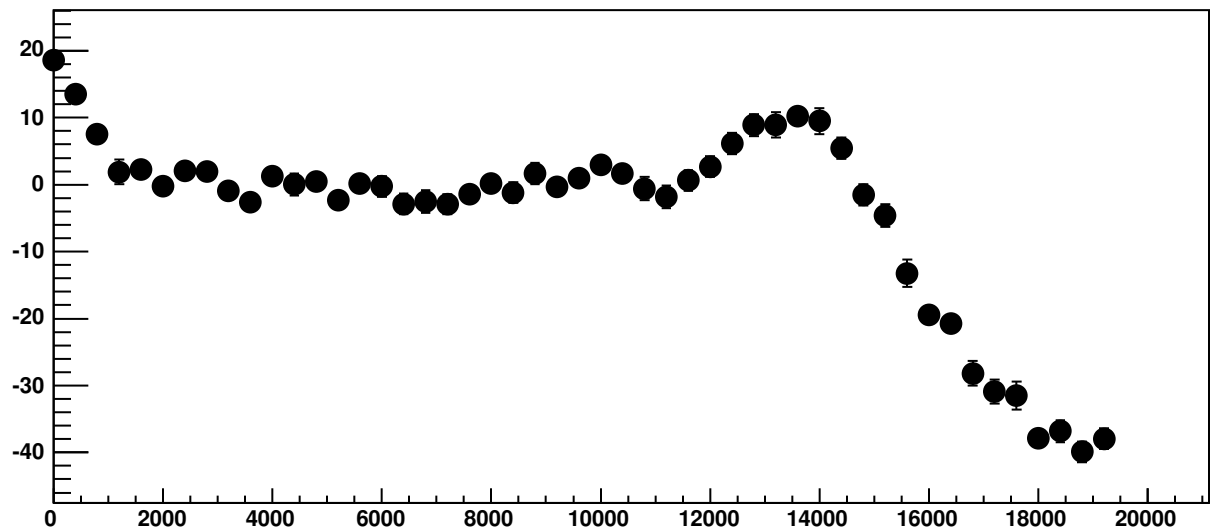
Chip 4, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 4, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

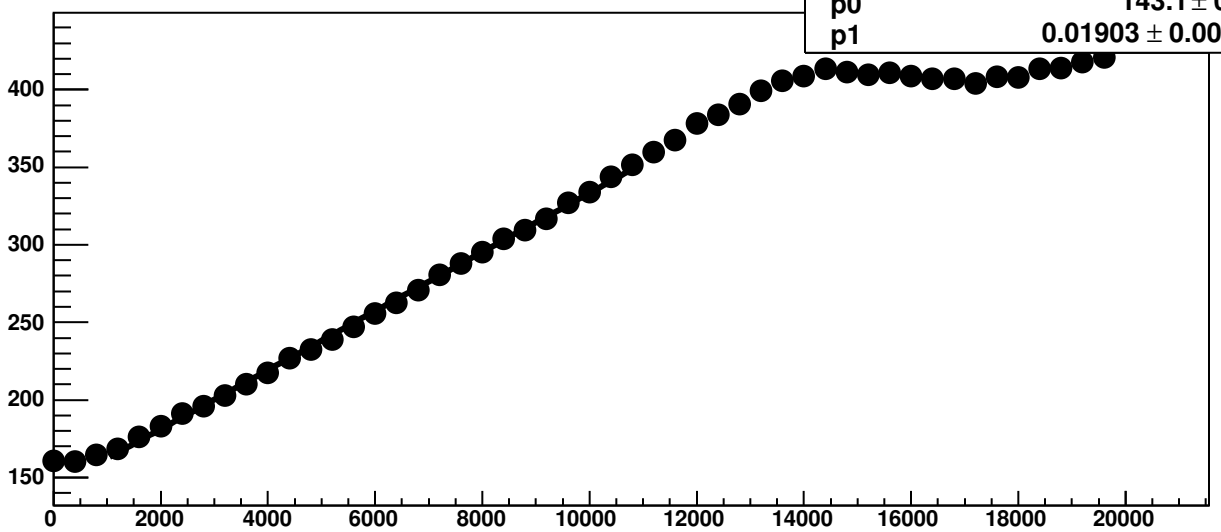


Chip 4, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

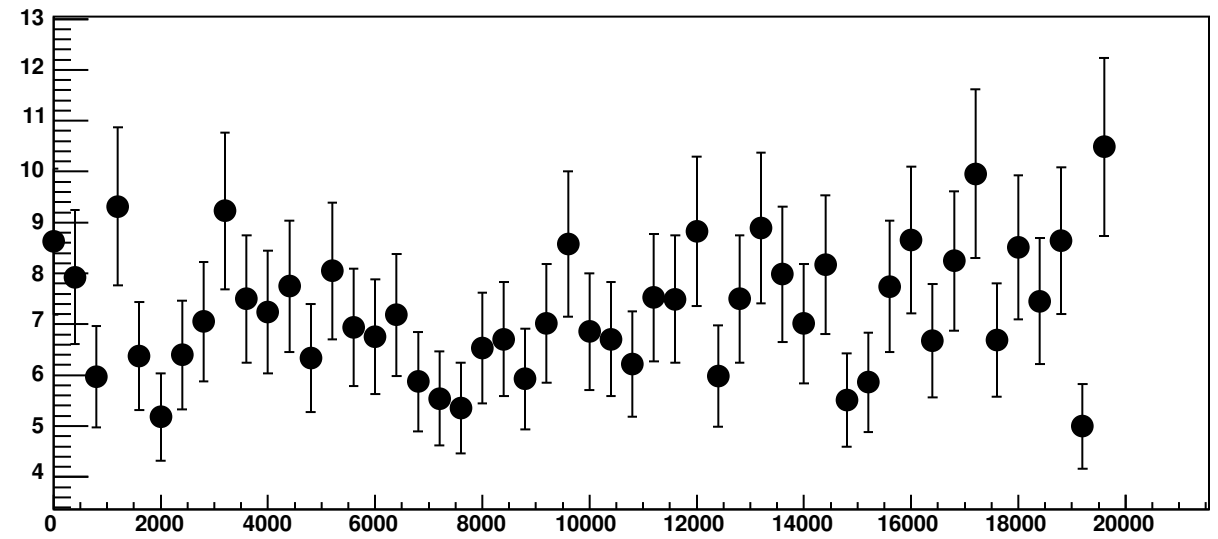


Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

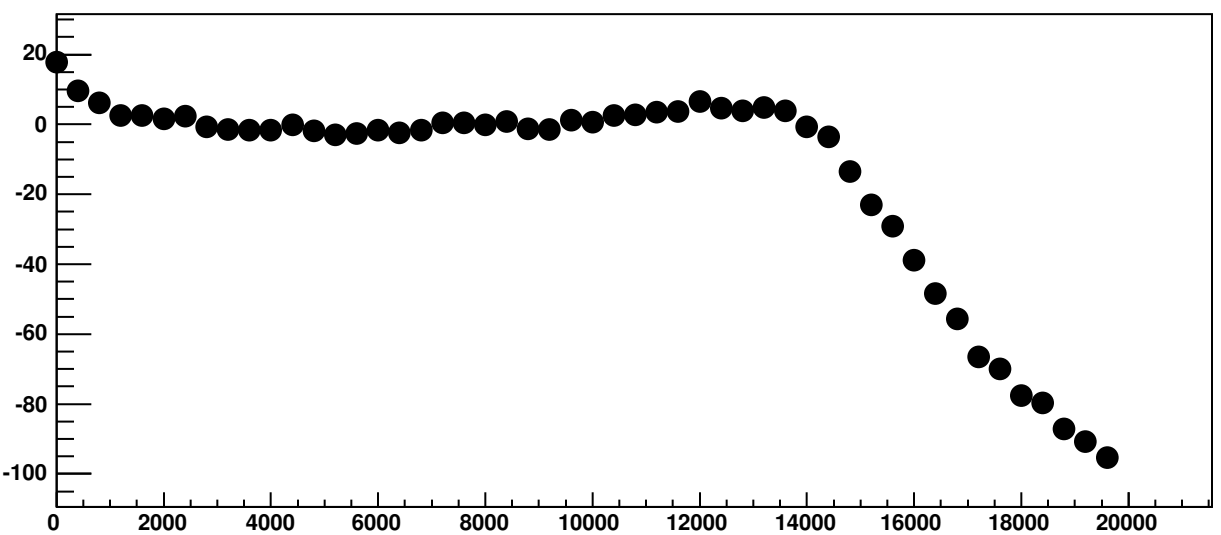
χ^2 / ndf	32.57 / 23
p0	143.1 ± 0.7255
p1	0.01903 ± 0.0001073

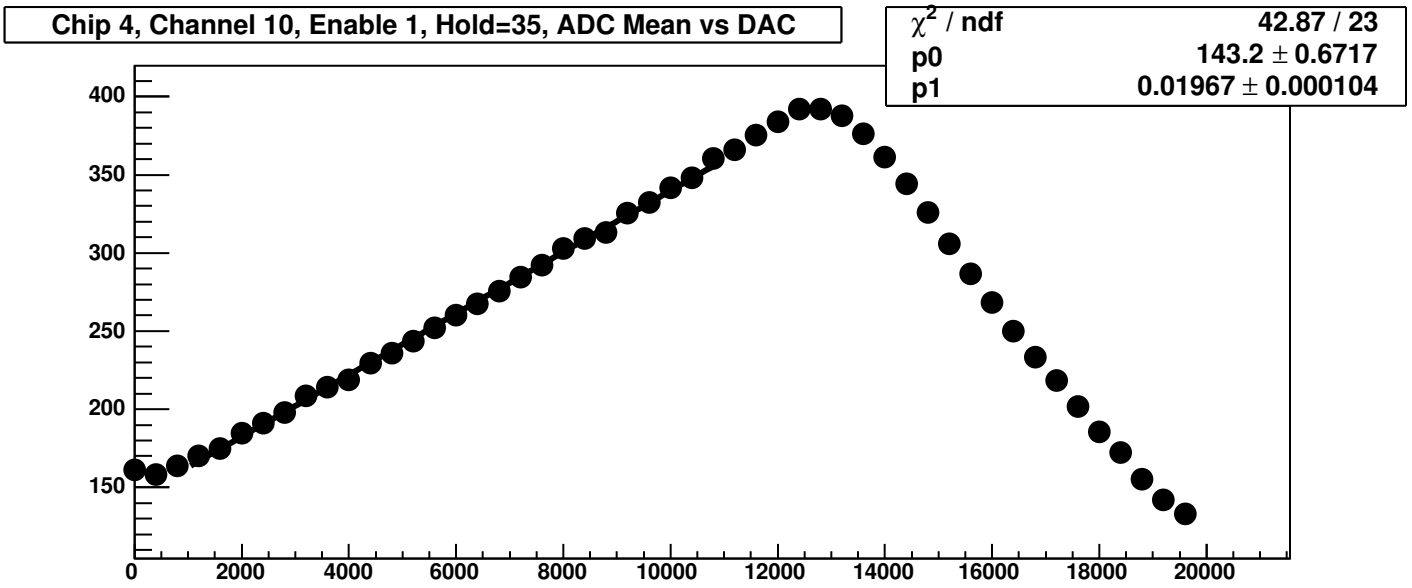


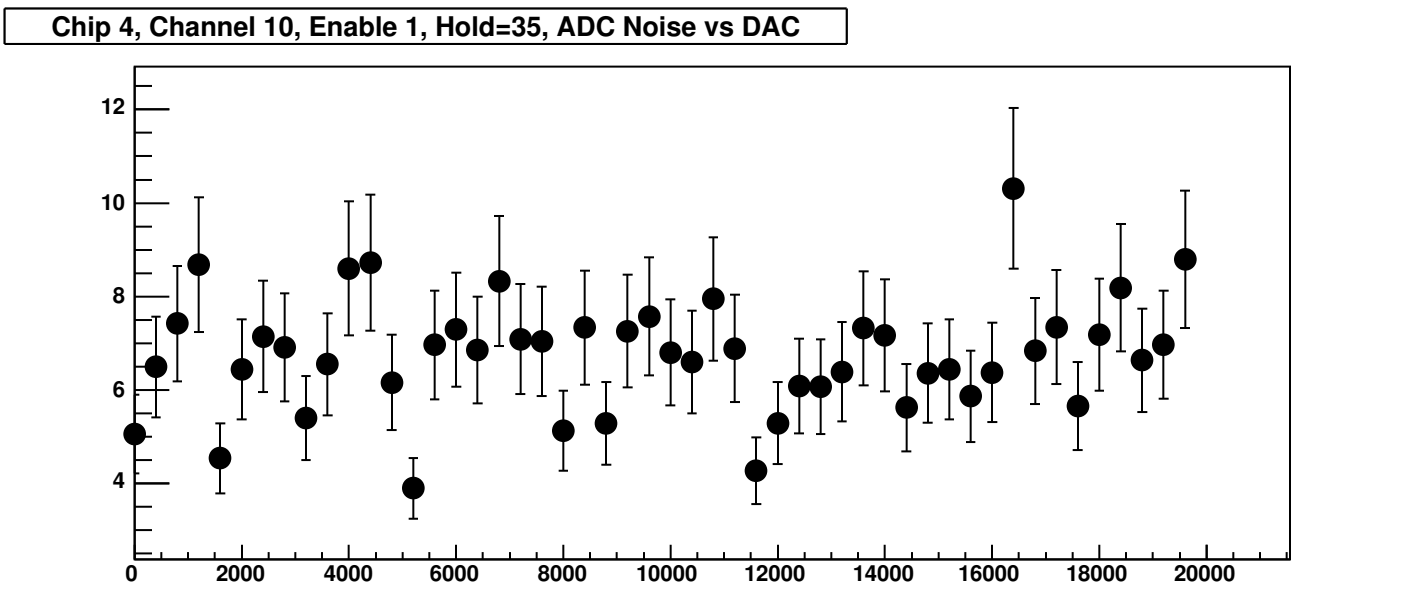
Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC

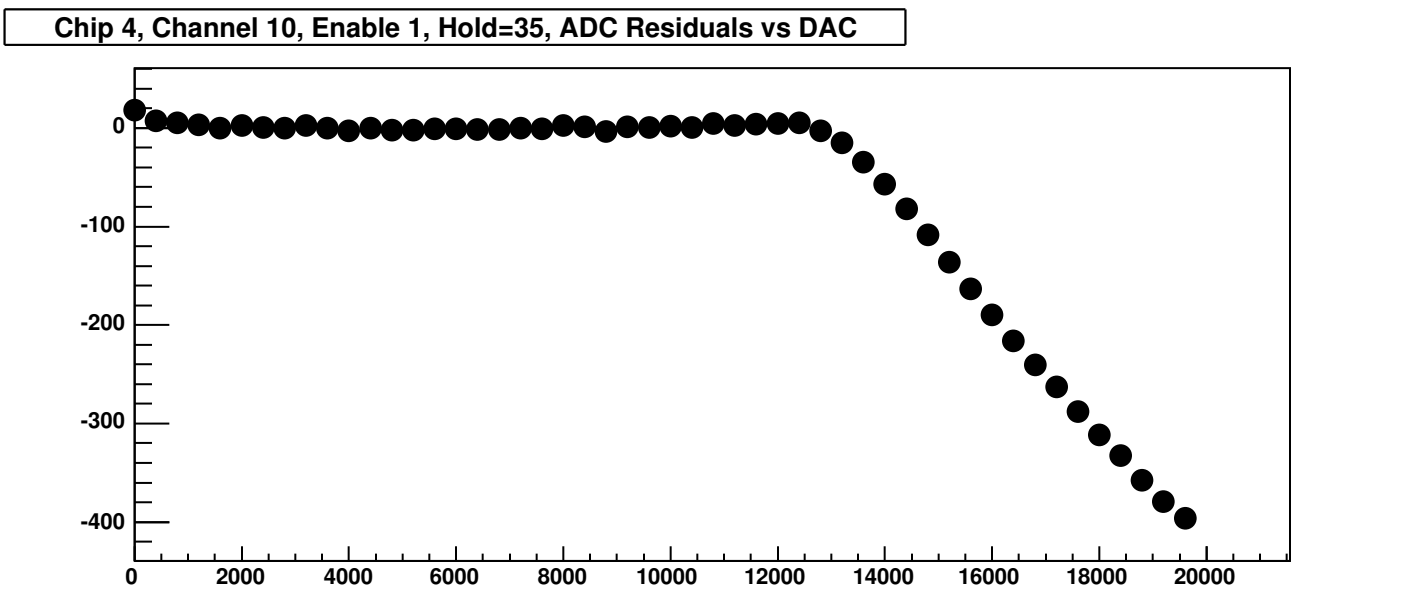


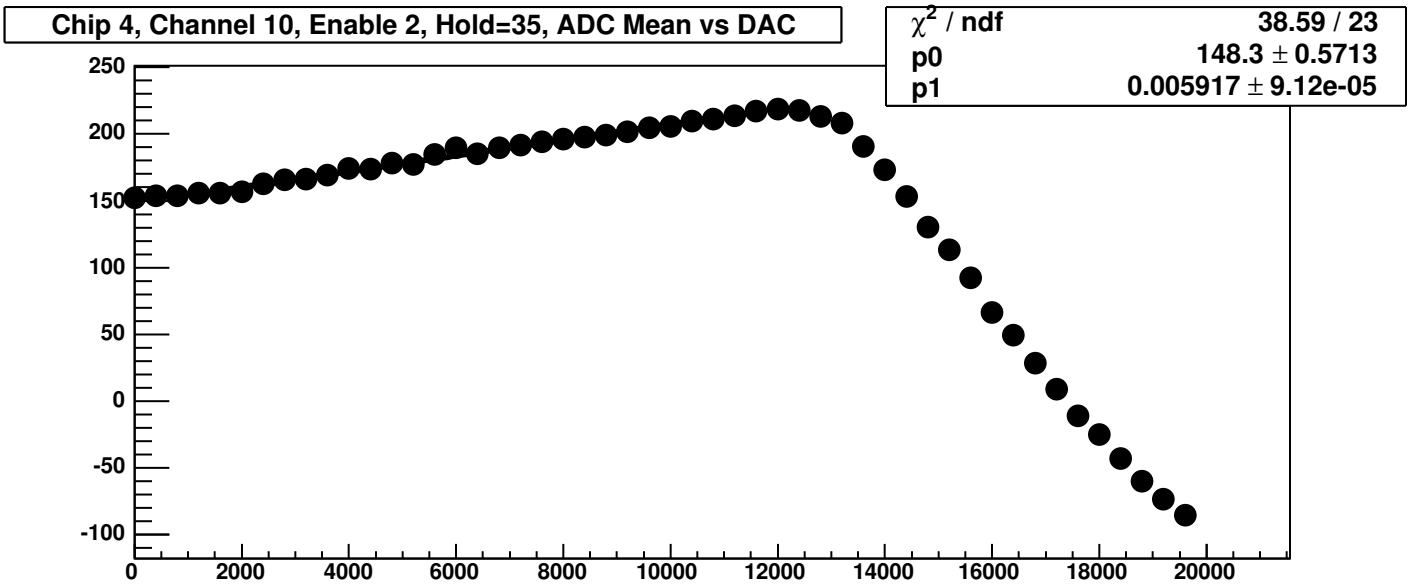
Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

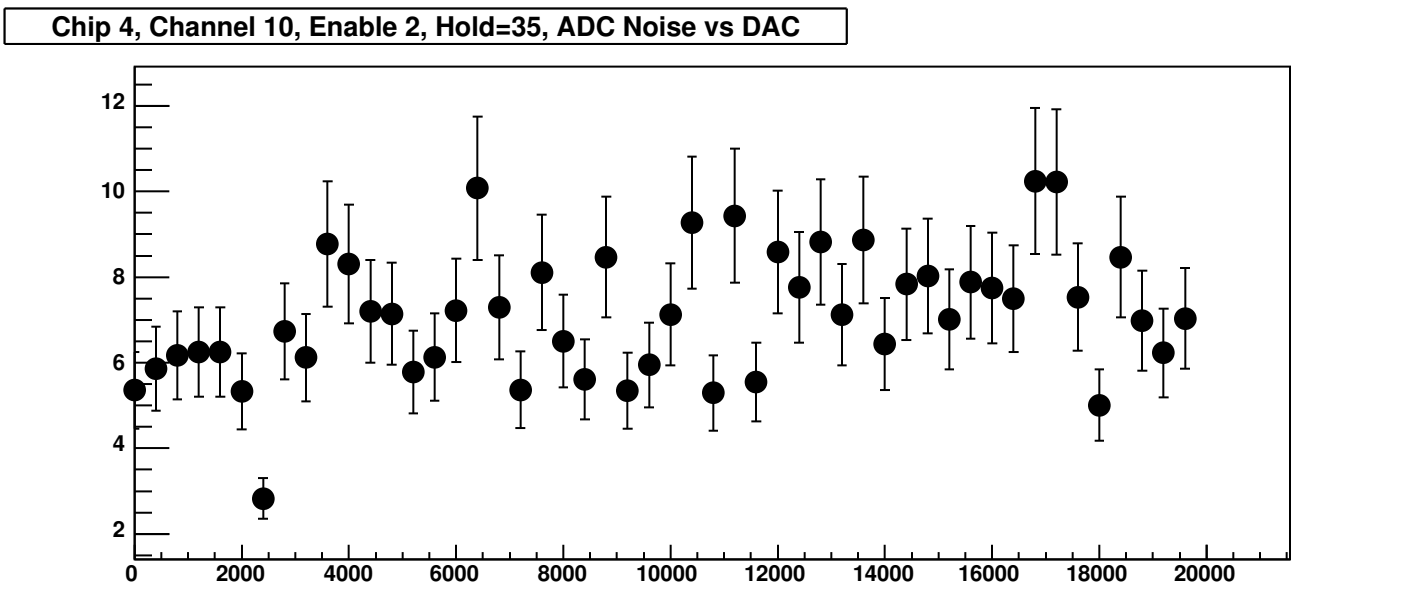


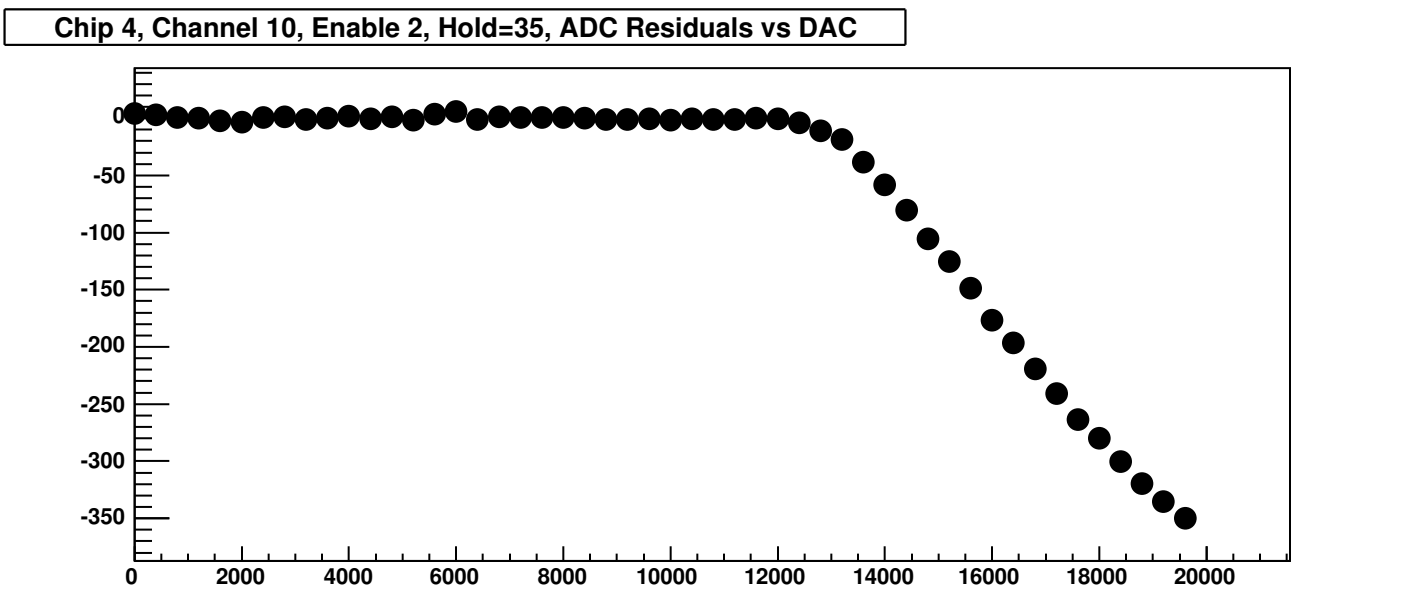

Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.87 / 23
p0 143.2 ± 0.6717
p1 0.01967 ± 0.000104
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

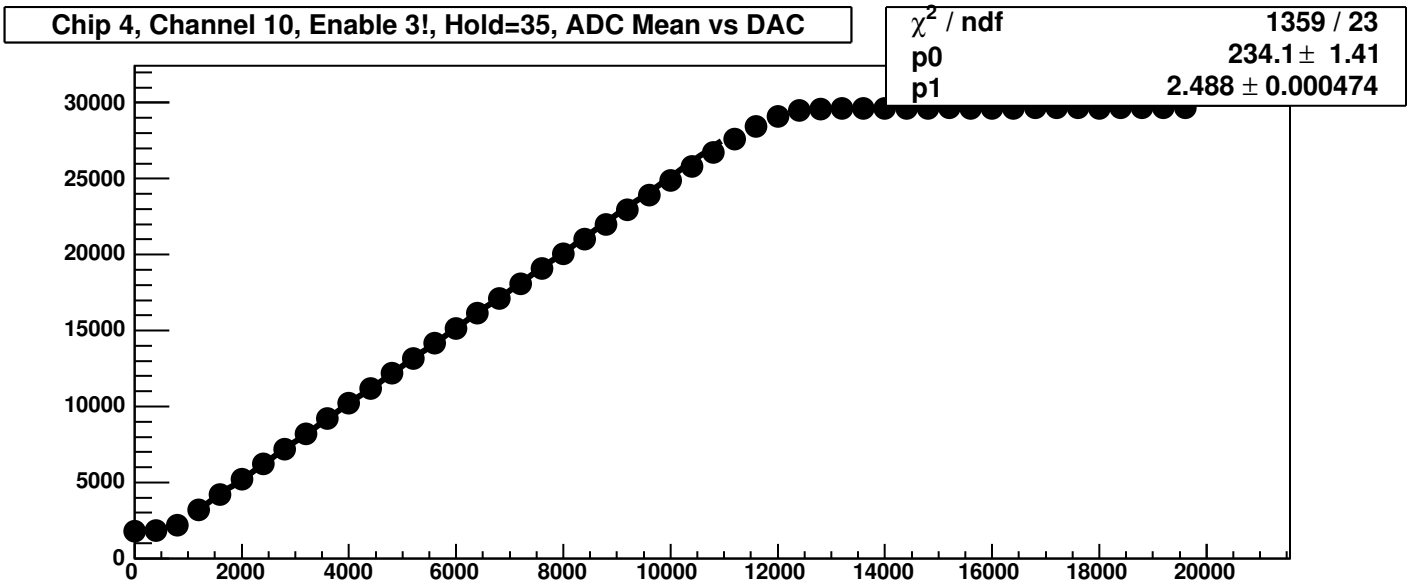

Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

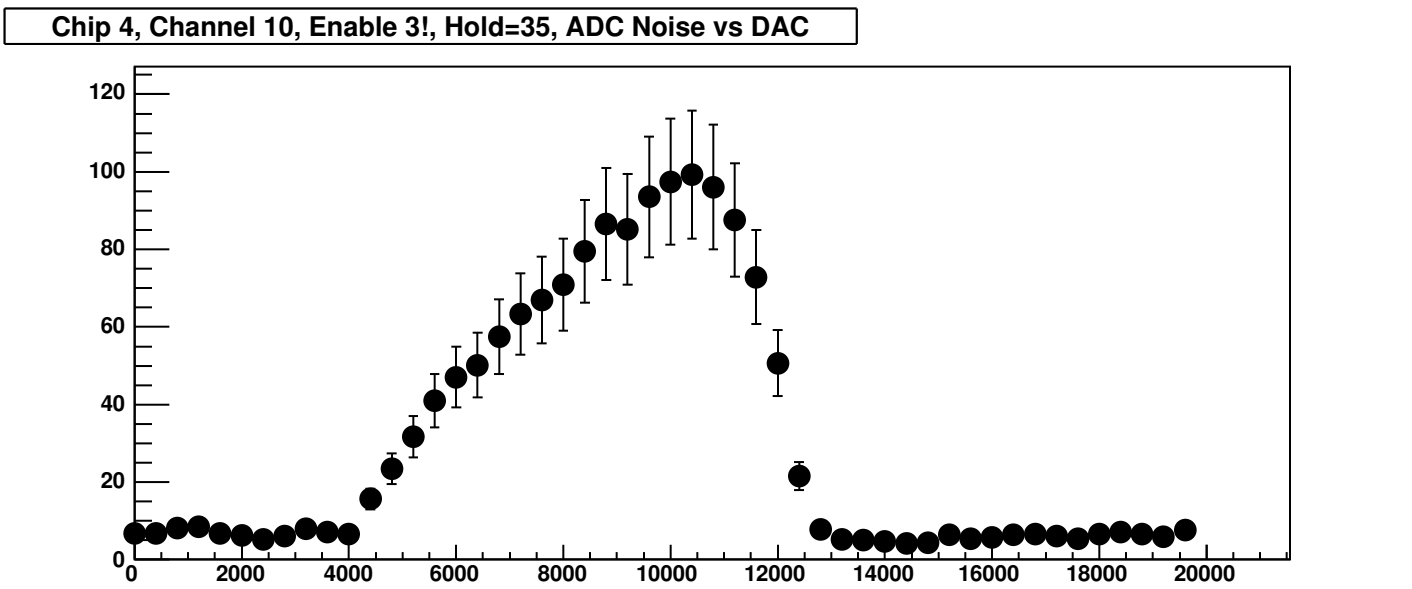

Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

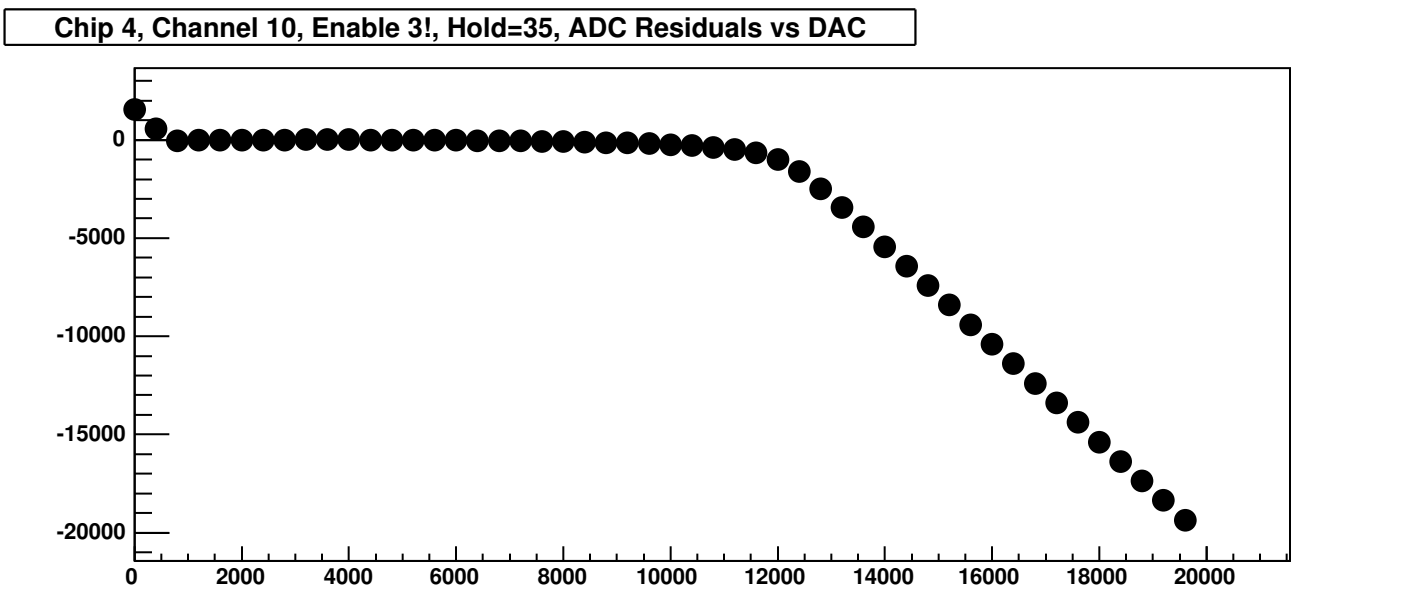

Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 38.59 / 23
p0 148.3 ± 0.5713
p1 0.005917 ± 9.12e-05
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

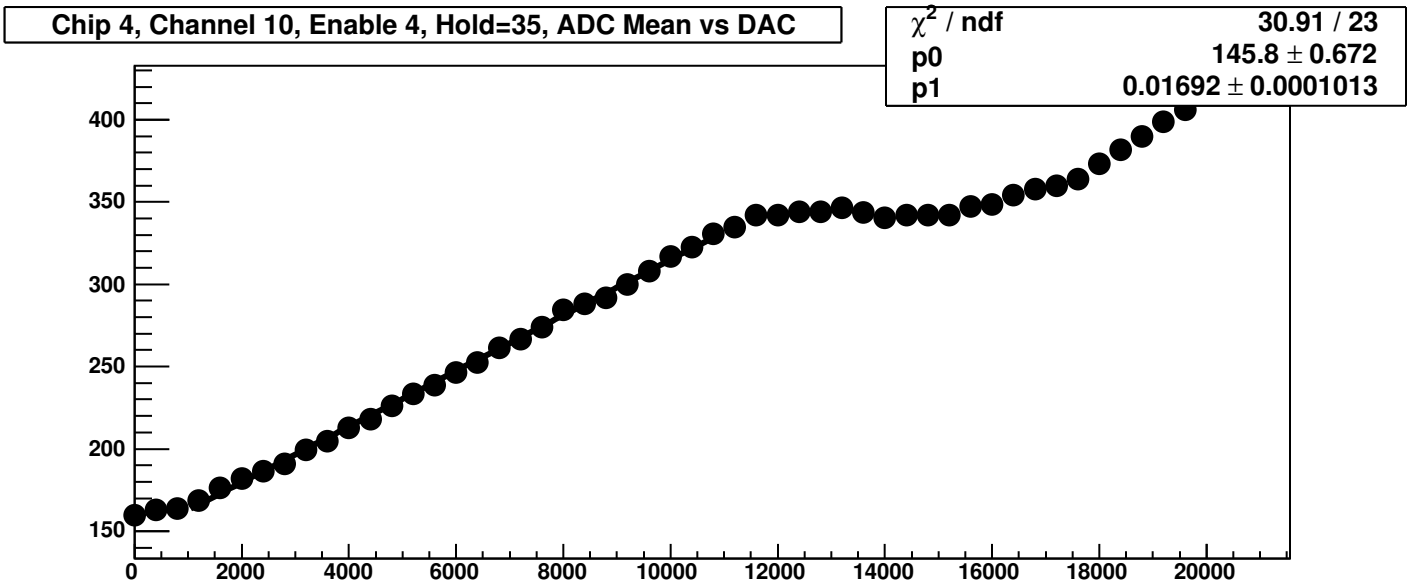

Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

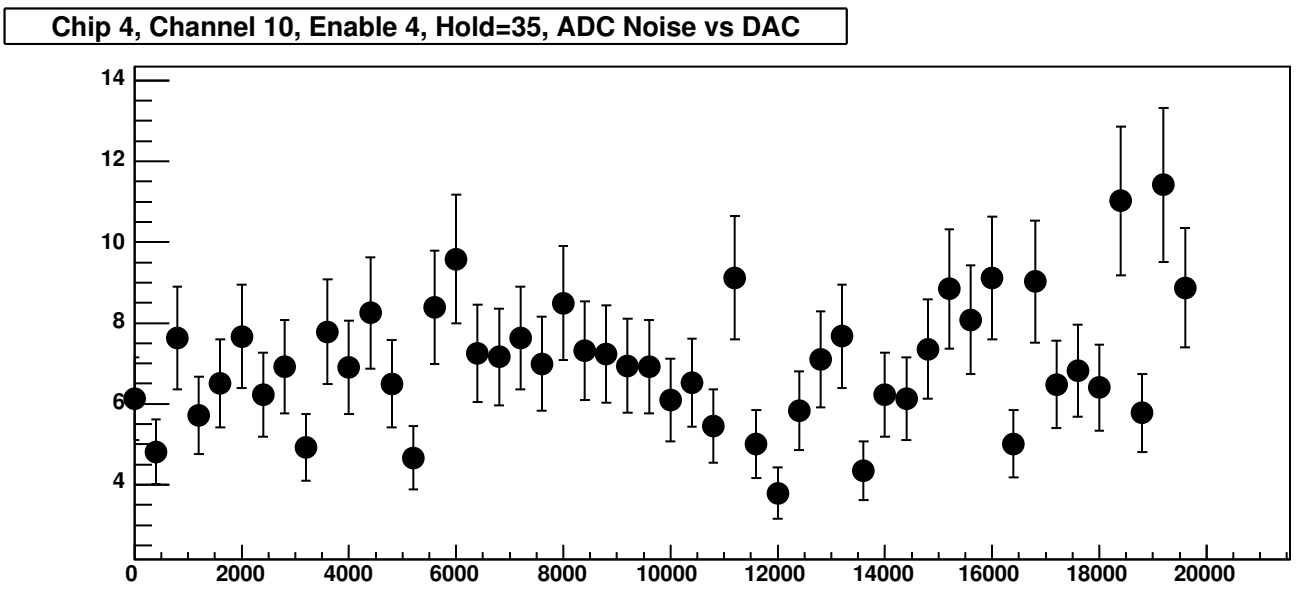

Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

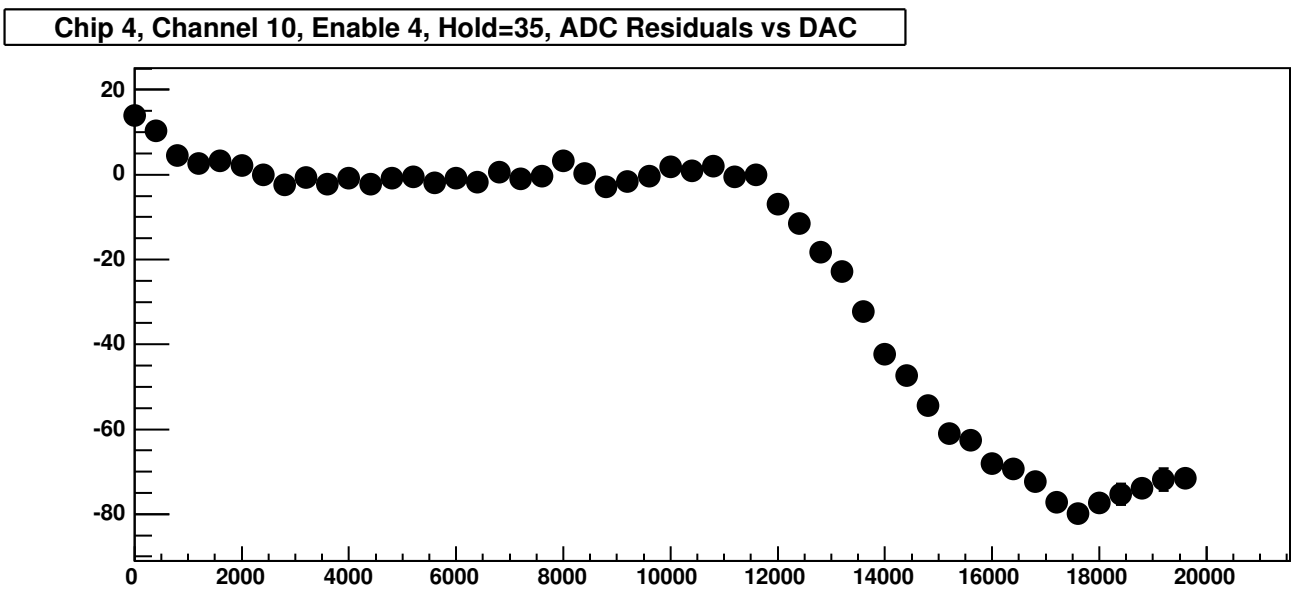

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1359 / 23
p0 234.1 ± 1.41
p1 2.488 ± 0.000474
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

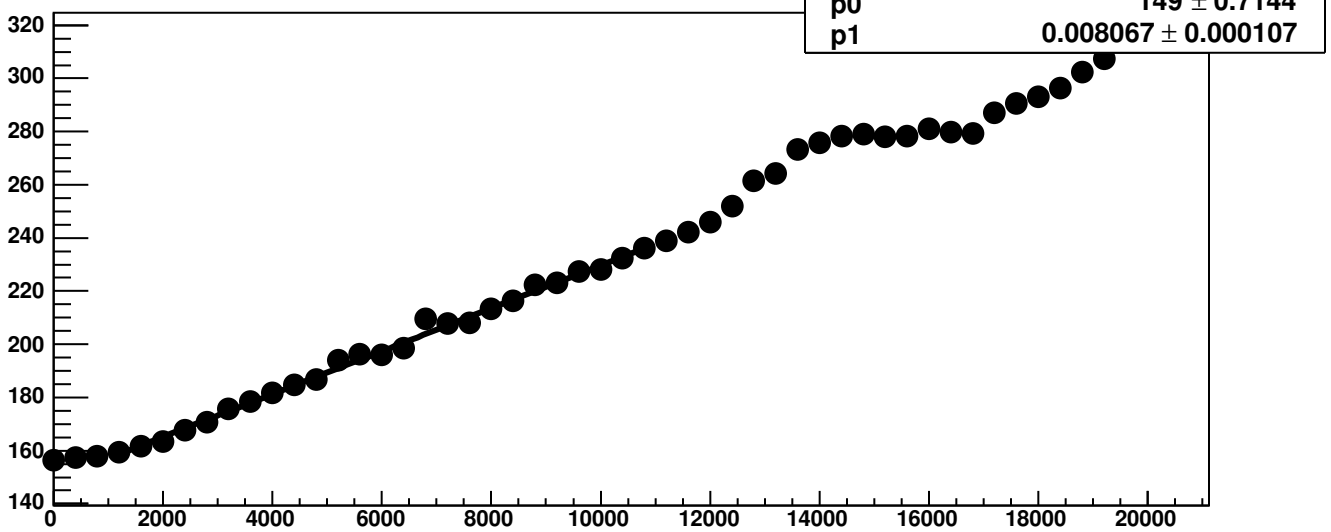

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.91 / 23
p0 145.8 ± 0.672
p1 0.01692 ± 0.0001013
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

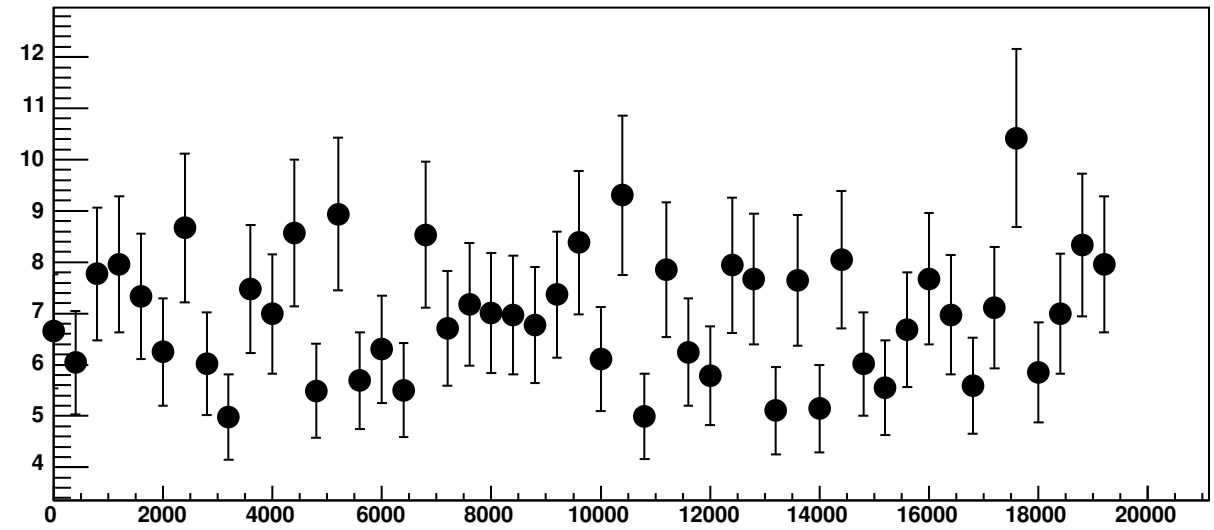

Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

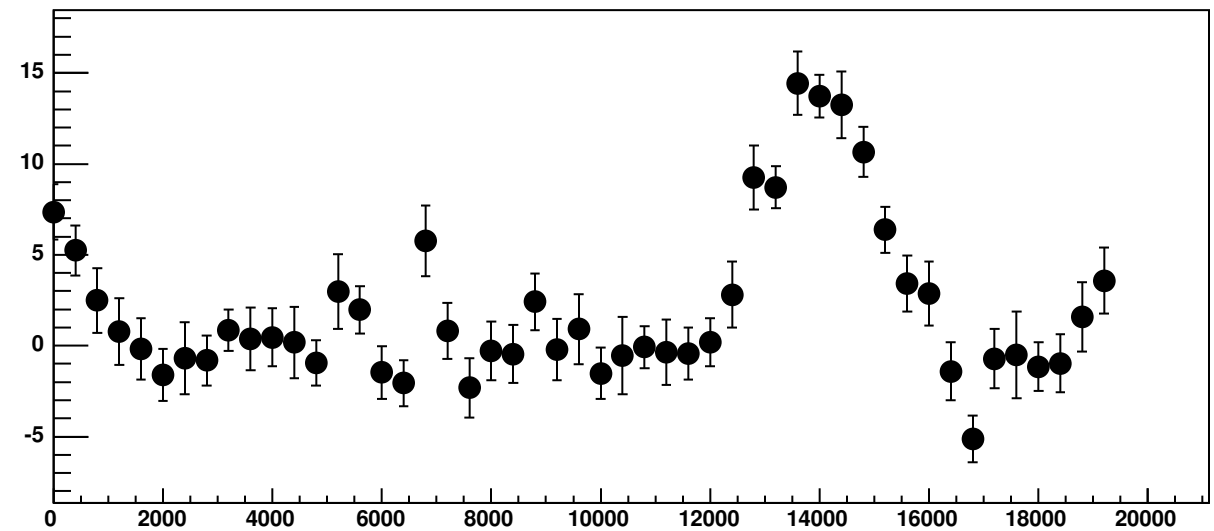
Chip 4, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

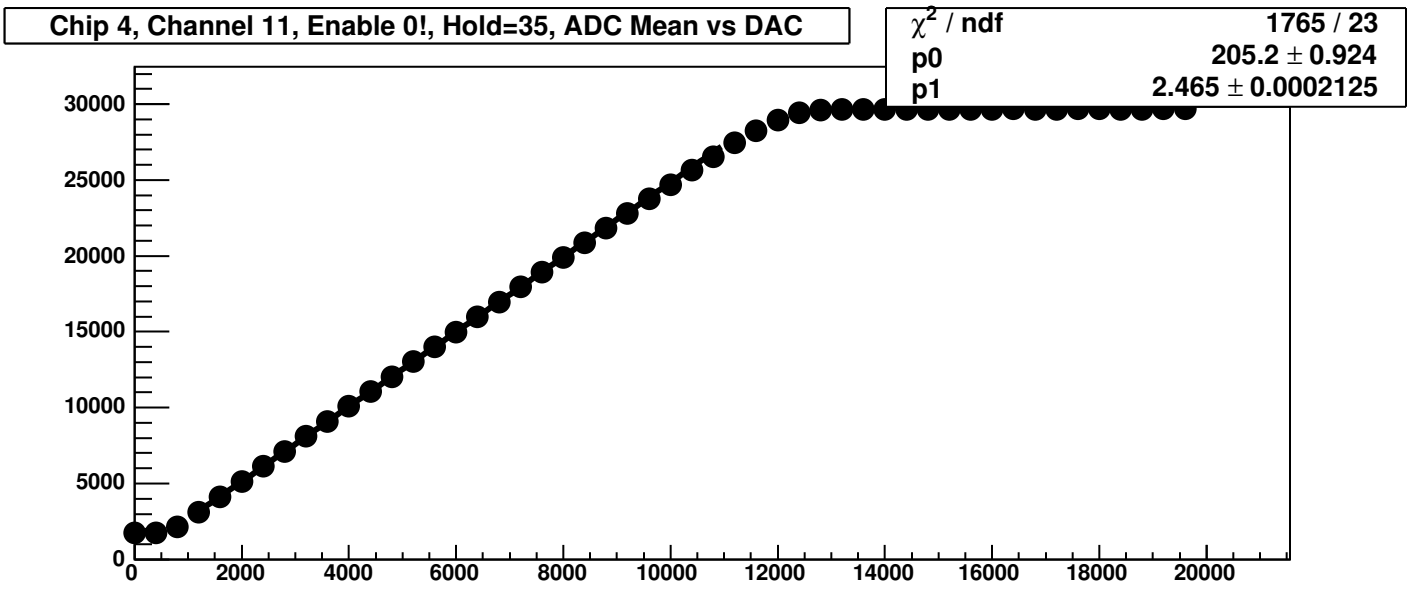


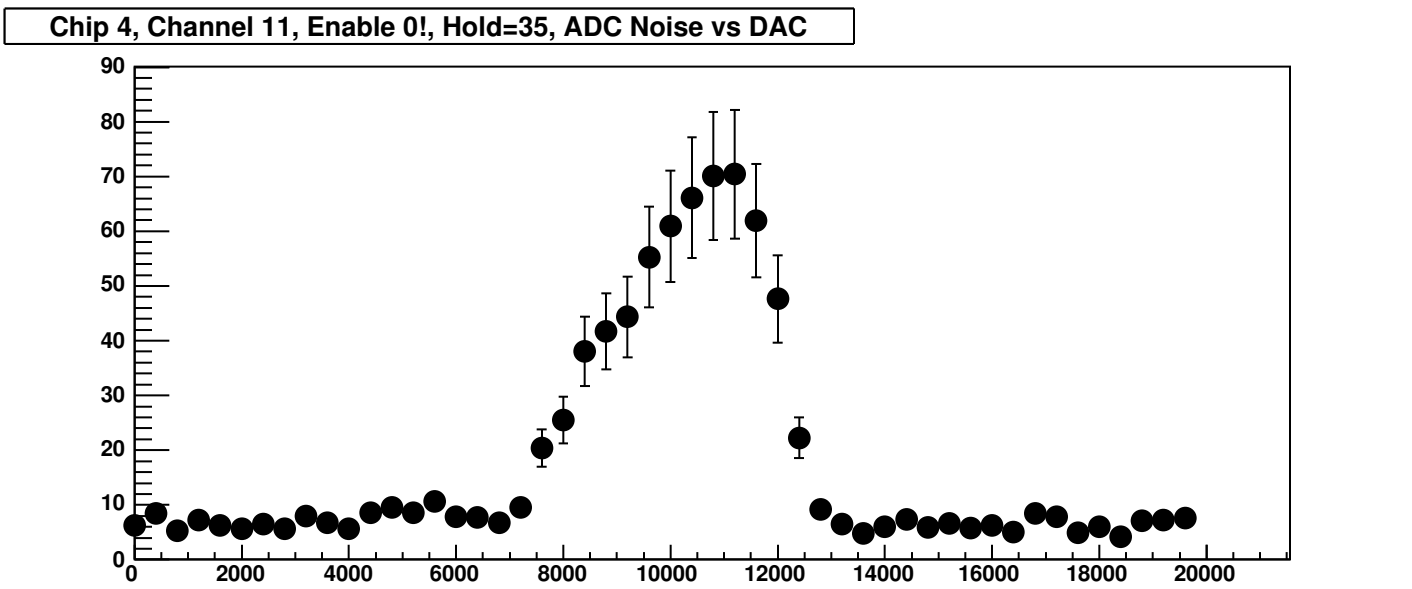
Chip 4, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

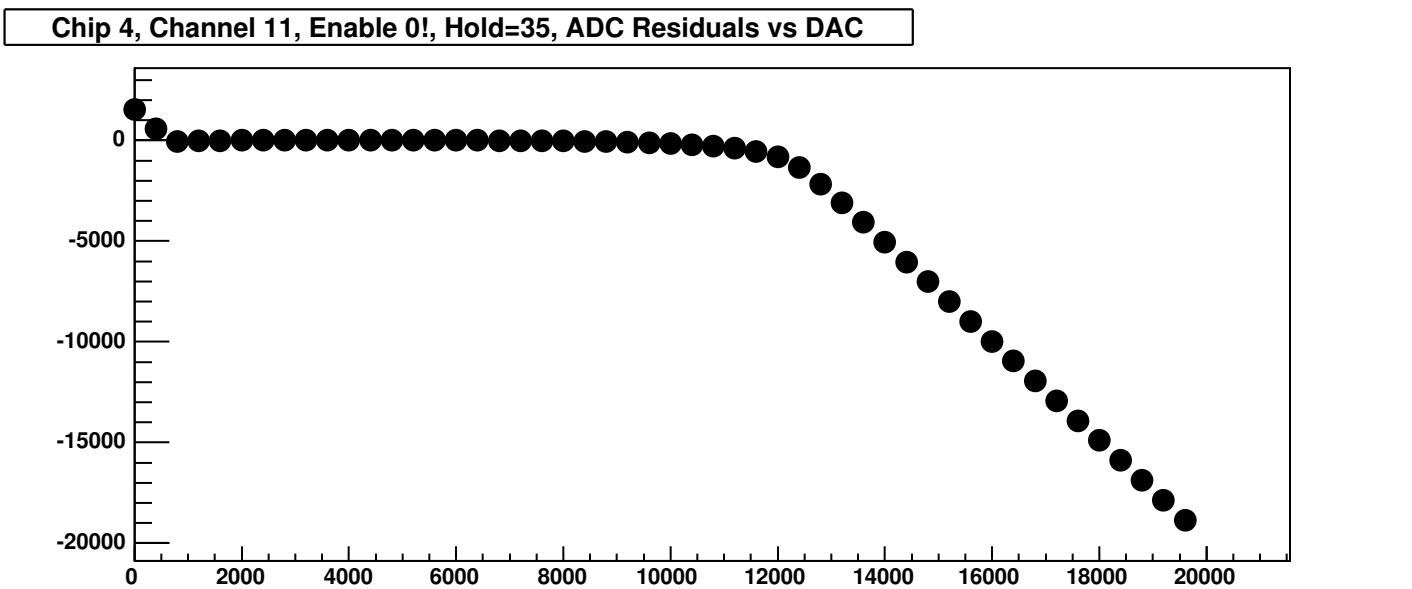


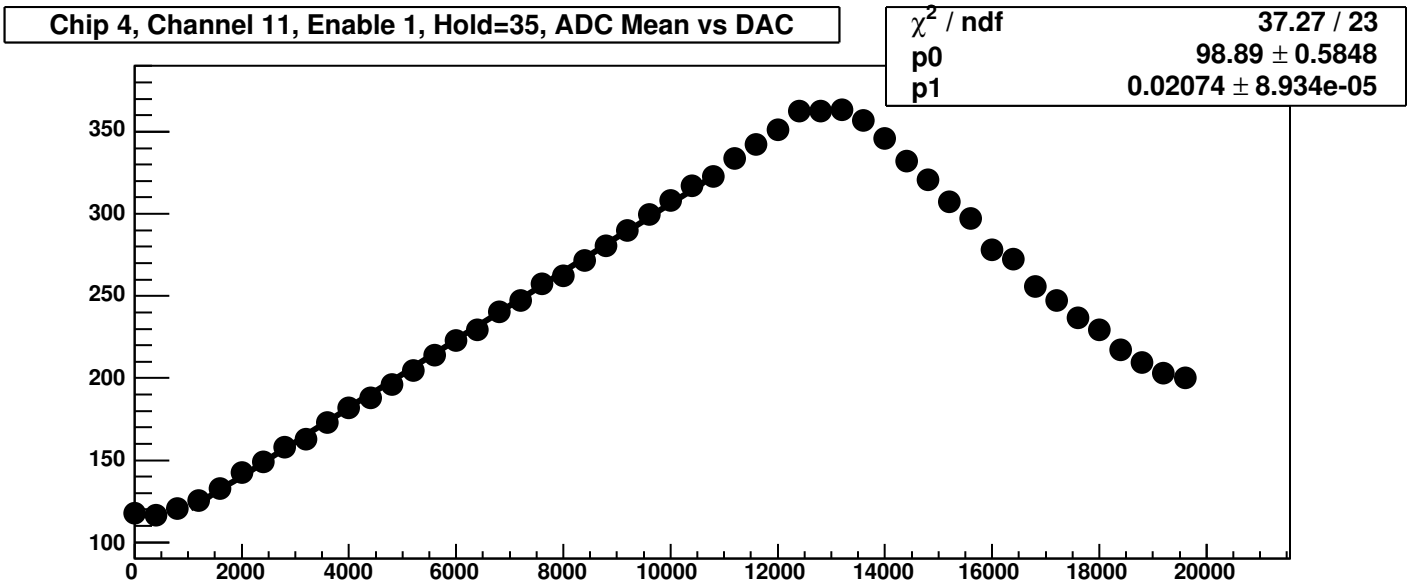
Chip 4, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

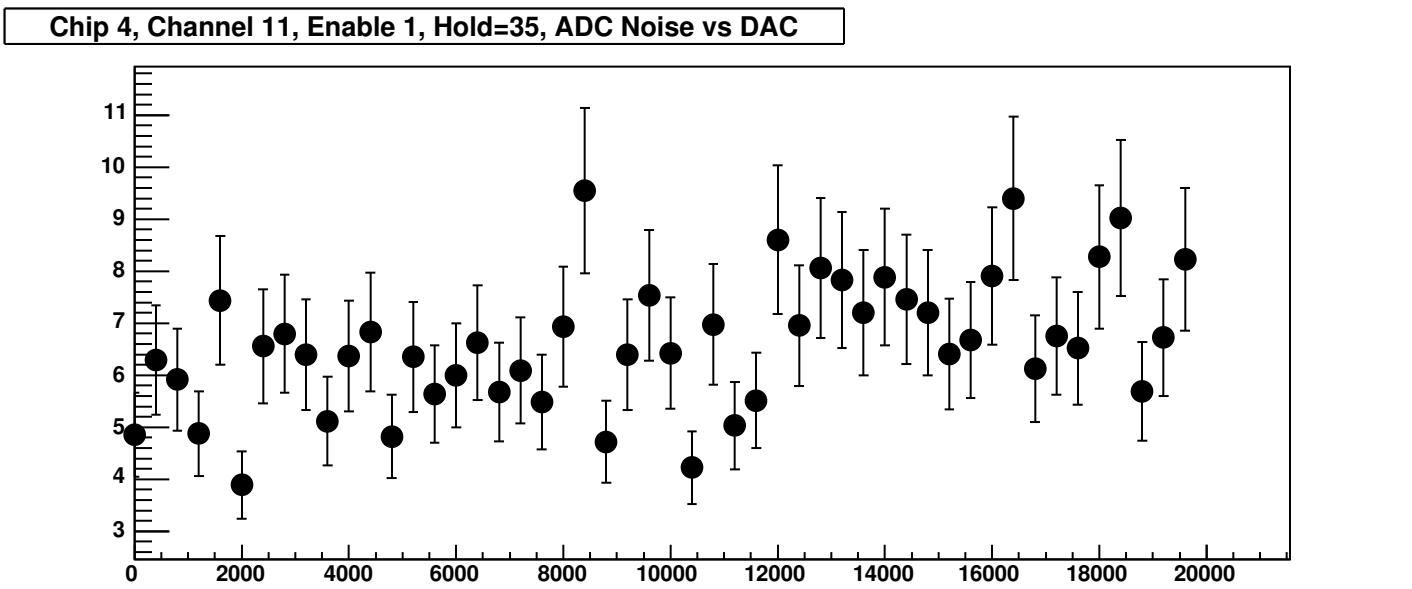


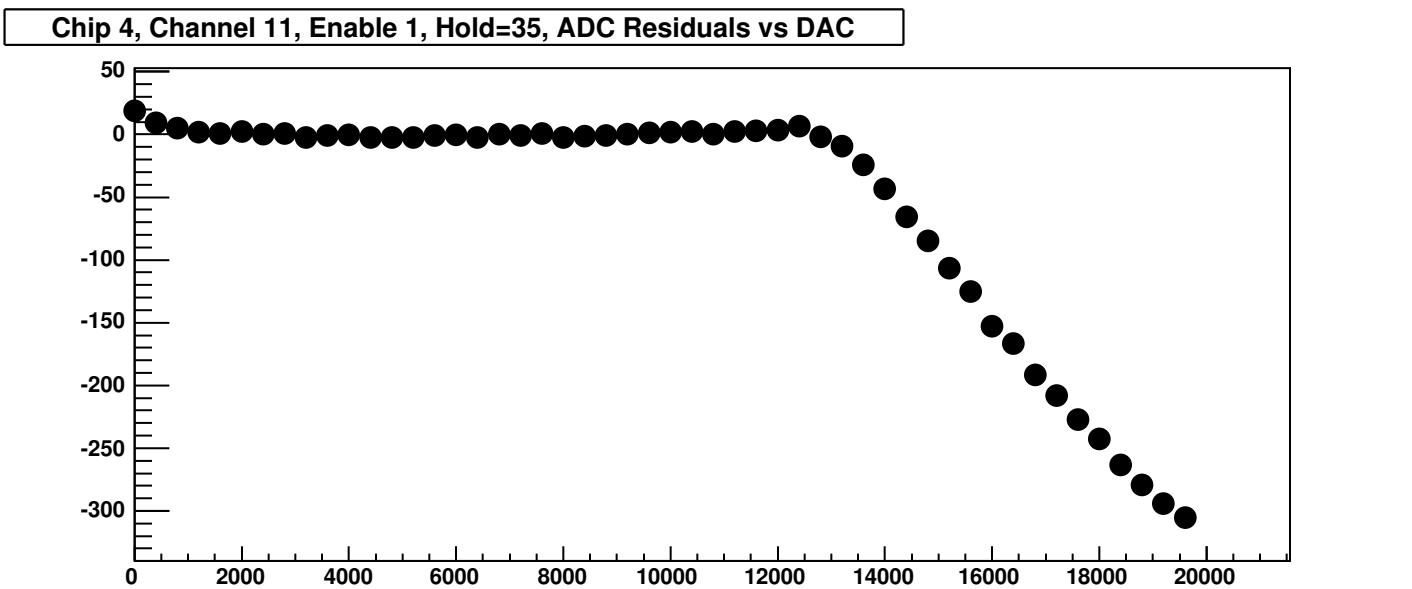

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1765 / 23
p0 205.2 ± 0.924
p1 2.465 ± 0.0002125
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

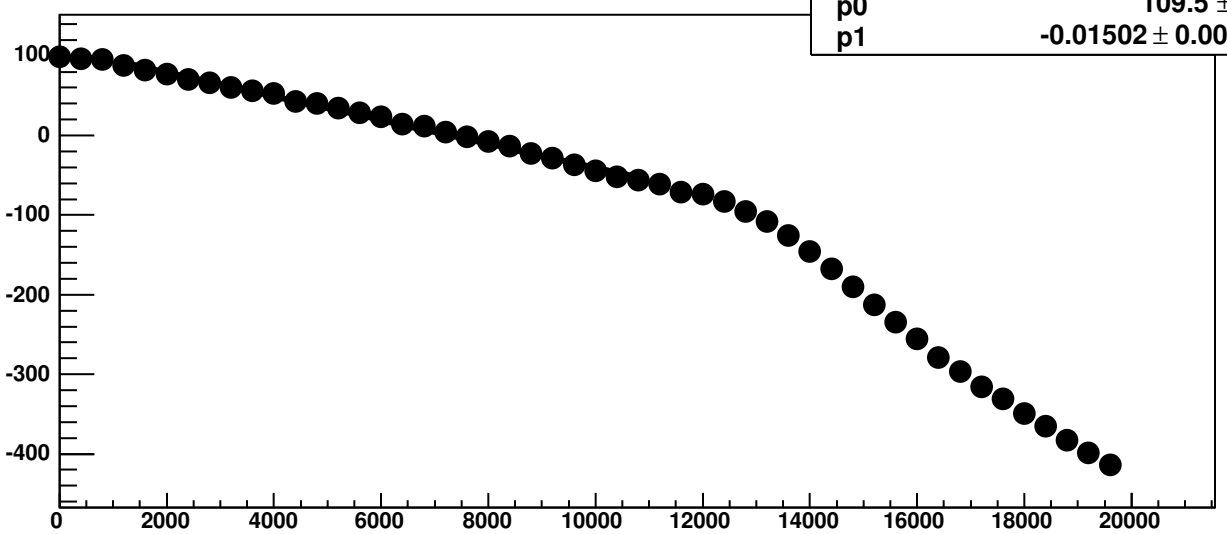

Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.27 / 23
p0 98.89 ± 0.5848
p1 0.02074 ± 8.934e-05
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

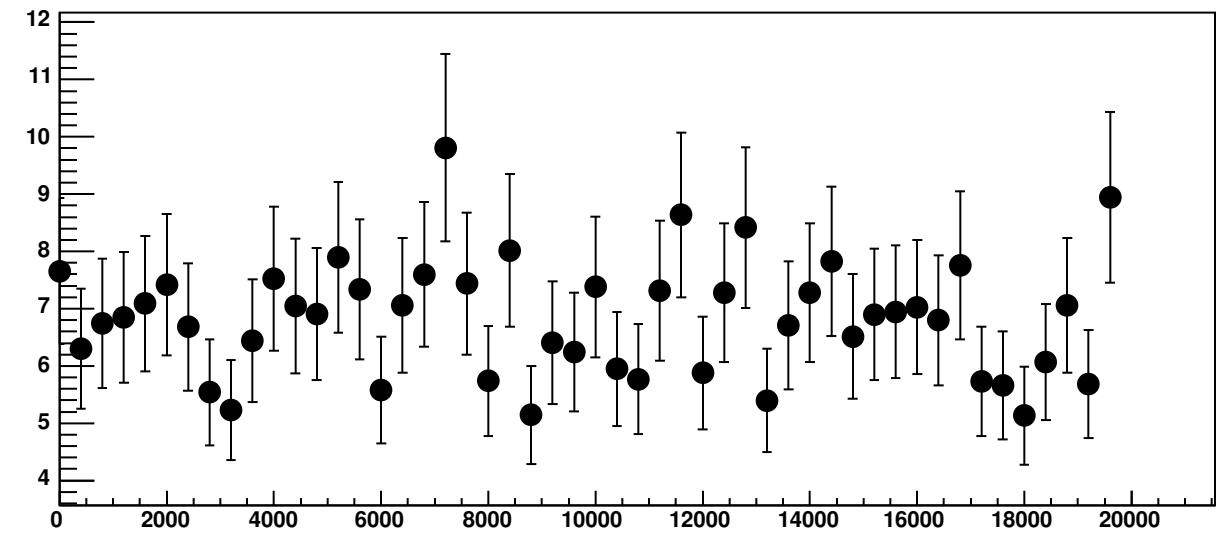

Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

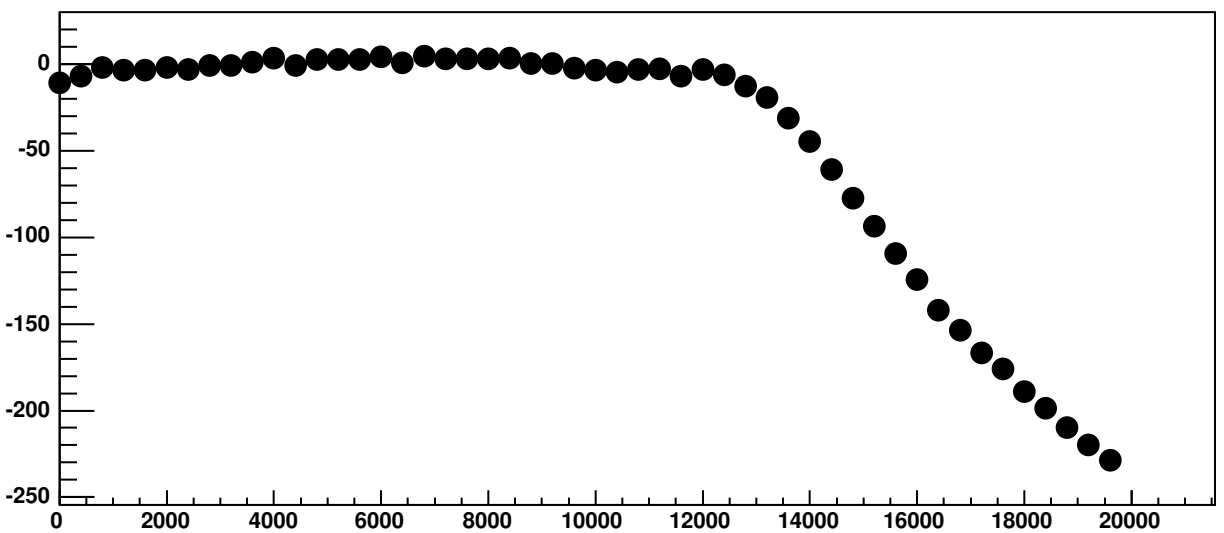
χ^2 / ndf	83.7 / 23
p0	109.5 ± 0.691
p1	-0.01502 ± 0.0001018



Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

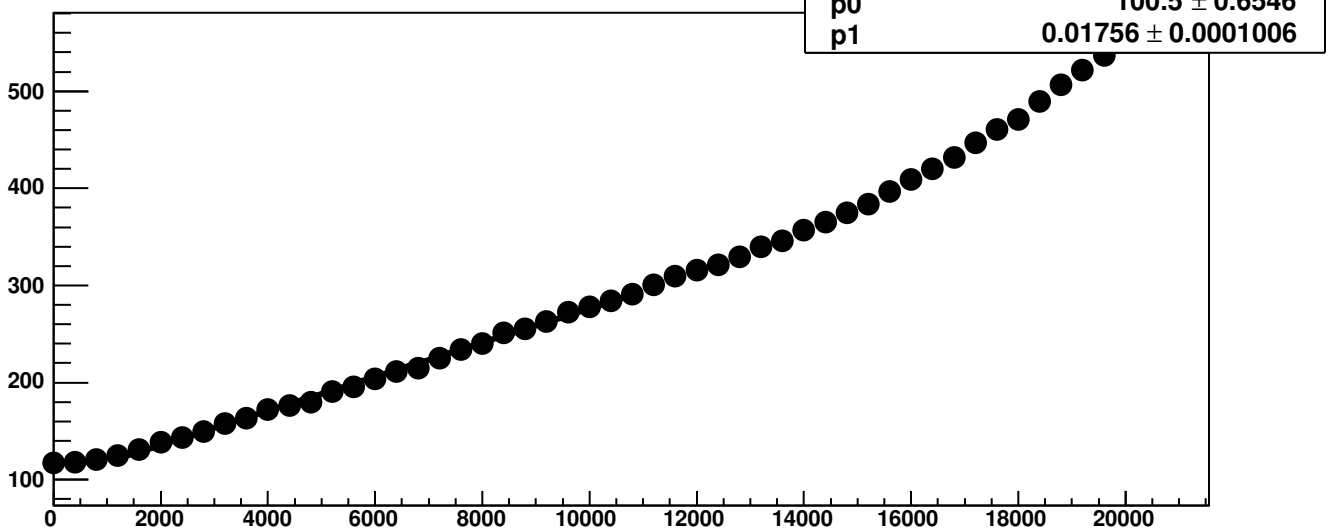


Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

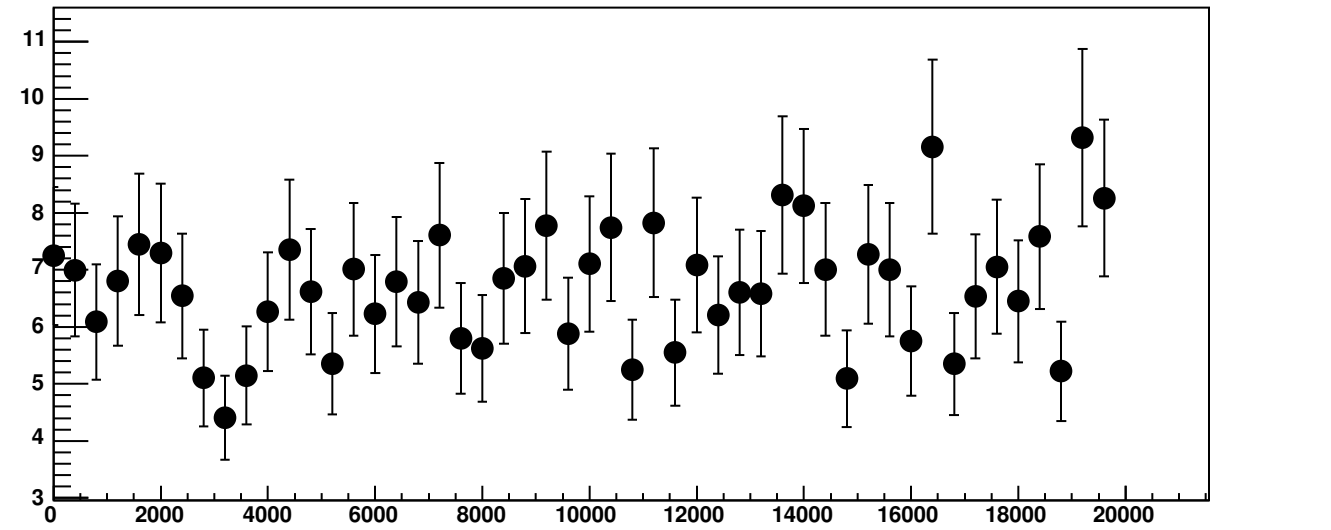


Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

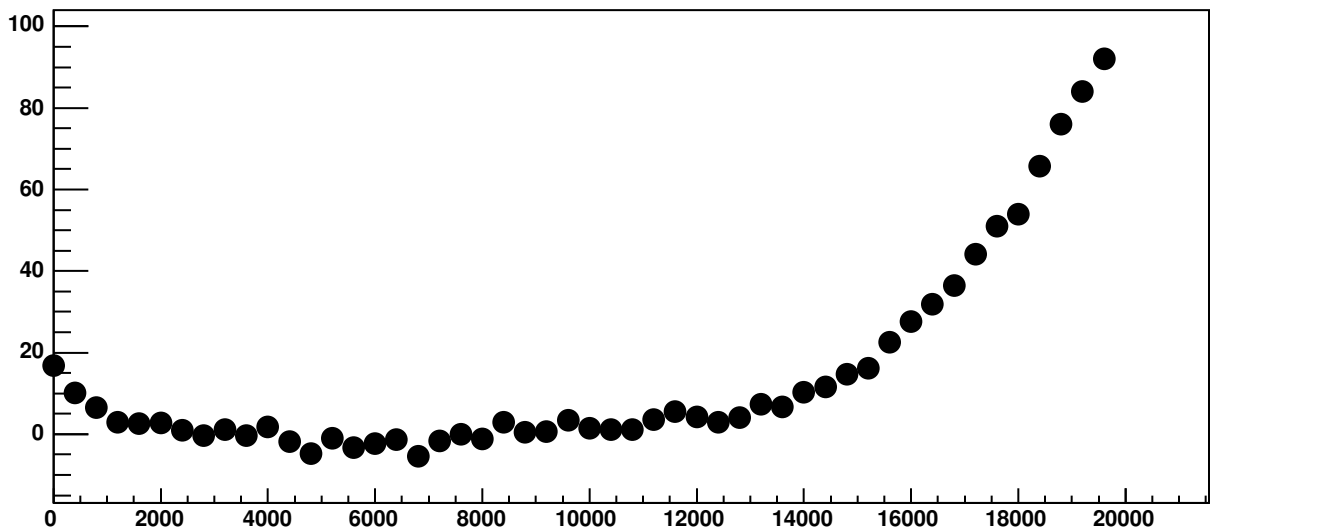
χ^2 / ndf	57.22 / 23
p0	100.5 ± 0.6546
p1	0.01756 ± 0.0001006



Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC

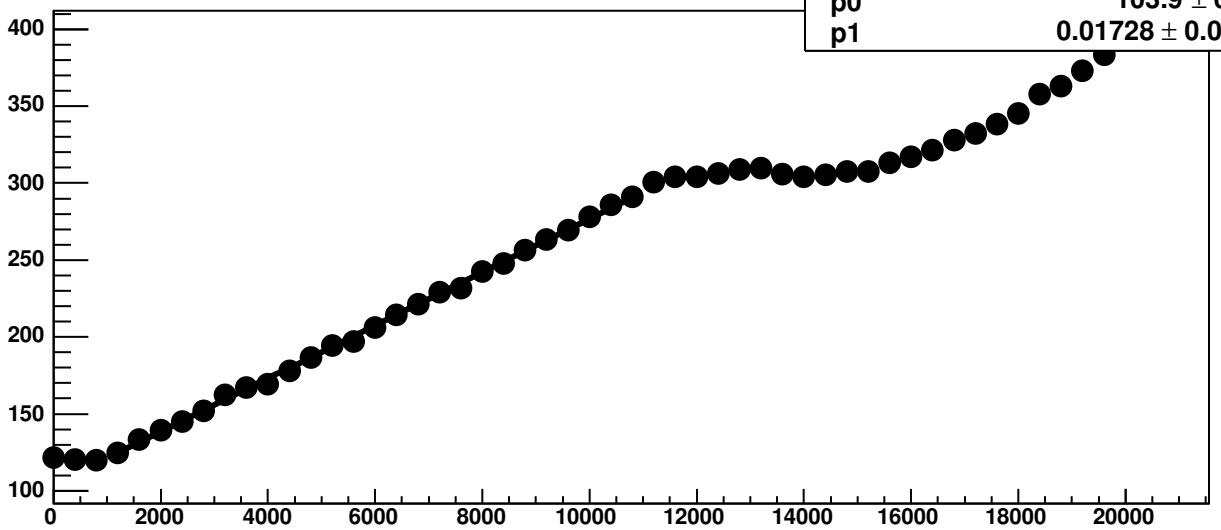


Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

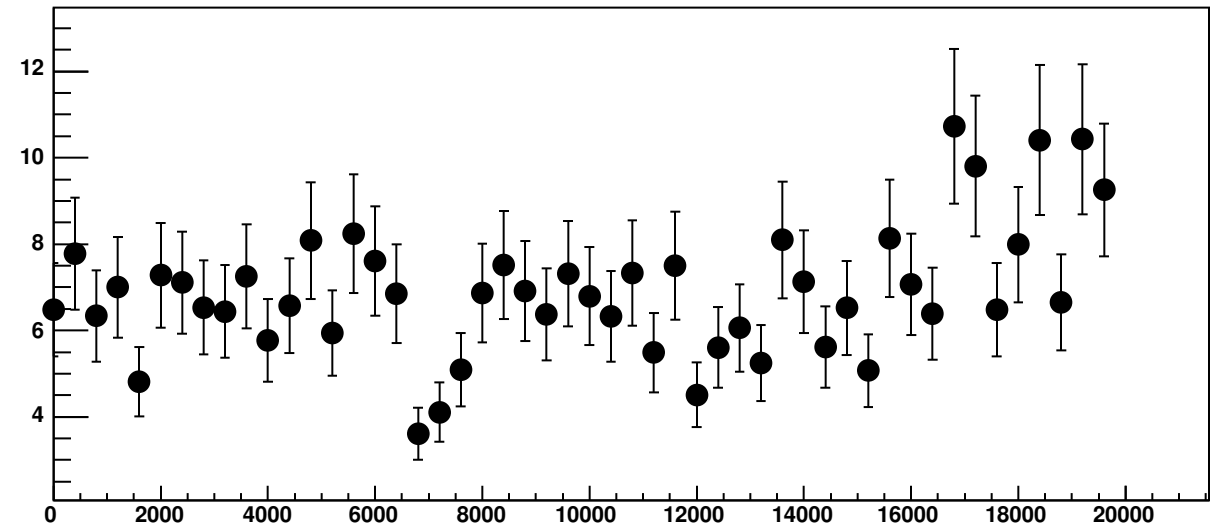


Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

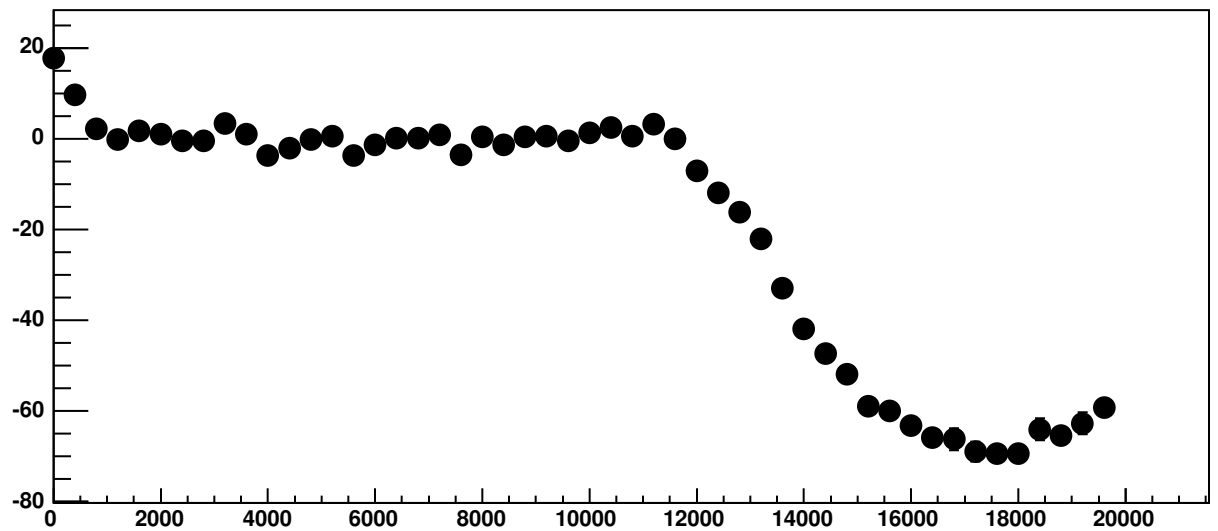
χ^2 / ndf	37.74 / 23
p0	103.9 ± 0.6809
p1	0.01728 ± 0.000103



Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

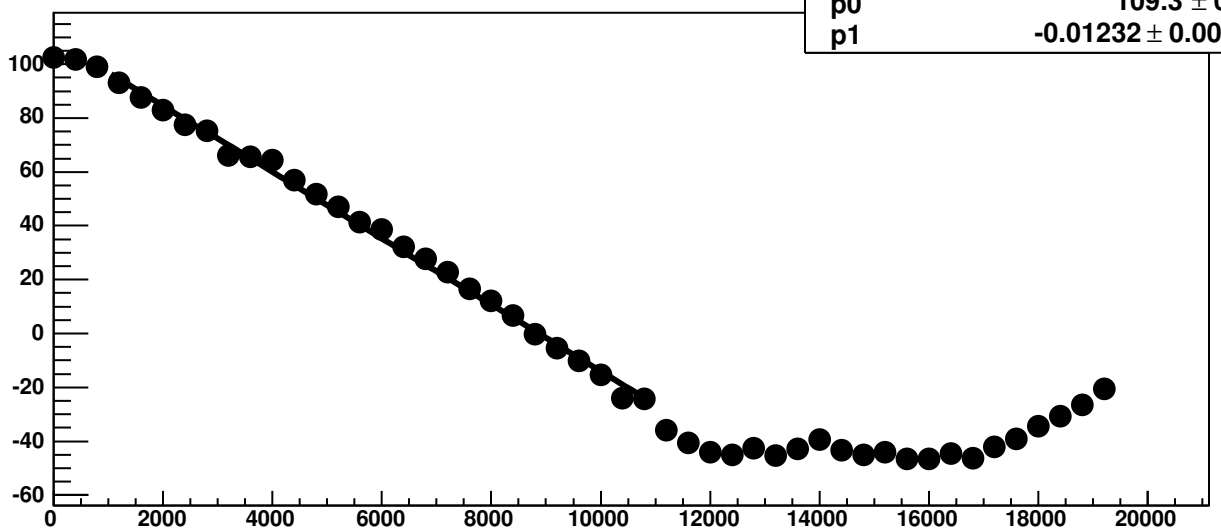


Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

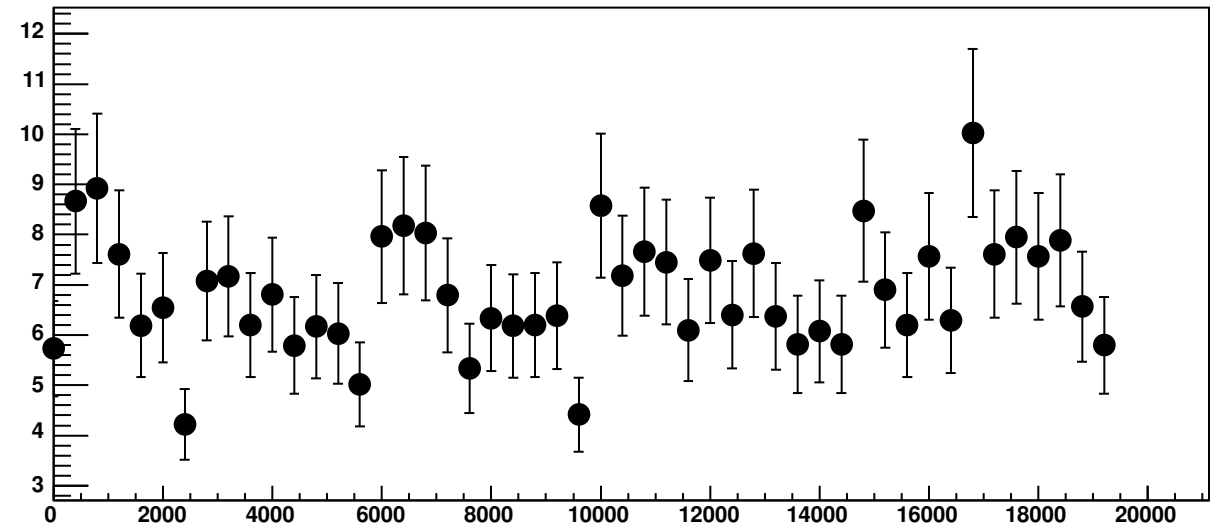


Chip 4, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

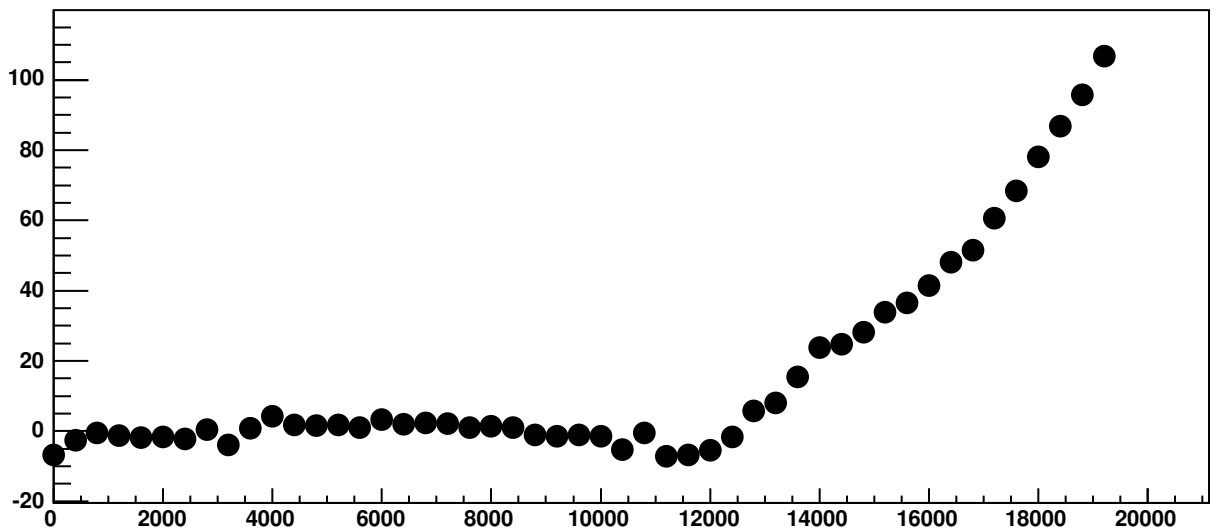
χ^2 / ndf	50.68 / 23
p0	109.3 ± 0.6585
p1	-0.01232 ± 0.0001004

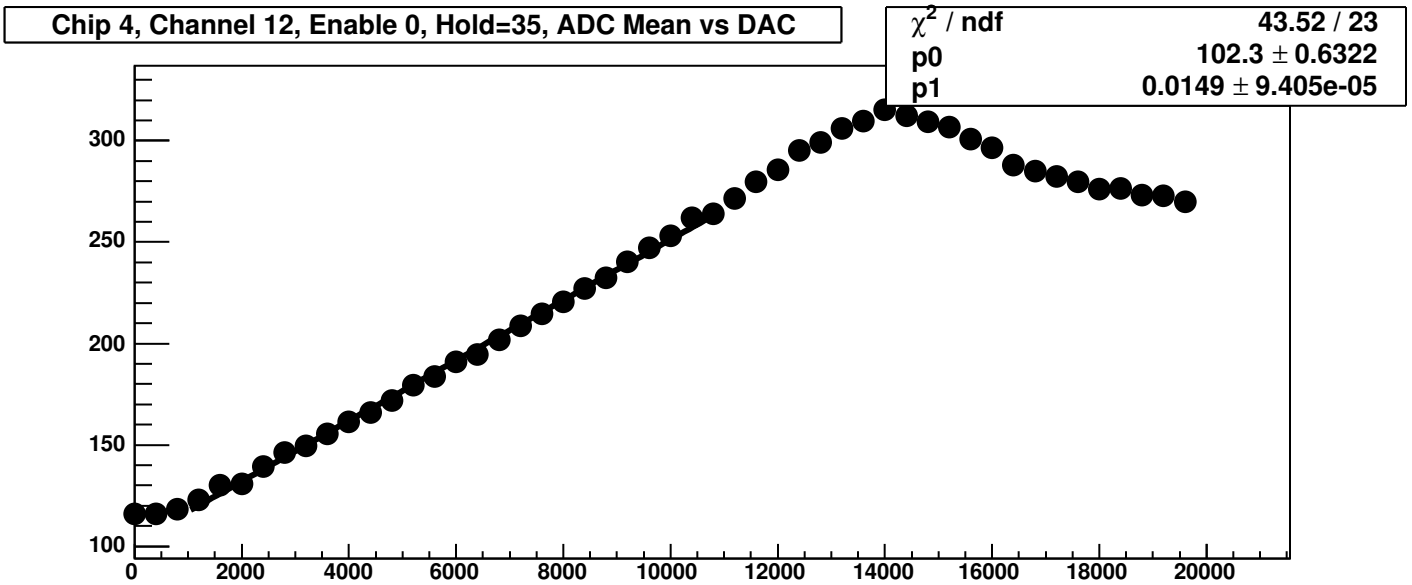


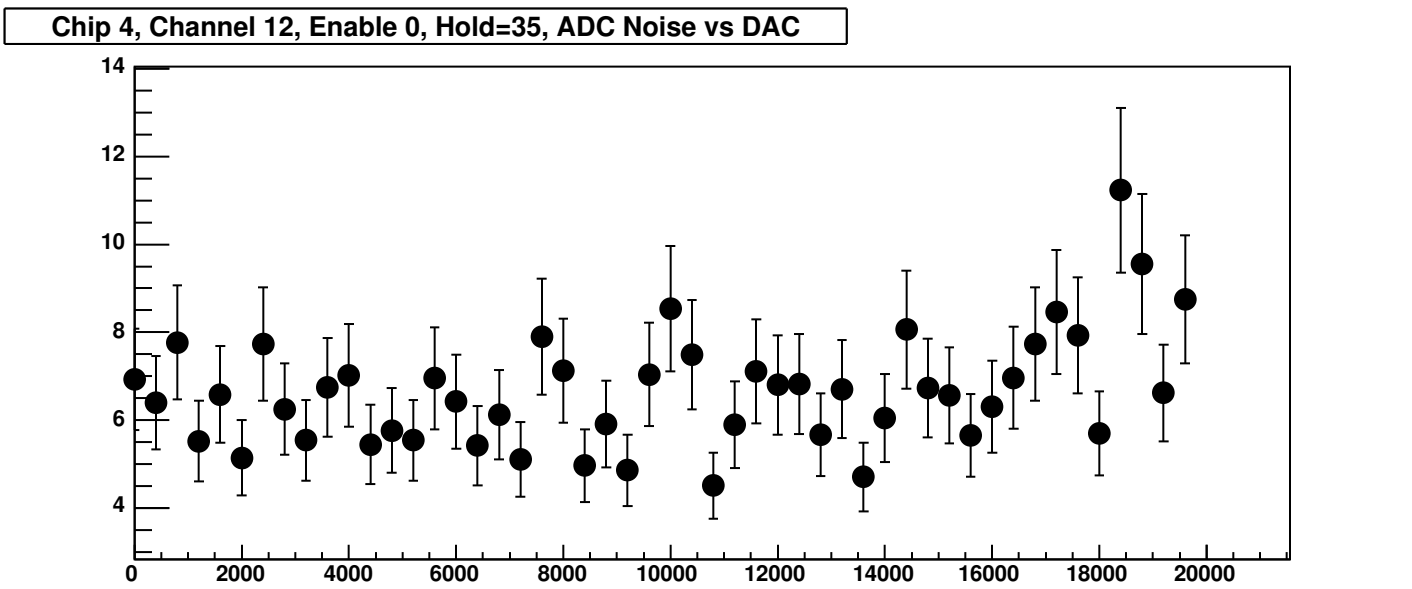
Chip 4, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

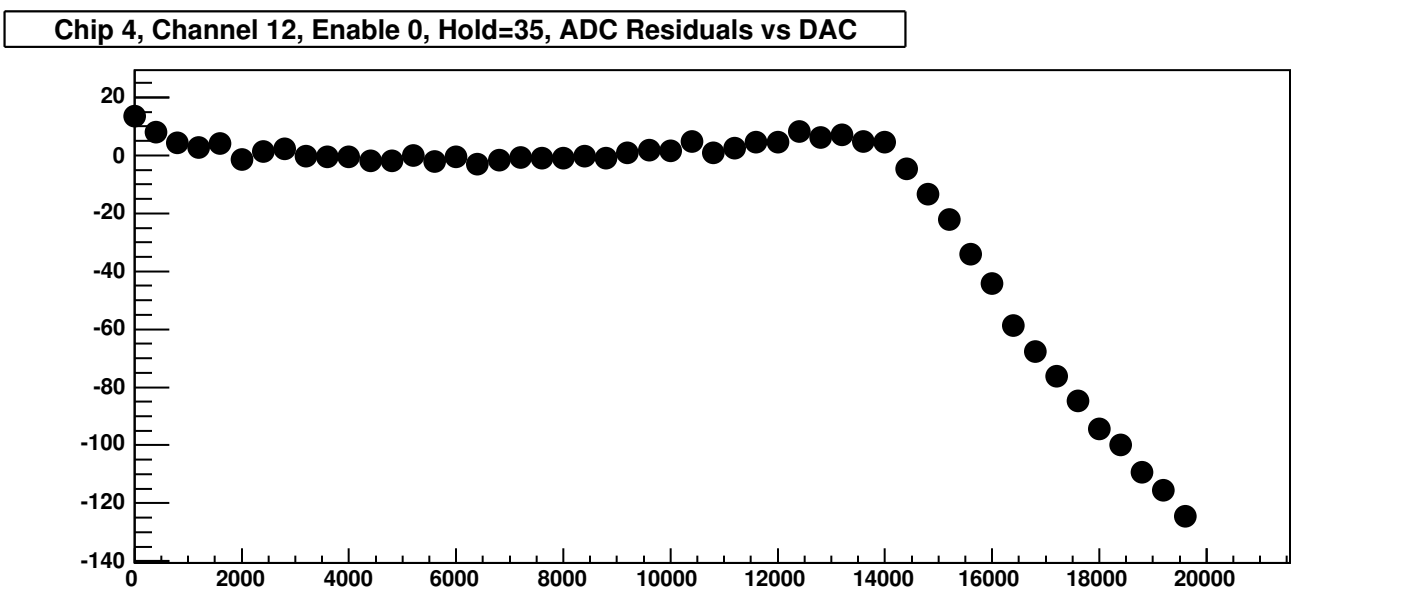


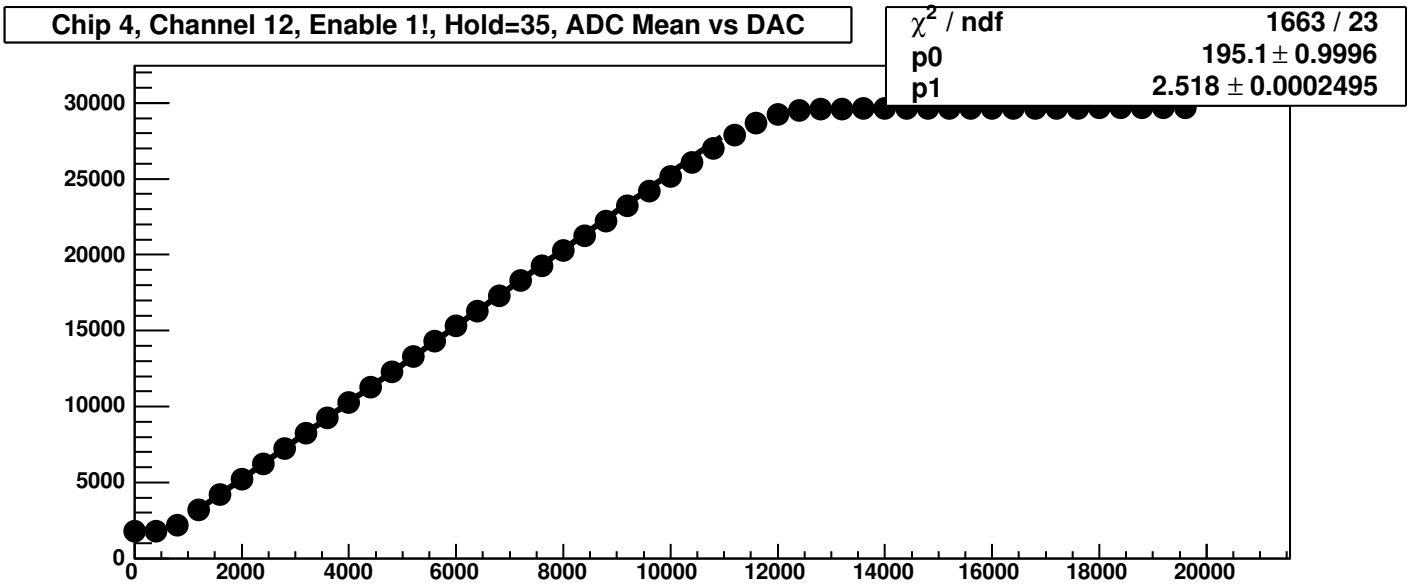
Chip 4, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

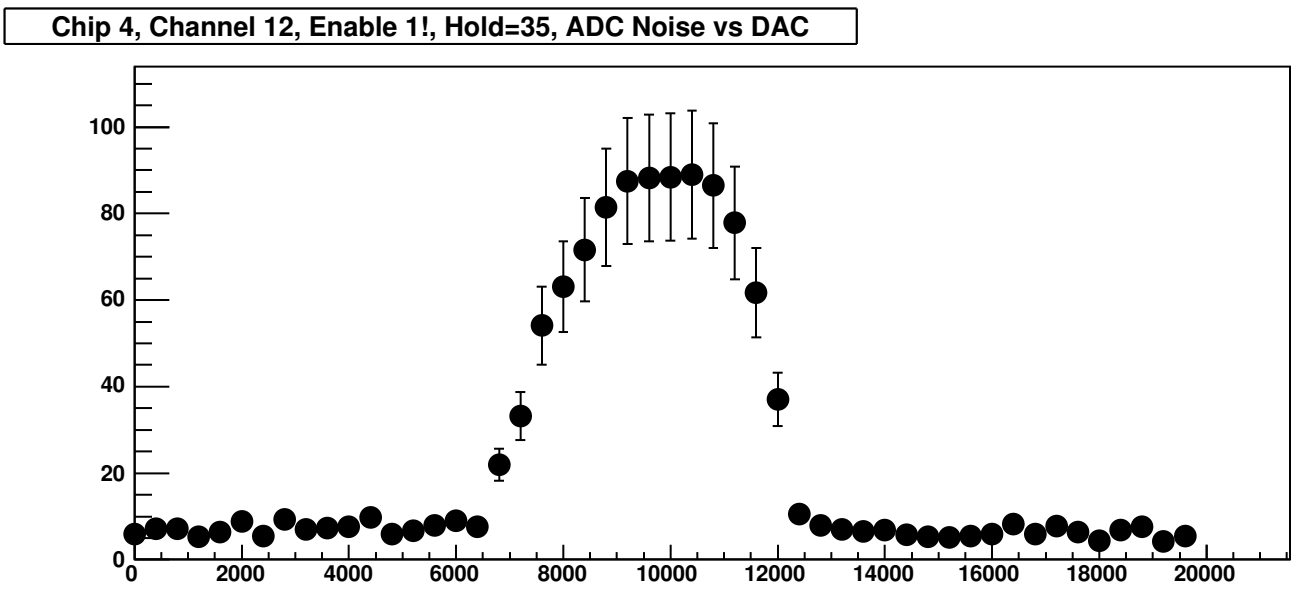


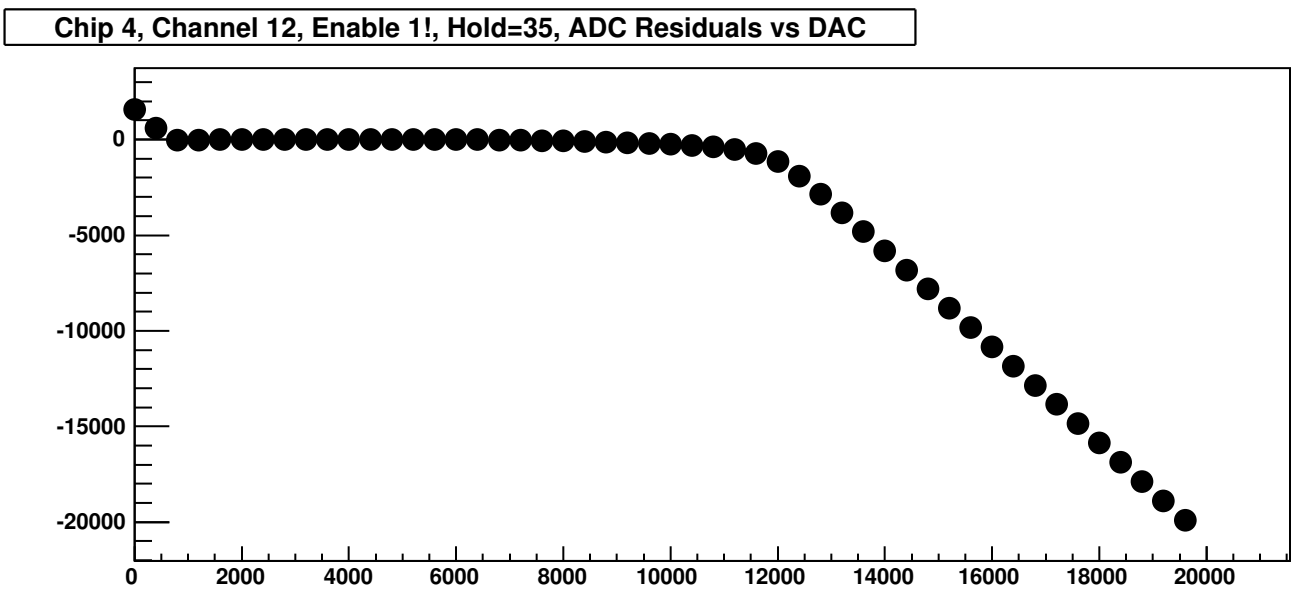

Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 43.52 / 23
p0 102.3 ± 0.6322
p1 0.0149 ± 9.405e-05
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

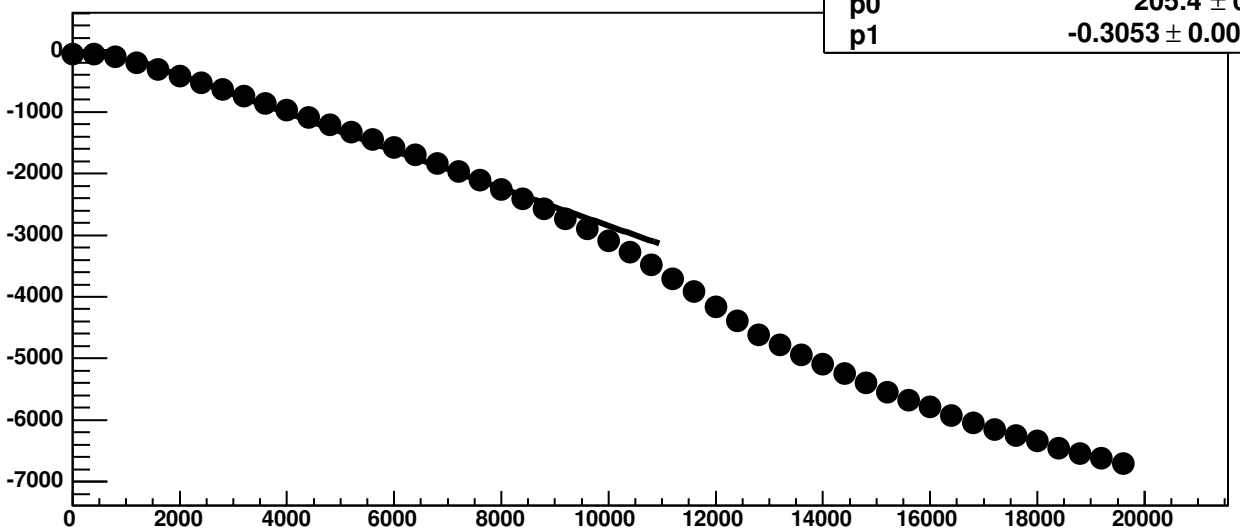

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1663 / 23
p0 195.1 ± 0.9996
p1 2.518 ± 0.0002495
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

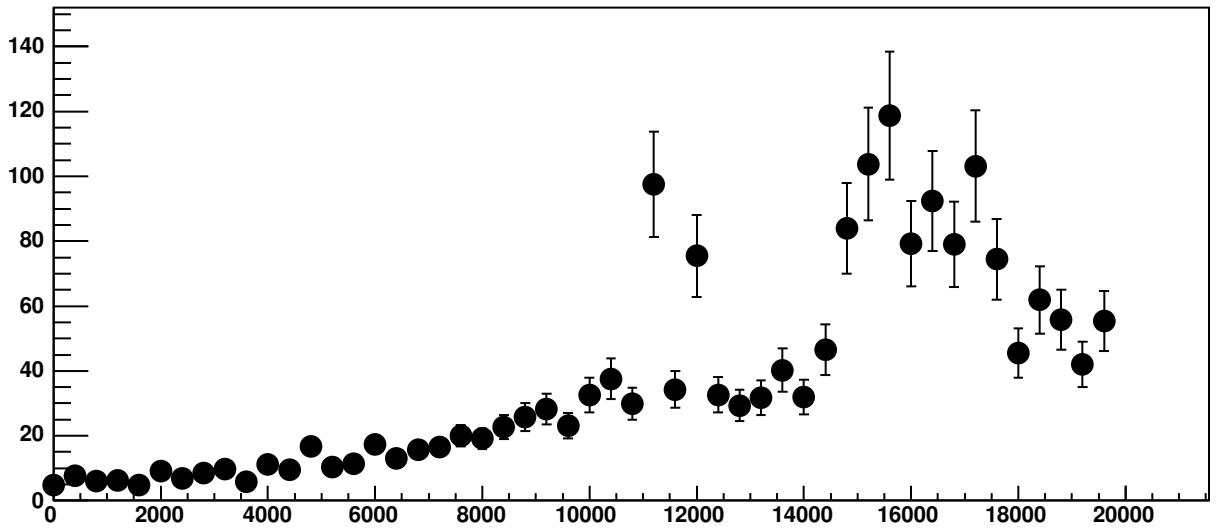

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

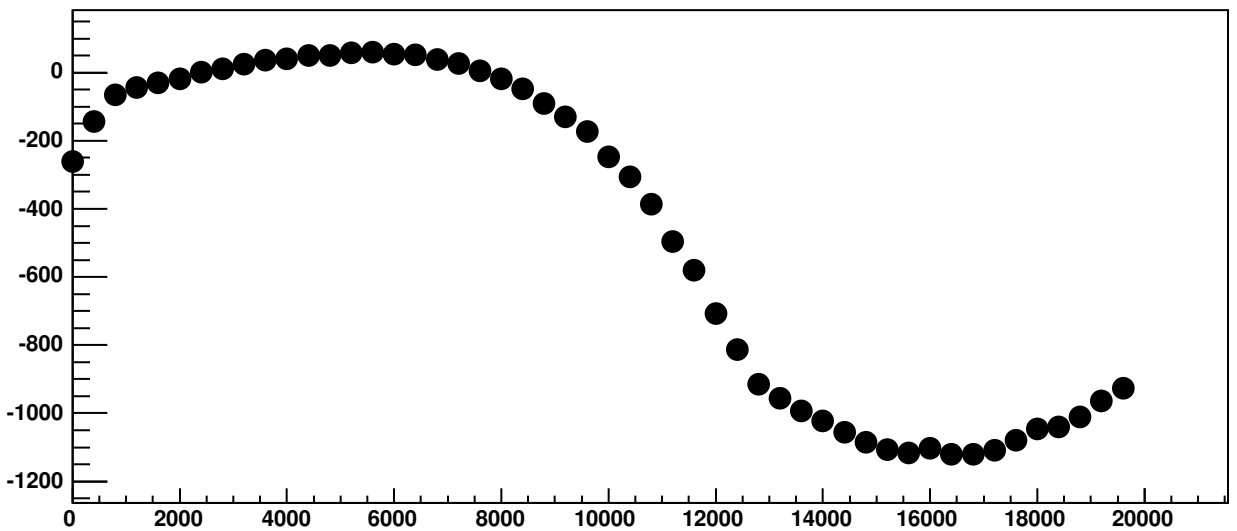
χ^2 / ndf	1.281e+04 / 23
p0	205.4 ± 0.9149
p1	-0.3053 ± 0.0002263



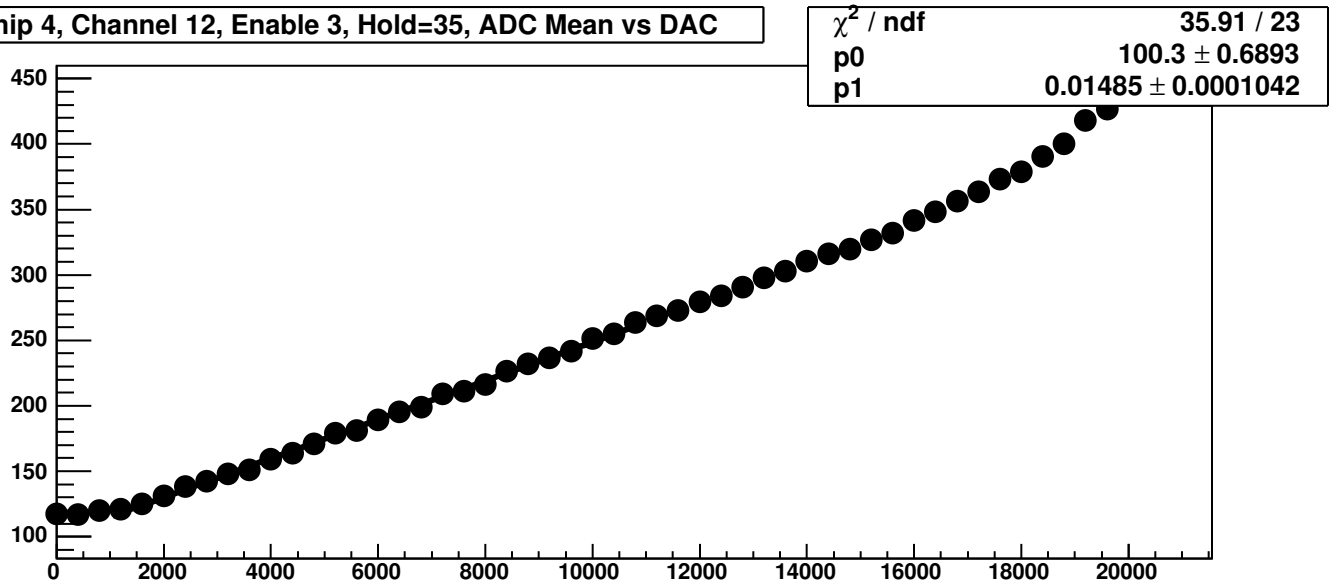
Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



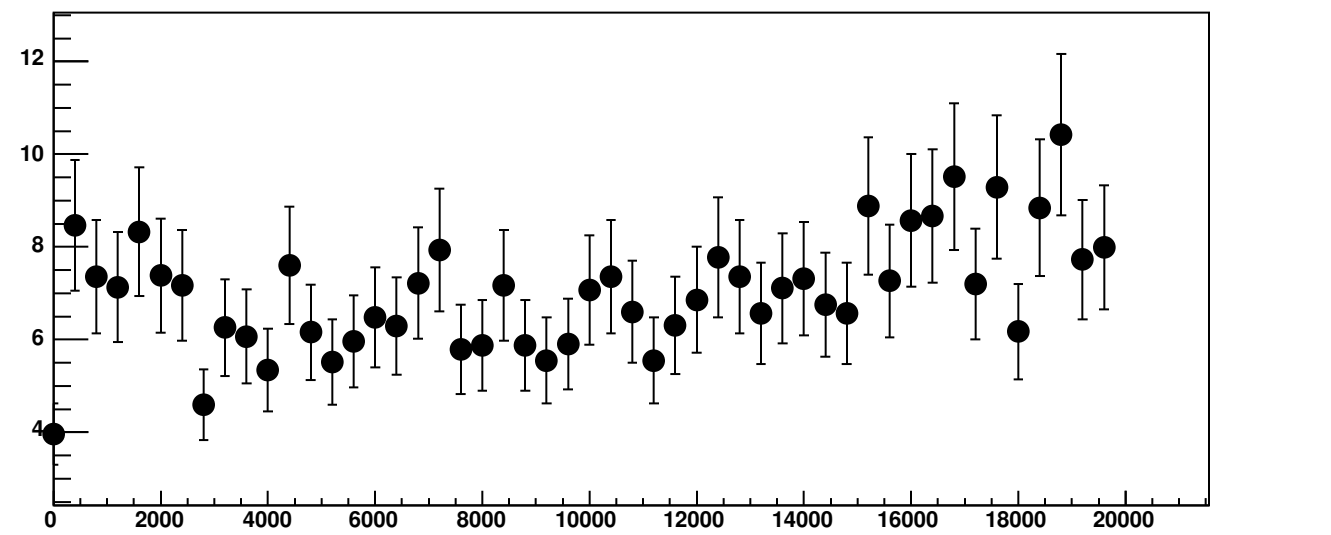
Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



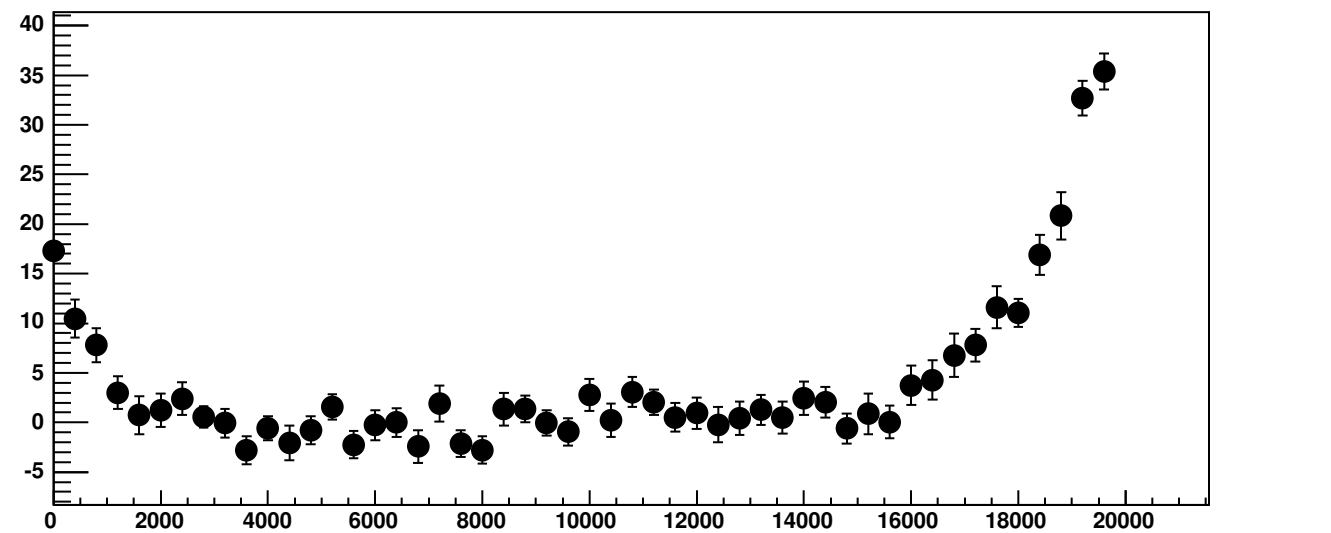
Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



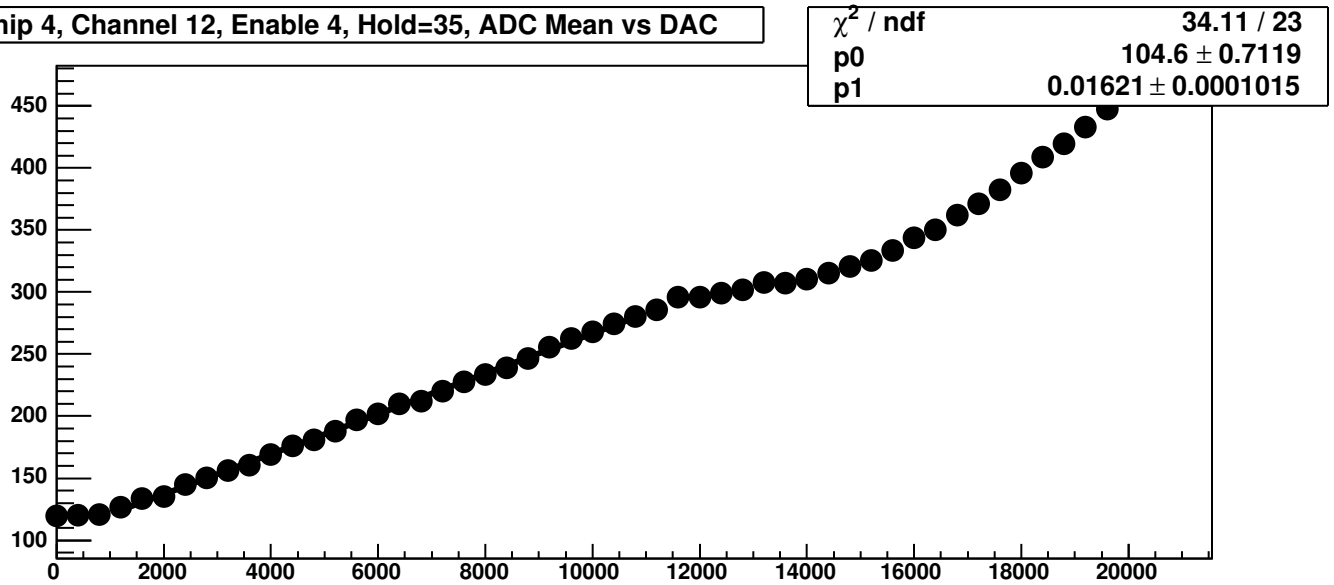
Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



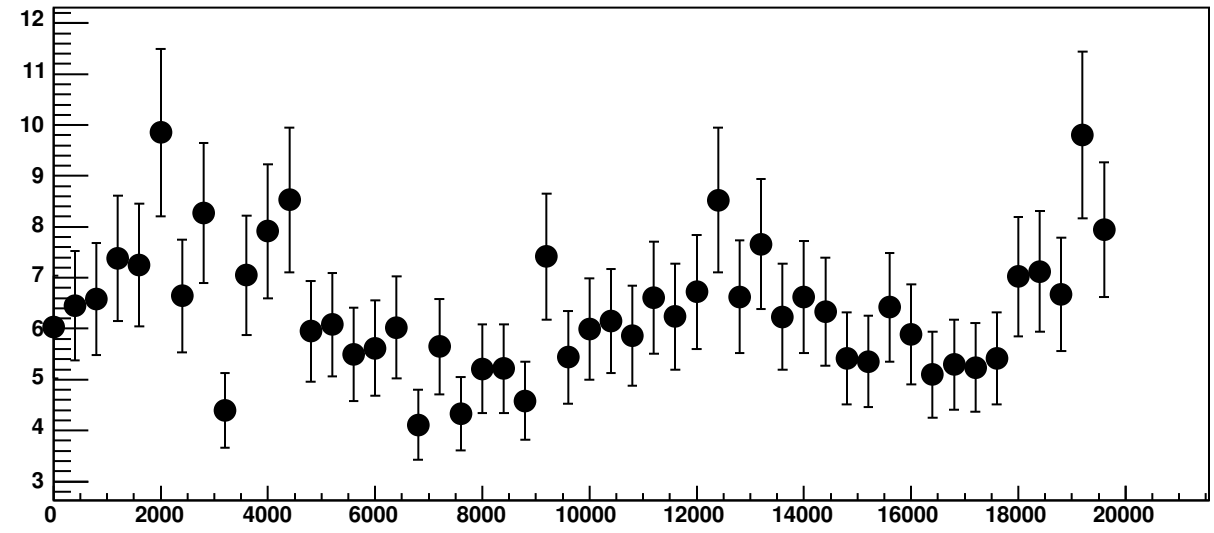
Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



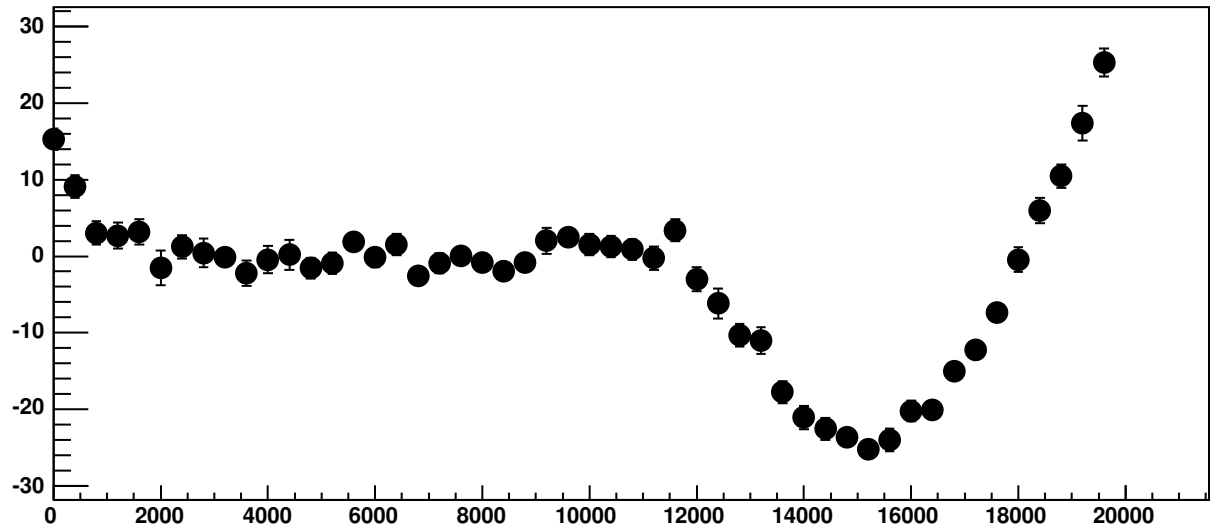
Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

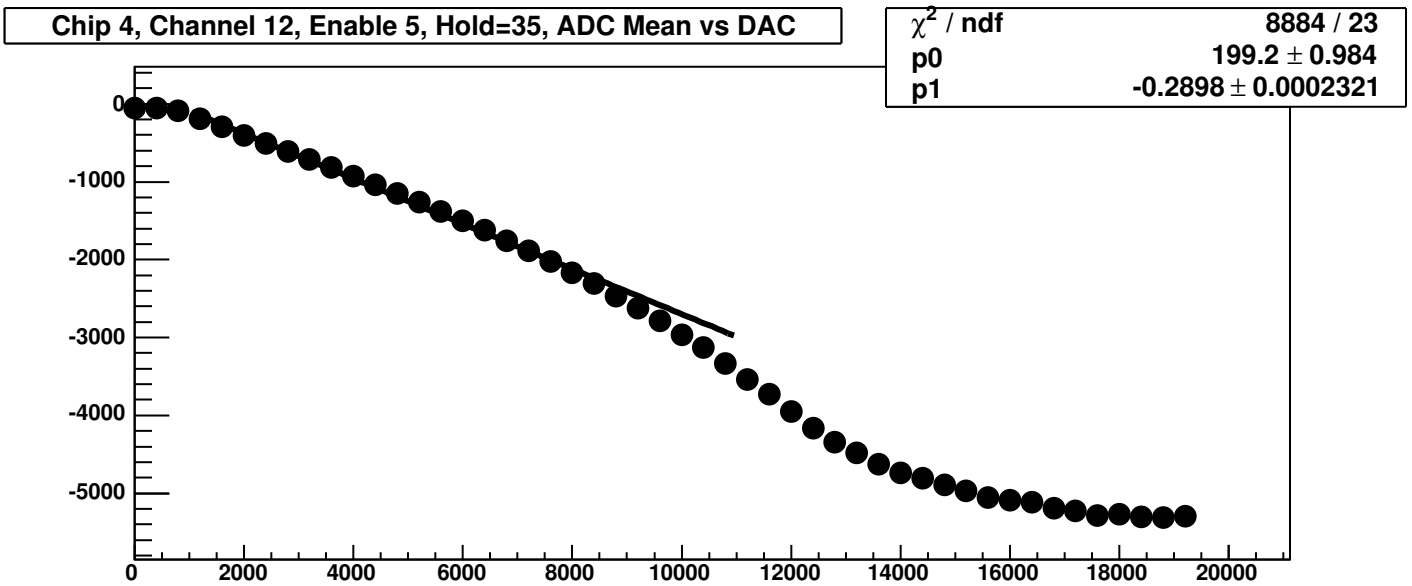


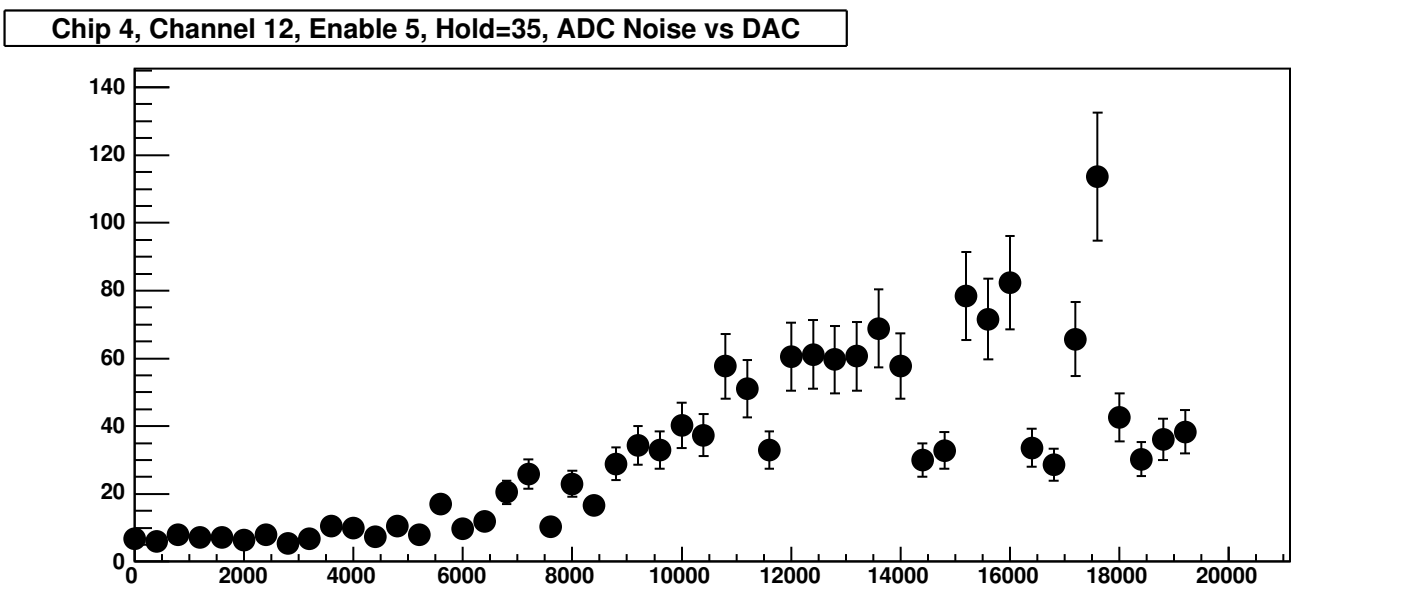
Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC

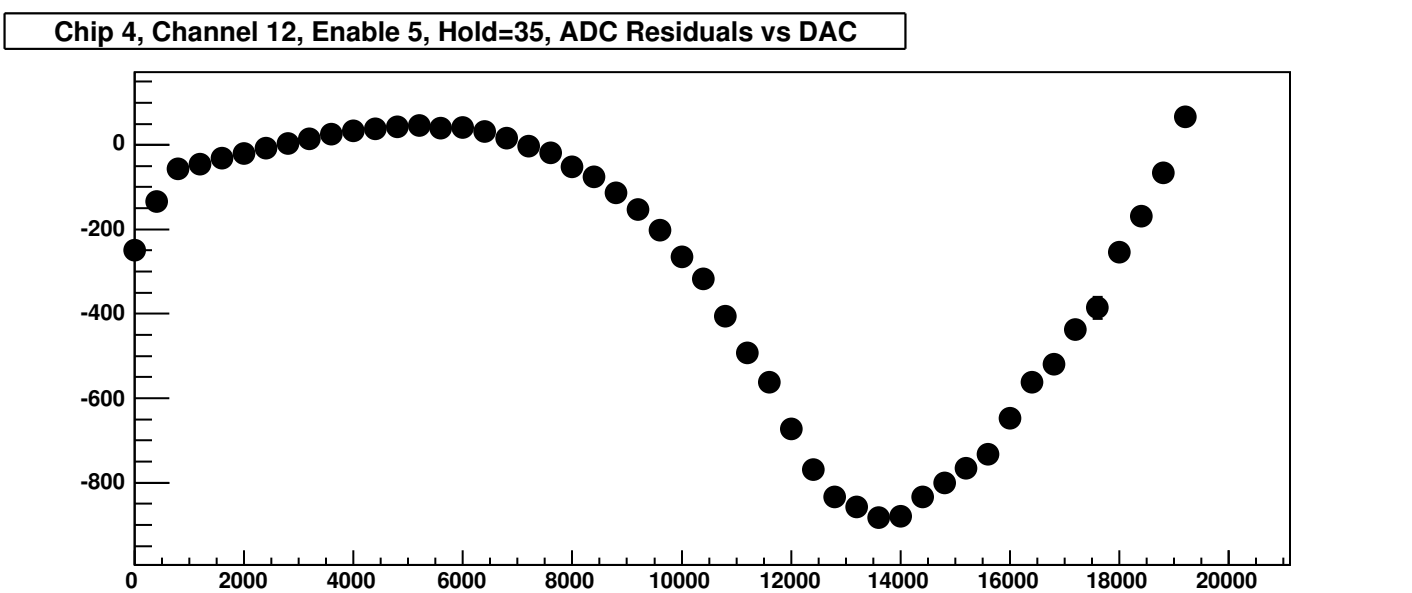


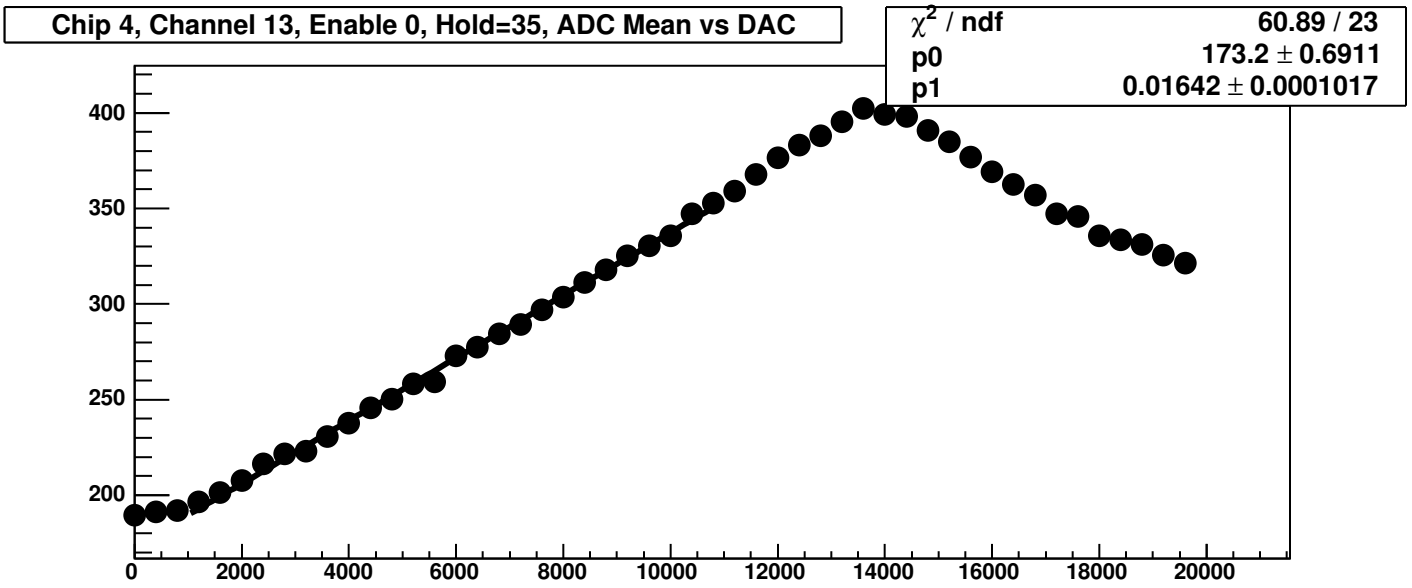
Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

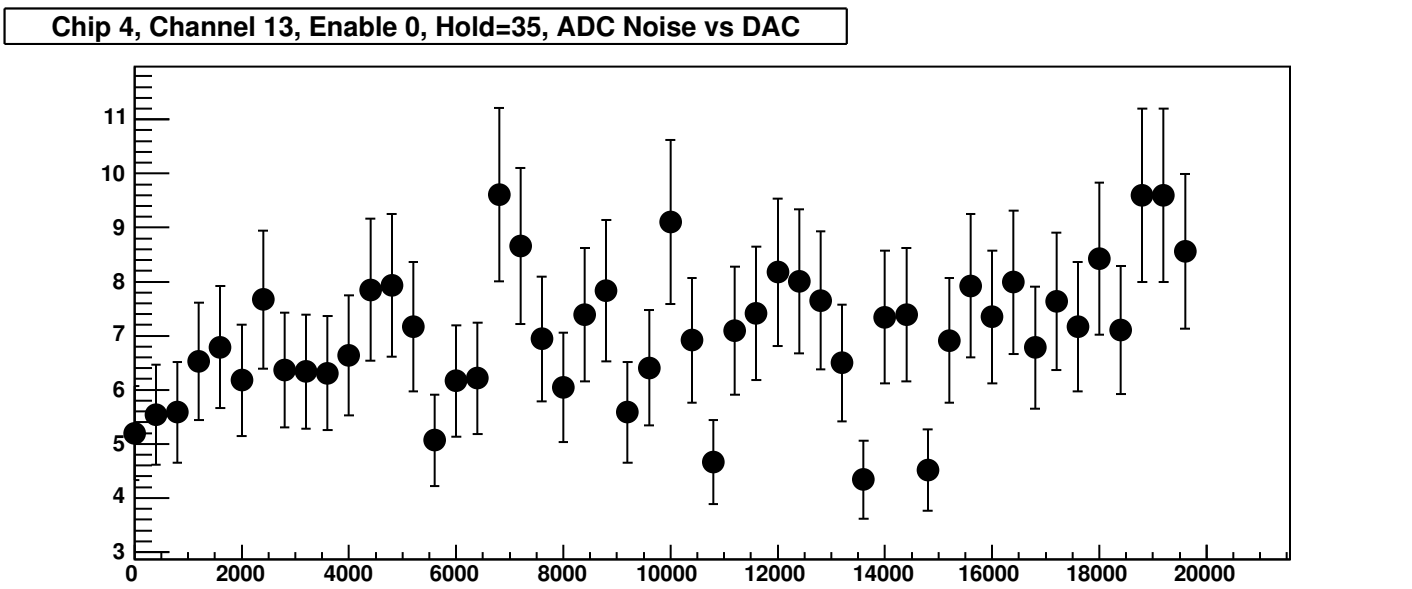


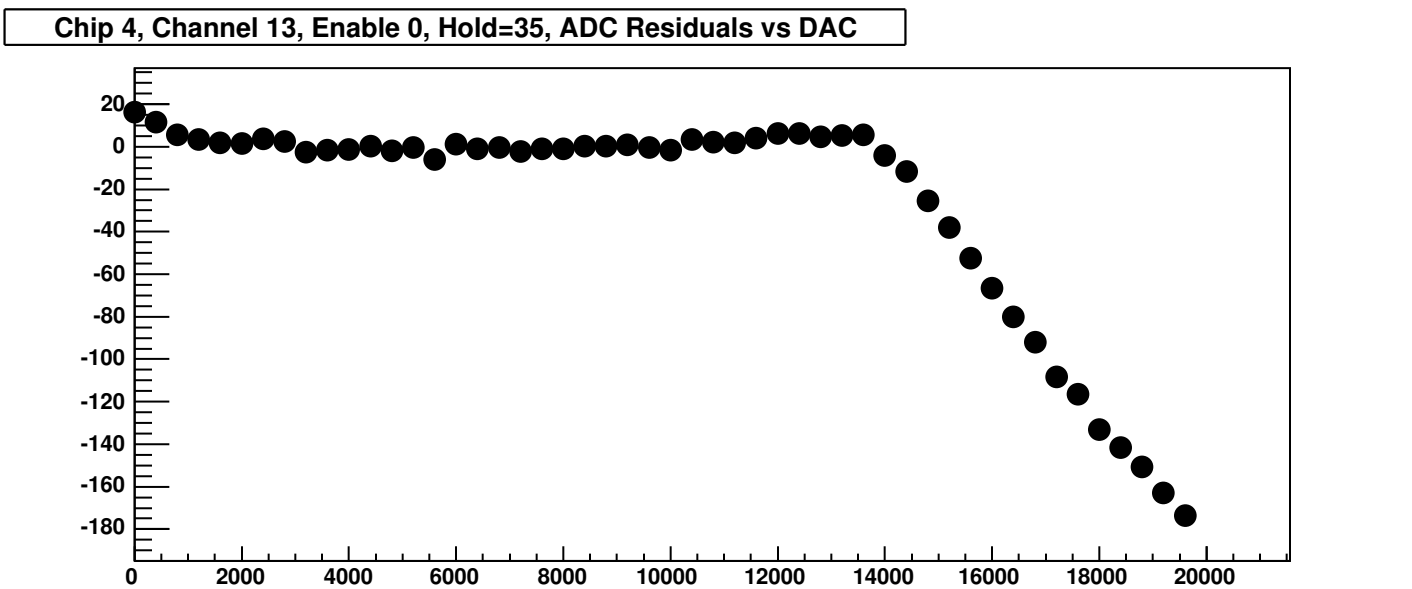

Chip 4, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 8884 / 23
p0 199.2 ± 0.984
p1 -0.2898 ± 0.0002321
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

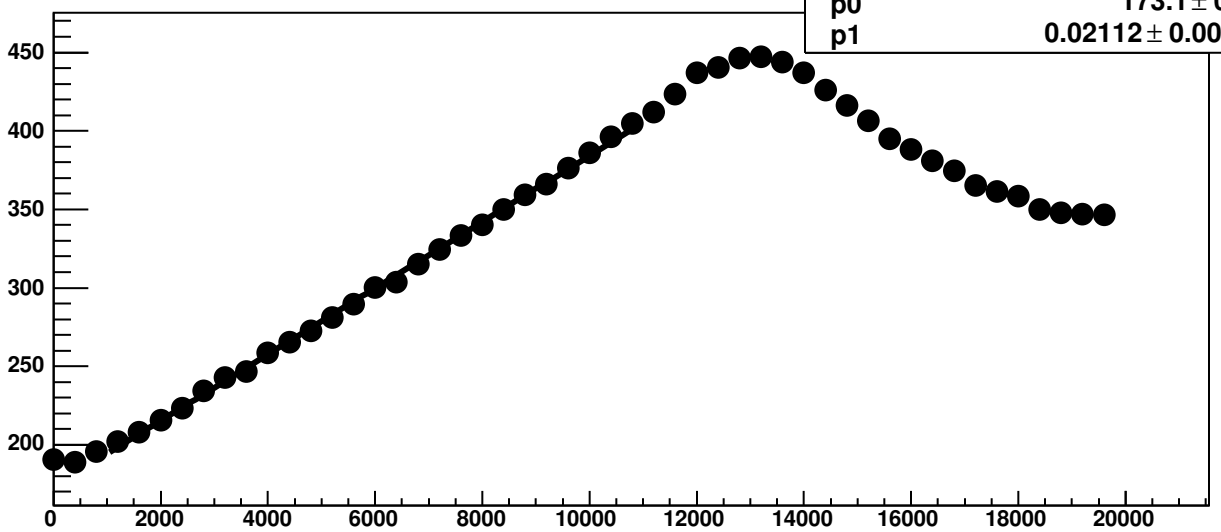

Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 60.89 / 23
p0 173.2 ± 0.6911
p1 0.01642 ± 0.0001017
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

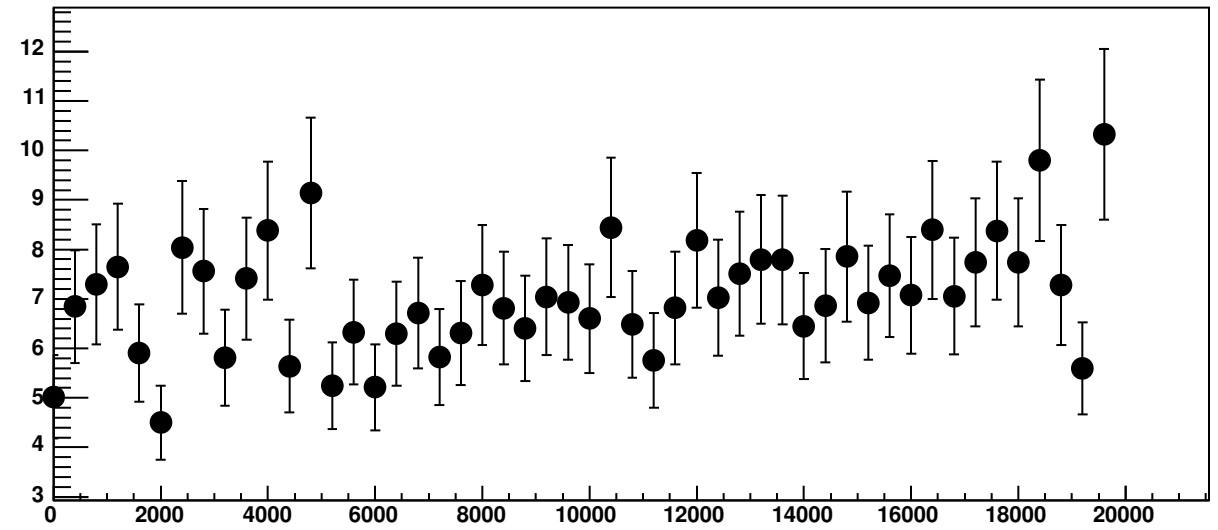

Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

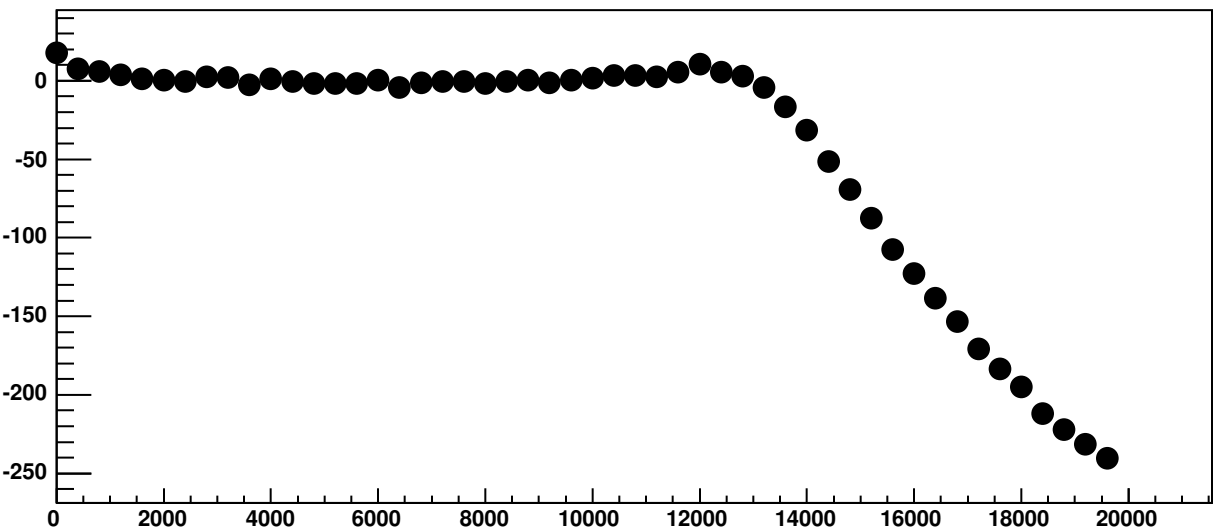
χ^2 / ndf	39.46 / 23
p0	173.1 ± 0.6778
p1	0.02112 ± 0.0001047

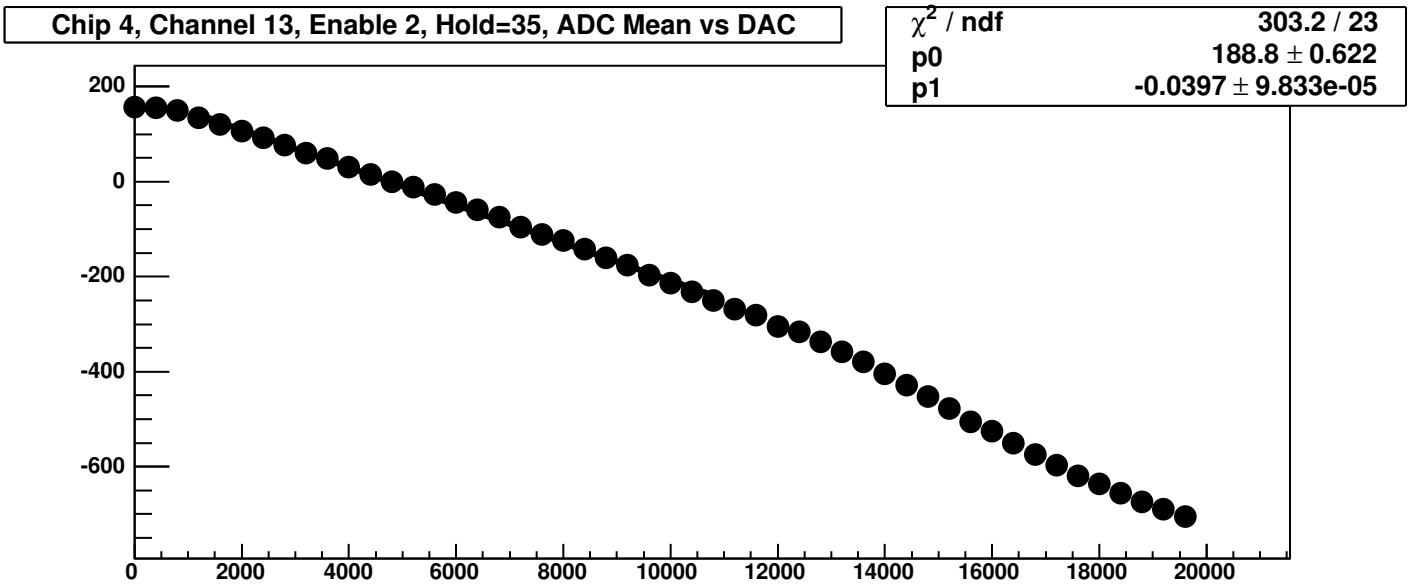


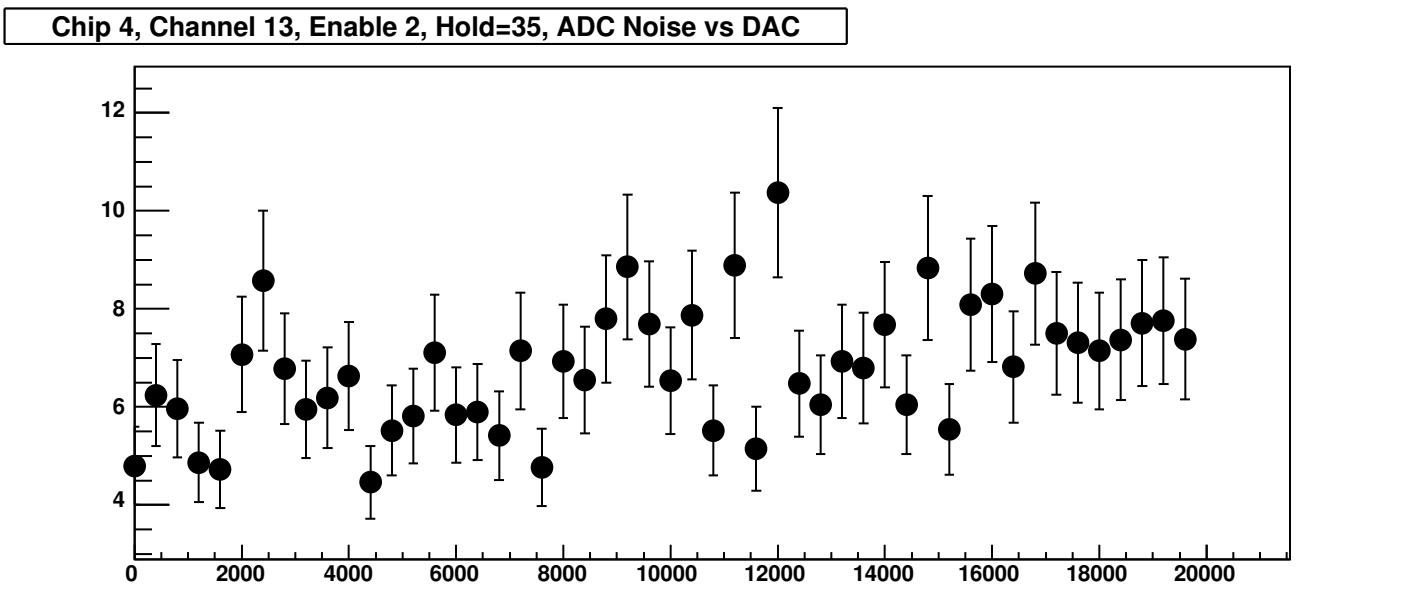
Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

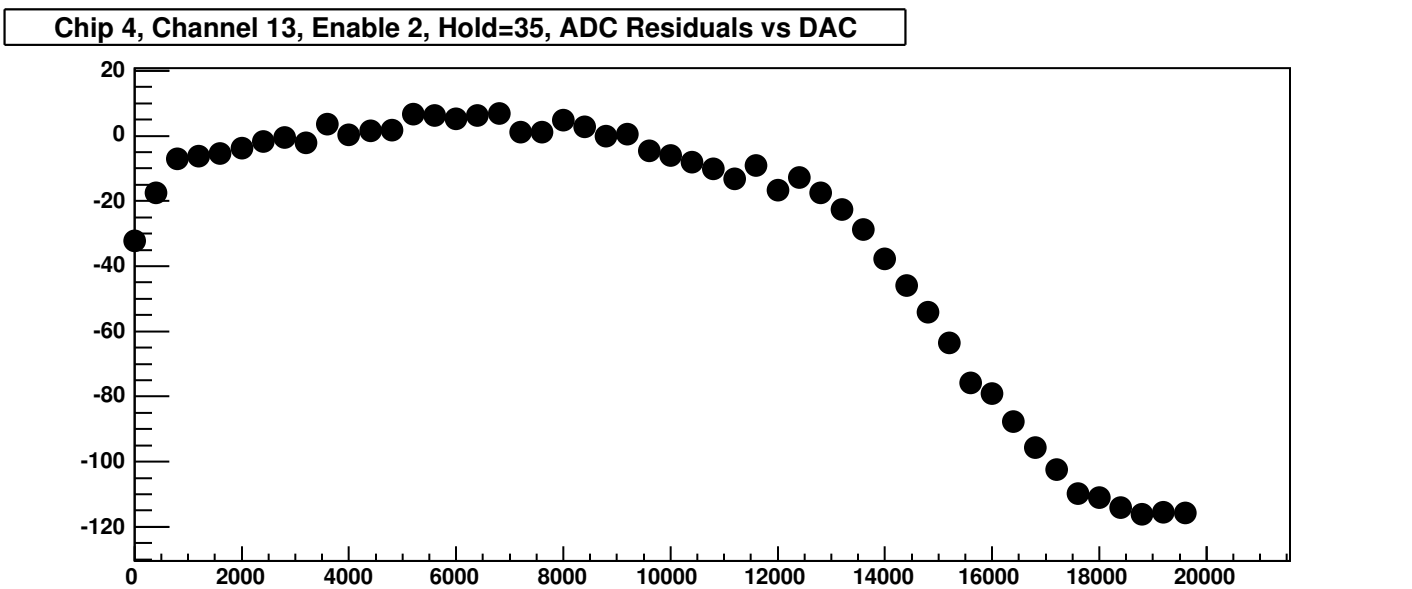


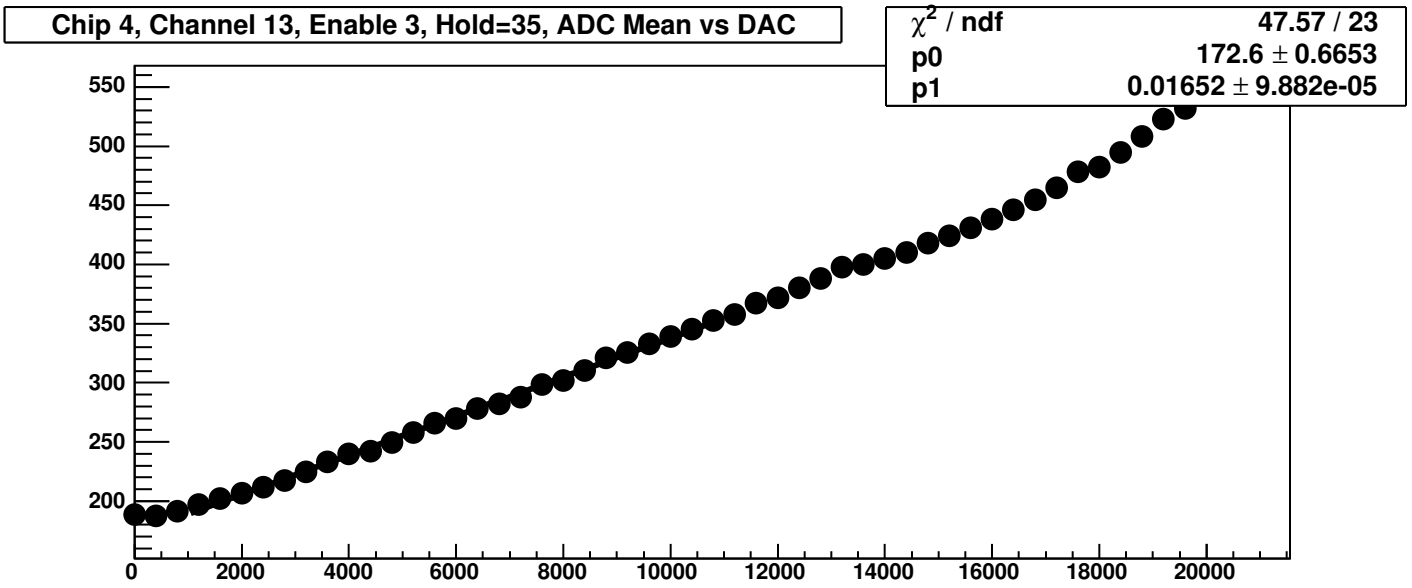
Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

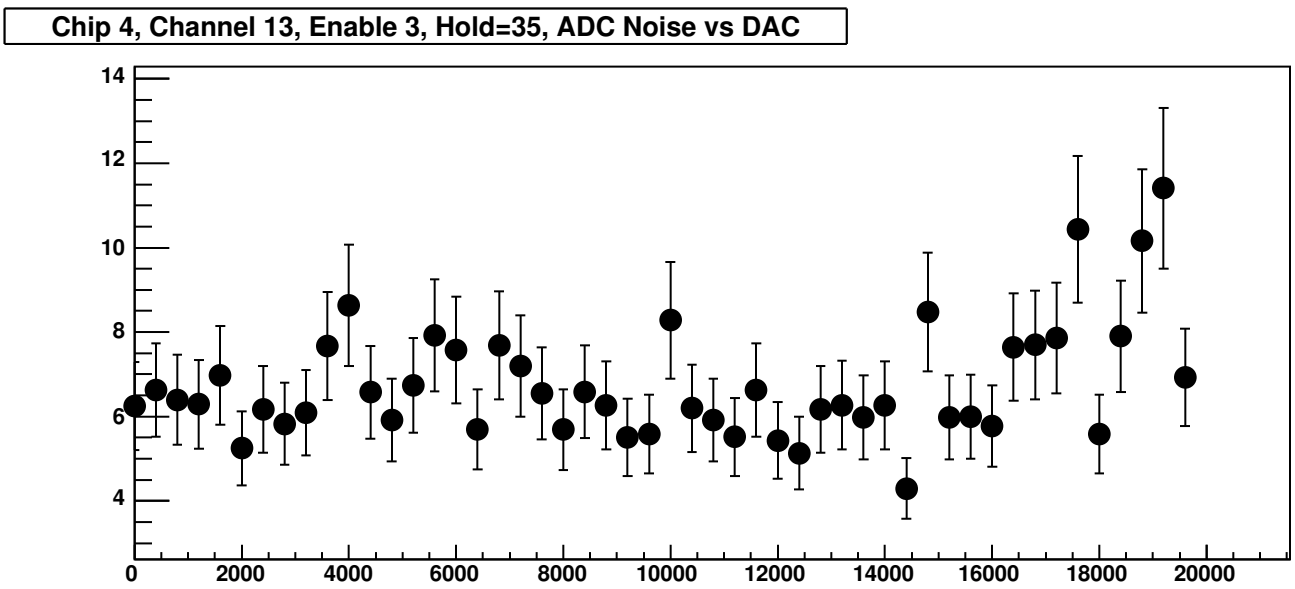


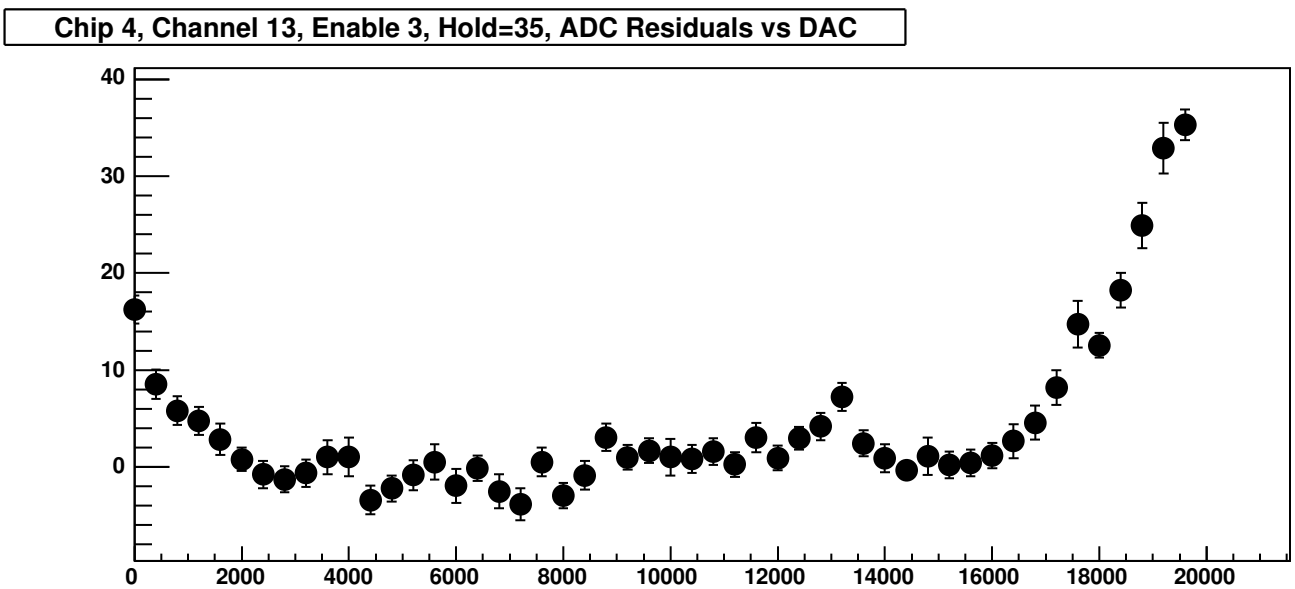

Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 303.2 / 23
p0 188.8 ± 0.622
p1 -0.0397 ± 9.833e-05
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

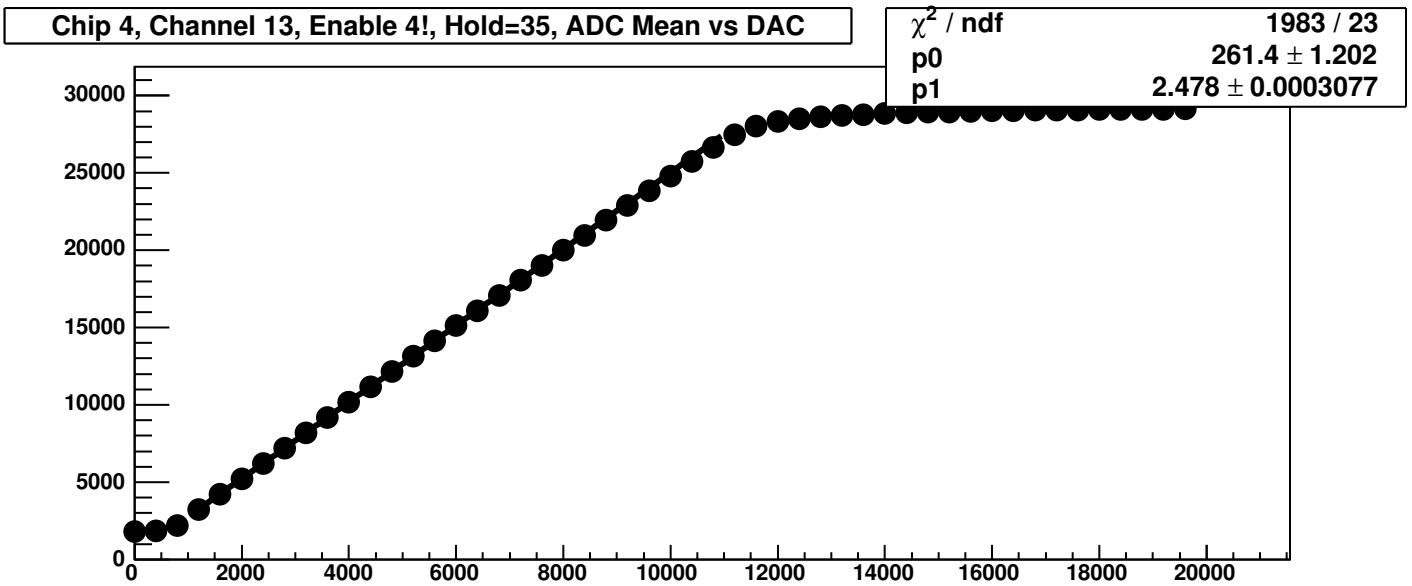

Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

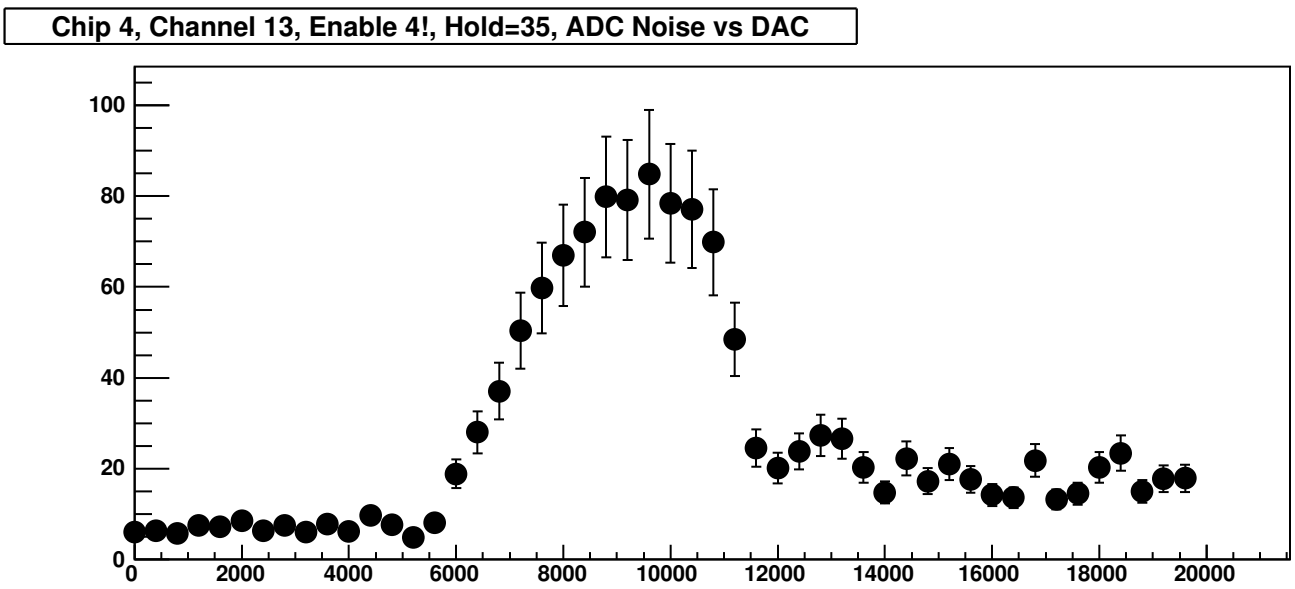

Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

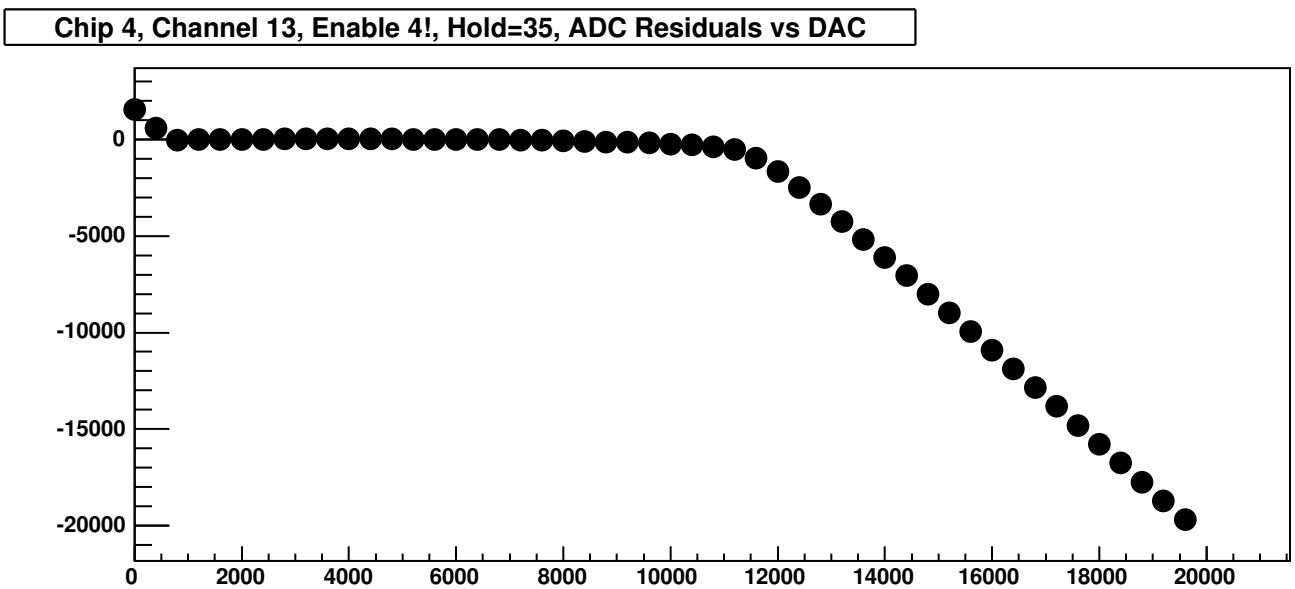

Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 47.57 / 23
p0 172.6 ± 0.6653
p1 0.01652 ± 9.882e-05
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

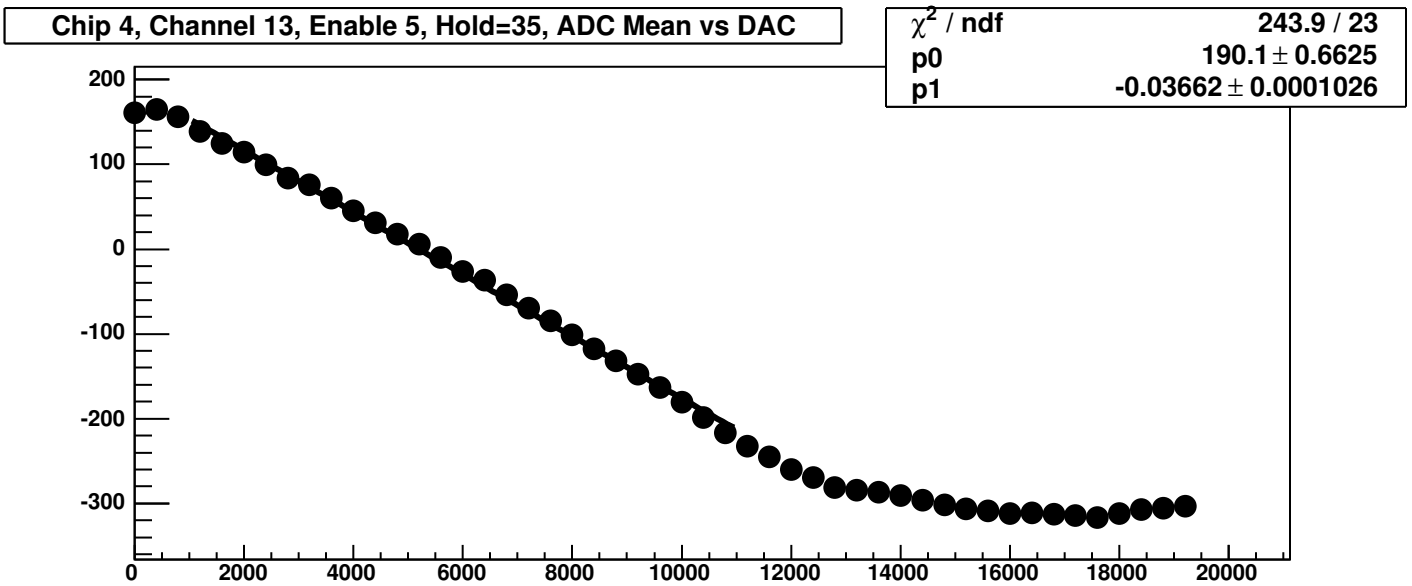

Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

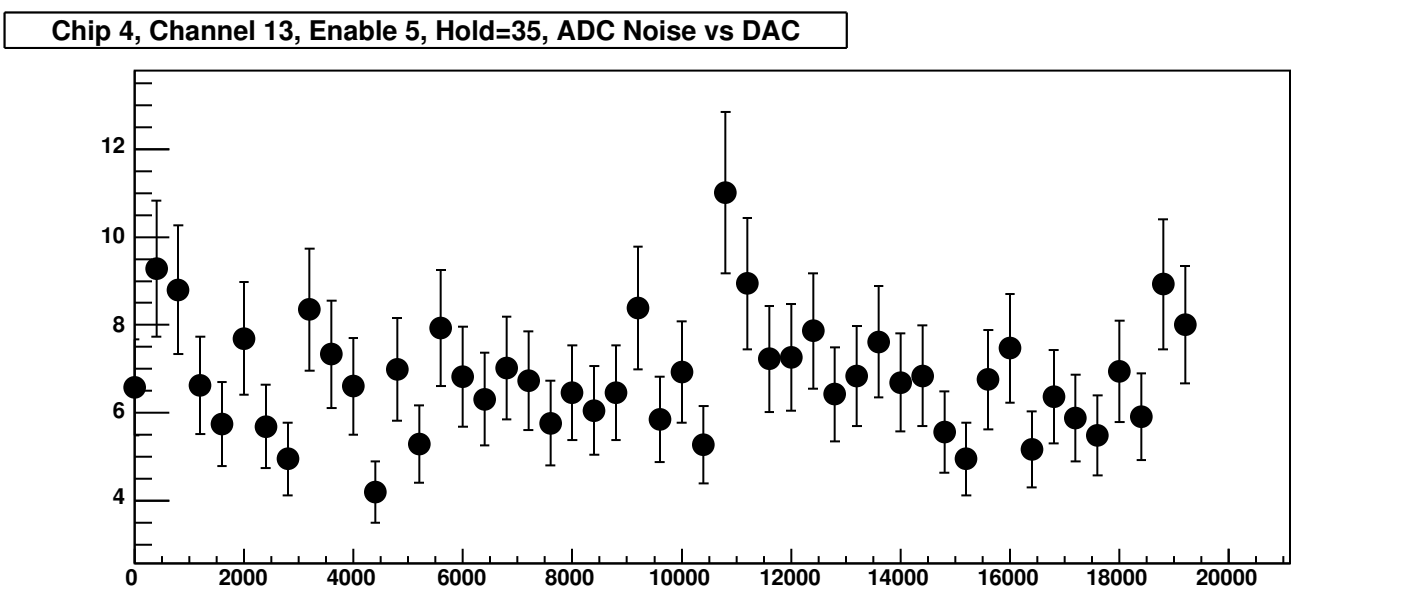

Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

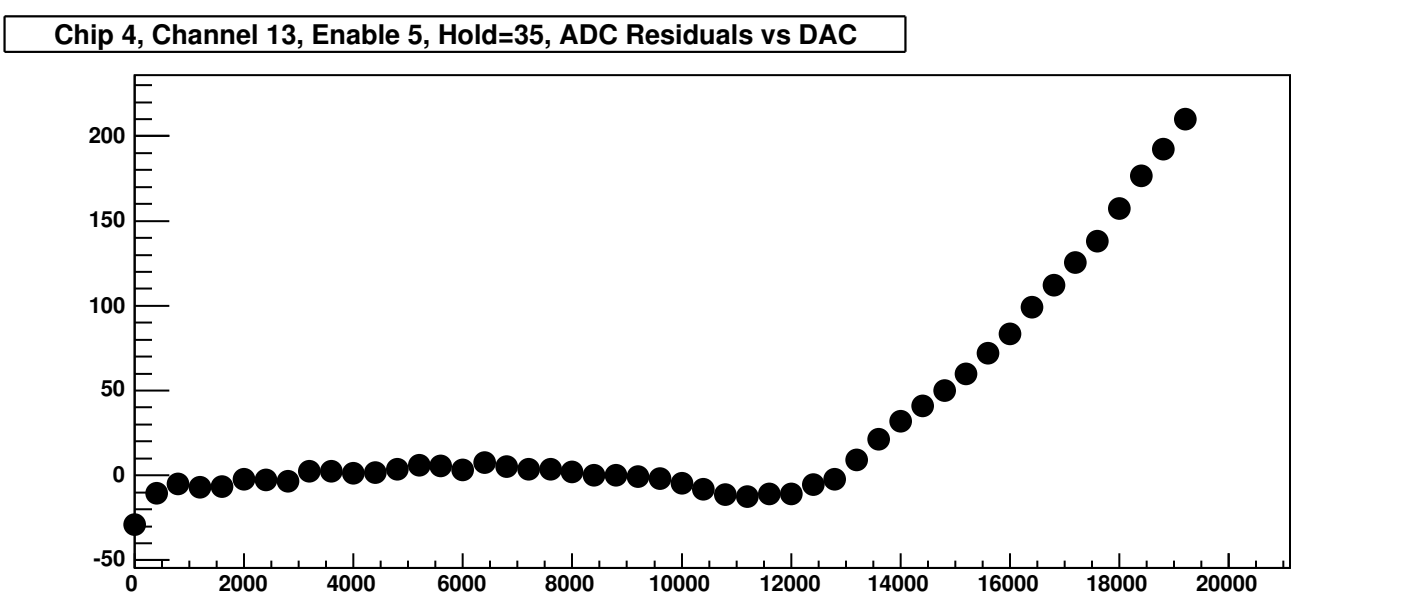

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1983 / 23
p0 261.4 ± 1.202
p1 2.478 ± 0.0003077
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

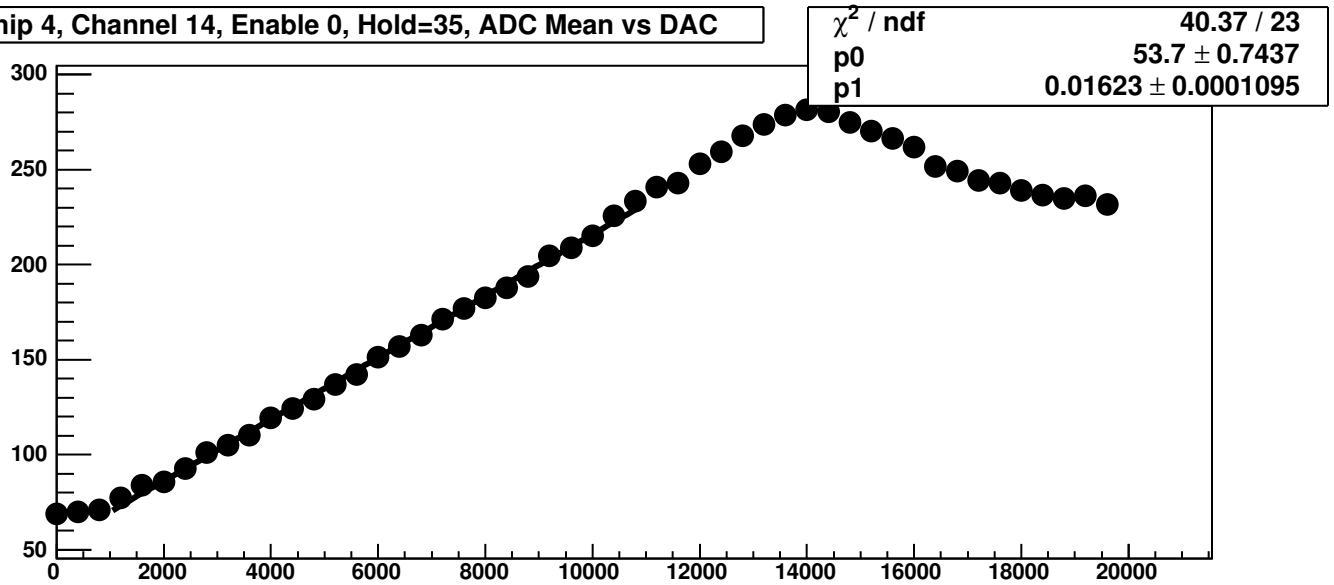

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 243.9 / 23
p0 190.1 ± 0.6625
p1 -0.03662 ± 0.0001026
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

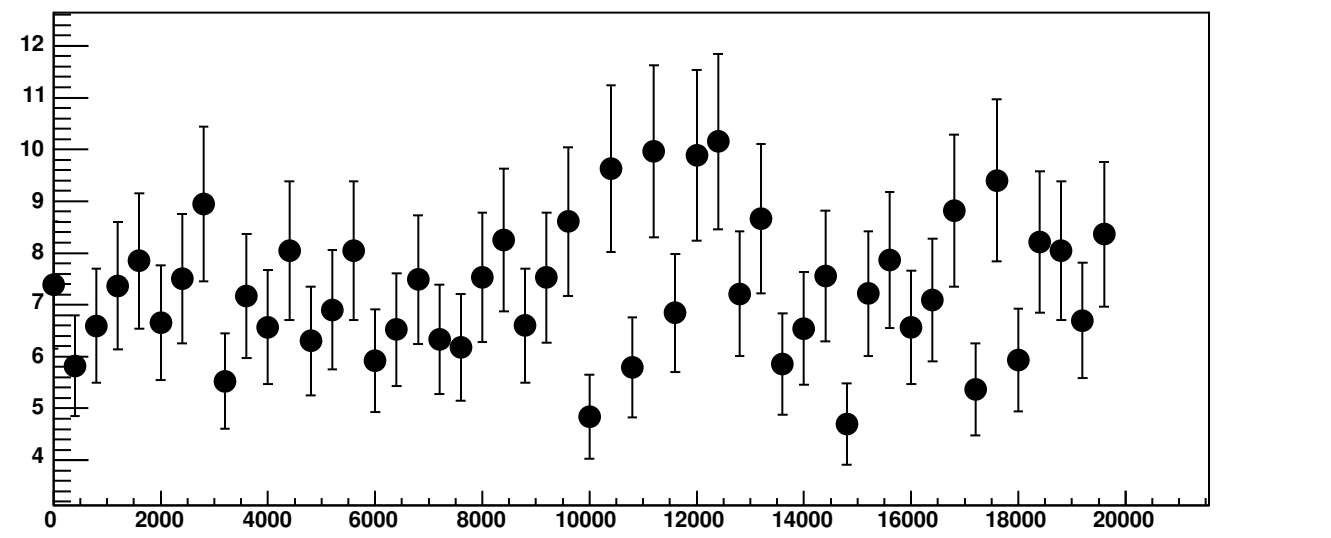

Chip 4, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

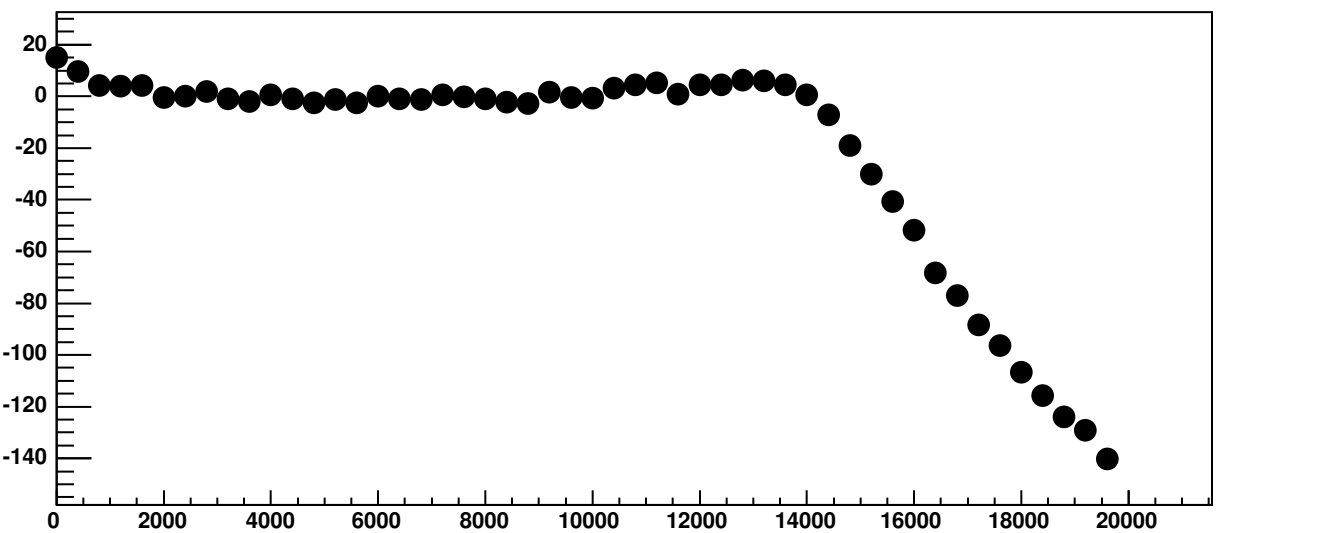
Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

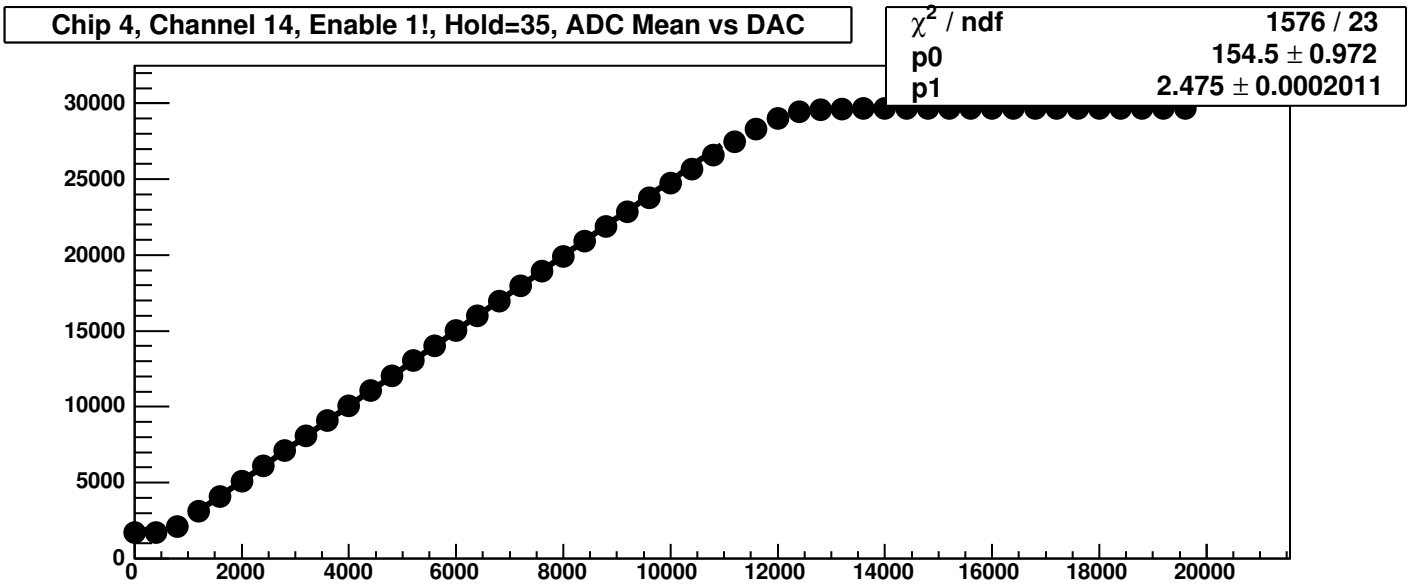


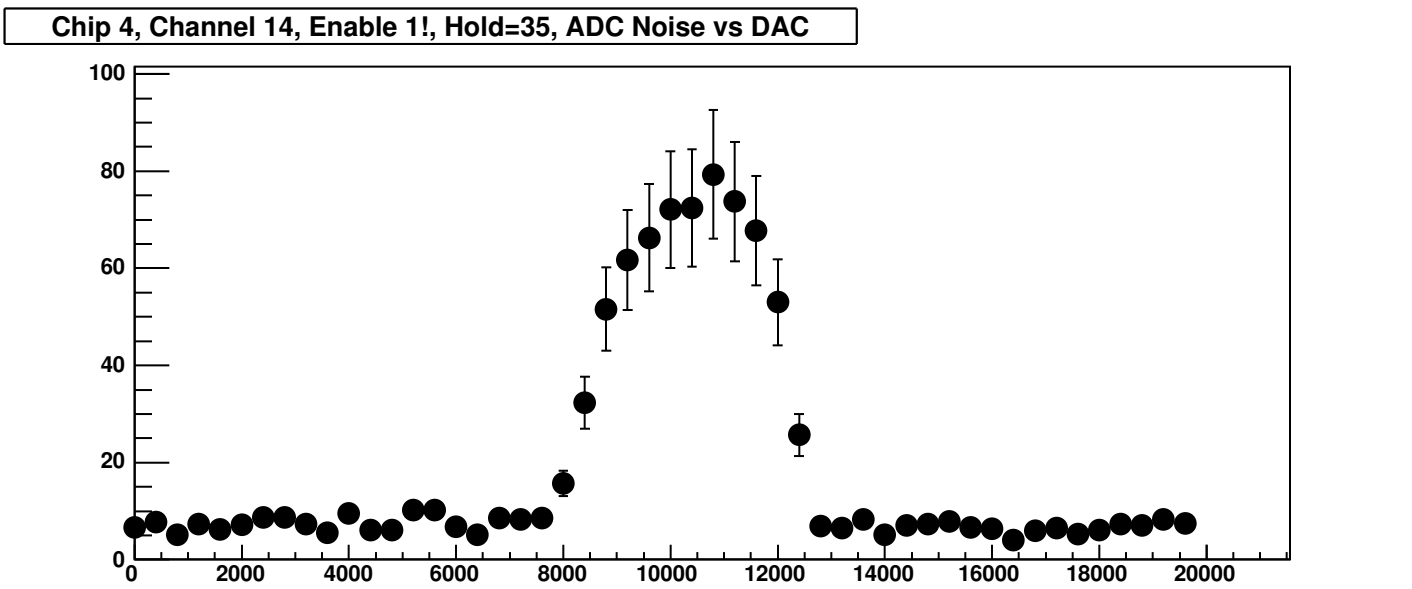
Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

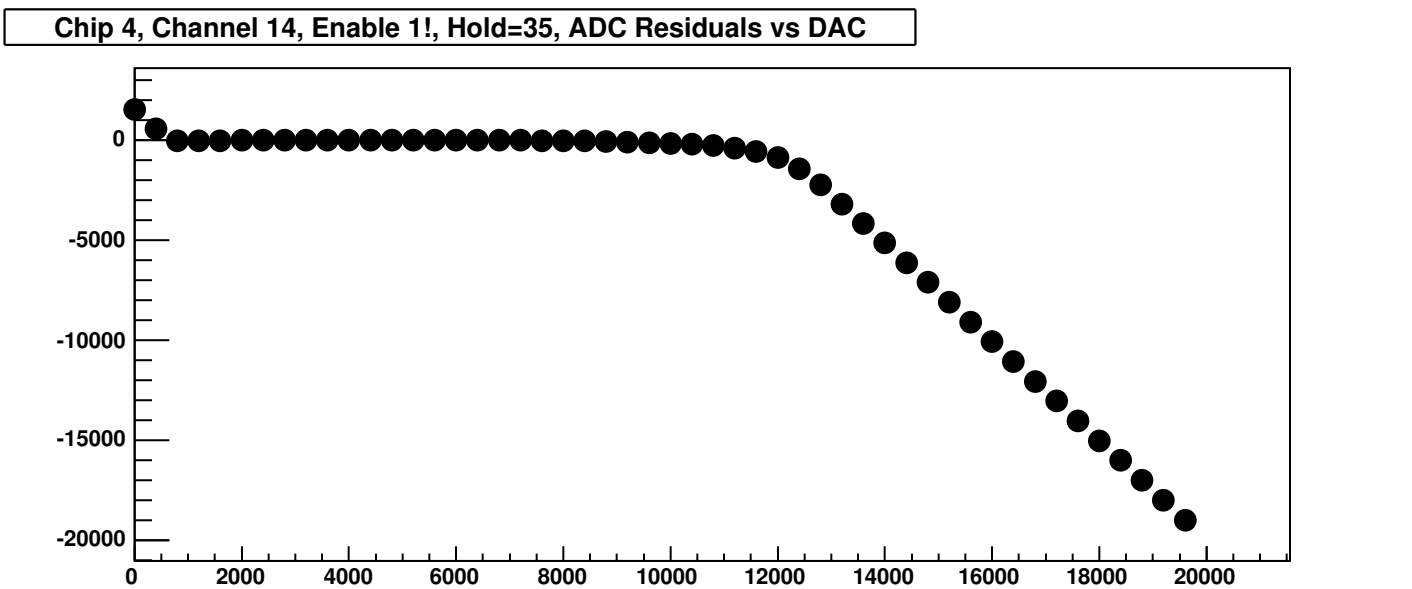


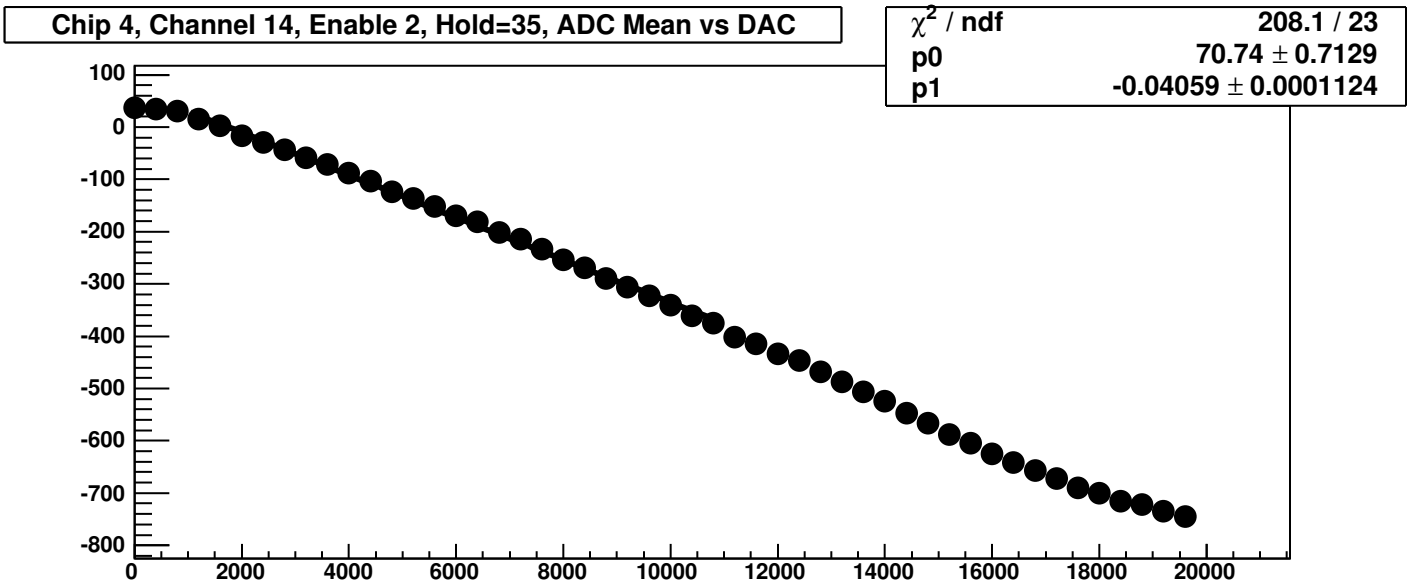
Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

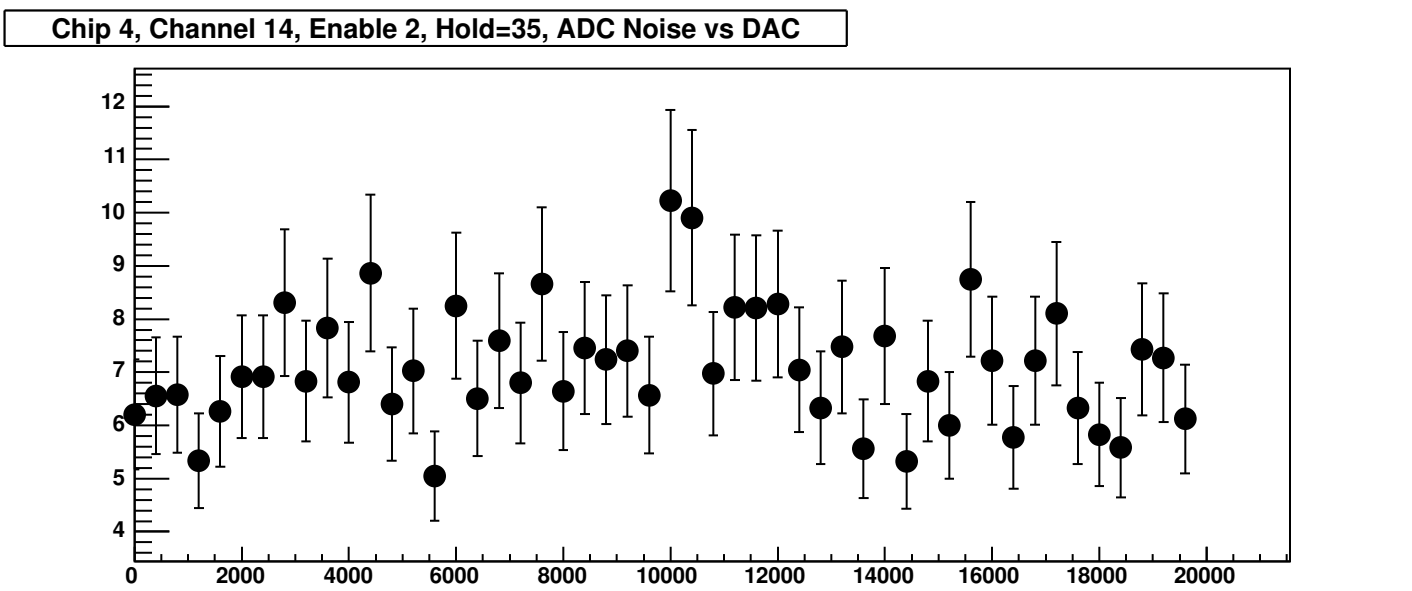


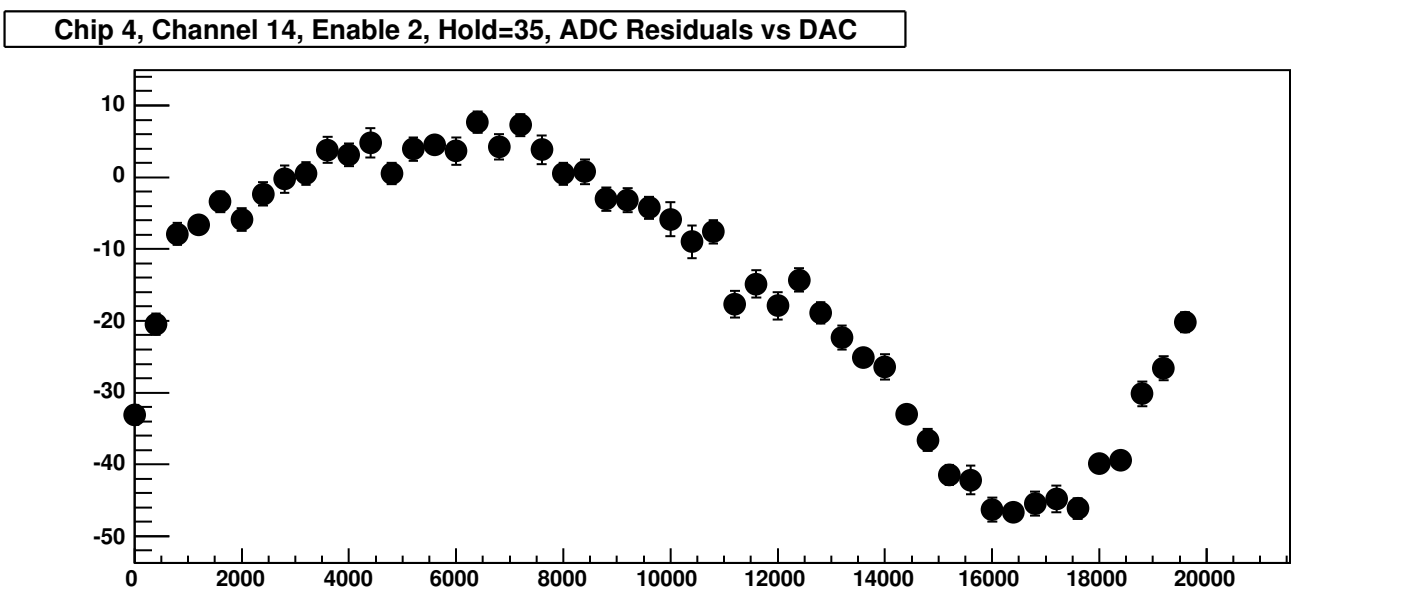

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1576 / 23
p0 154.5 ± 0.972
p1 2.475 ± 0.0002011
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

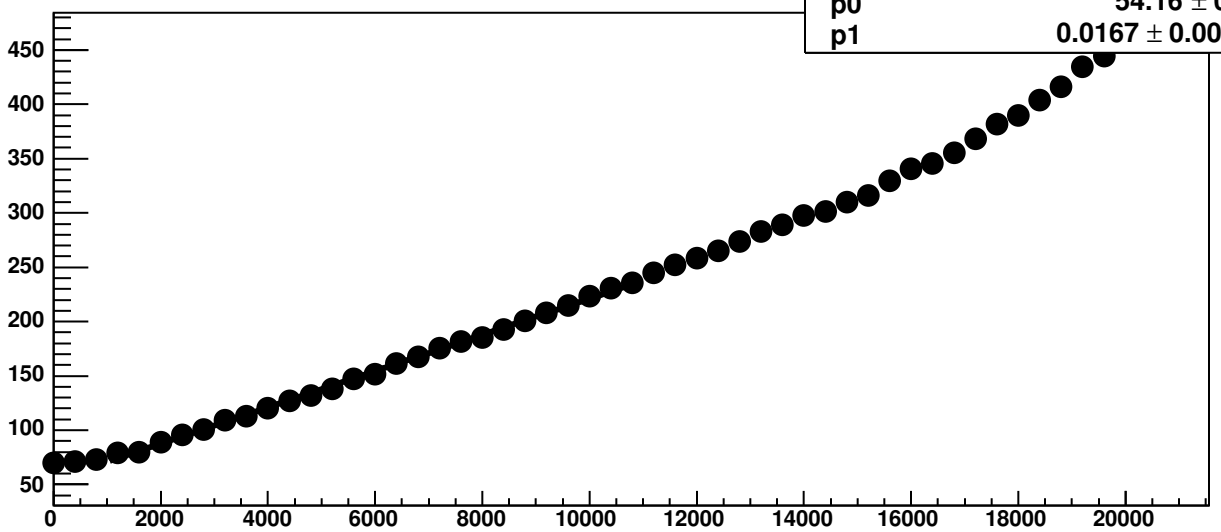

Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 208.1 / 23
p0 70.74 ± 0.7129
p1 -0.04059 ± 0.0001124
100
0
-100
-200
-300
-400
-500
-600
-700
-800
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

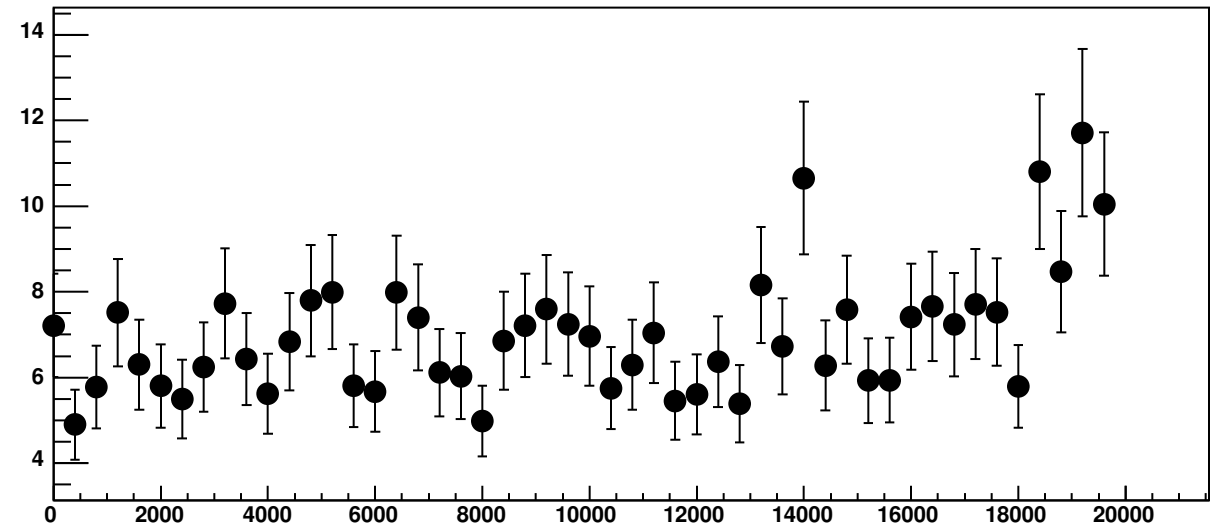

Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

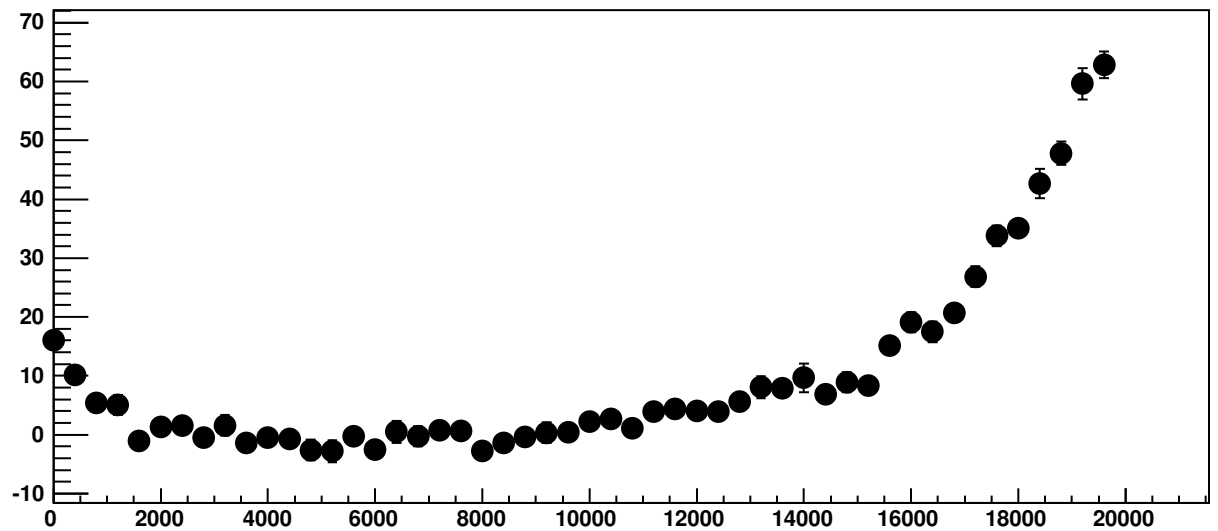
χ^2 / ndf	36.11 / 23
p0	54.16 ± 0.6785
p1	0.0167 ± 0.0001023

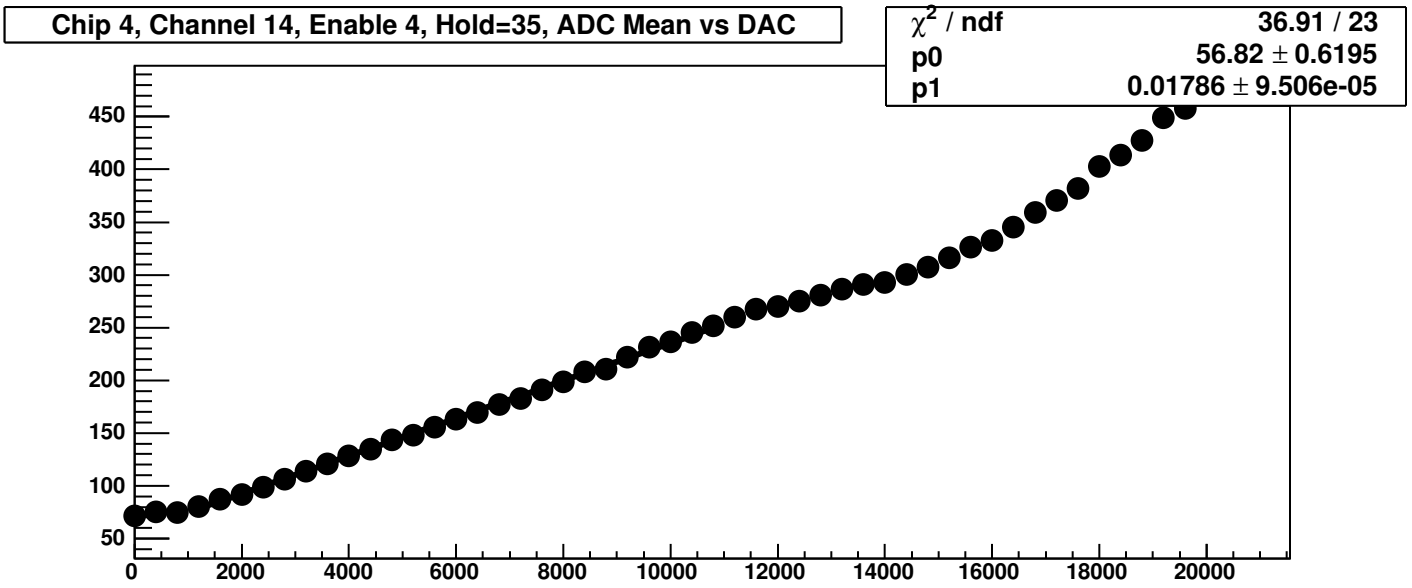


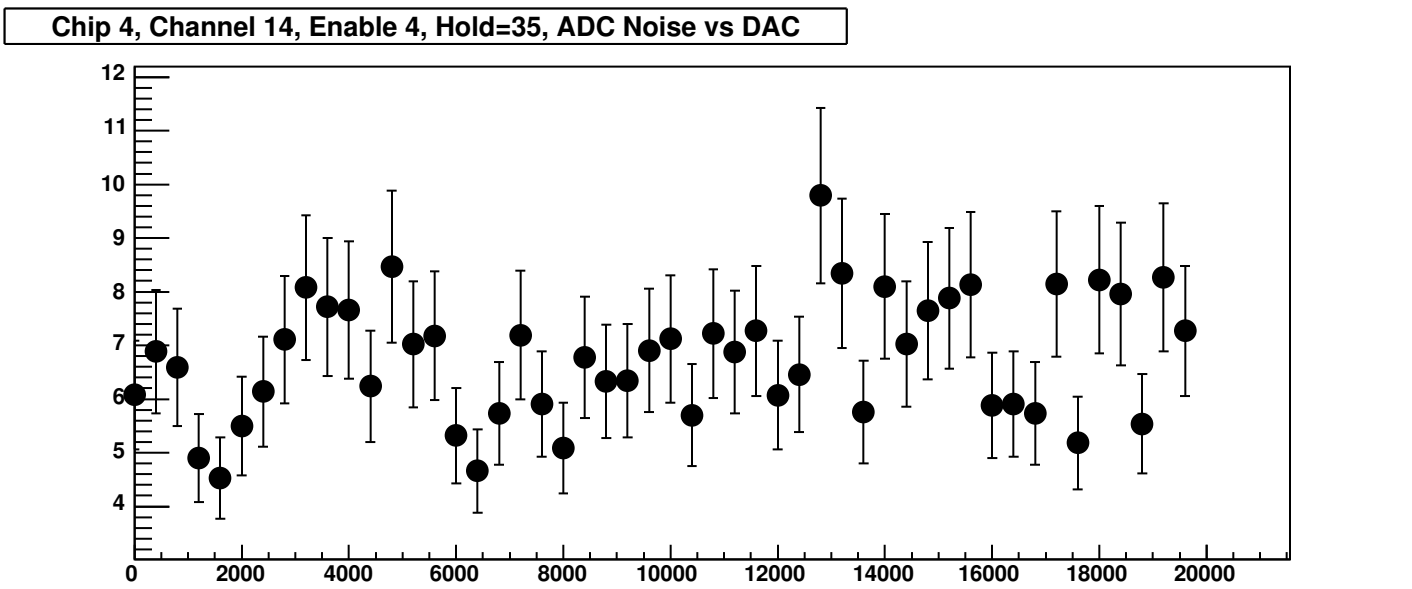
Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC

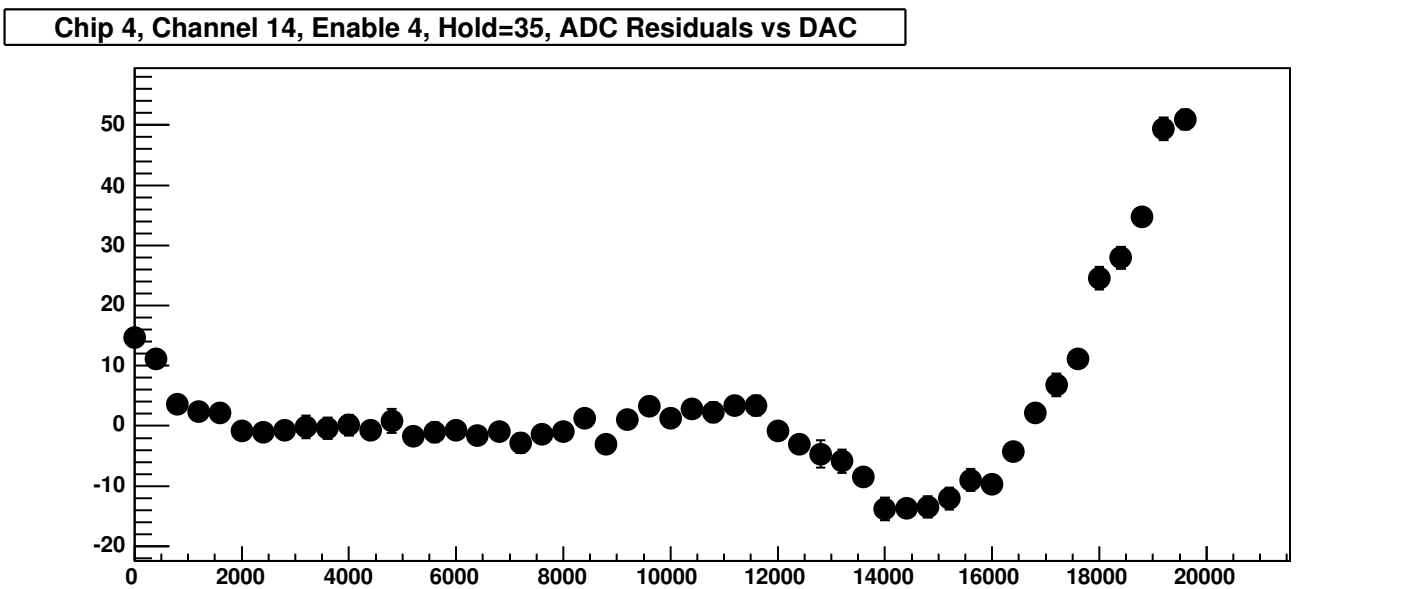


Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



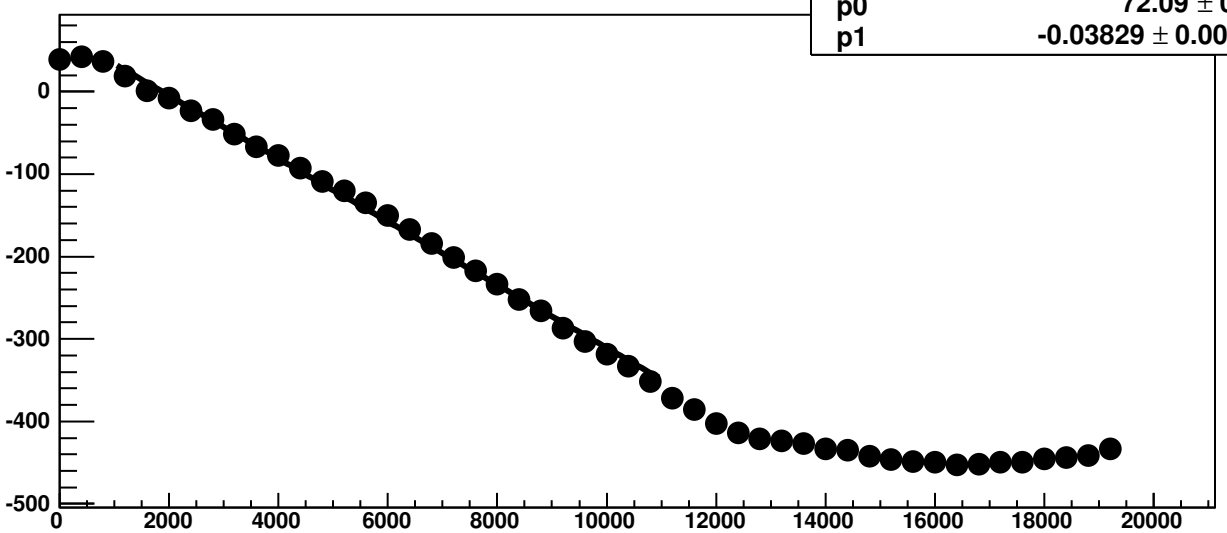

Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.91 / 23
p0 56.82 ± 0.6195
p1 0.01786 ± 9.506e-05
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

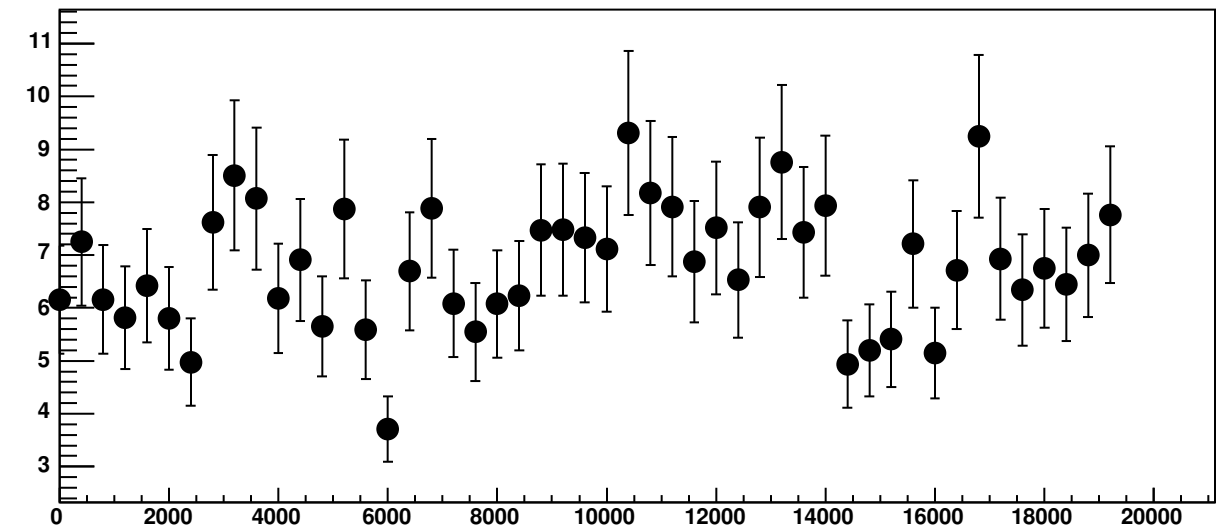

Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 4, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

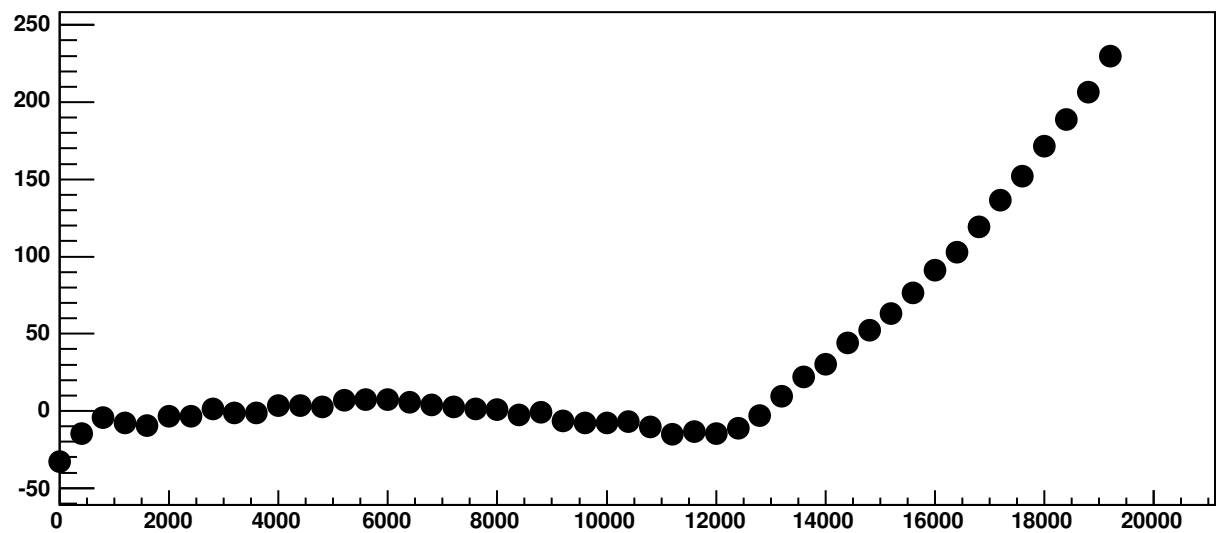
χ^2 / ndf	354.1 / 23
p0	72.09 ± 0.6792
p1	-0.03829 ± 0.0001079



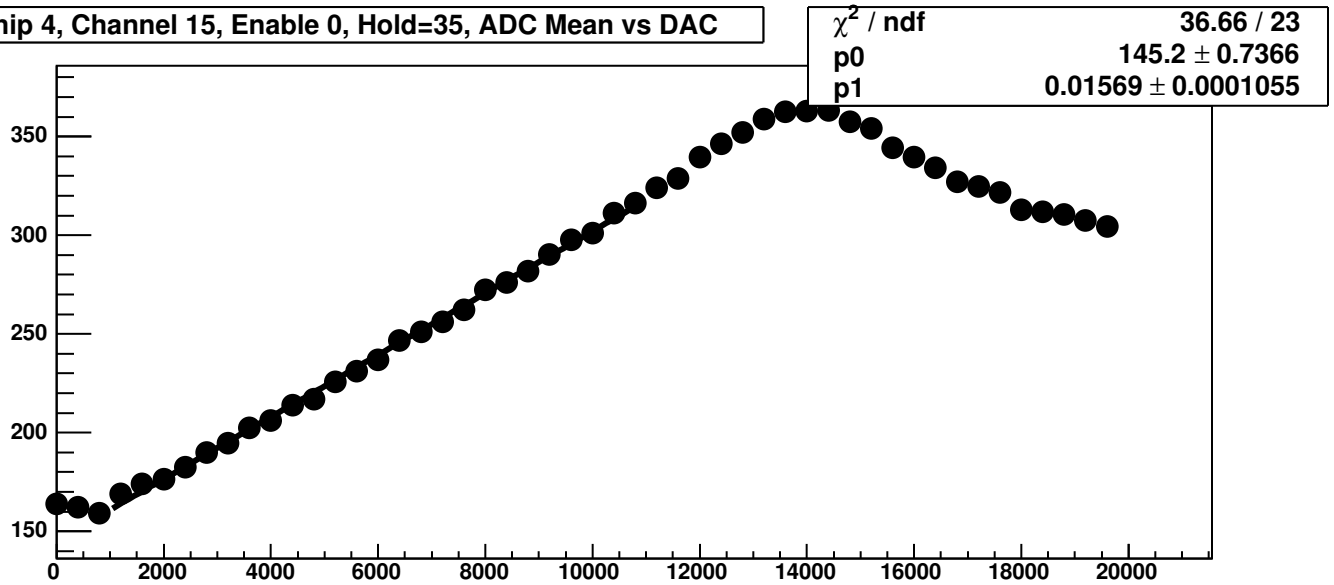
Chip 4, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



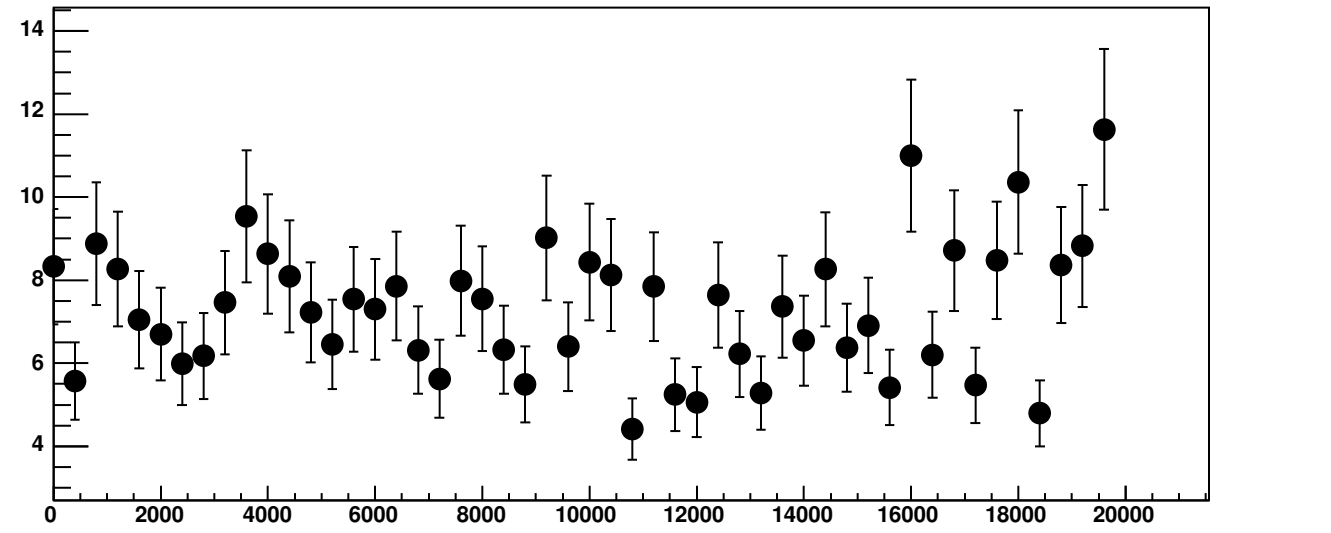
Chip 4, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



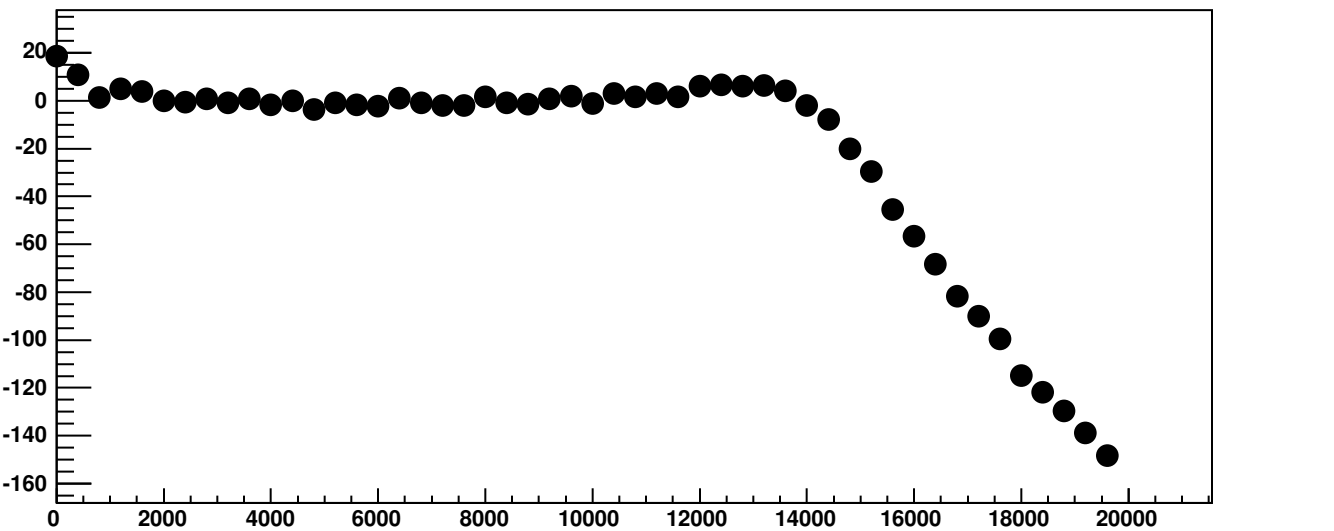
Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

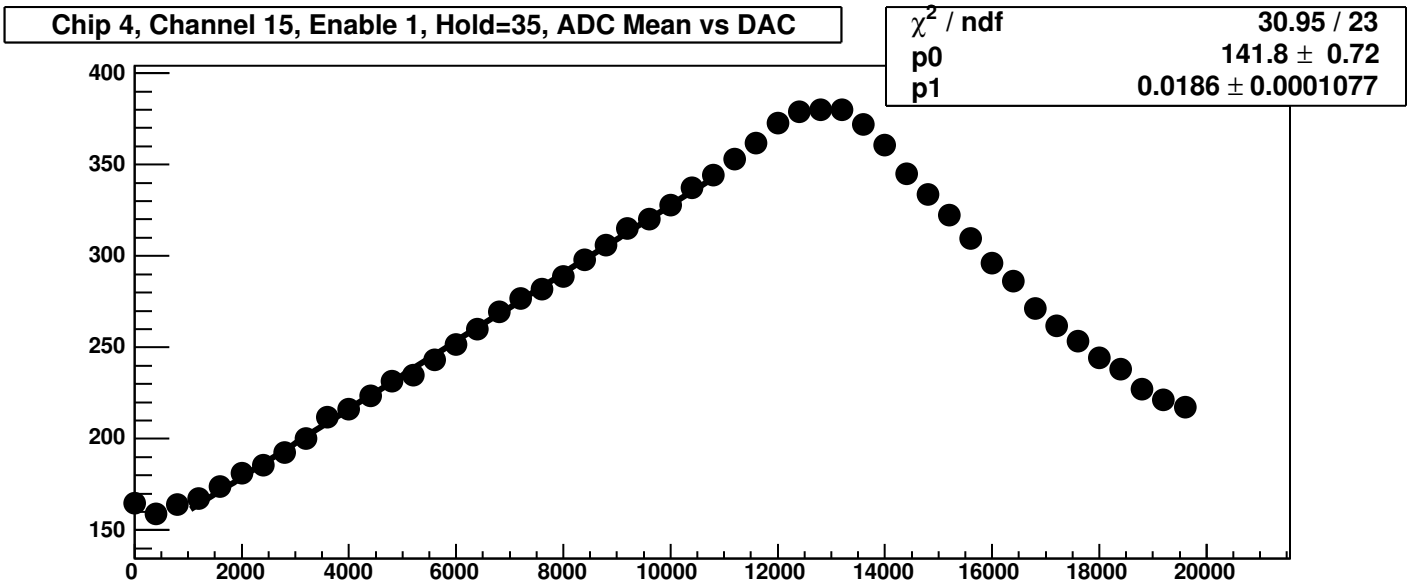


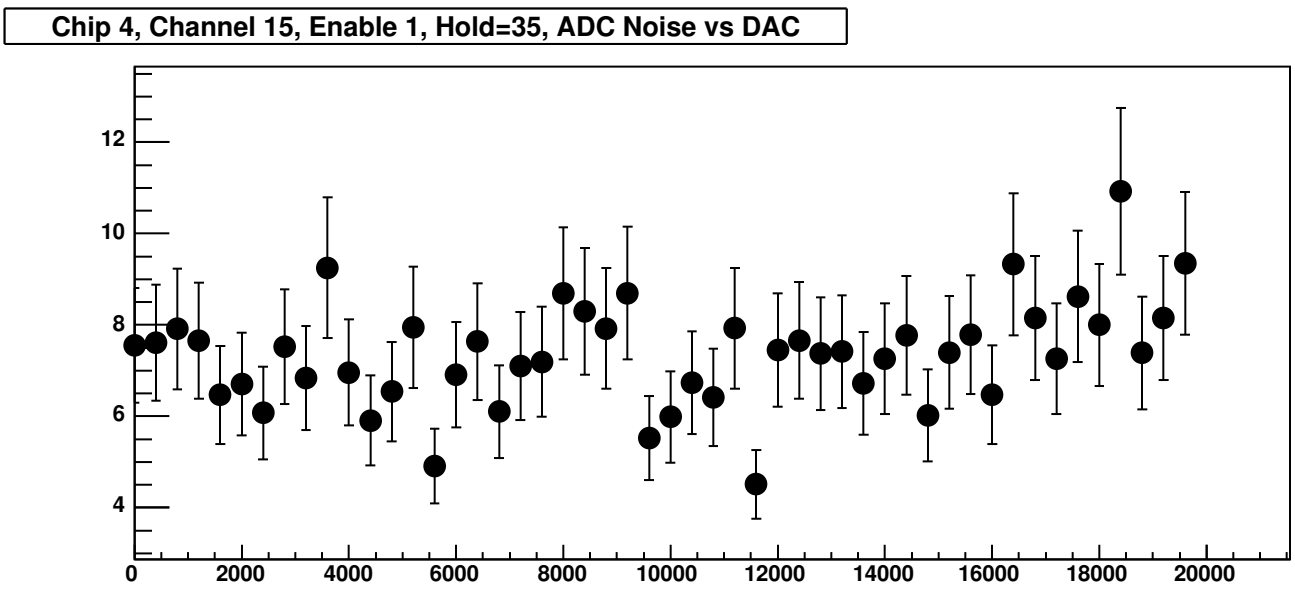
Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

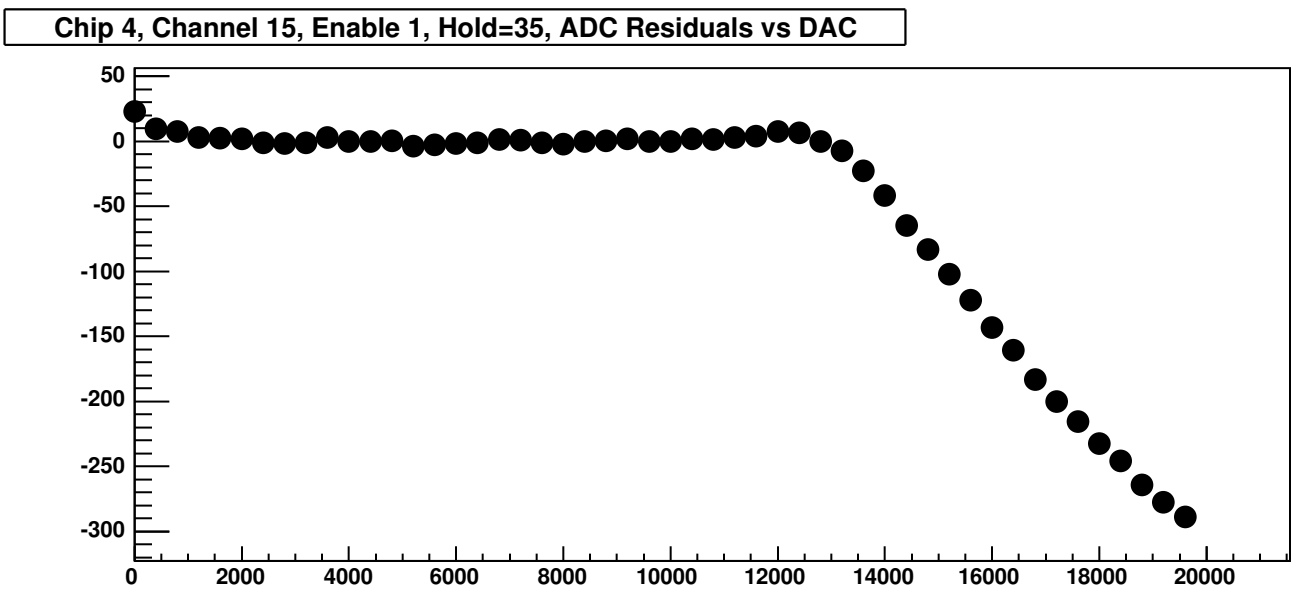


Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



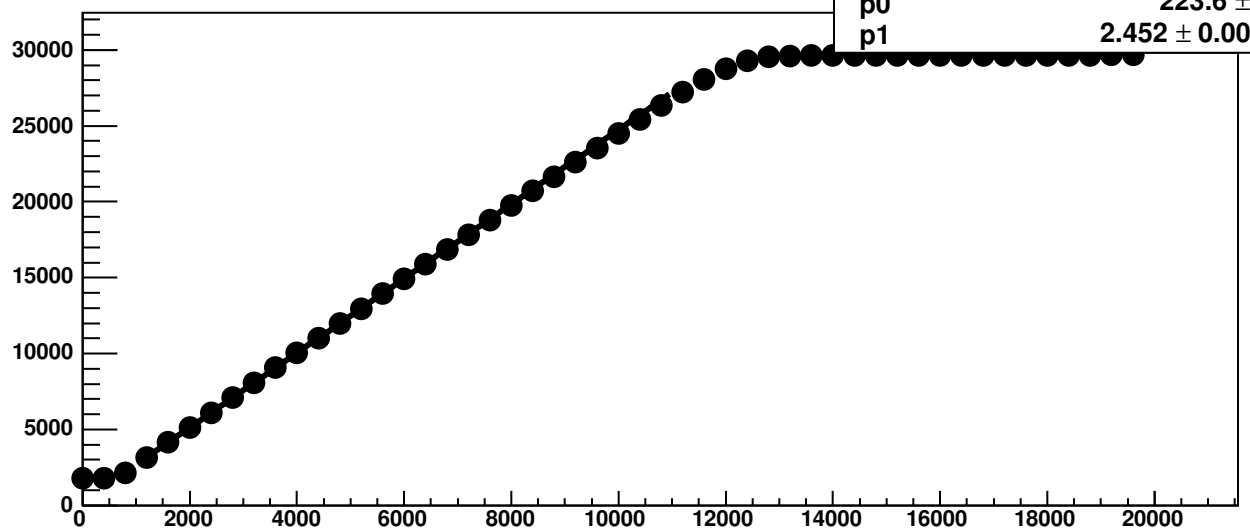

Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.95 / 23
p0 141.8 ± 0.72
p1 0.0186 ± 0.0001077
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

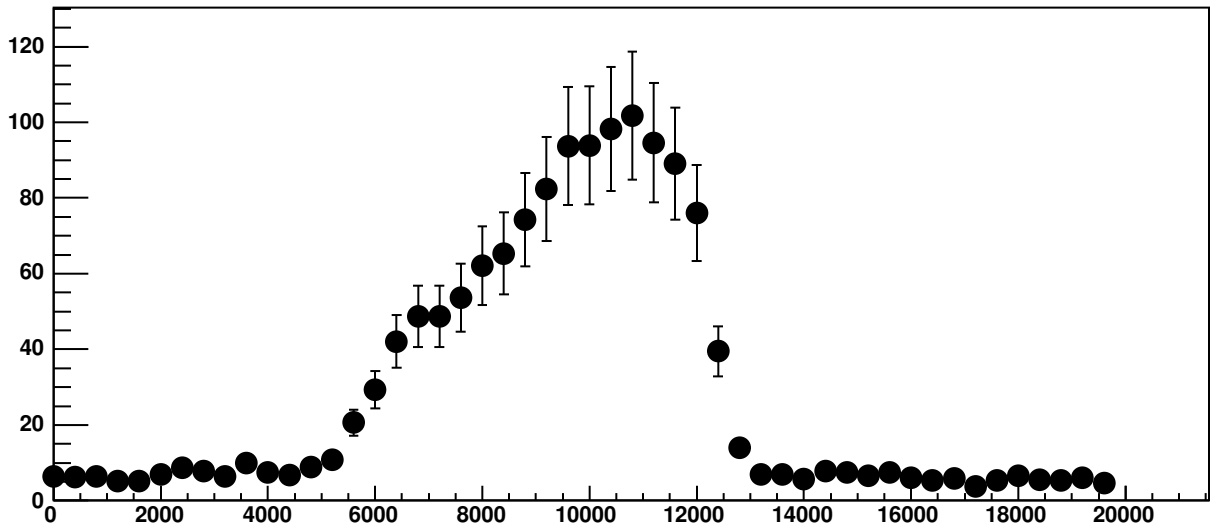

Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

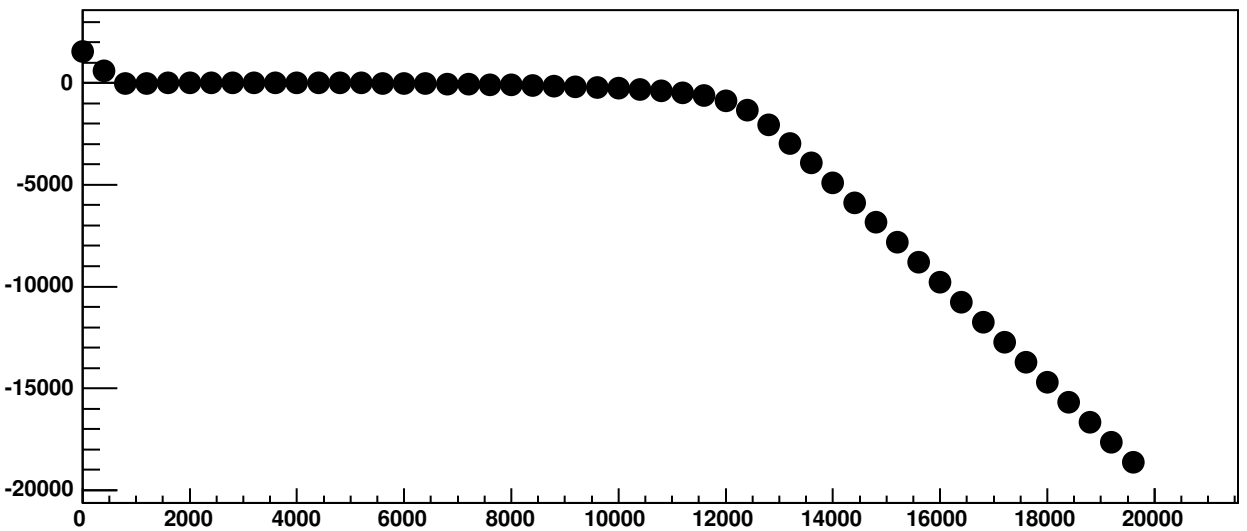
χ^2 / ndf	1655 / 23
p0	223.6 ± 1.088
p1	2.452 ± 0.0003373



Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC

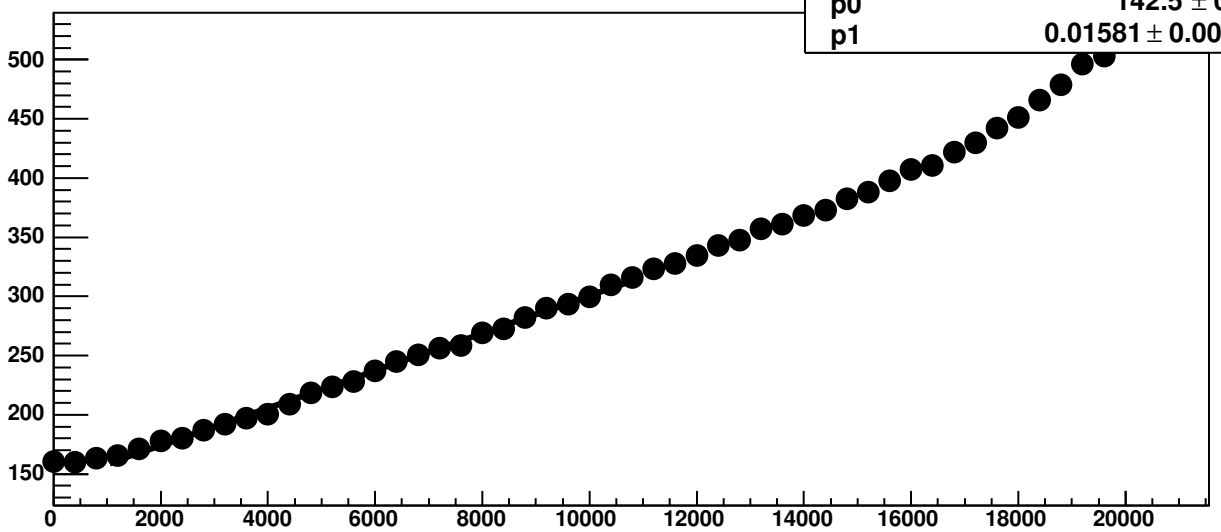


Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

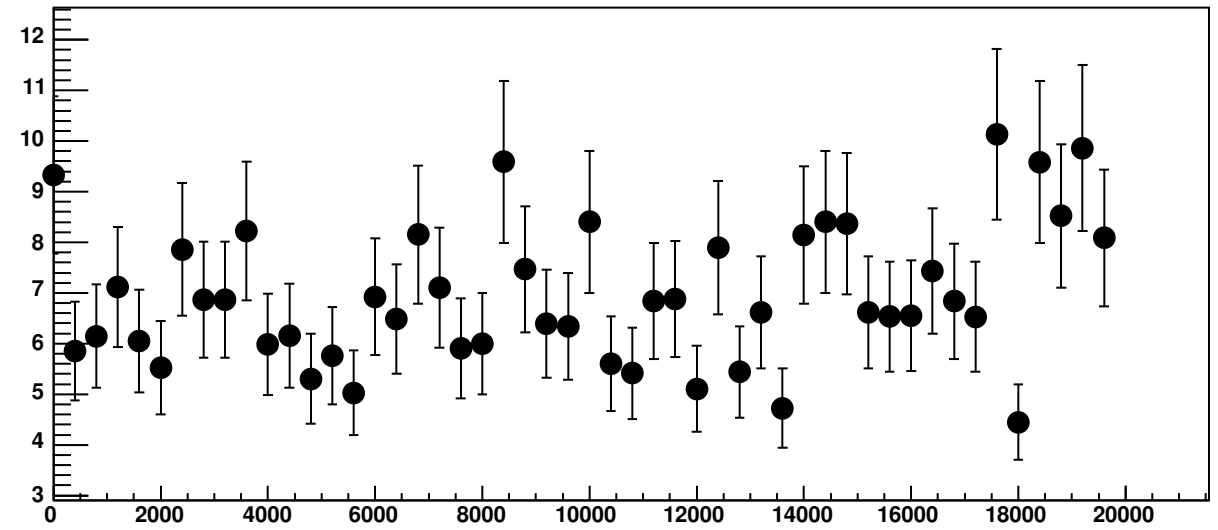


Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

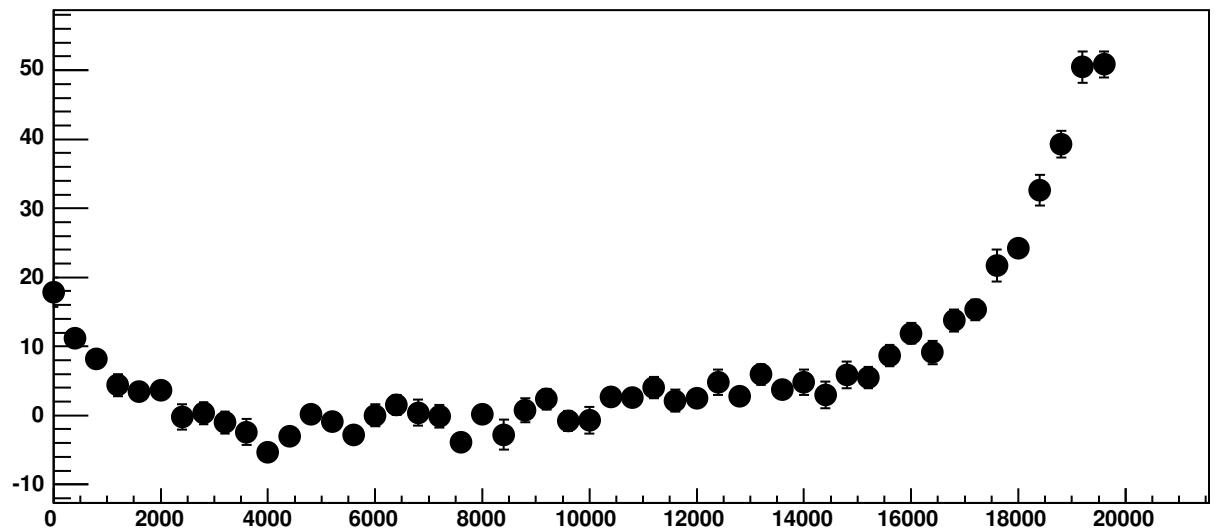
χ^2 / ndf	73.34 / 23
p0	142.5 ± 0.6789
p1	0.01581 ± 0.0001018



Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC

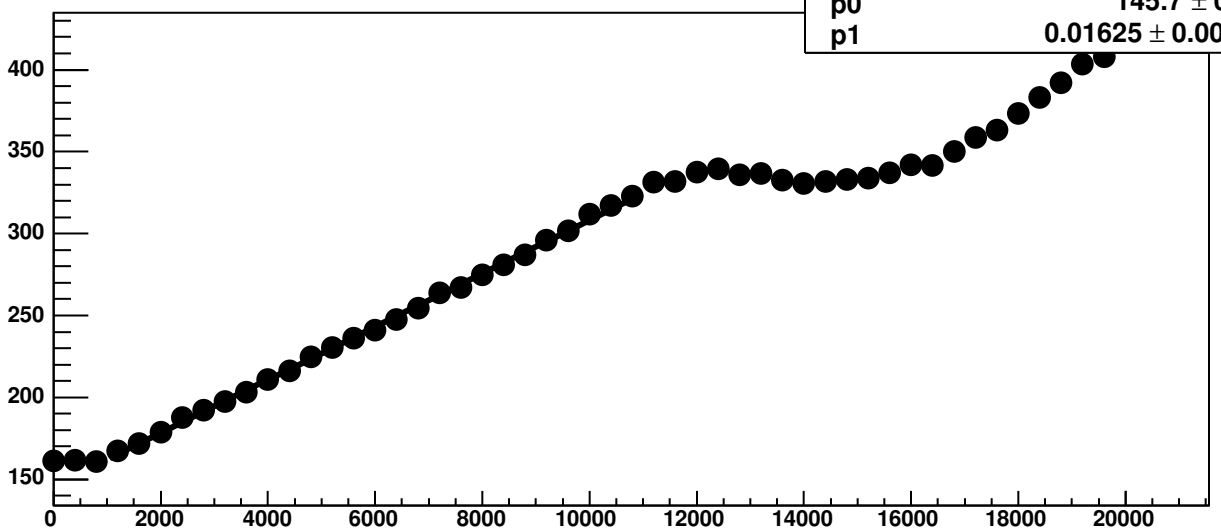


Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

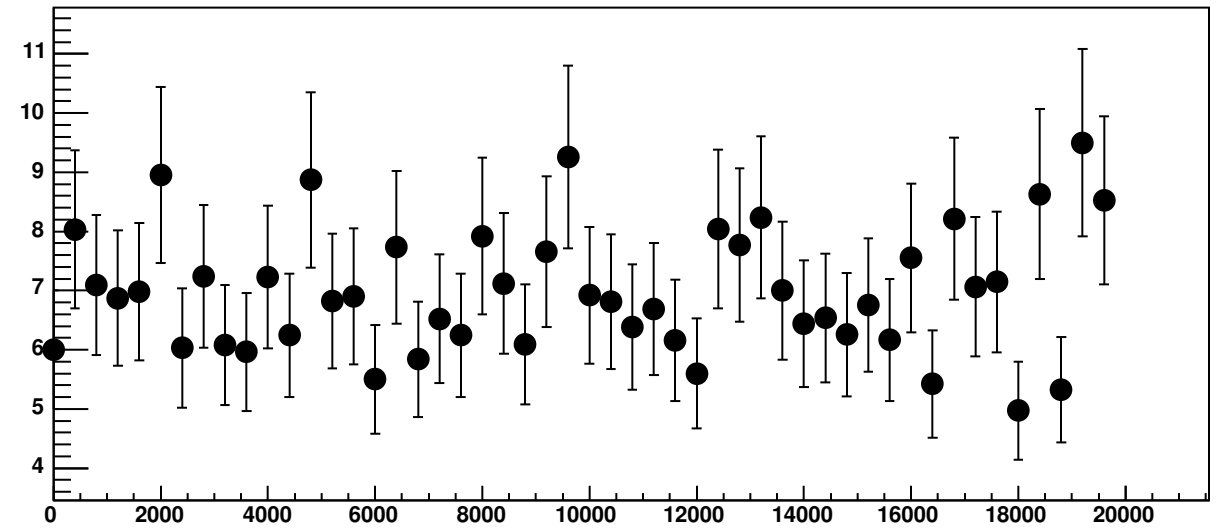


Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

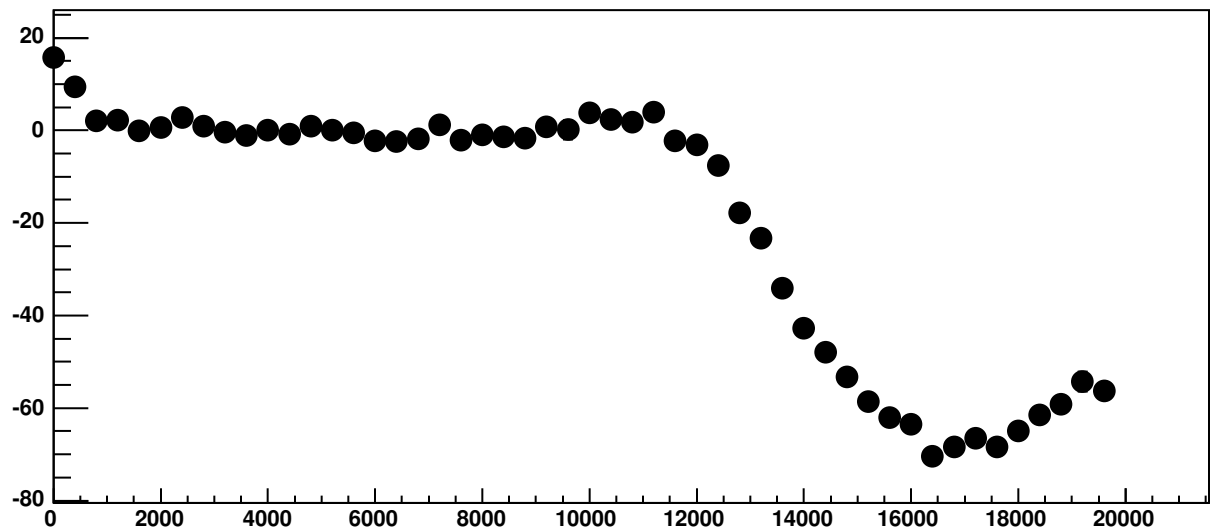
χ^2 / ndf	29.48 / 23
p0	145.7 ± 0.7274
p1	0.01625 ± 0.0001102



Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

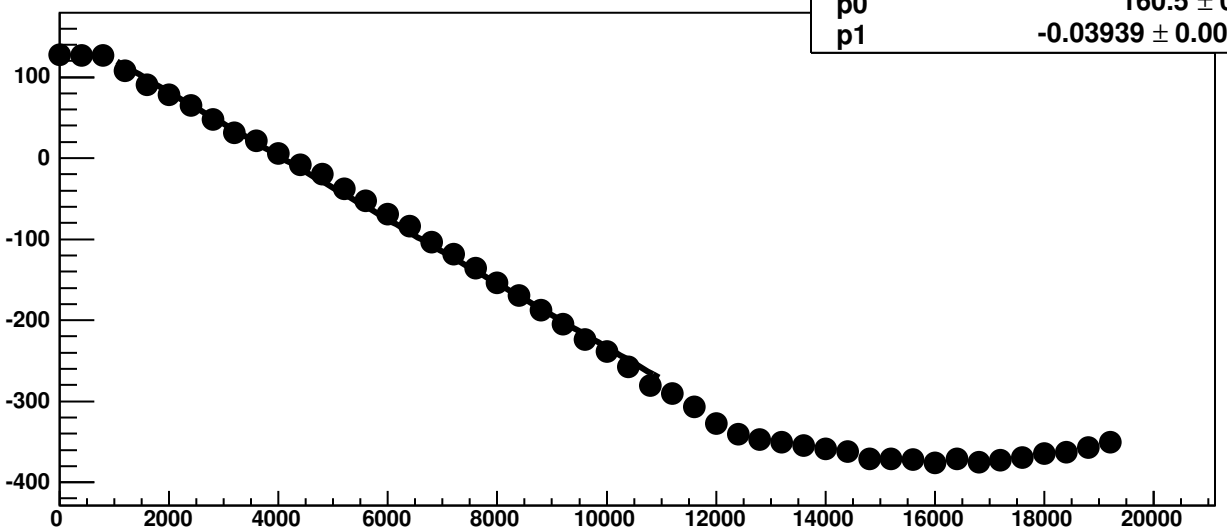


Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

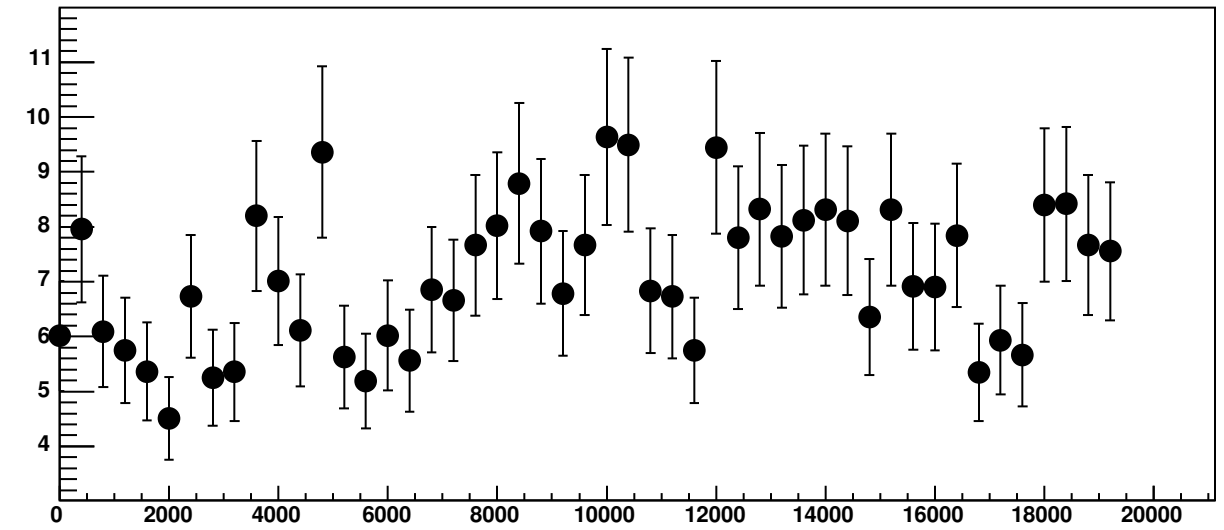


Chip 4, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

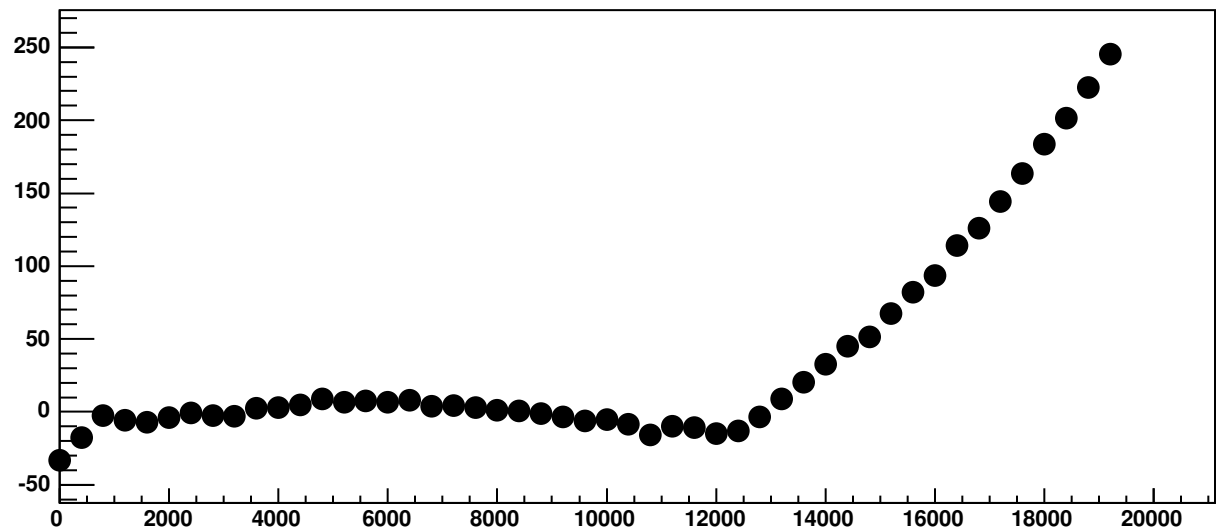
χ^2 / ndf	396.3 / 23
p0	160.5 ± 0.6297
p1	-0.03939 ± 0.0001057



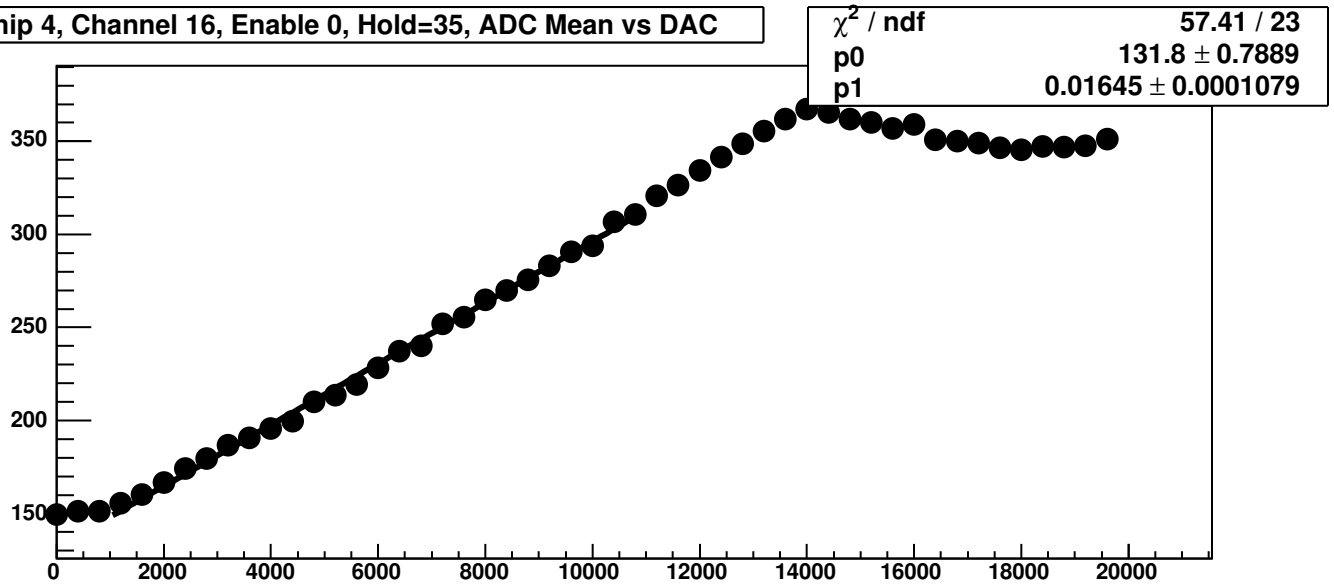
Chip 4, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



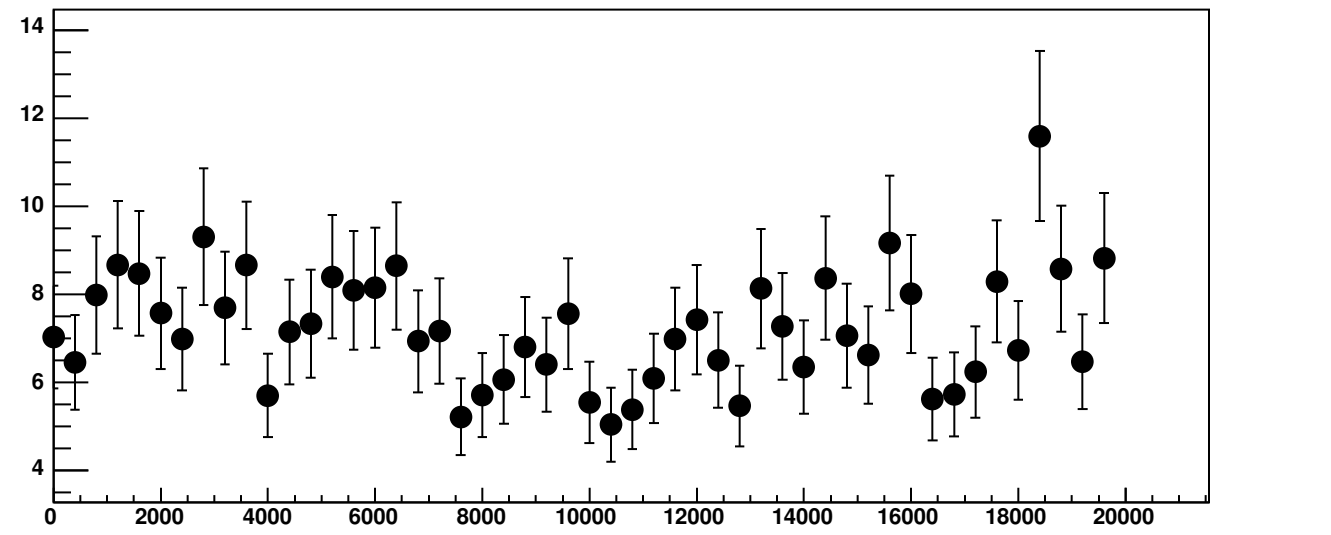
Chip 4, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



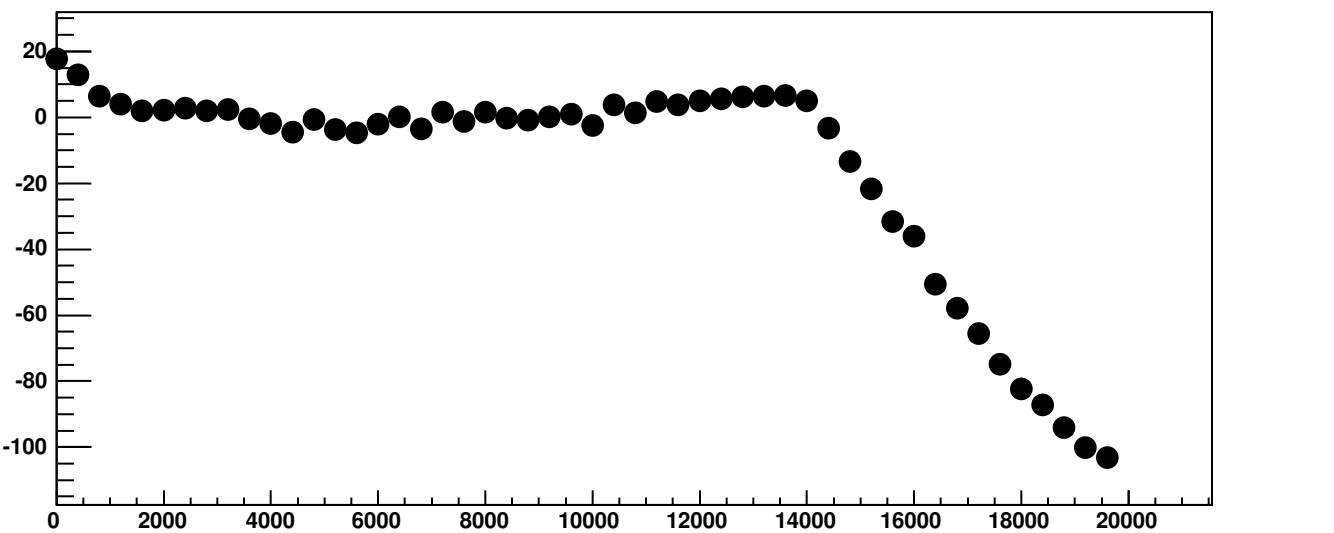
Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

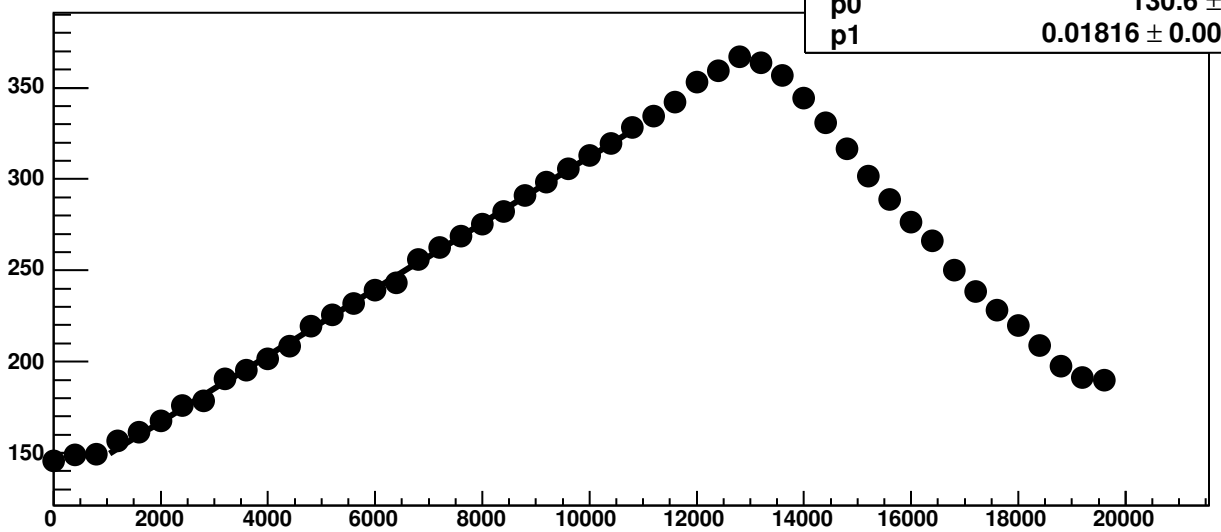


Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

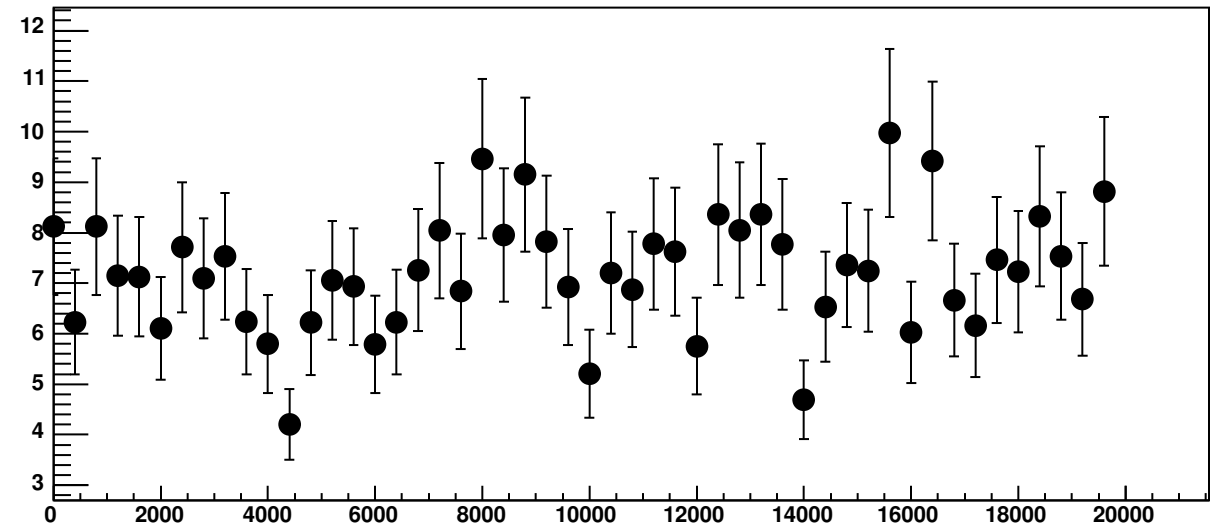


Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

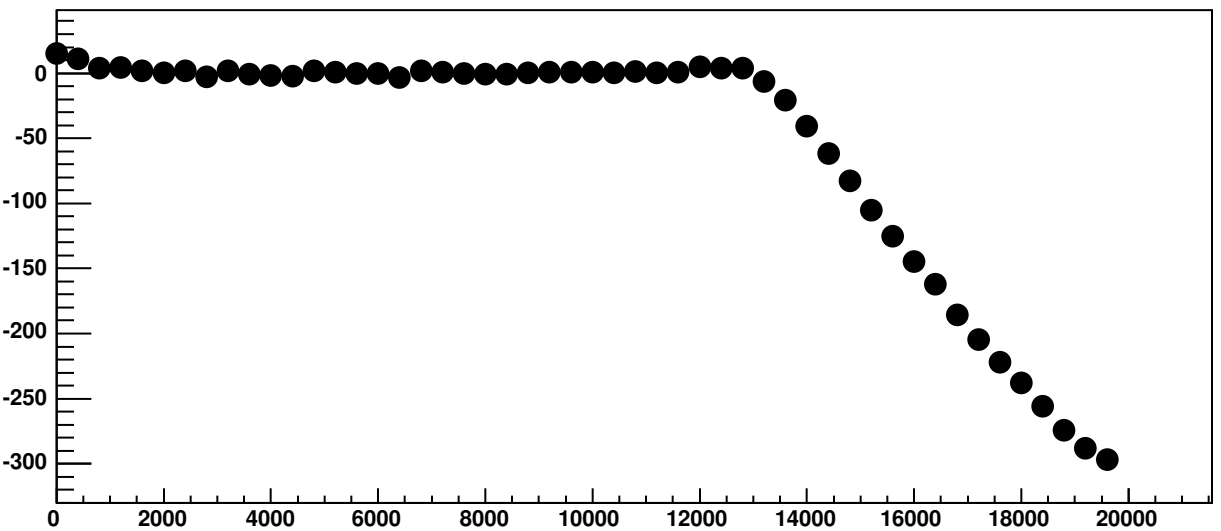
χ^2 / ndf	31.22 / 23
p0	130.6 ± 0.702
p1	0.01816 ± 0.0001084



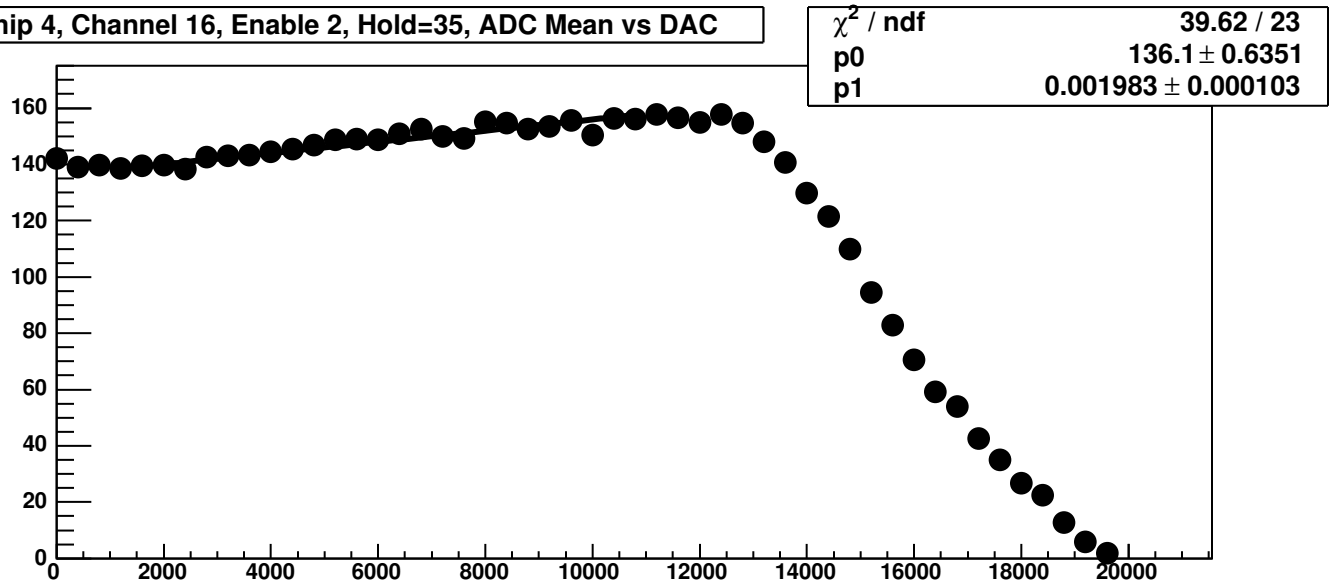
Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



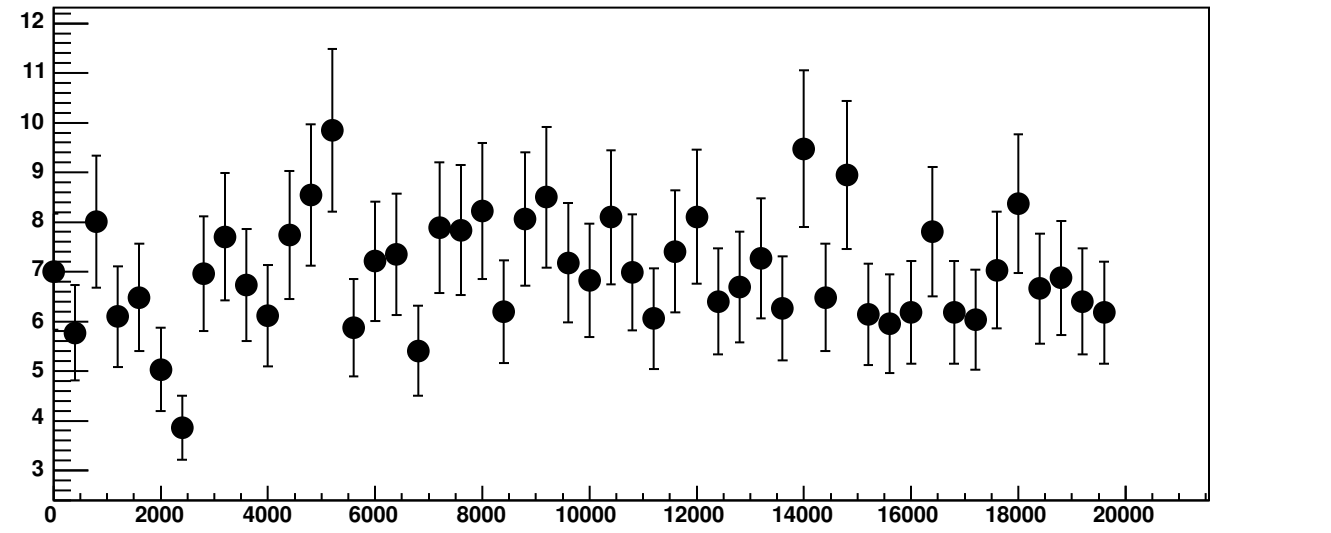
Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



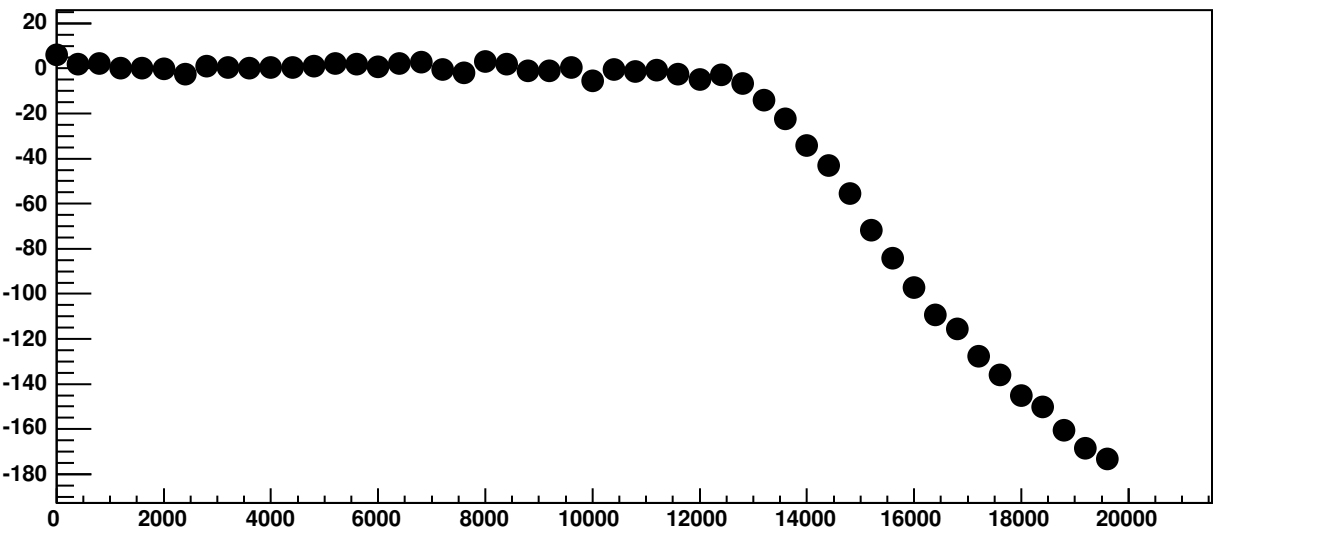
Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

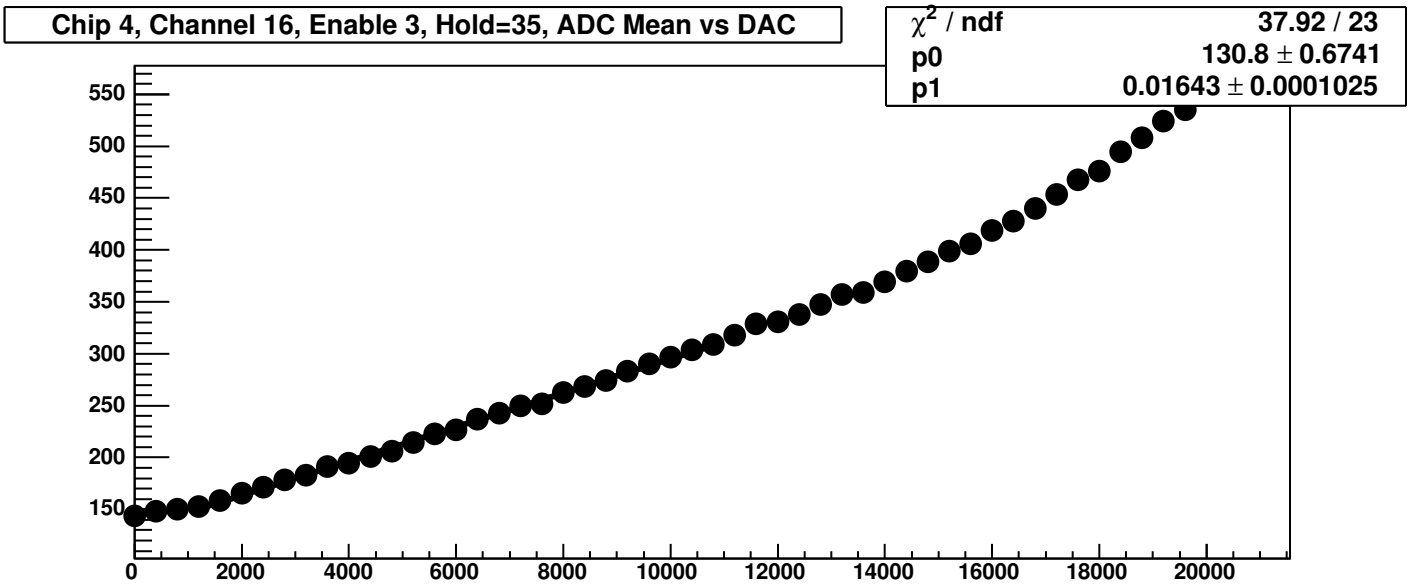


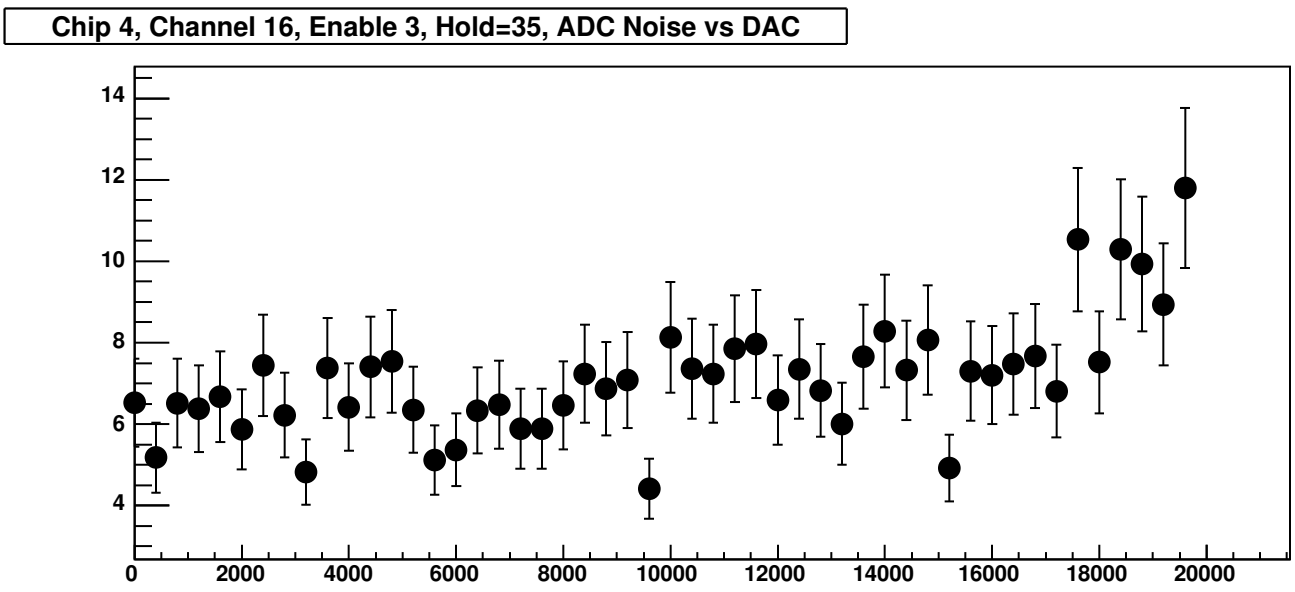
Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC

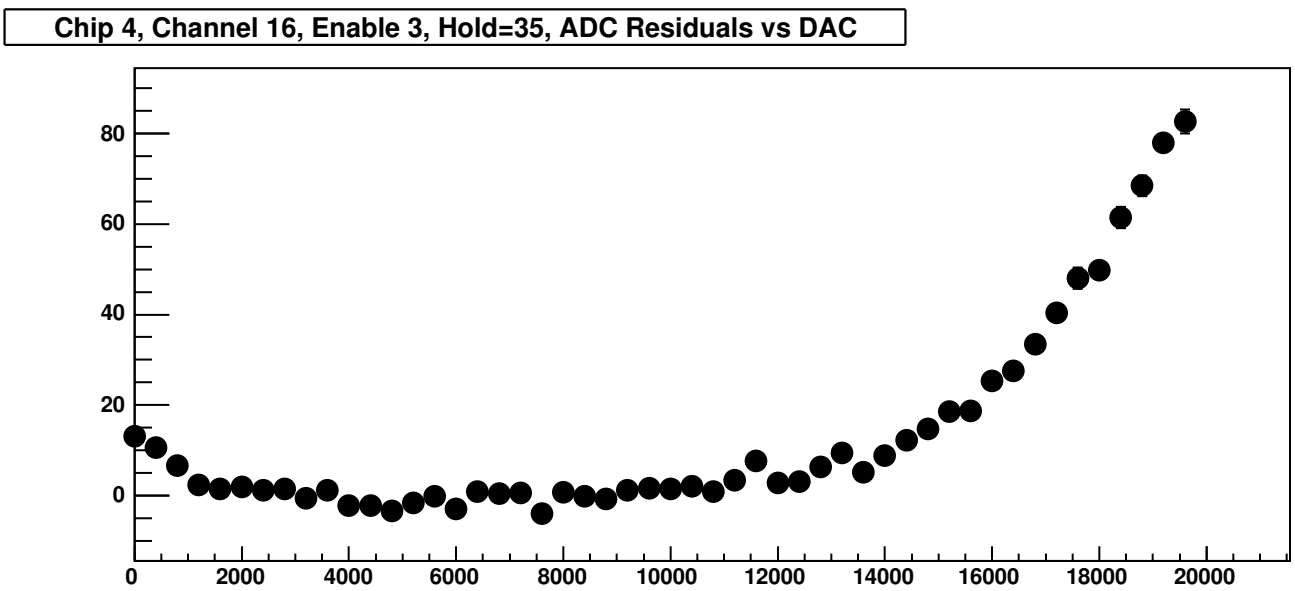


Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

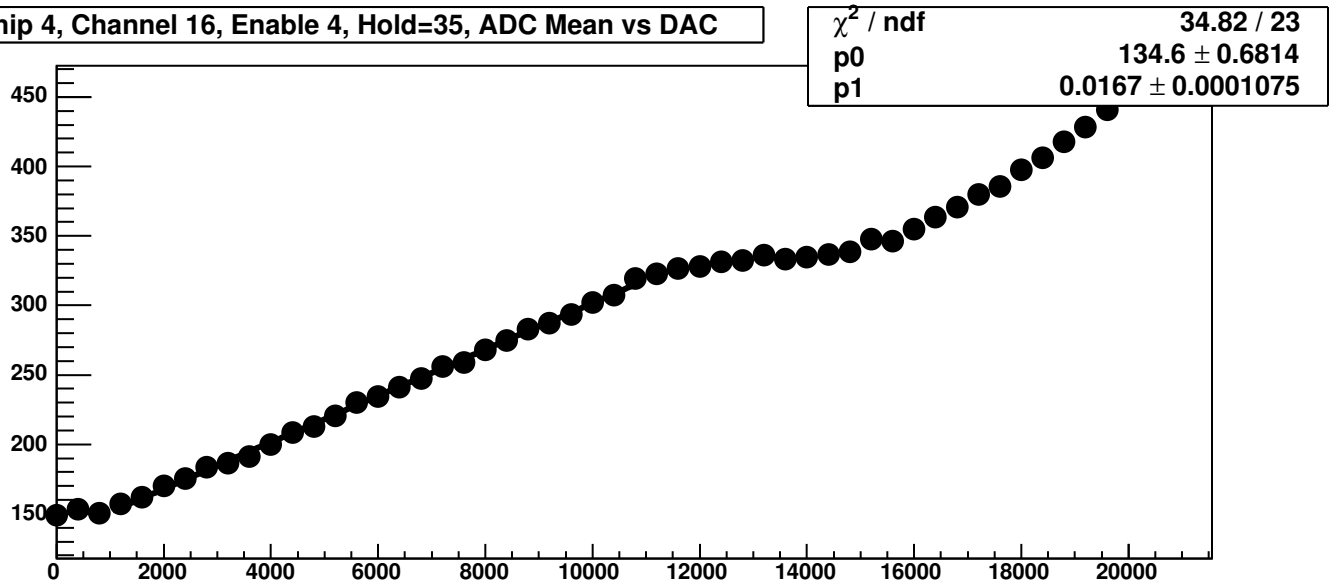



Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.92 / 23
p0 130.8 ± 0.6741
p1 0.01643 ± 0.0001025
550
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

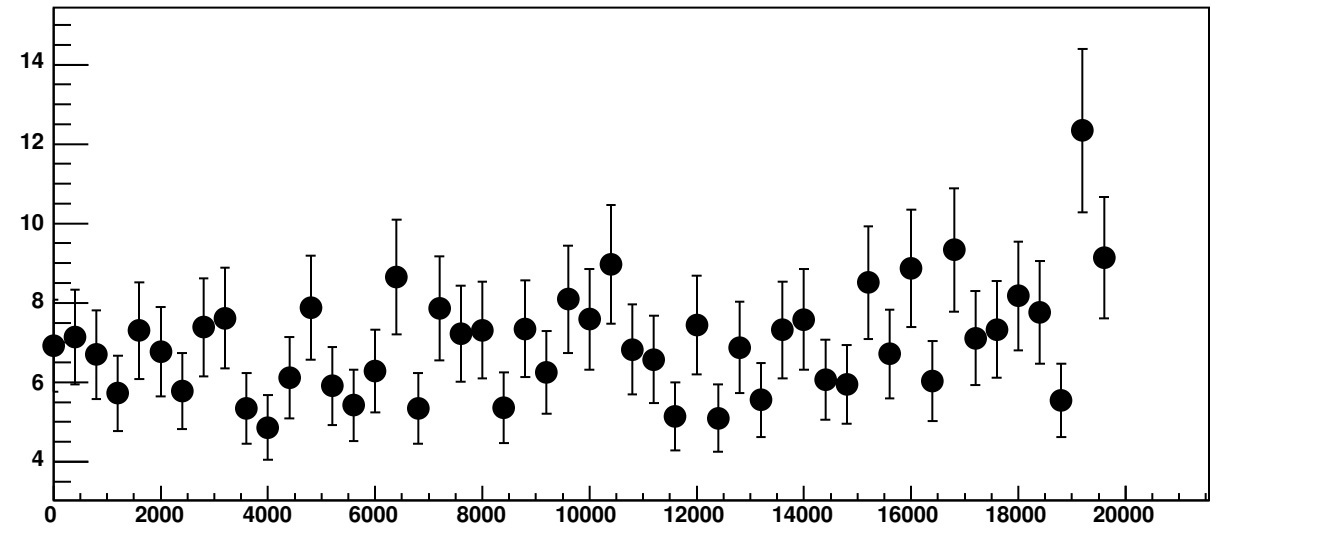

Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

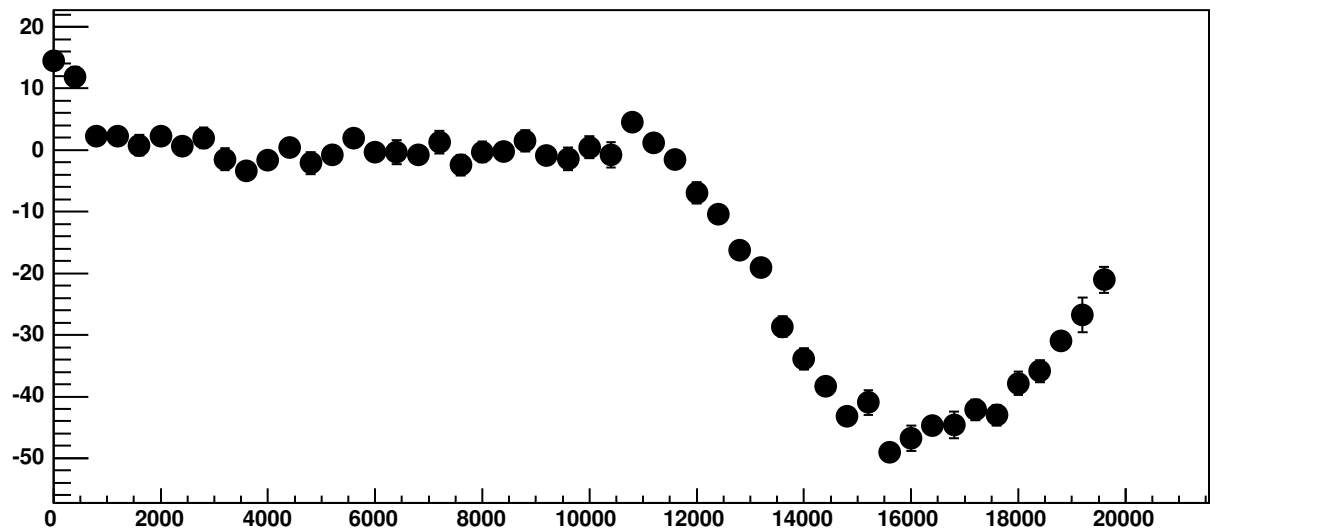
Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

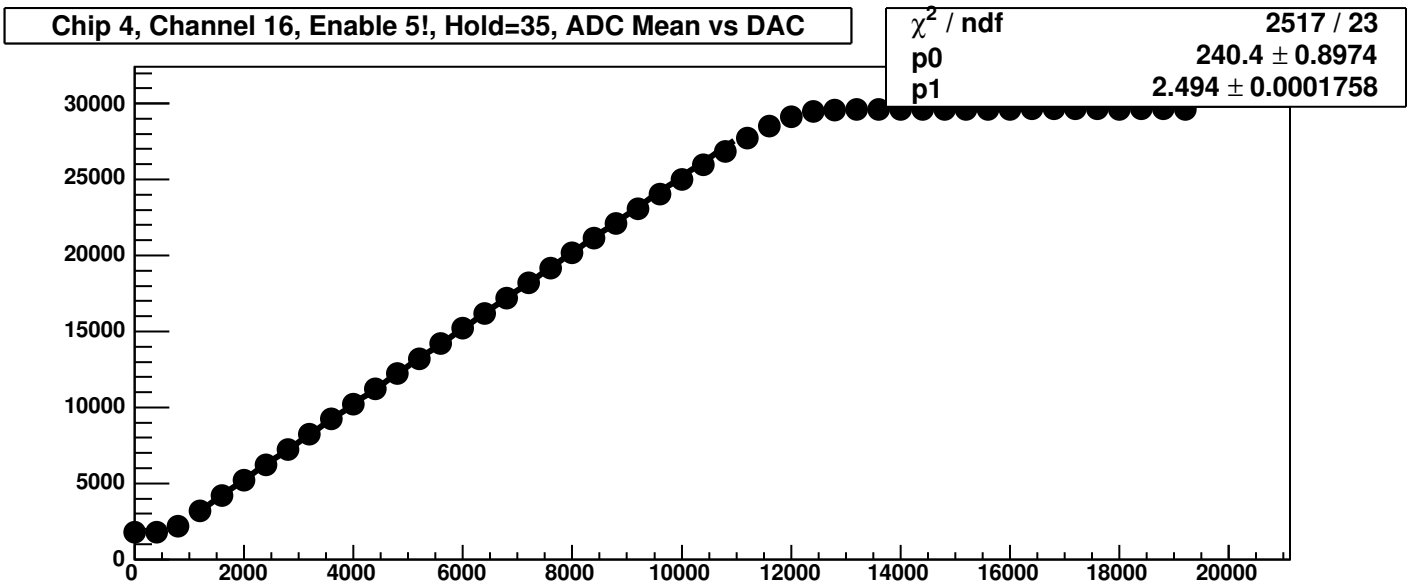


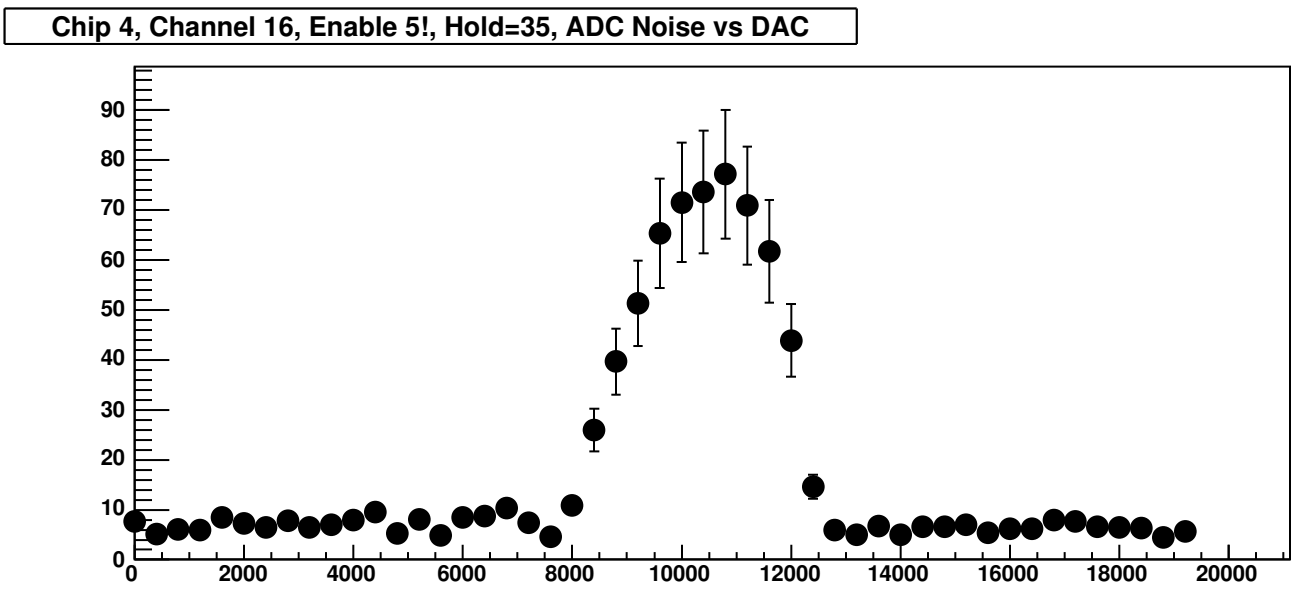
Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

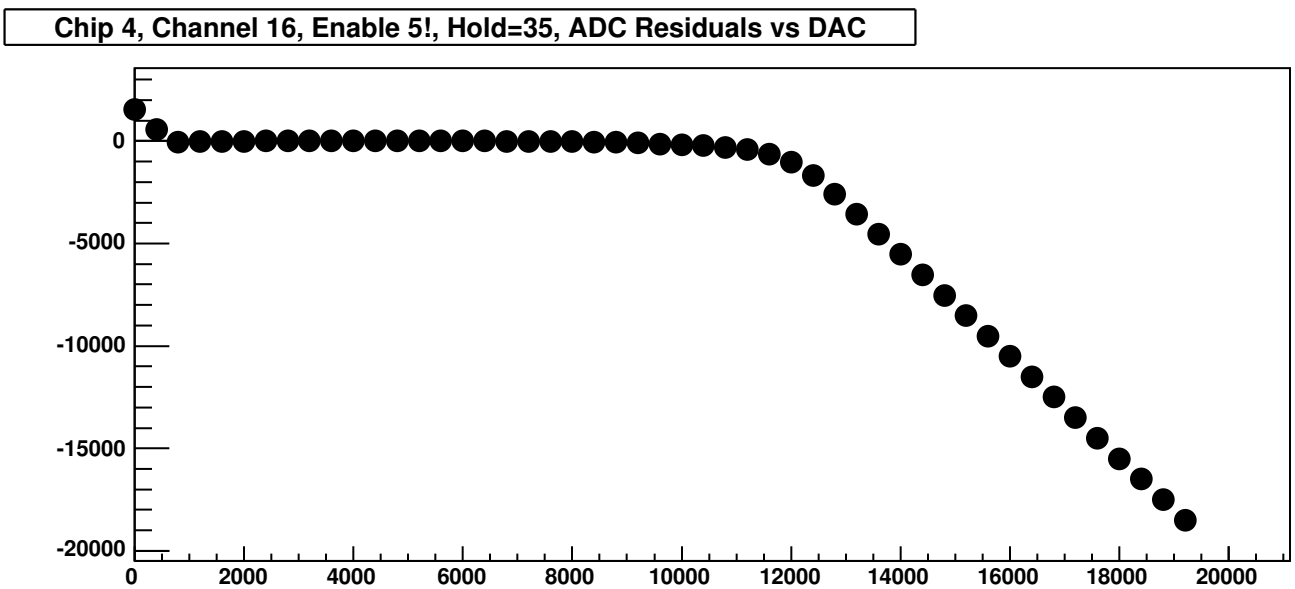


Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

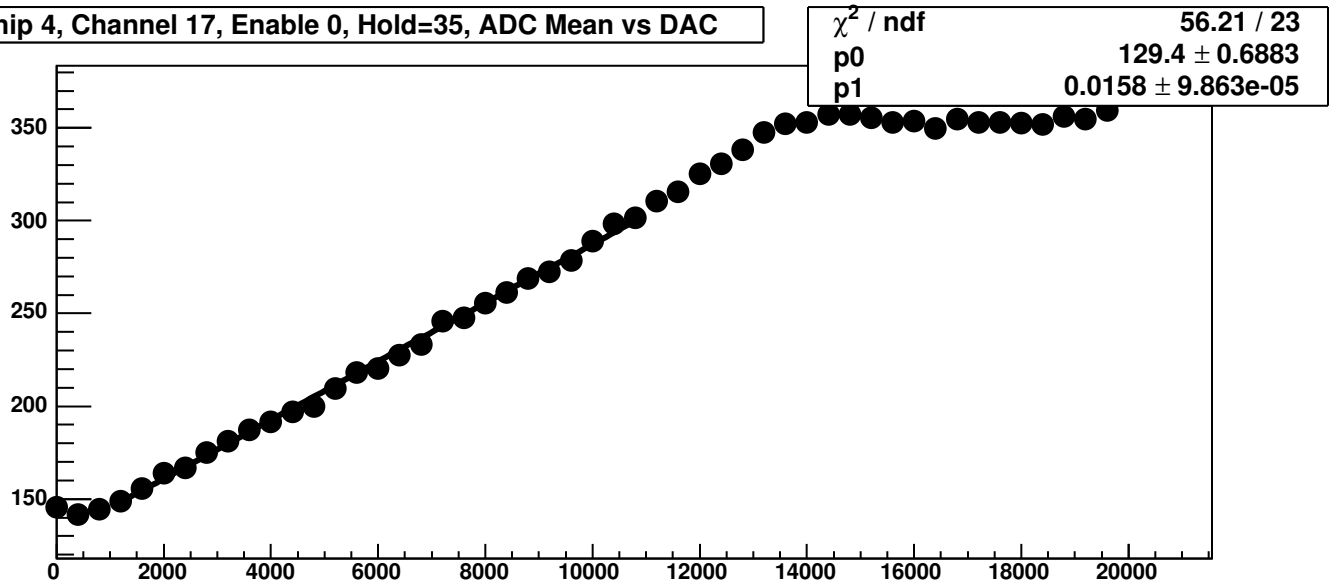



Chip 4, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2517 / 23
p0 240.4 ± 0.8974
p1 2.494 ± 0.0001758
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

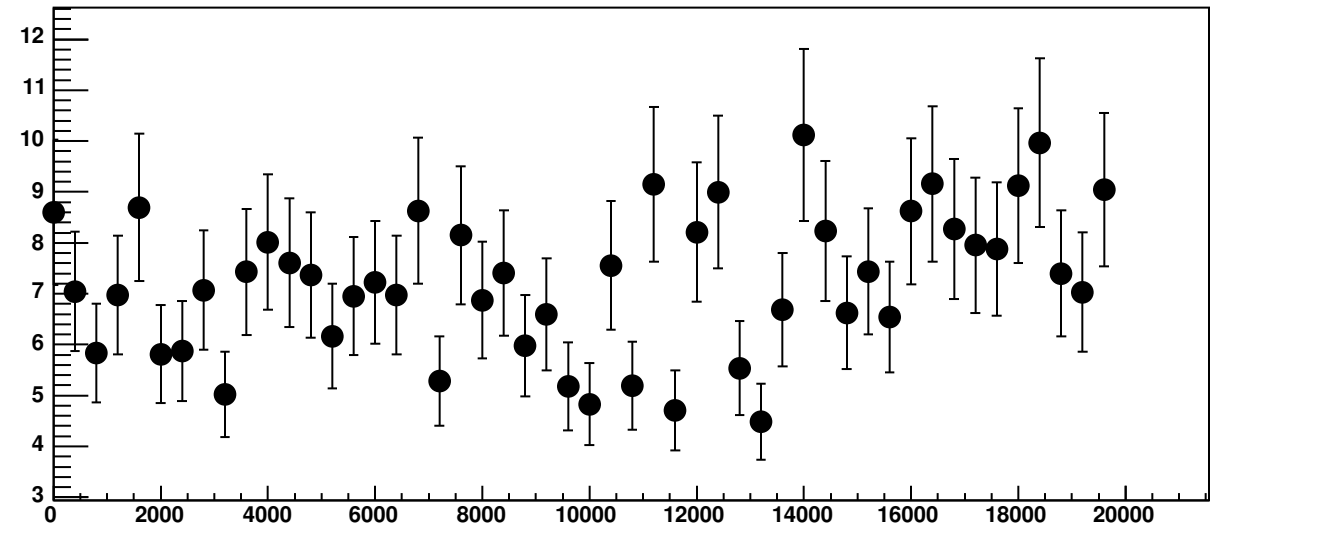

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

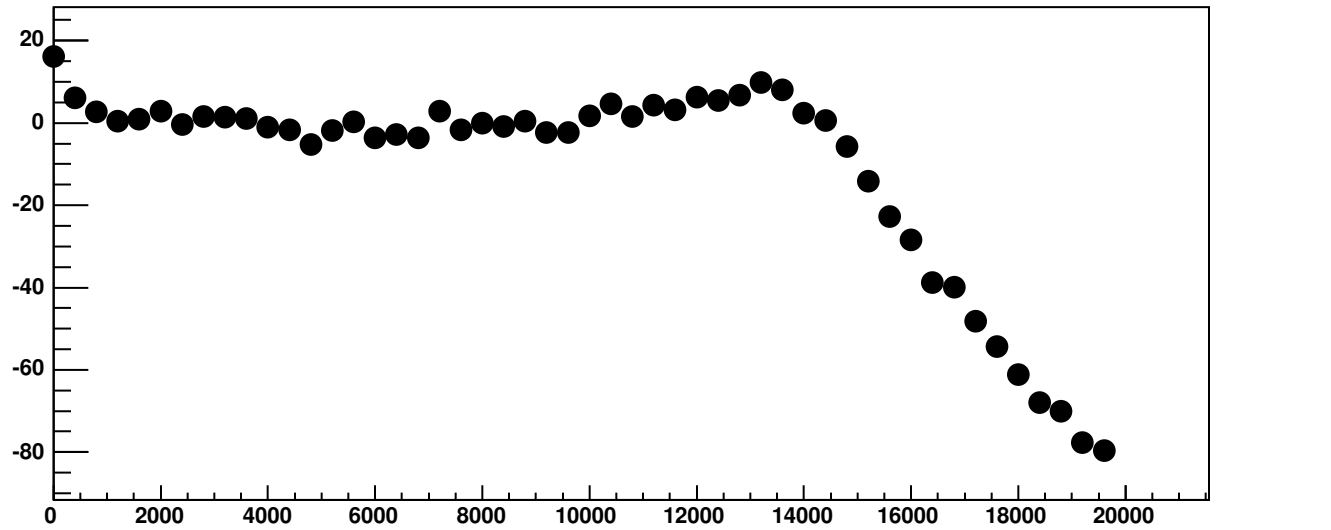
Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

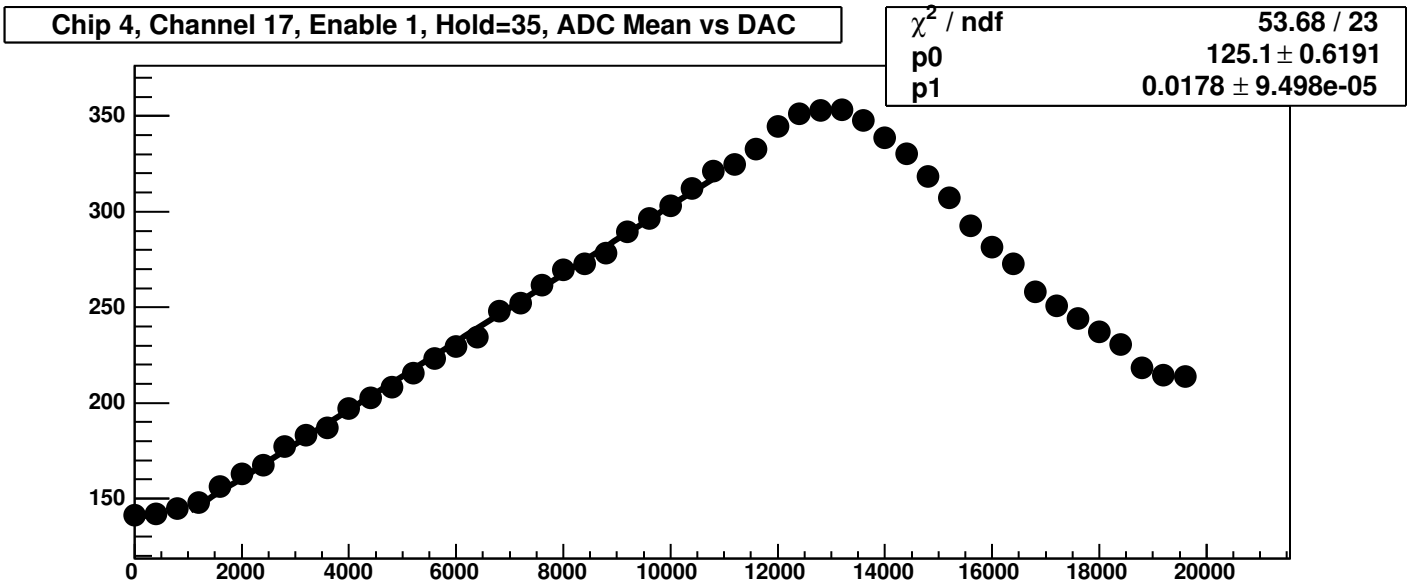


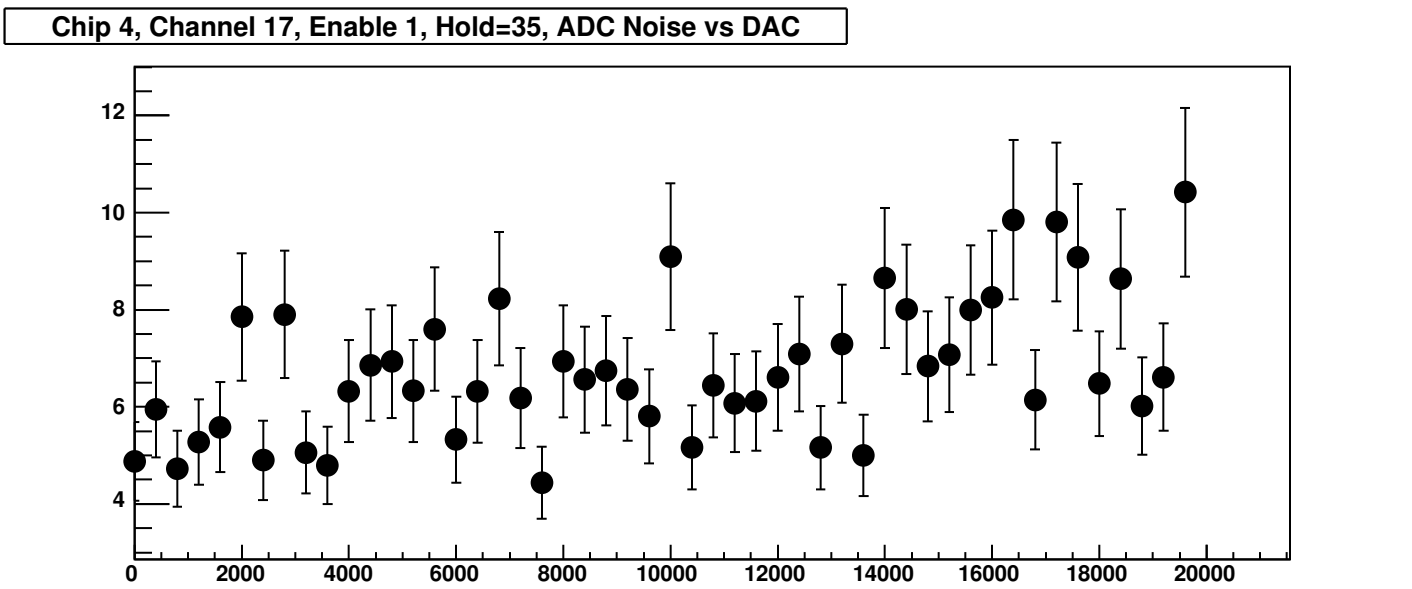
Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

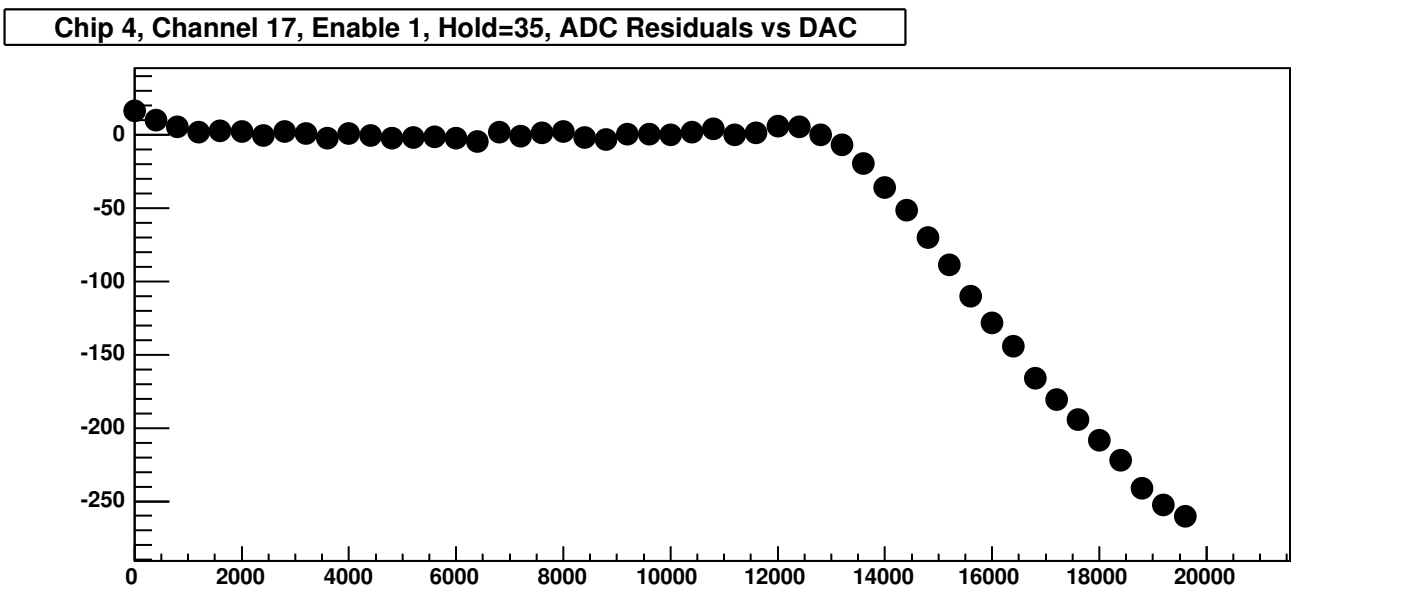


Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

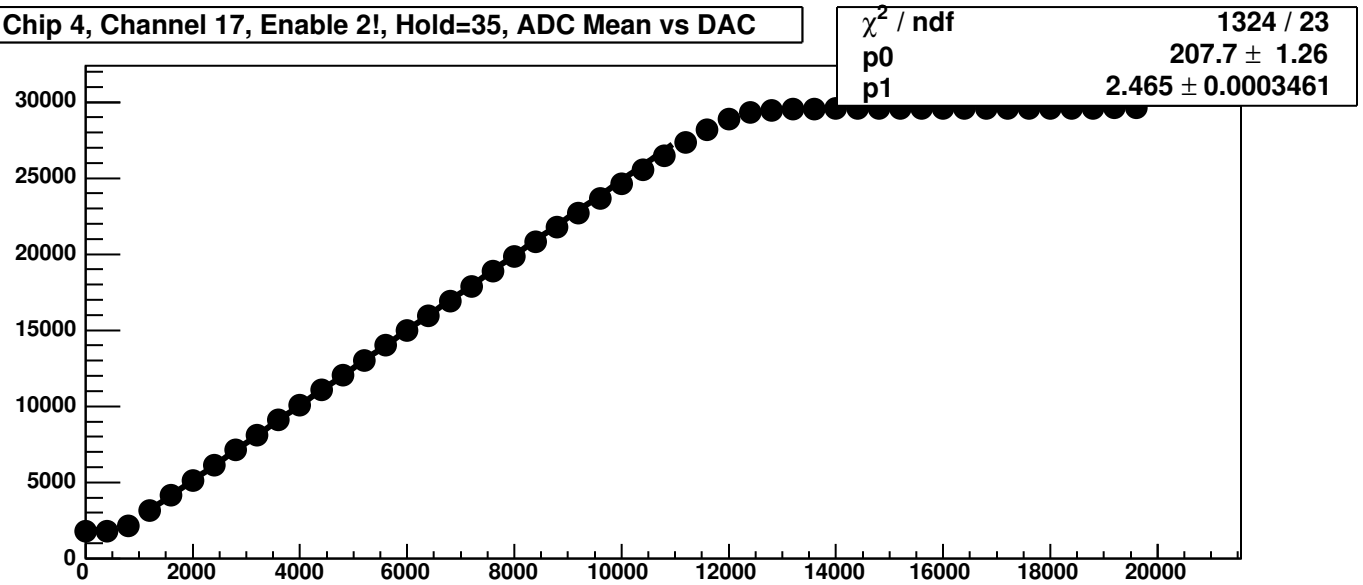



Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 53.68 / 23
p0 125.1 ± 0.6191
p1 0.0178 ± 9.498e-05
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

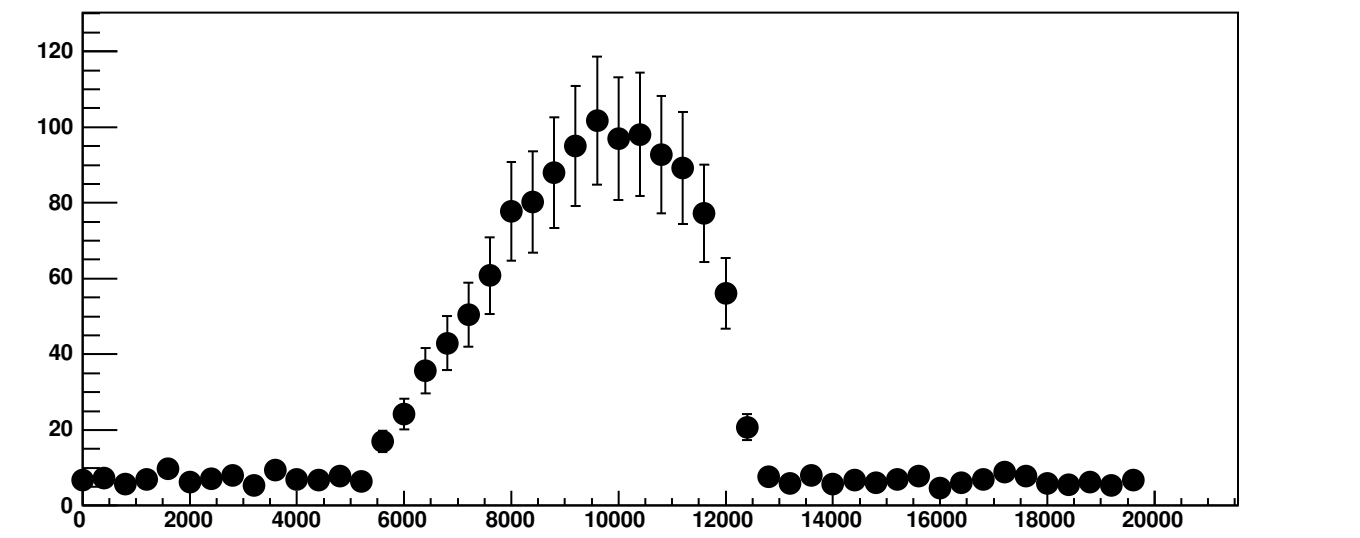

Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

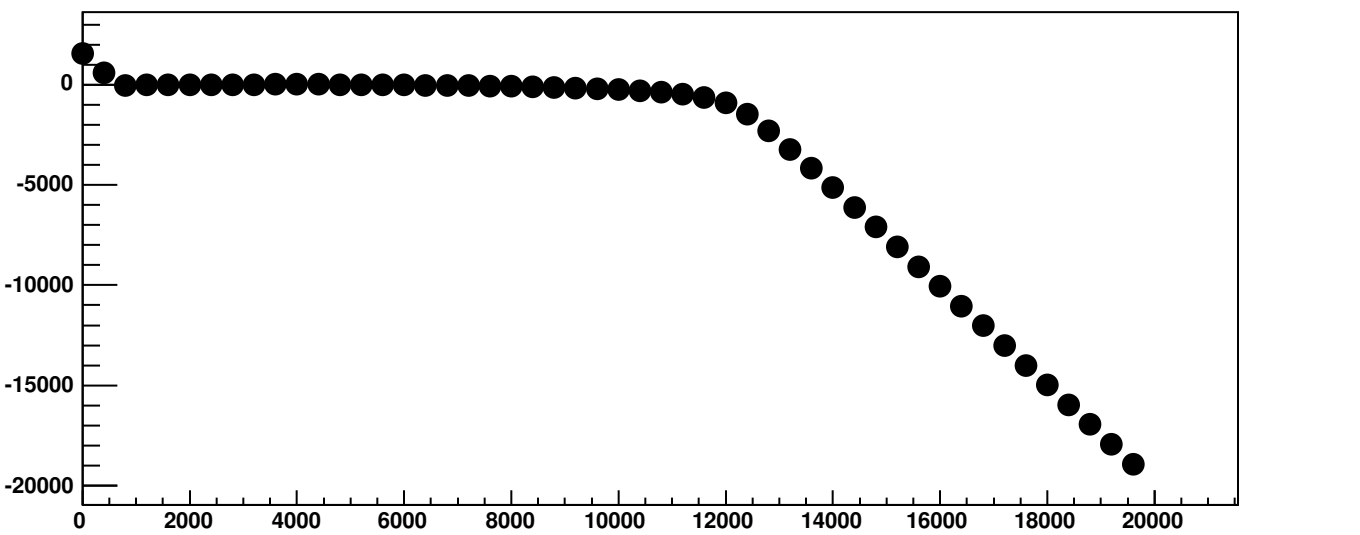
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



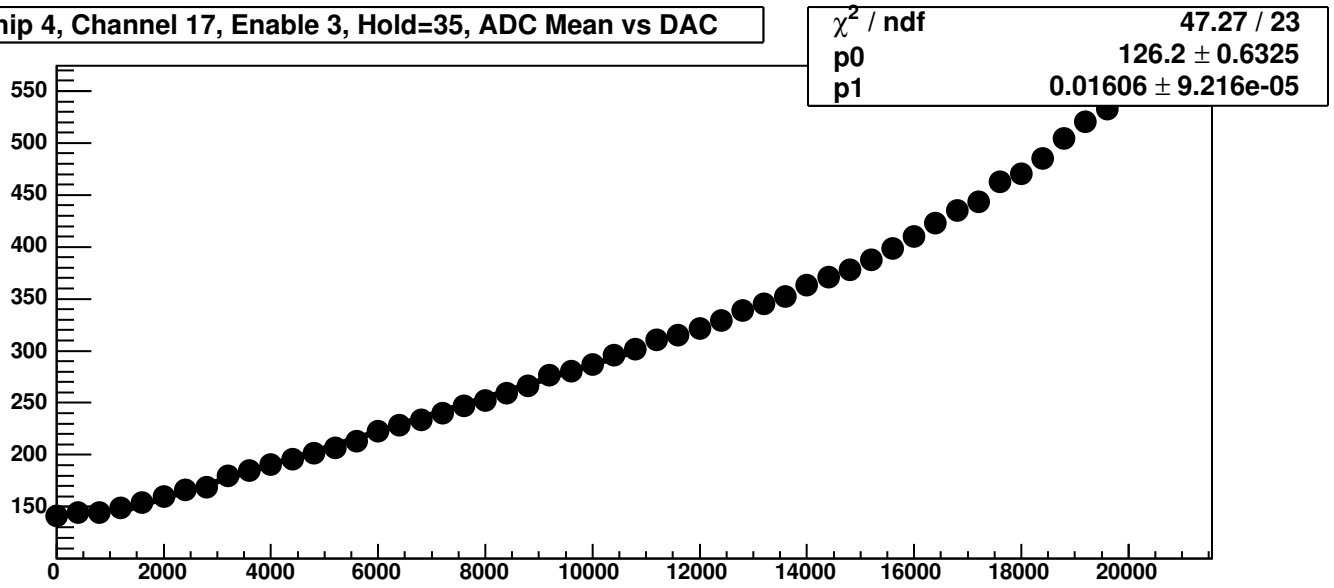
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



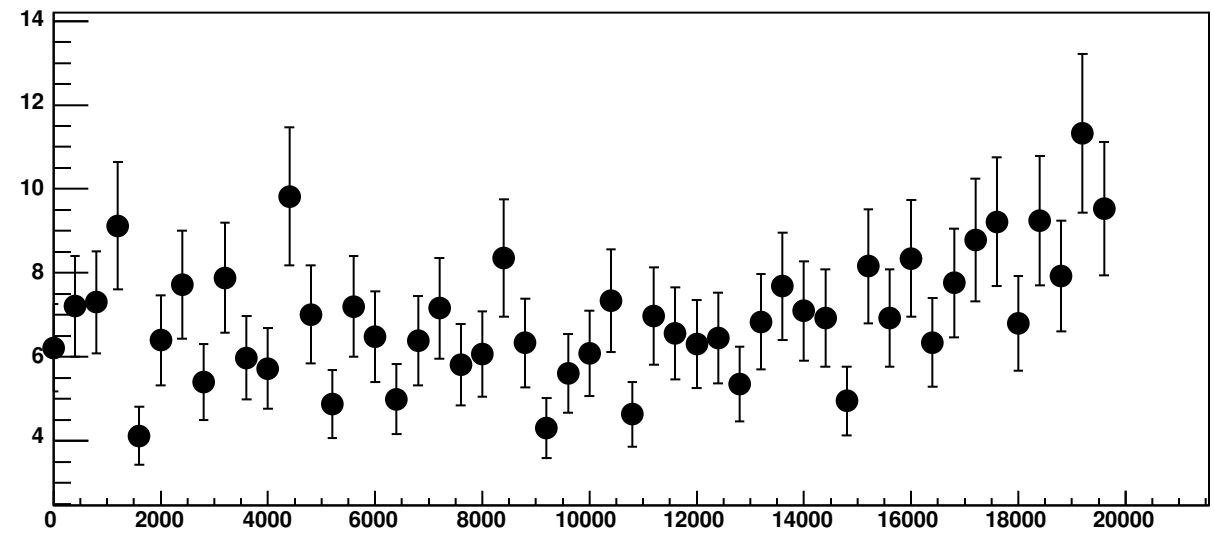
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC



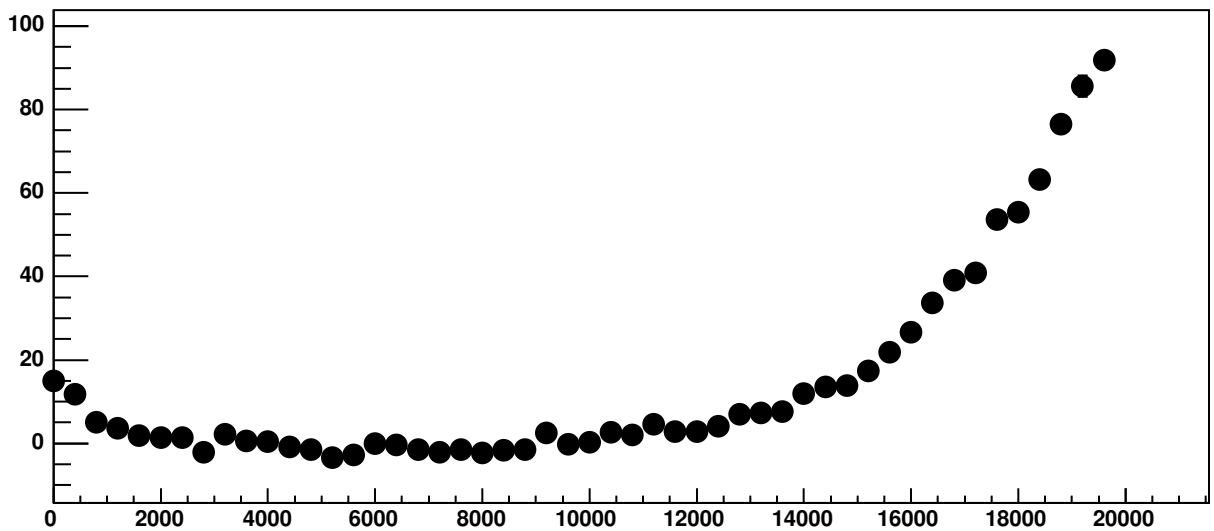
Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



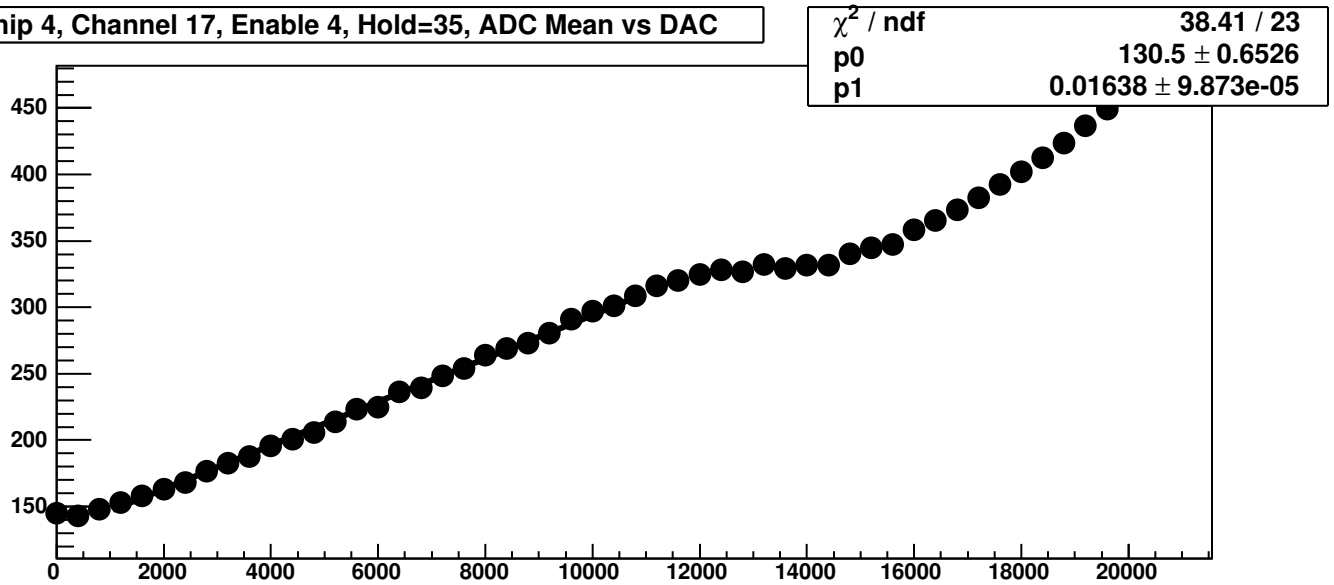
Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



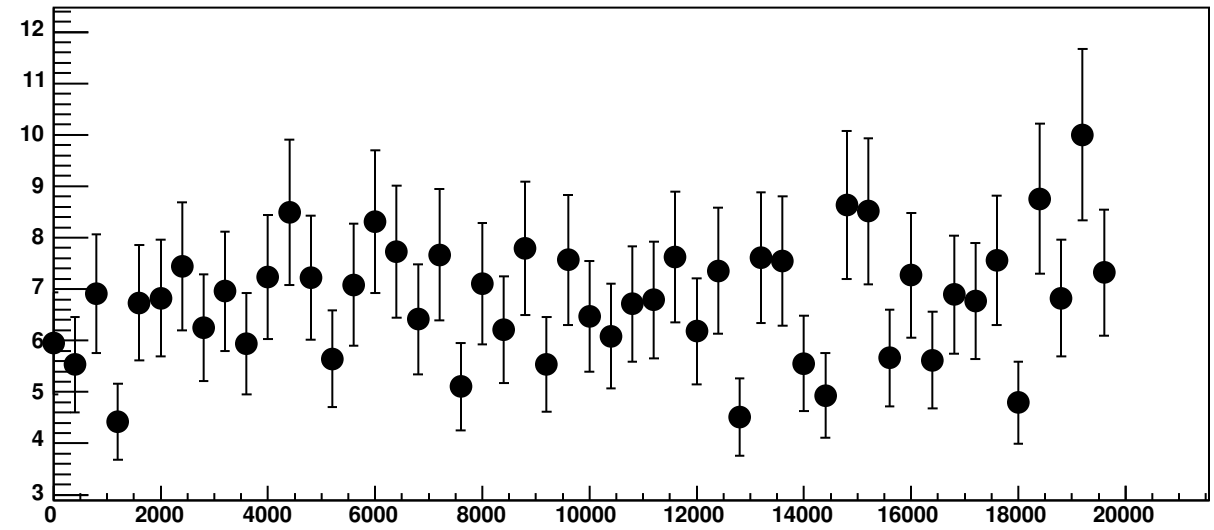
Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



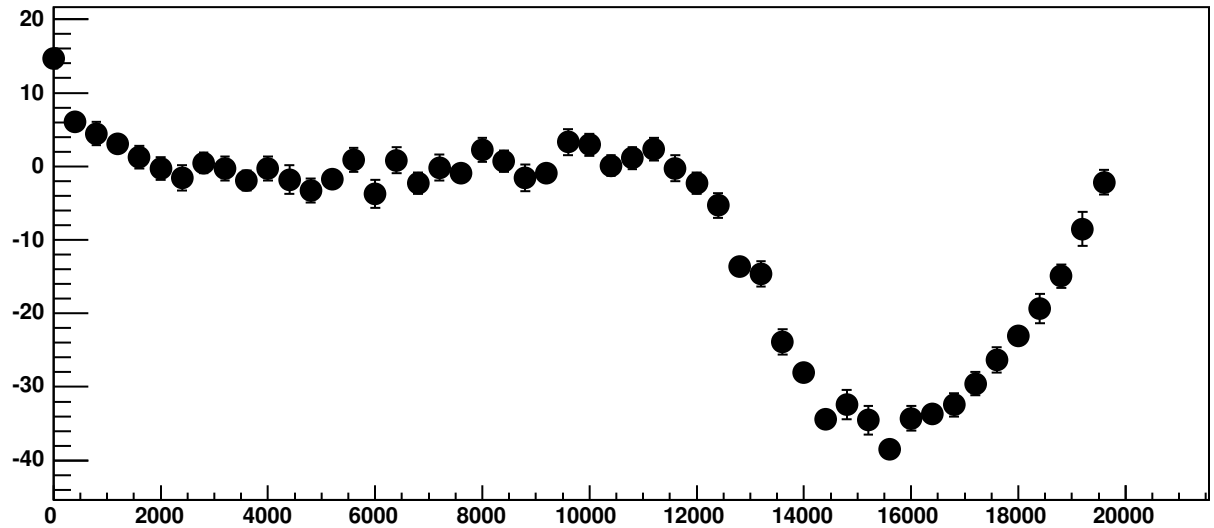
Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

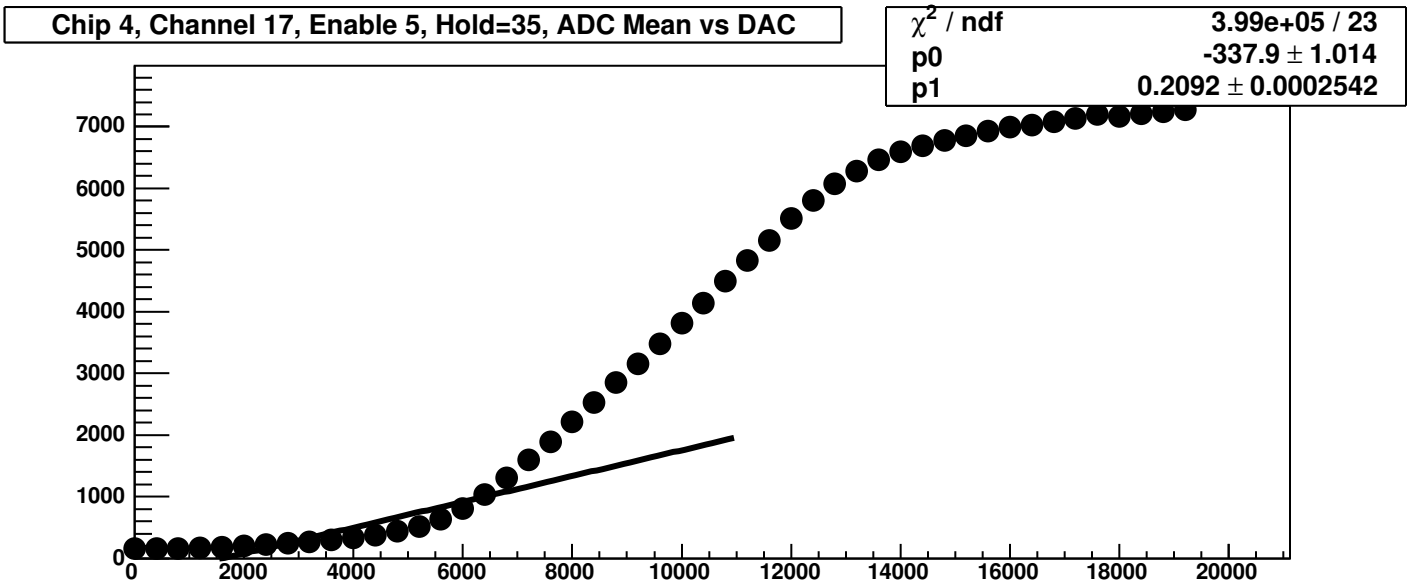


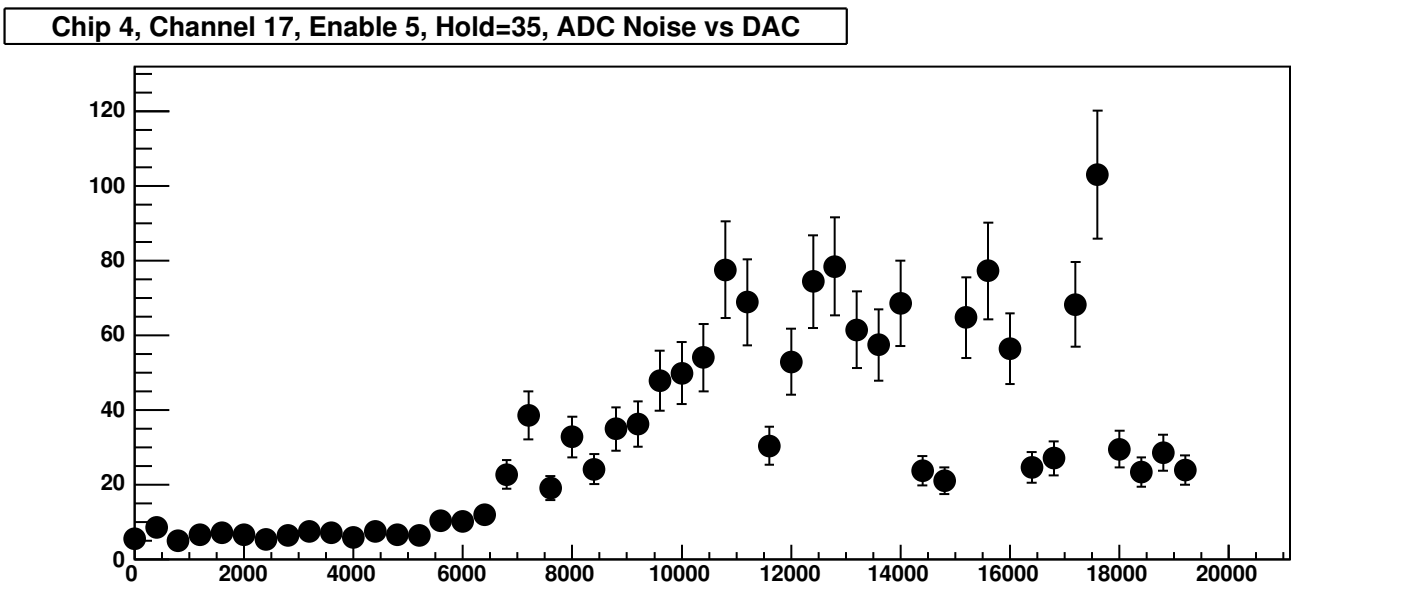
Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

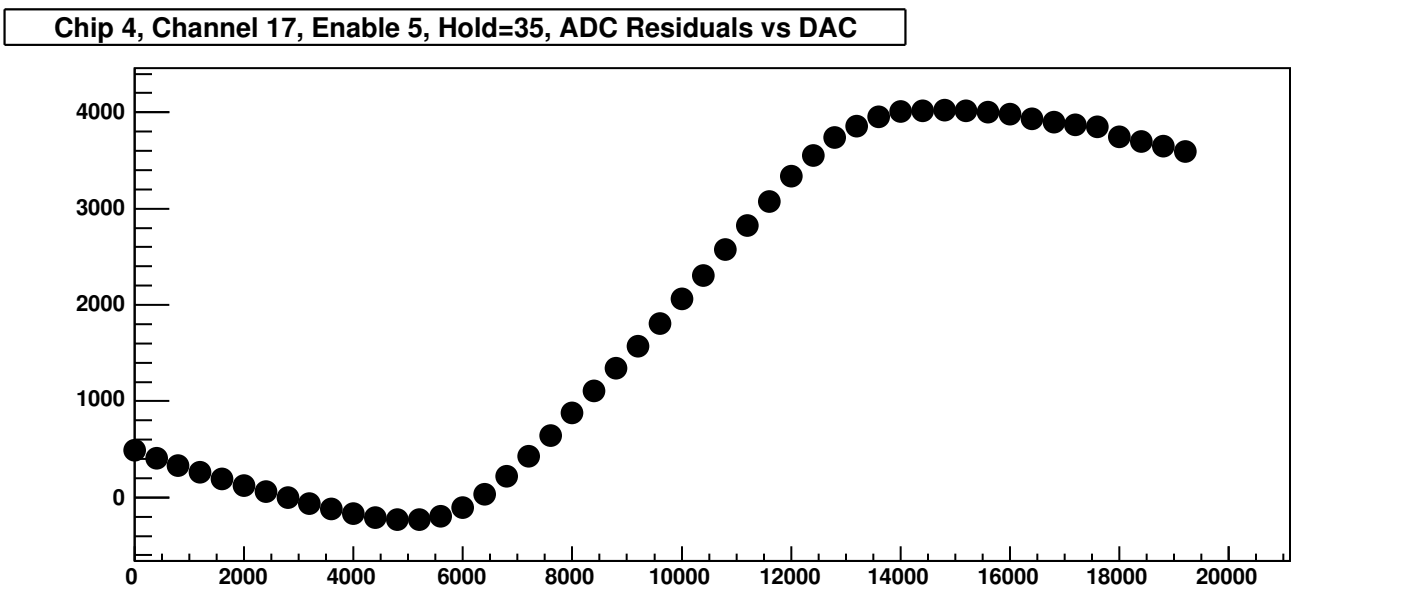


Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

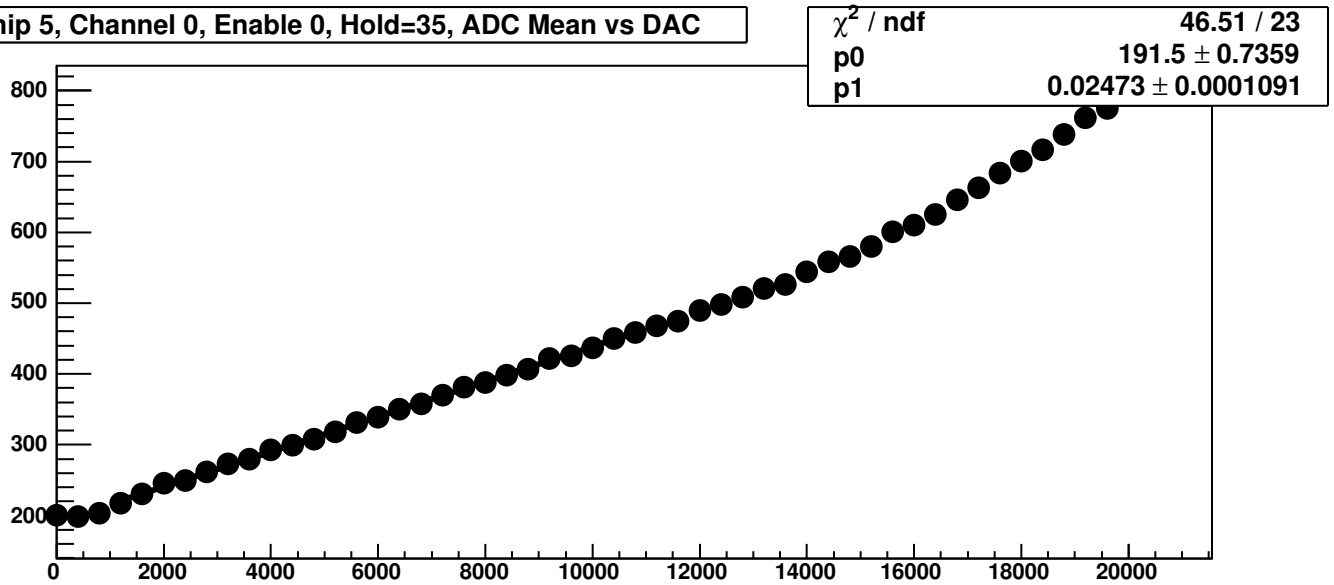



Chip 4, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.99e+05 / 23
p0 -337.9 ± 1.014
p1 0.2092 ± 0.0002542
0 1000 2000 3000 4000 5000 6000 7000
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

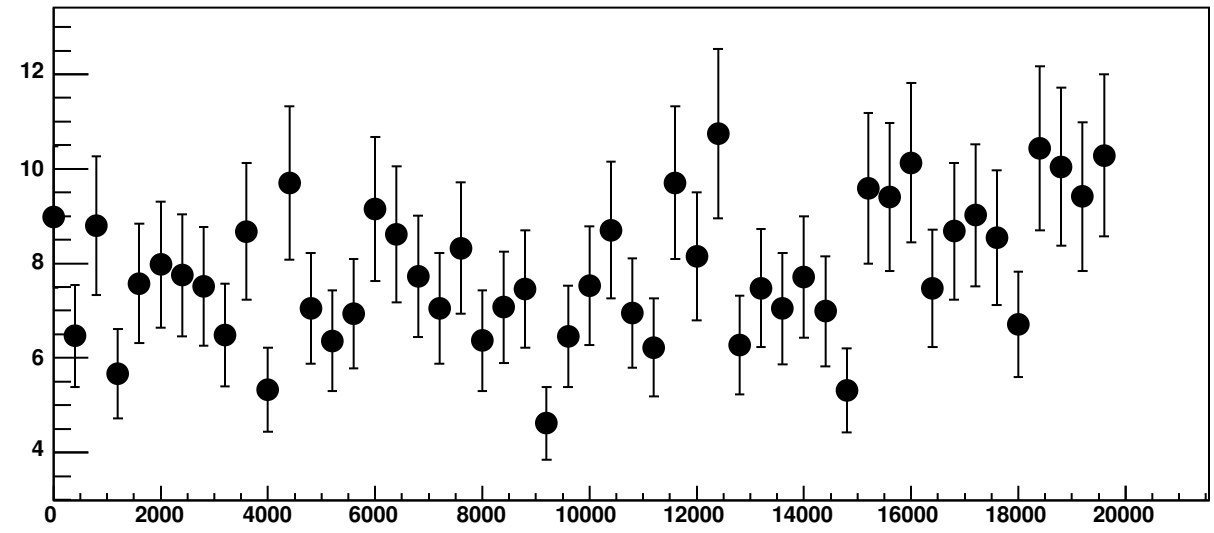

Chip 4, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
0 20 40 60 80 100 120
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000


Chip 4, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
0 1000 2000 3000 4000
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

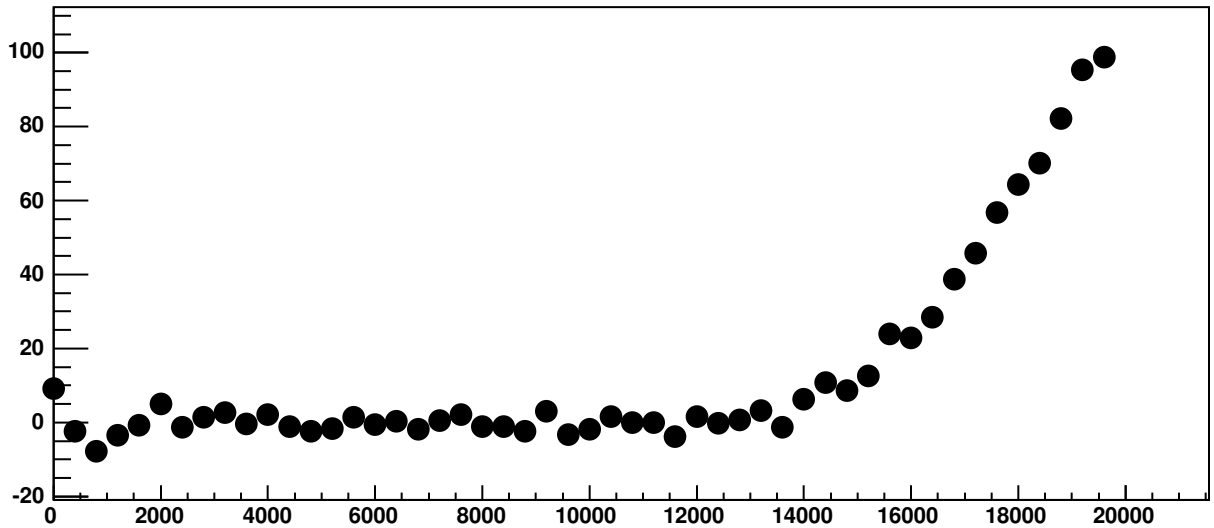
Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

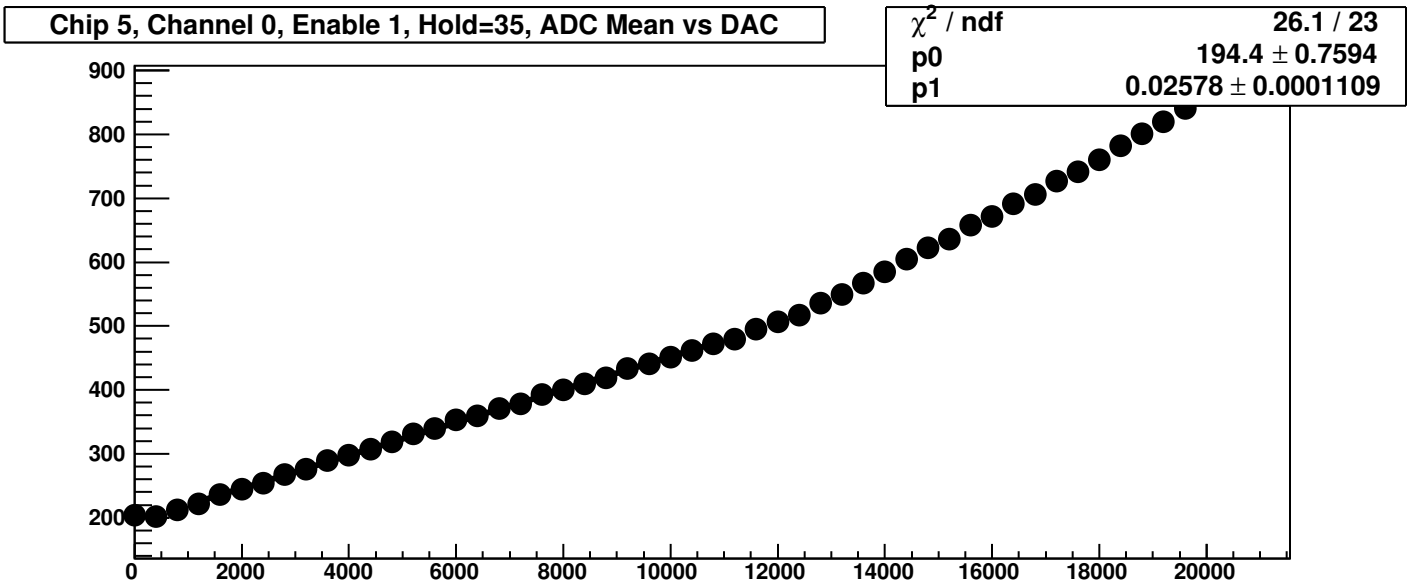


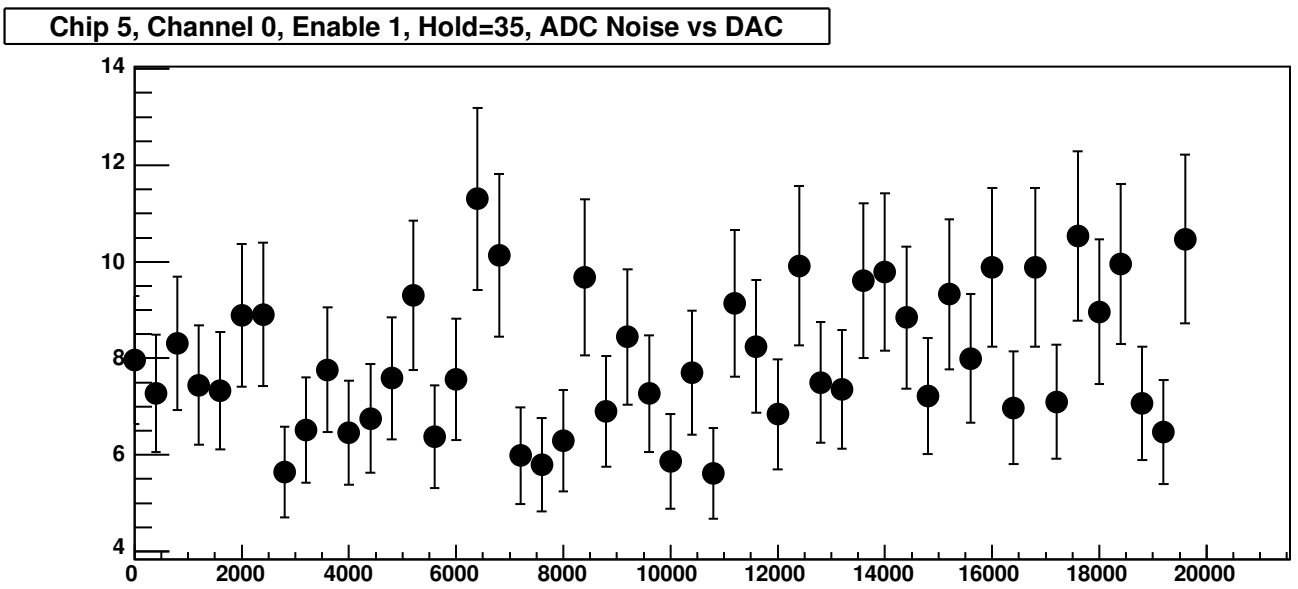
Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC

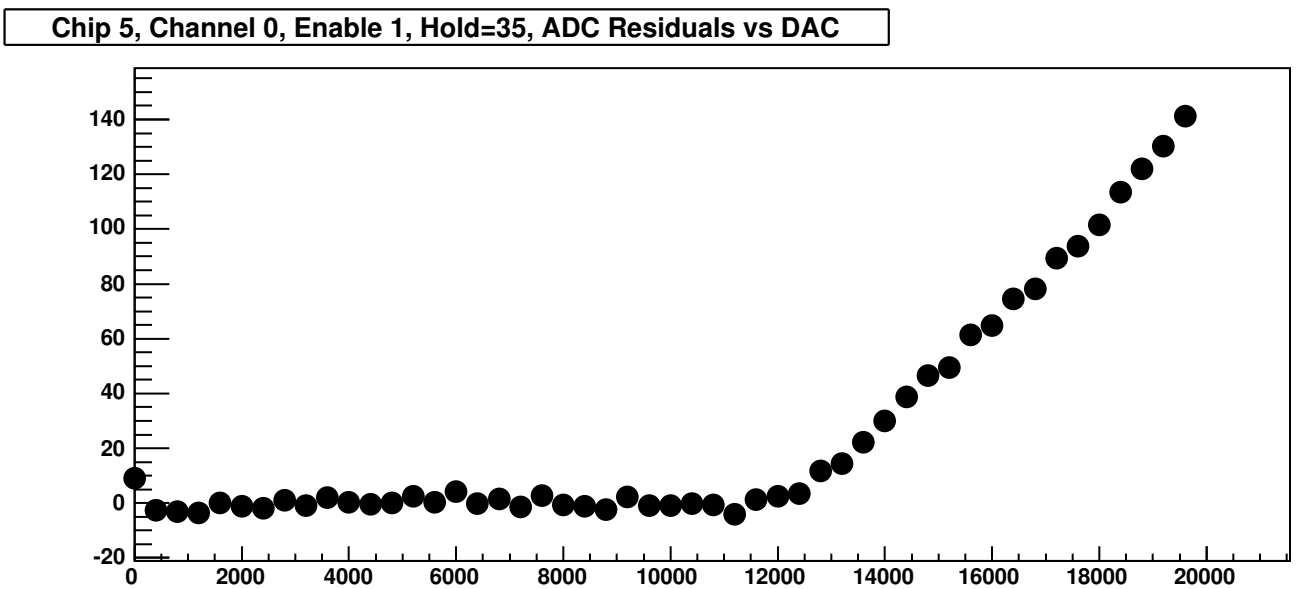


Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

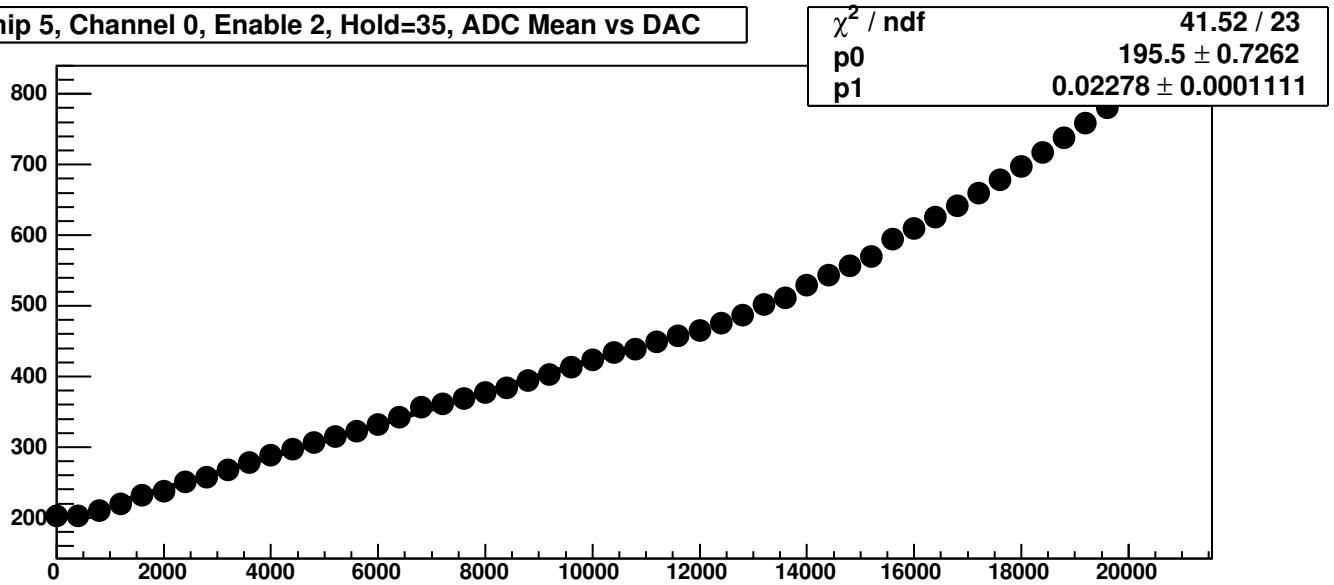



Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.1 / 23
p0 194.4 ± 0.7594
p1 0.02578 ± 0.0001109
900
800
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

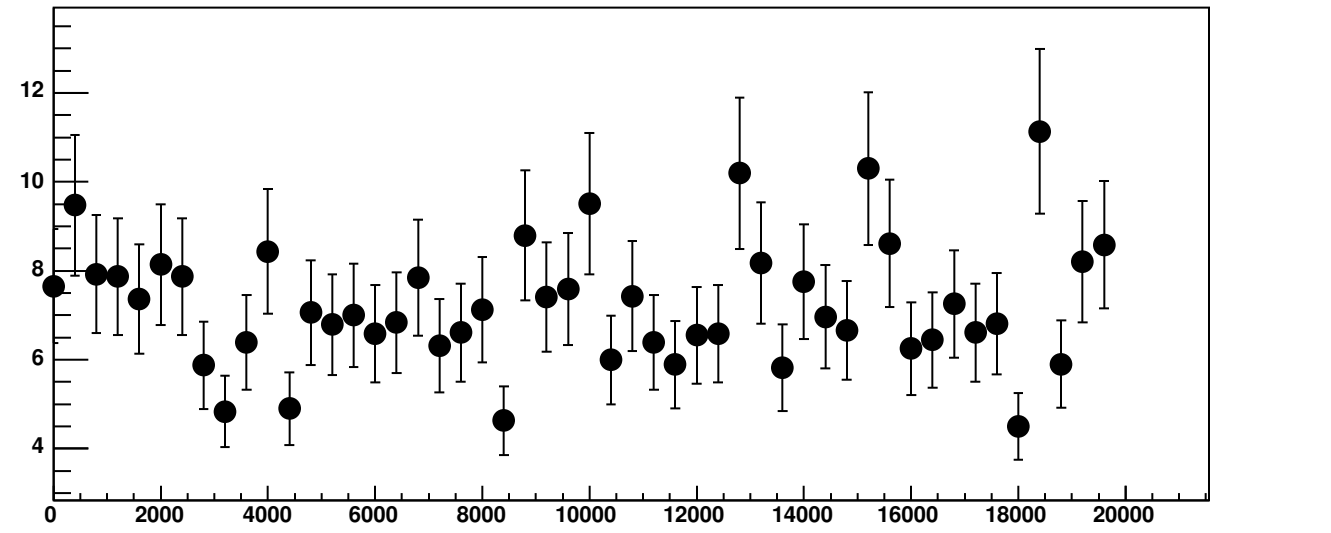

Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

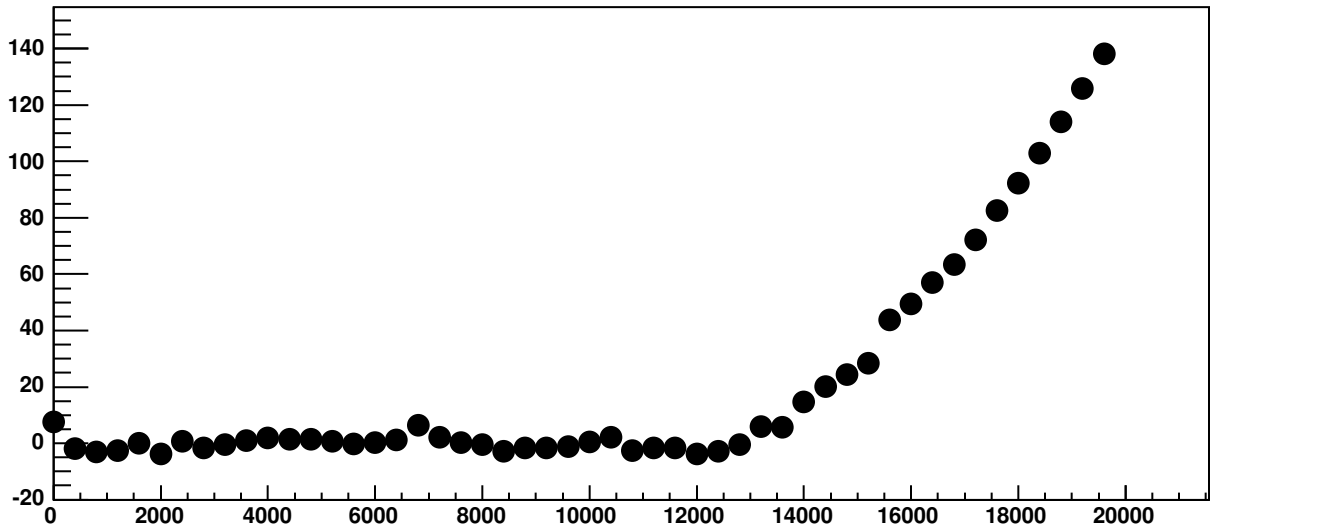
Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

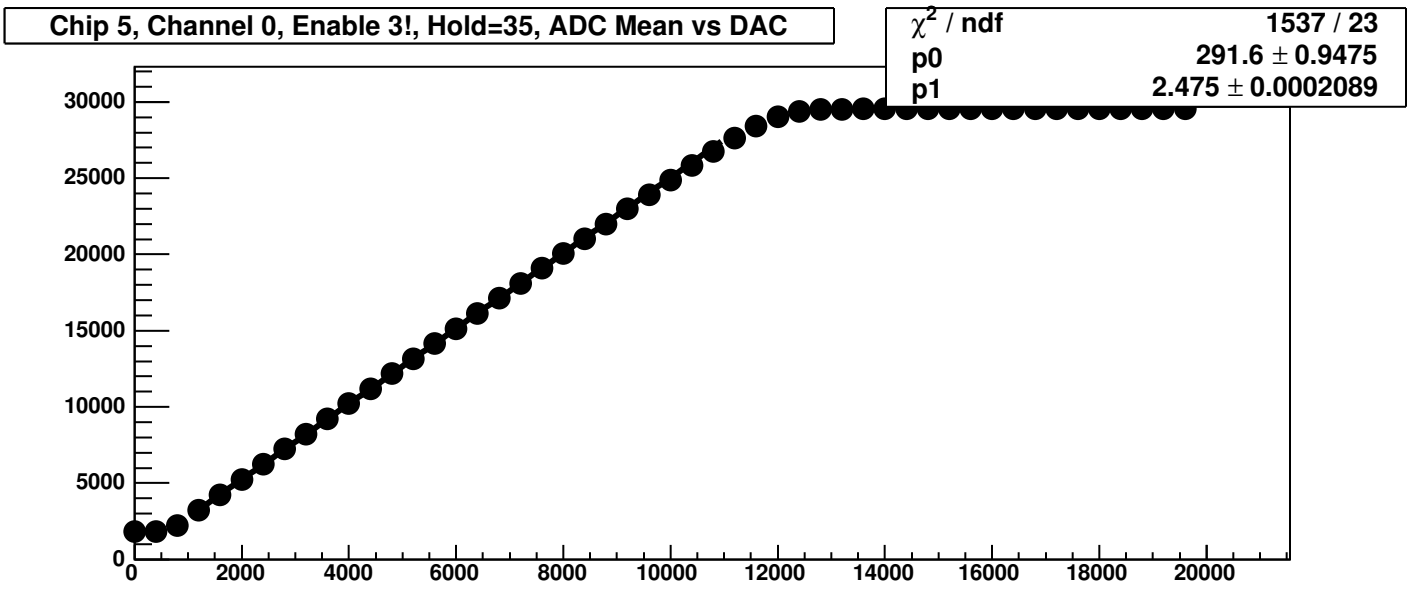


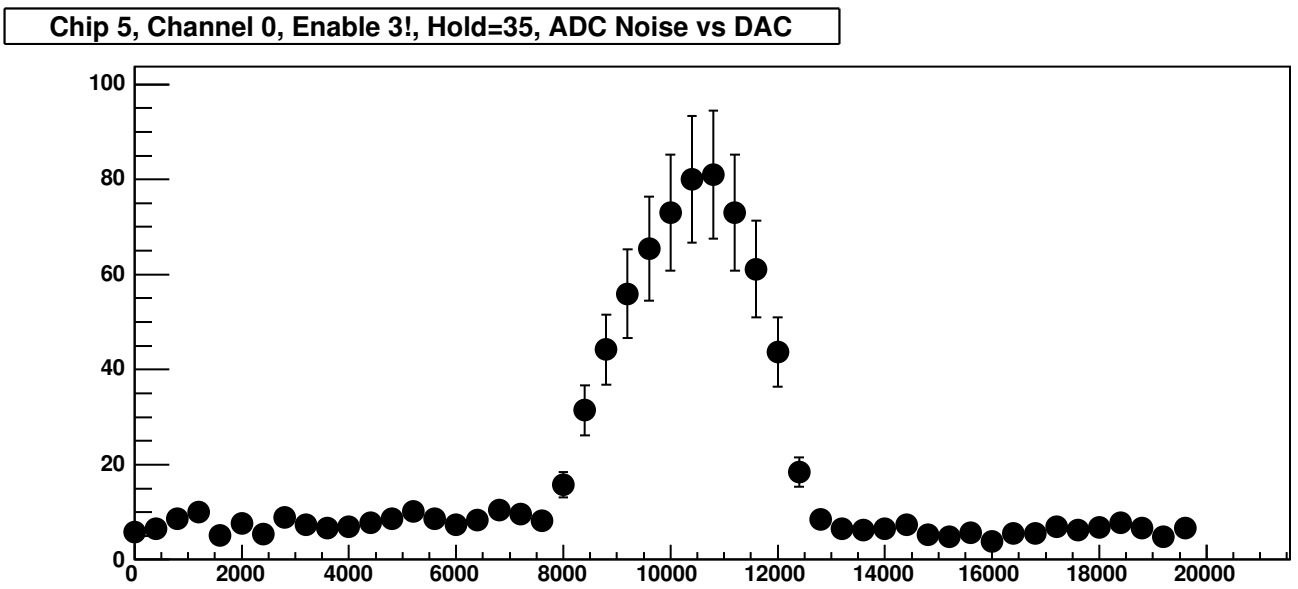
Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

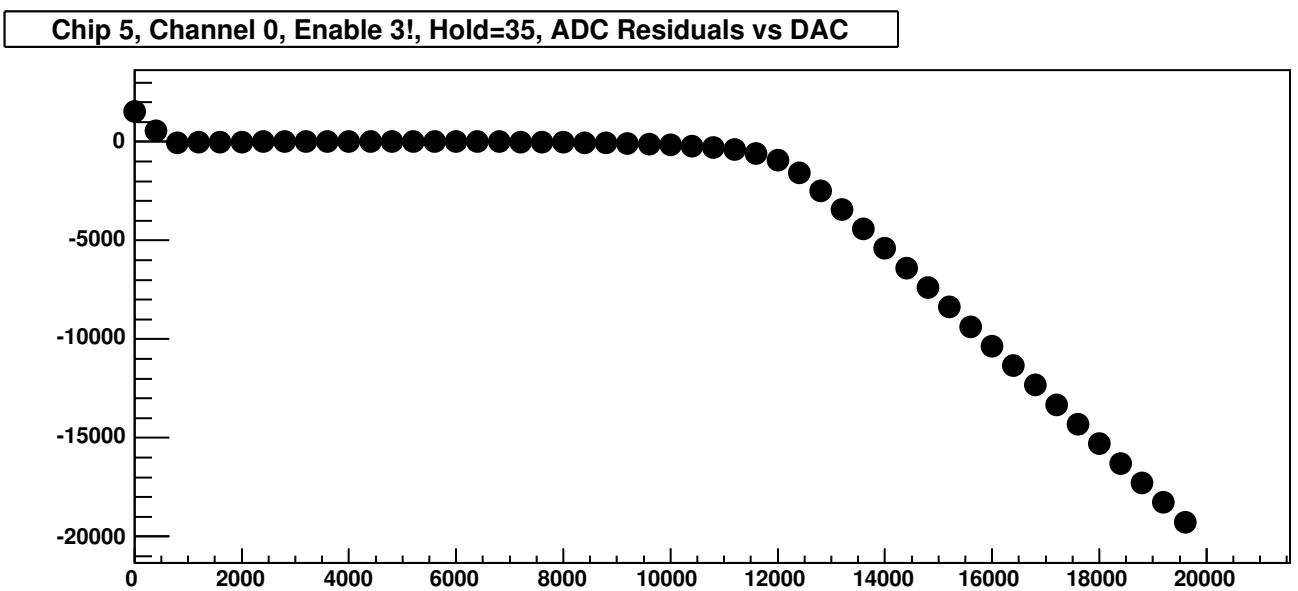


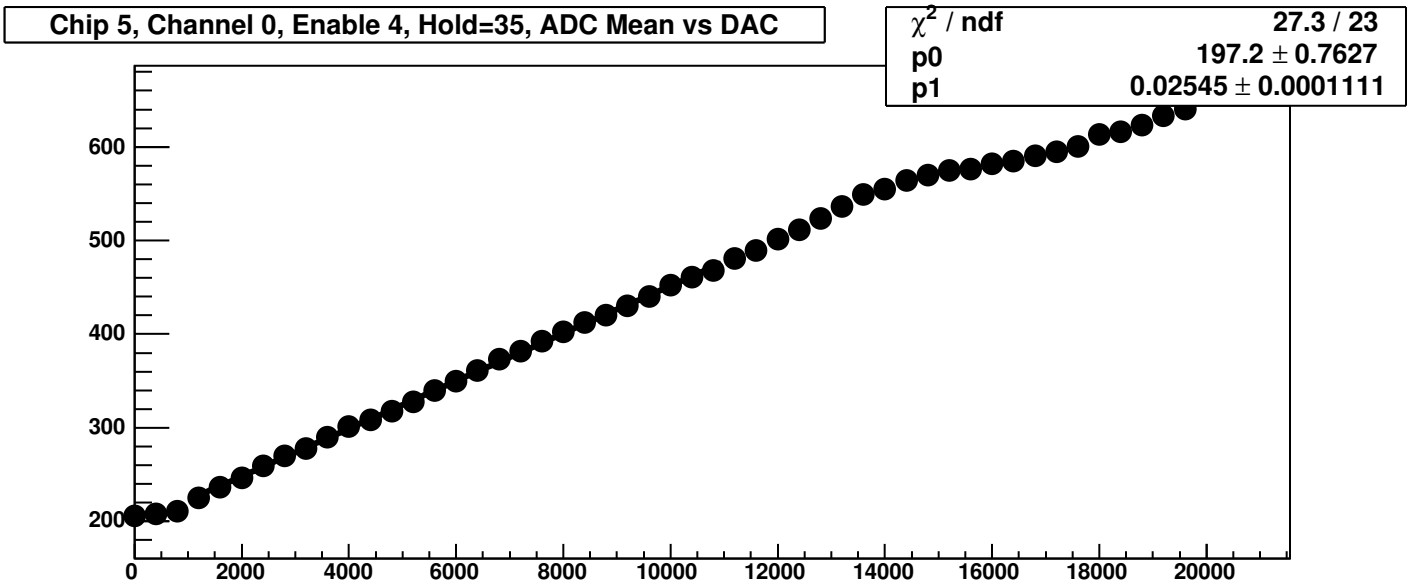
Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

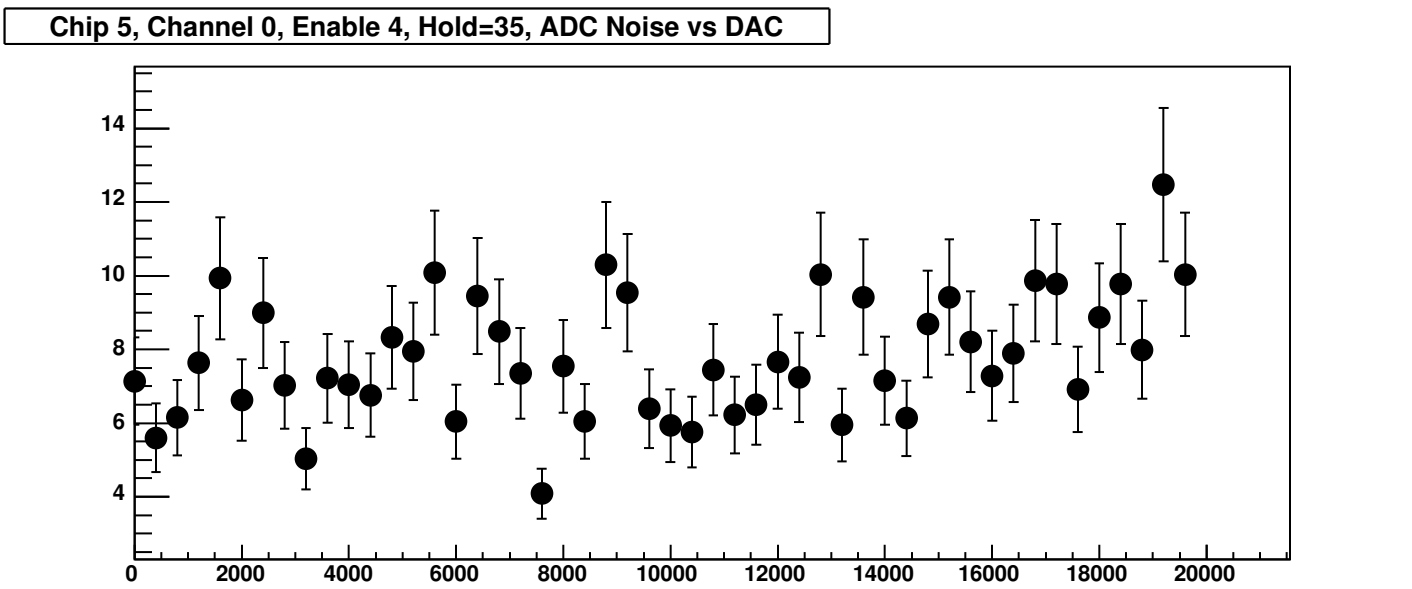


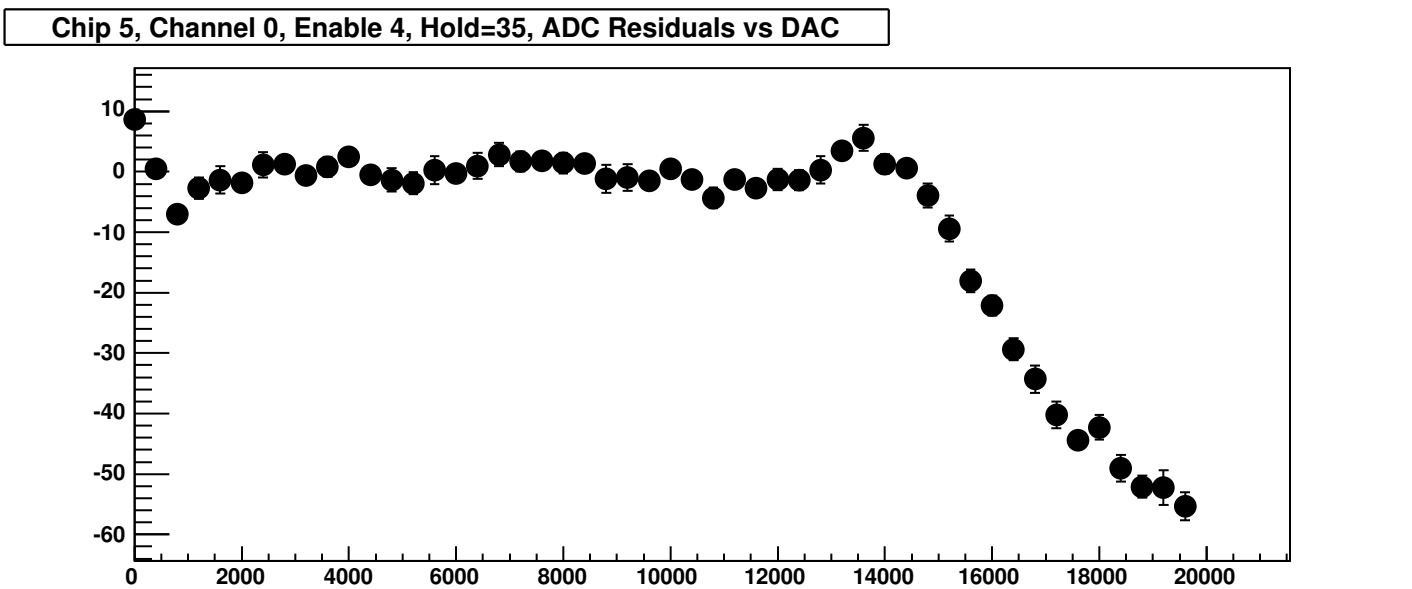

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1537 / 23
p0 291.6 ± 0.9475
p1 2.475 ± 0.0002089
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

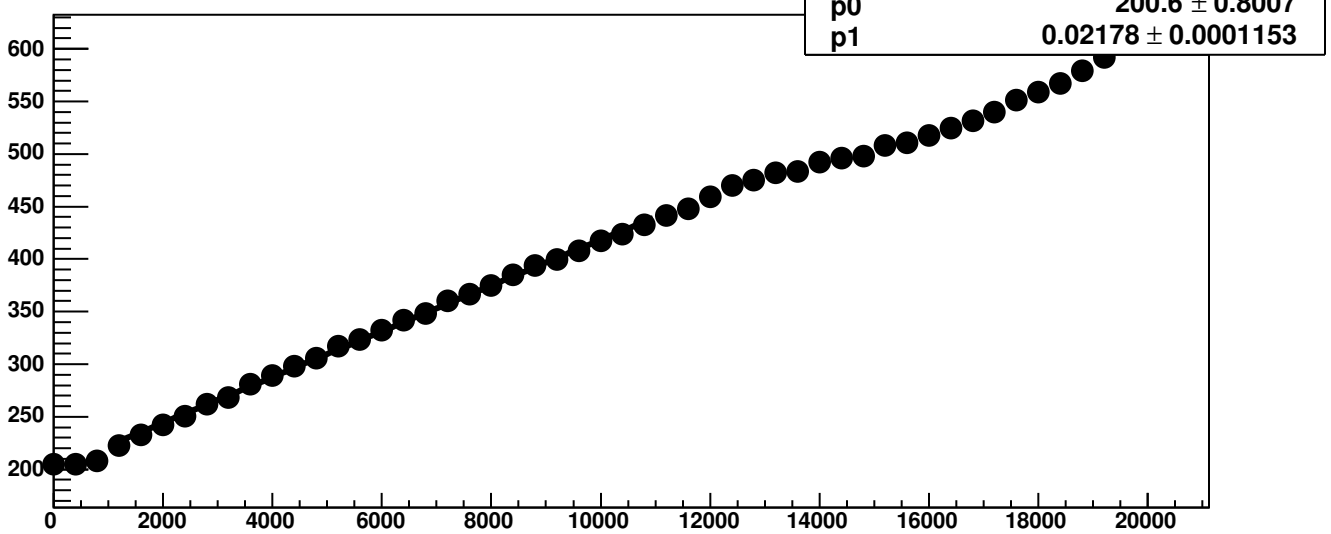

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.3 / 23
p0 197.2 ± 0.7627
p1 0.02545 ± 0.0001111
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

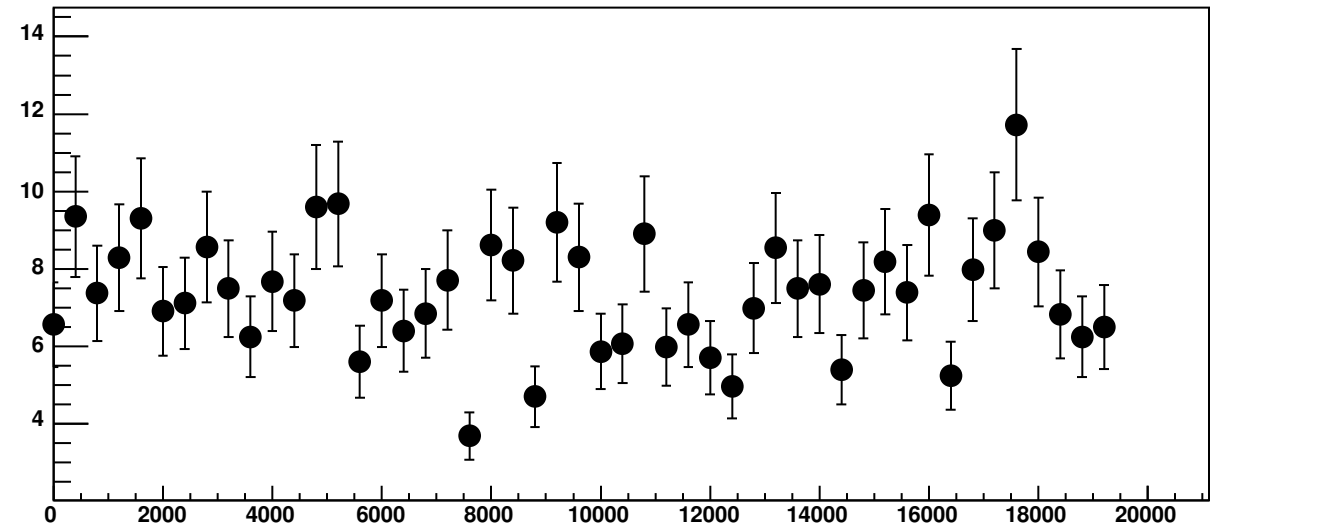

Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

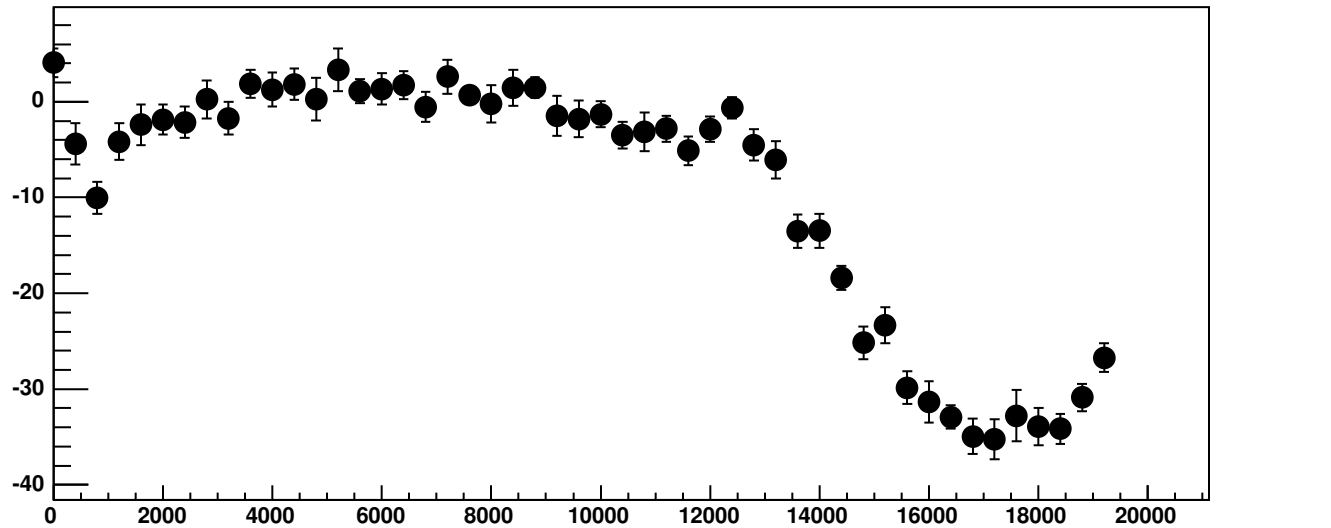
Chip 5, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

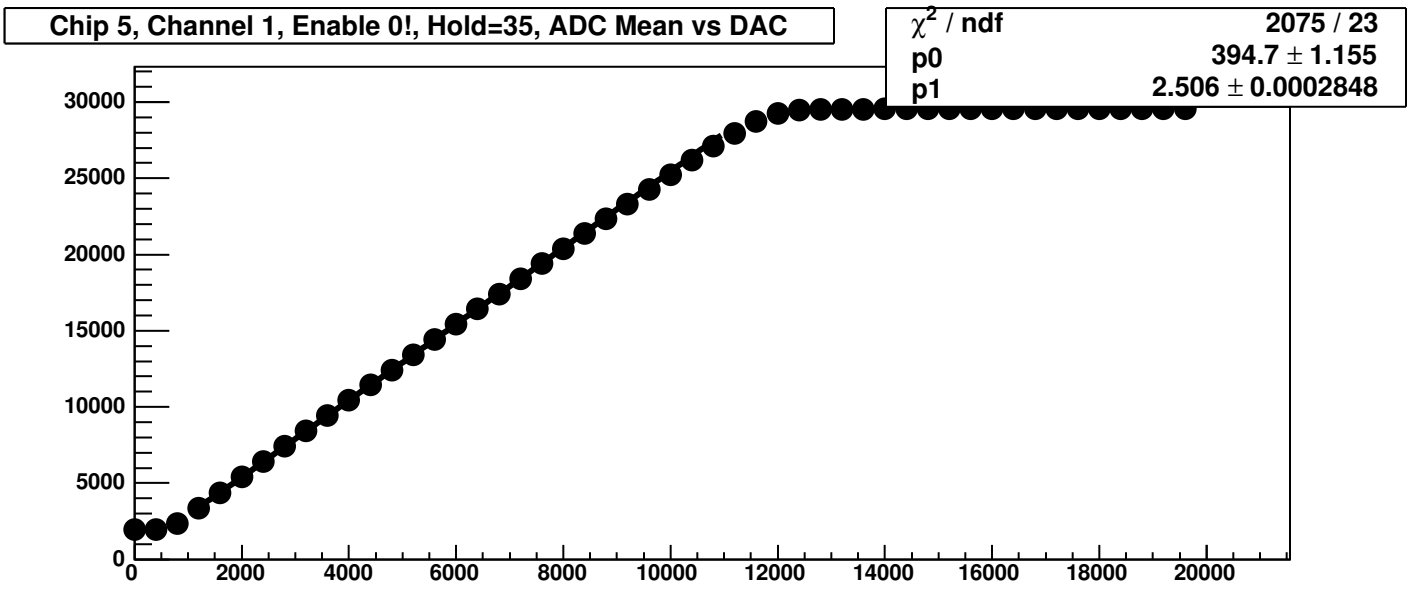


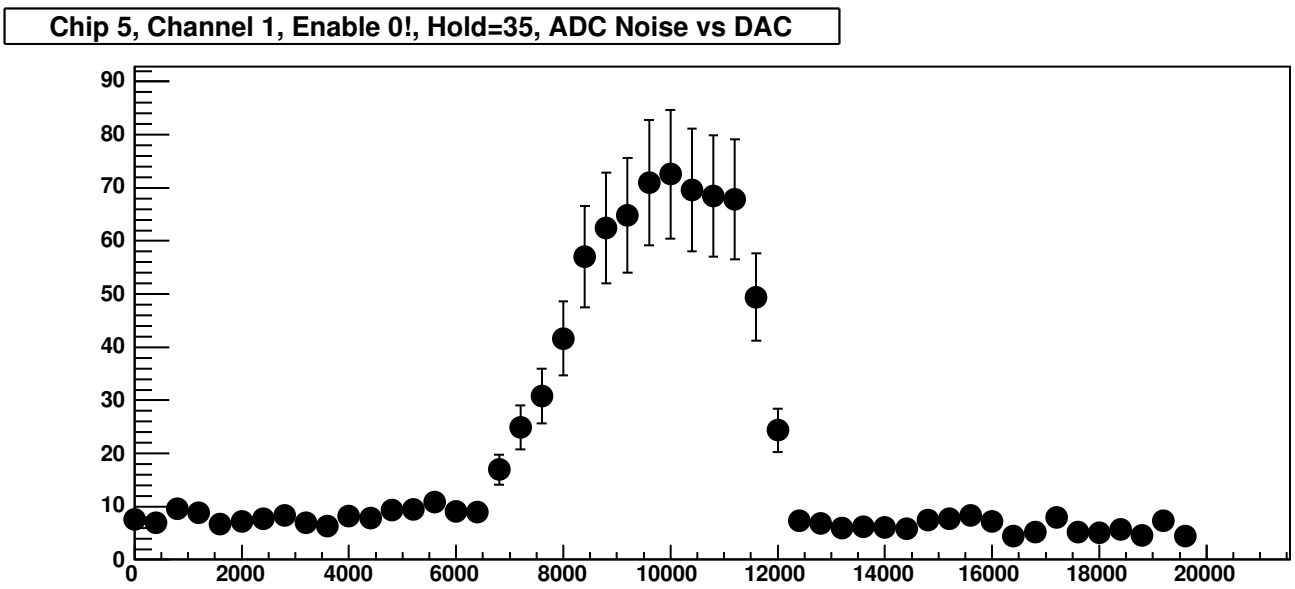
Chip 5, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

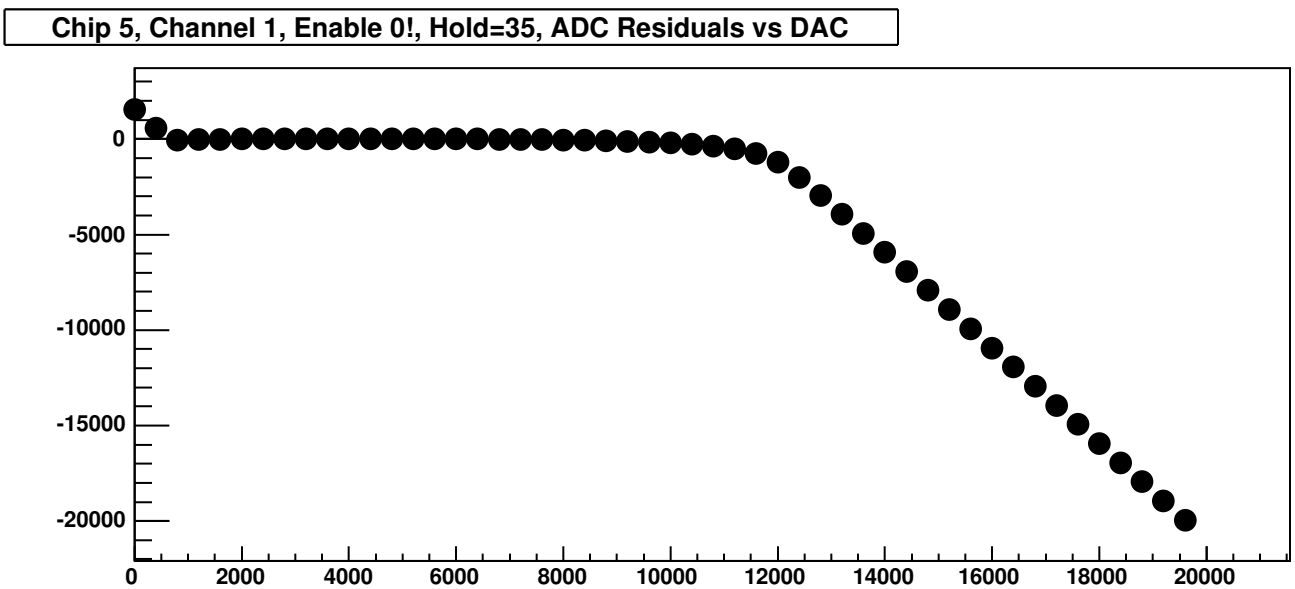


Chip 5, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

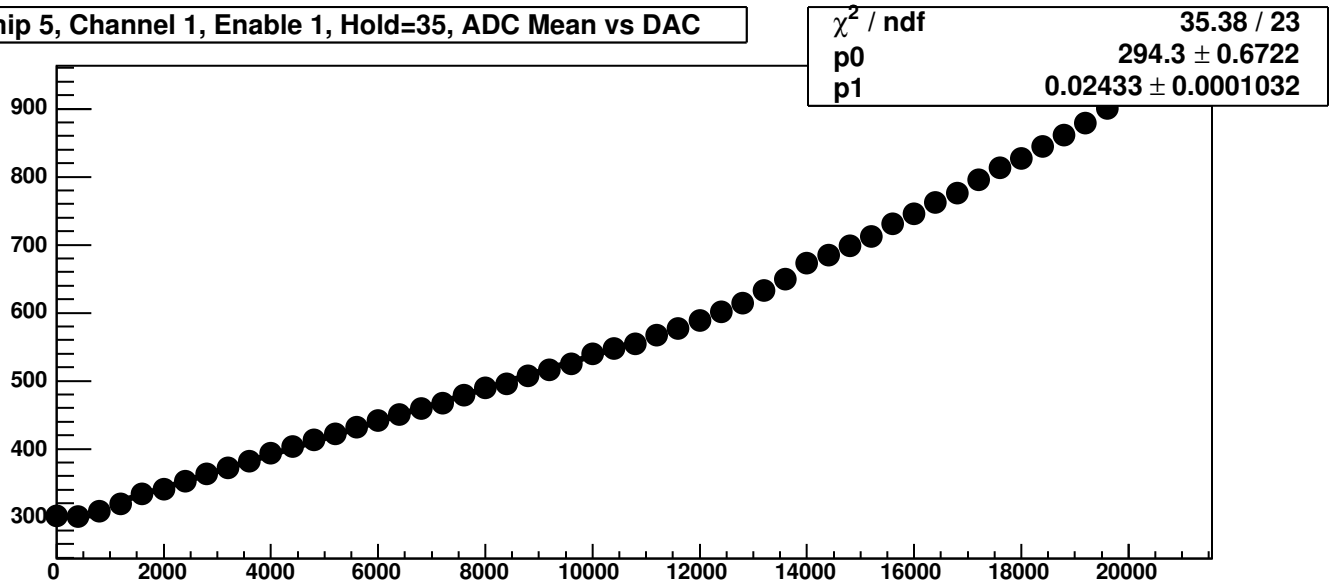



Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 2075 / 23
p0 394.7 ± 1.155
p1 2.506 ± 0.0002848
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

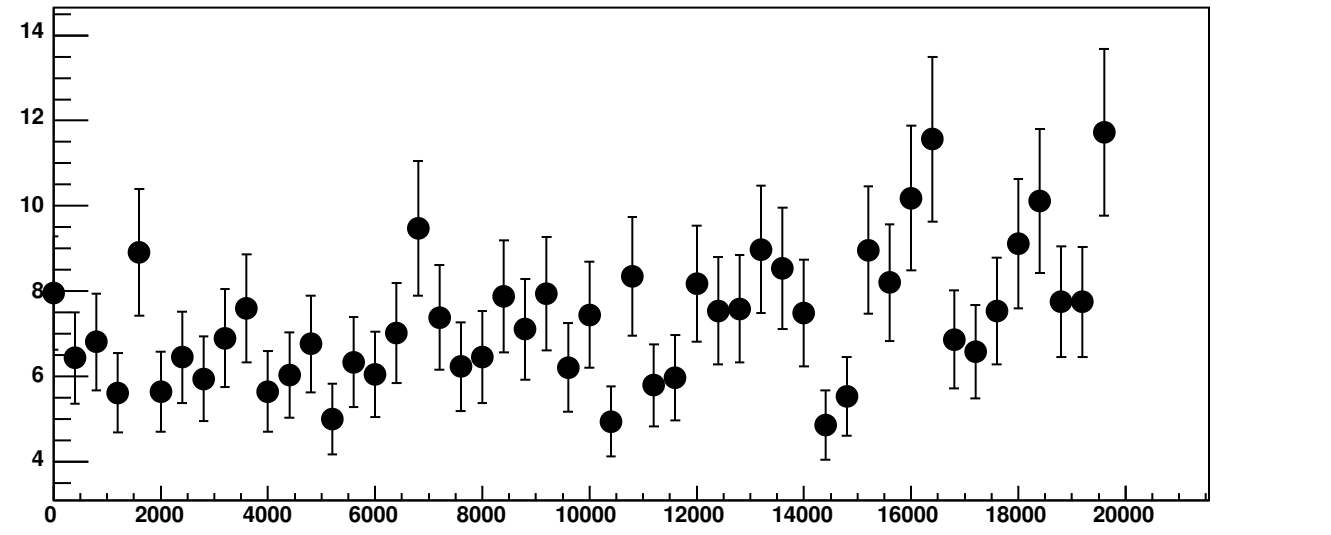

Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

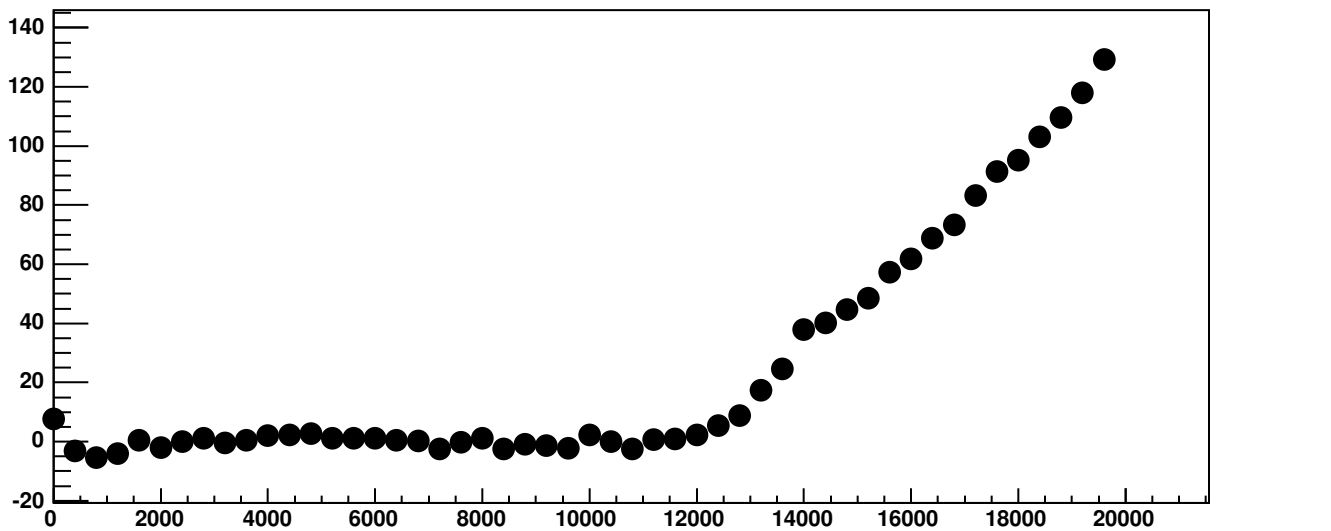
Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



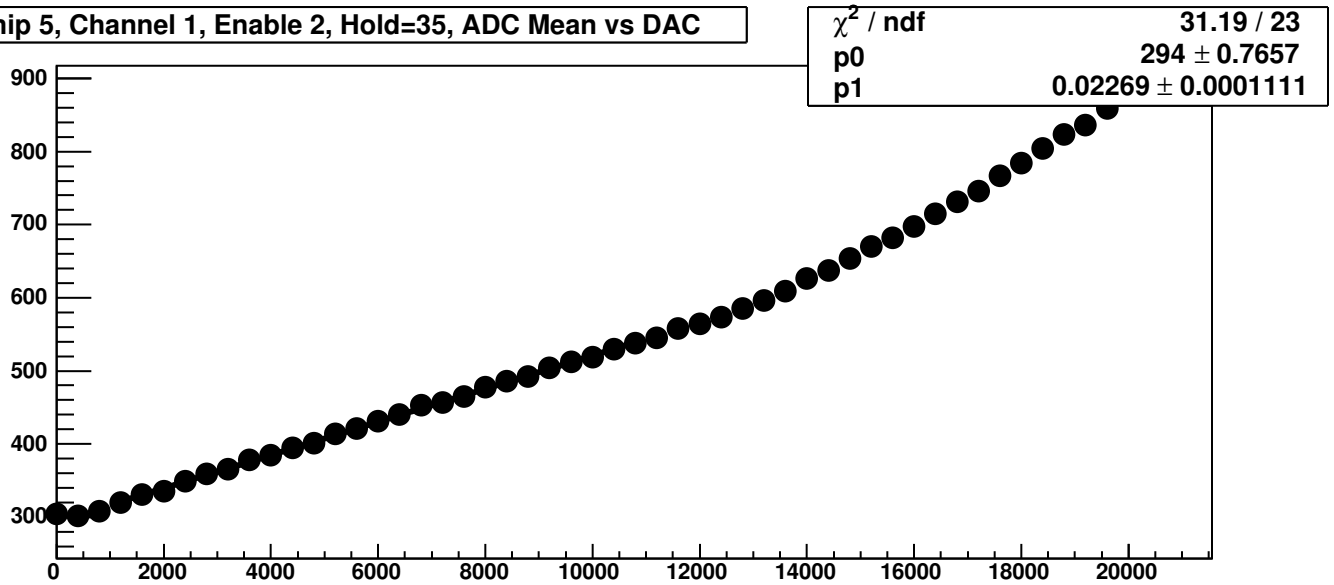
Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



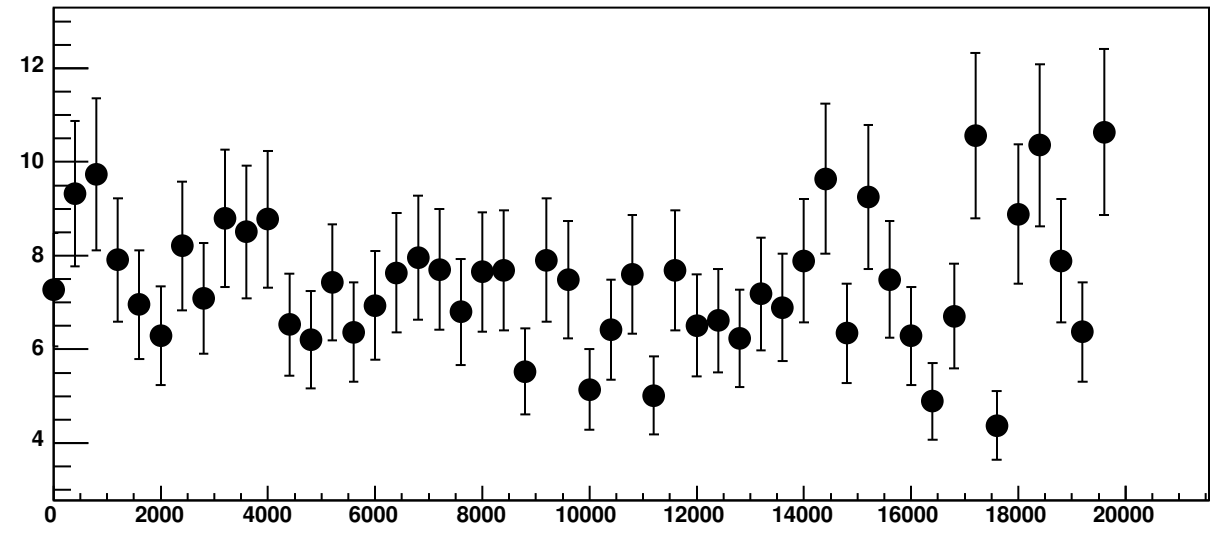
Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



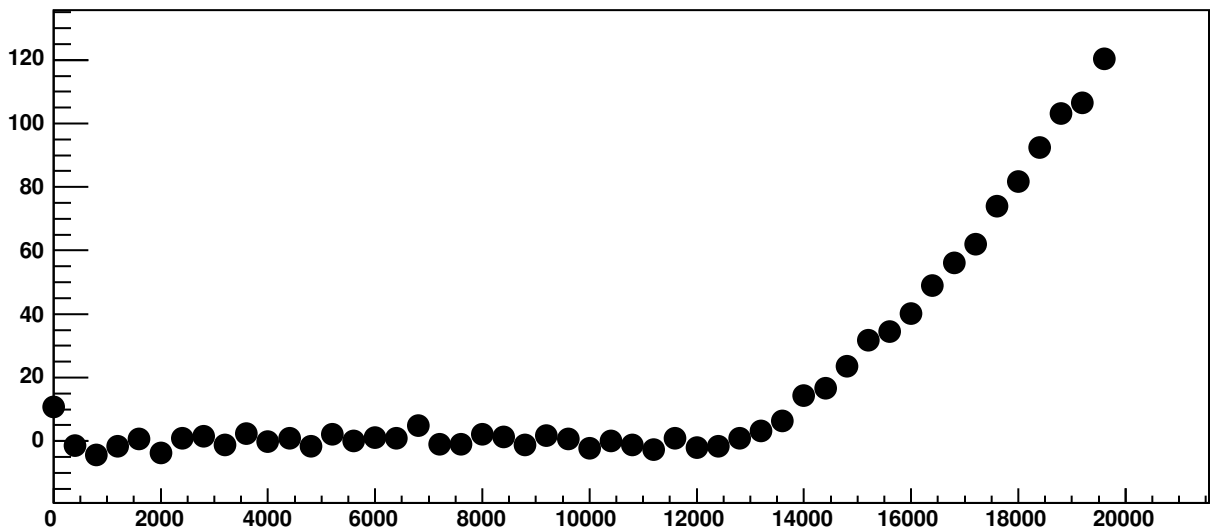
Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



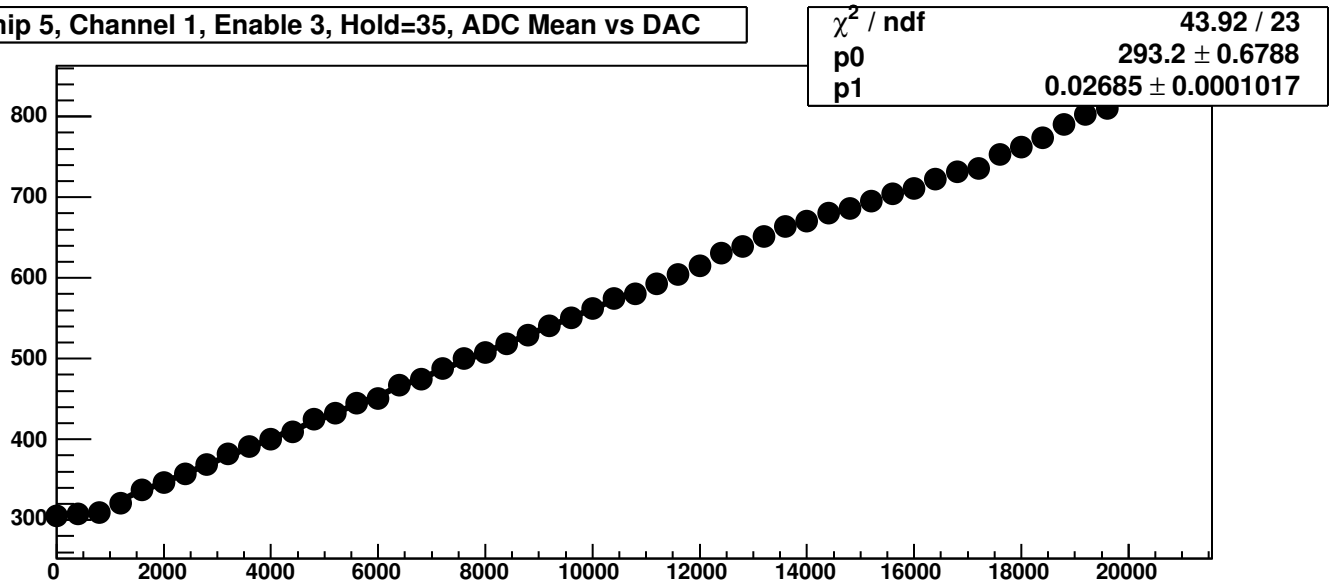
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



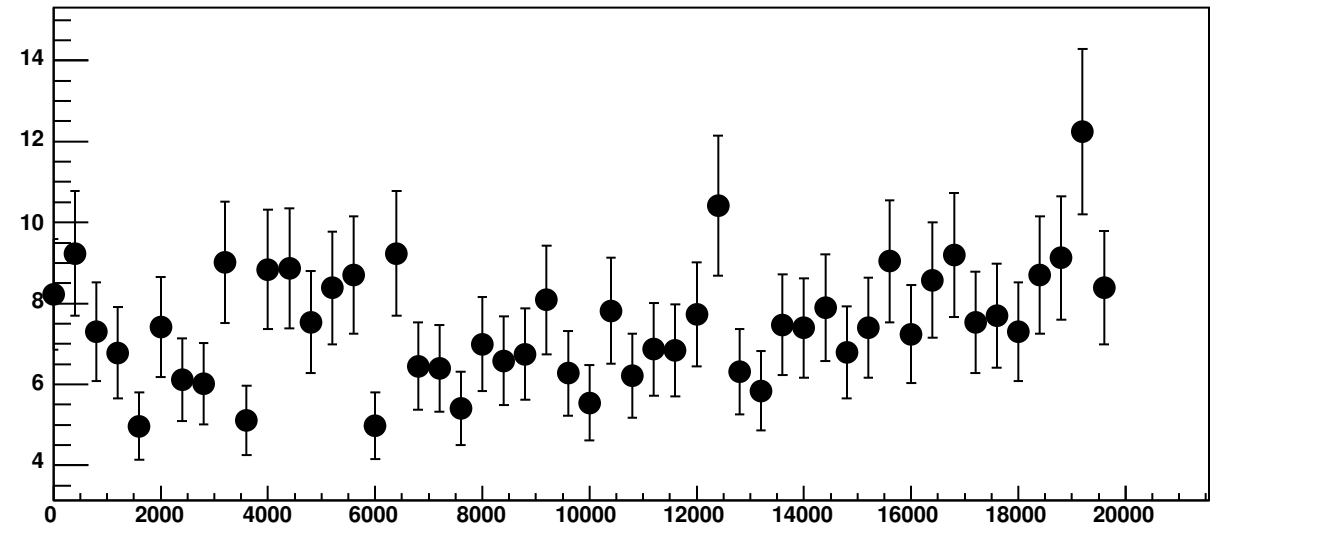
Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



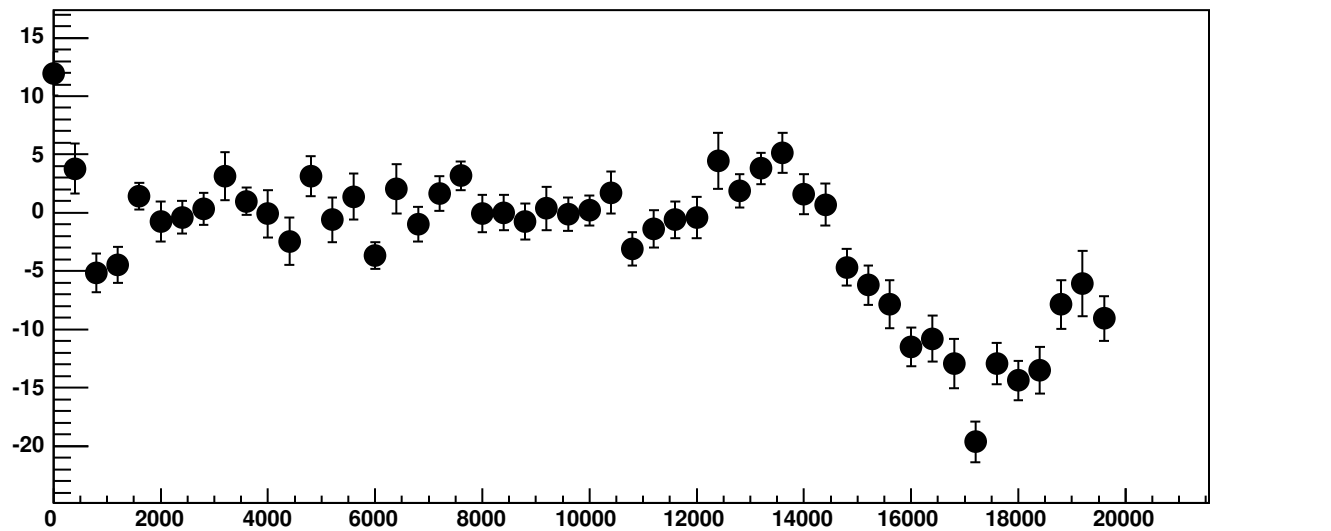
Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



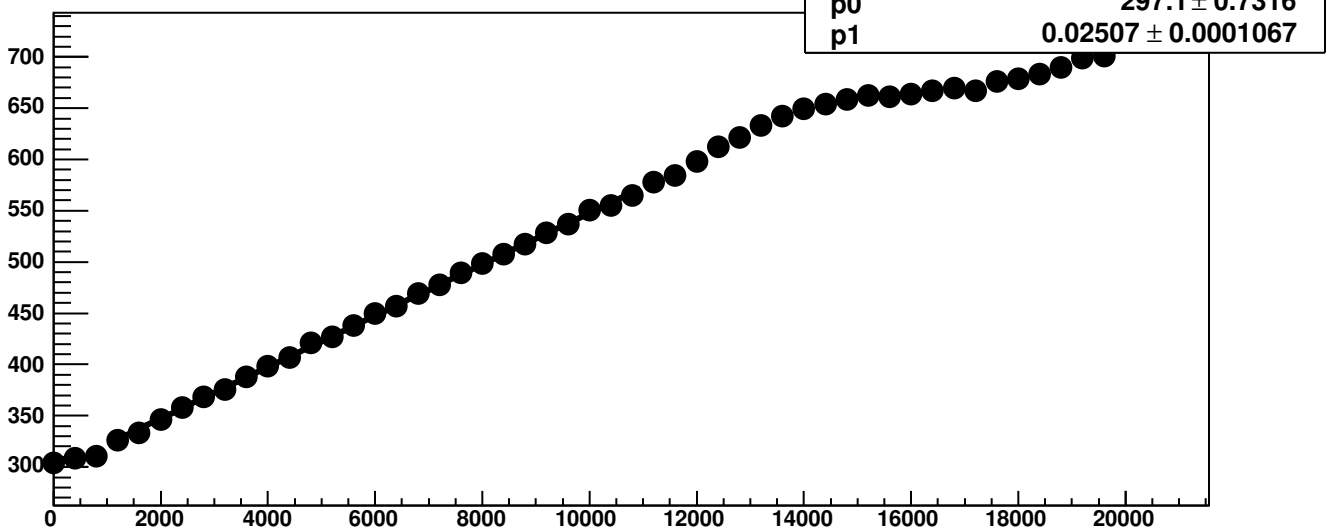
Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



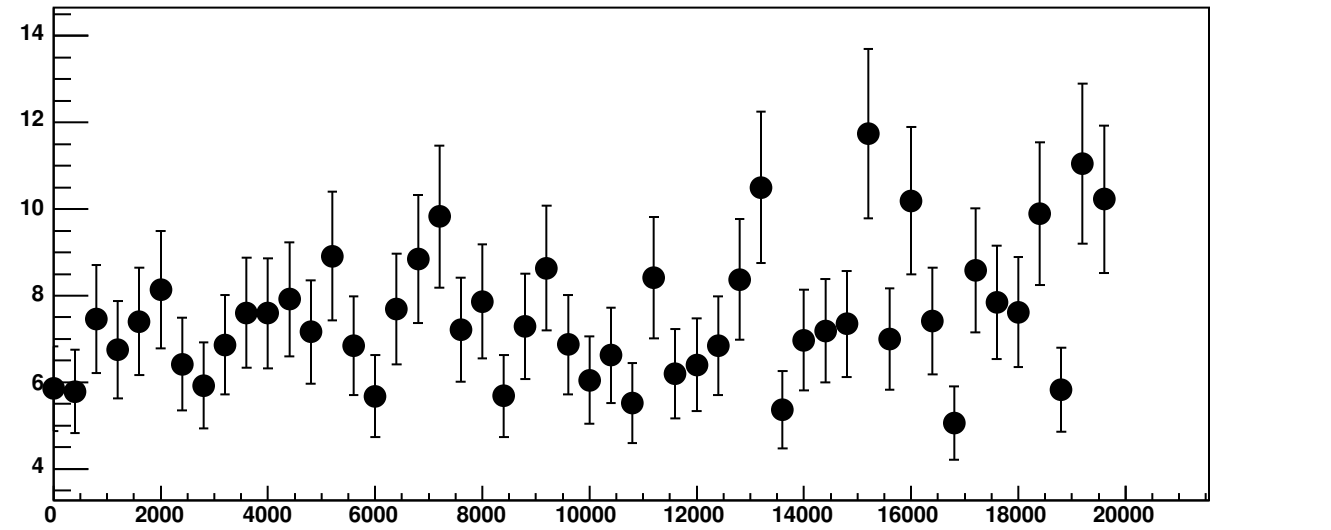
Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



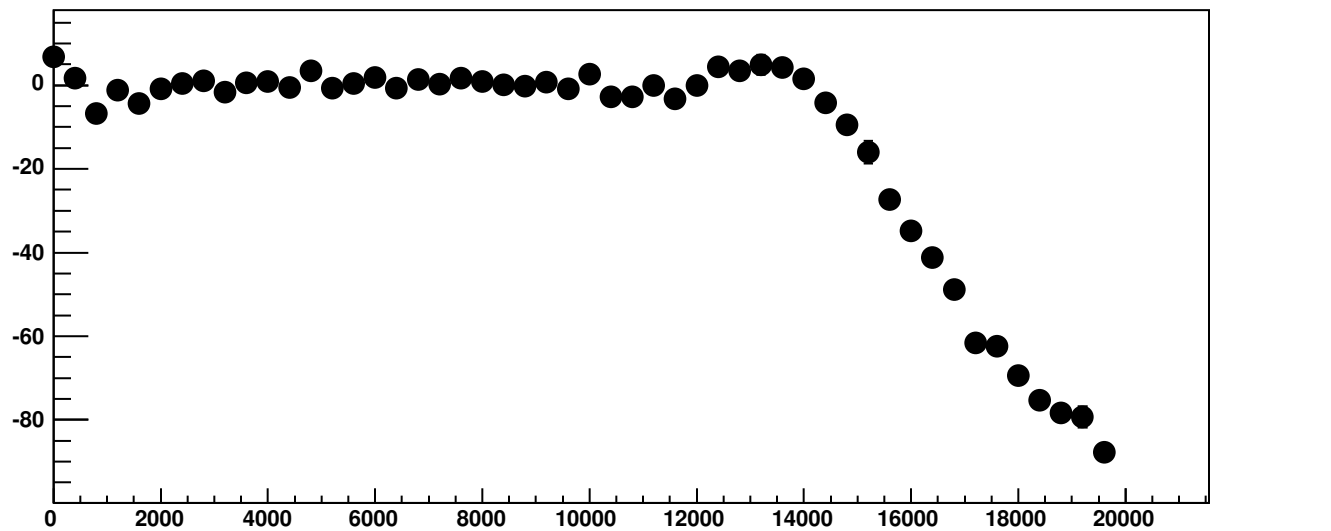
Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



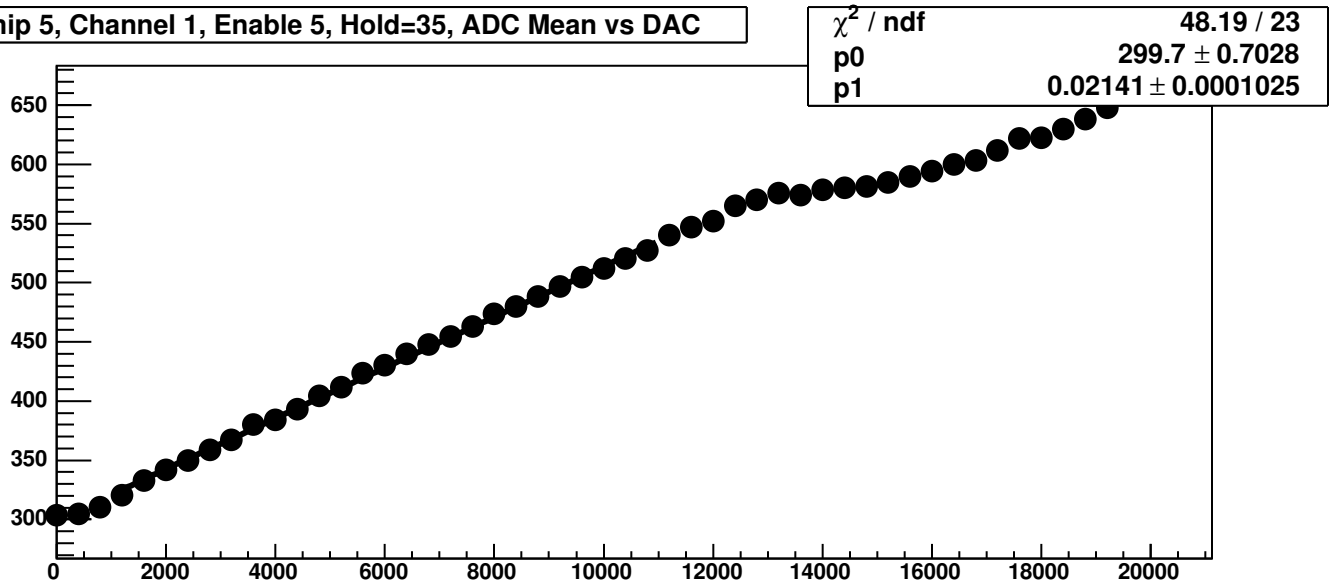
Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



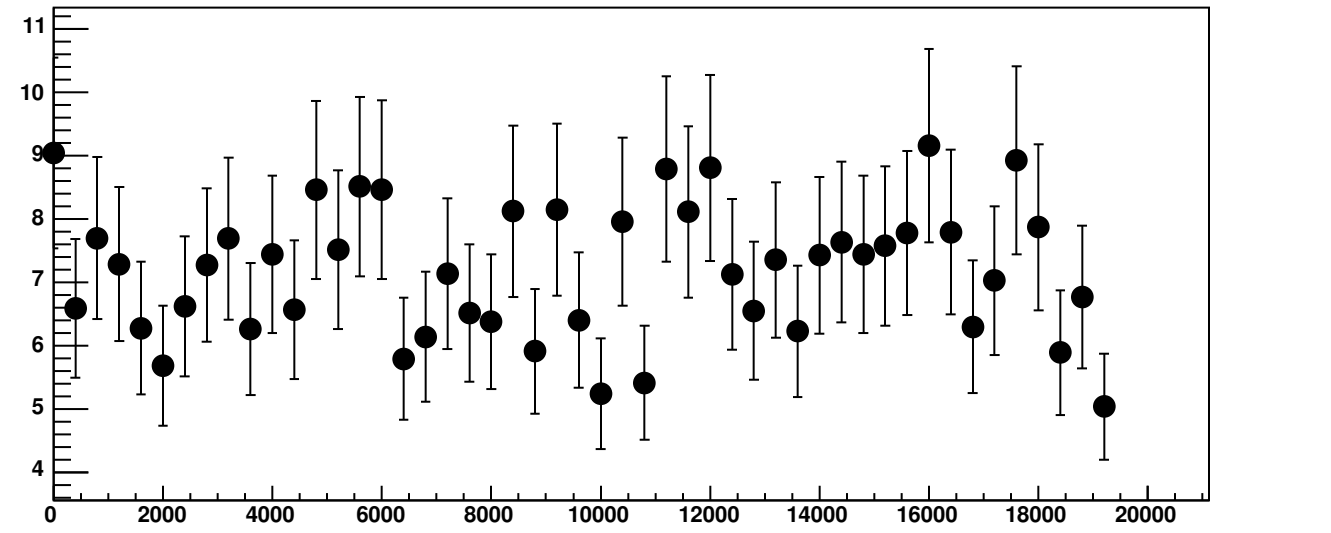
Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



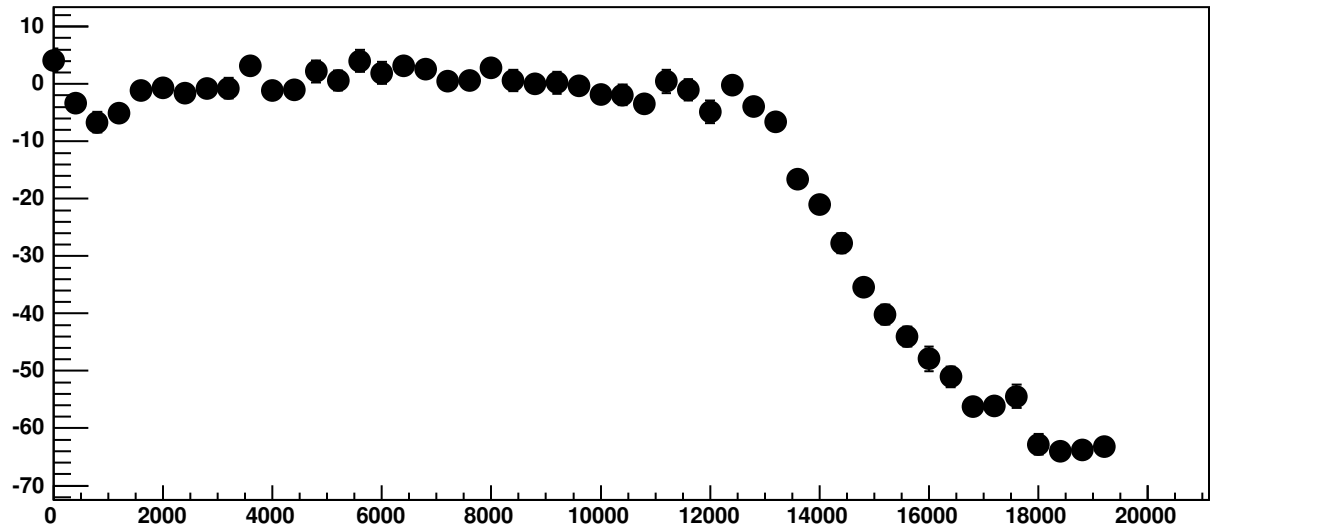
Chip 5, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

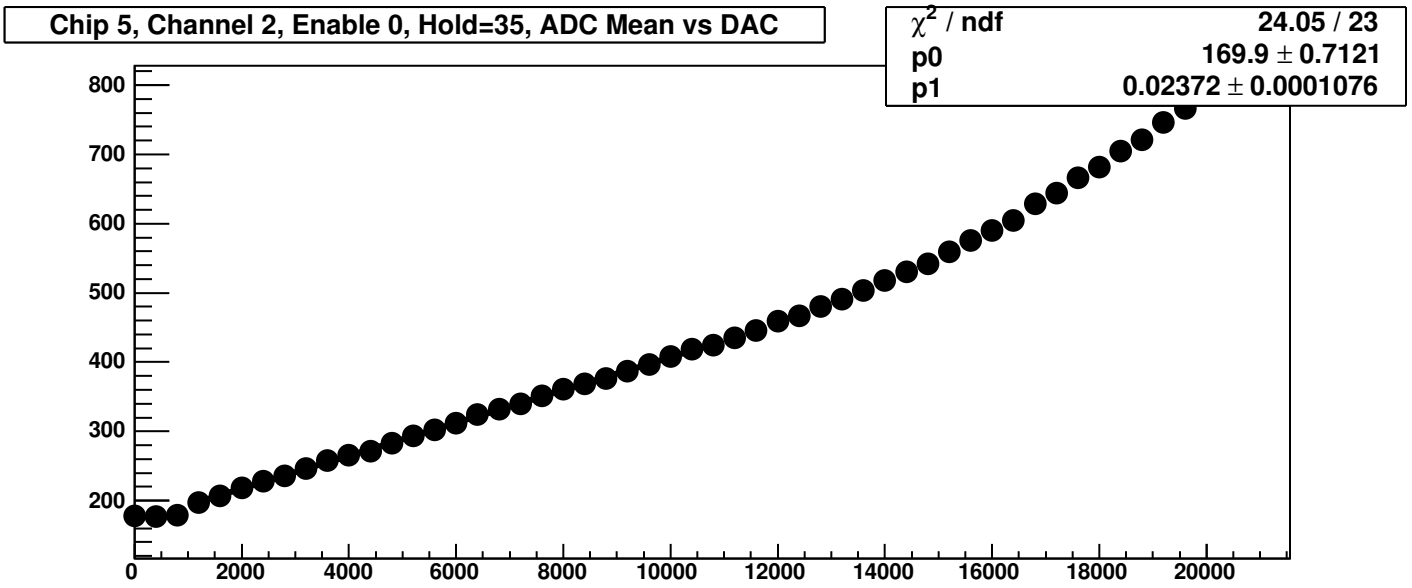


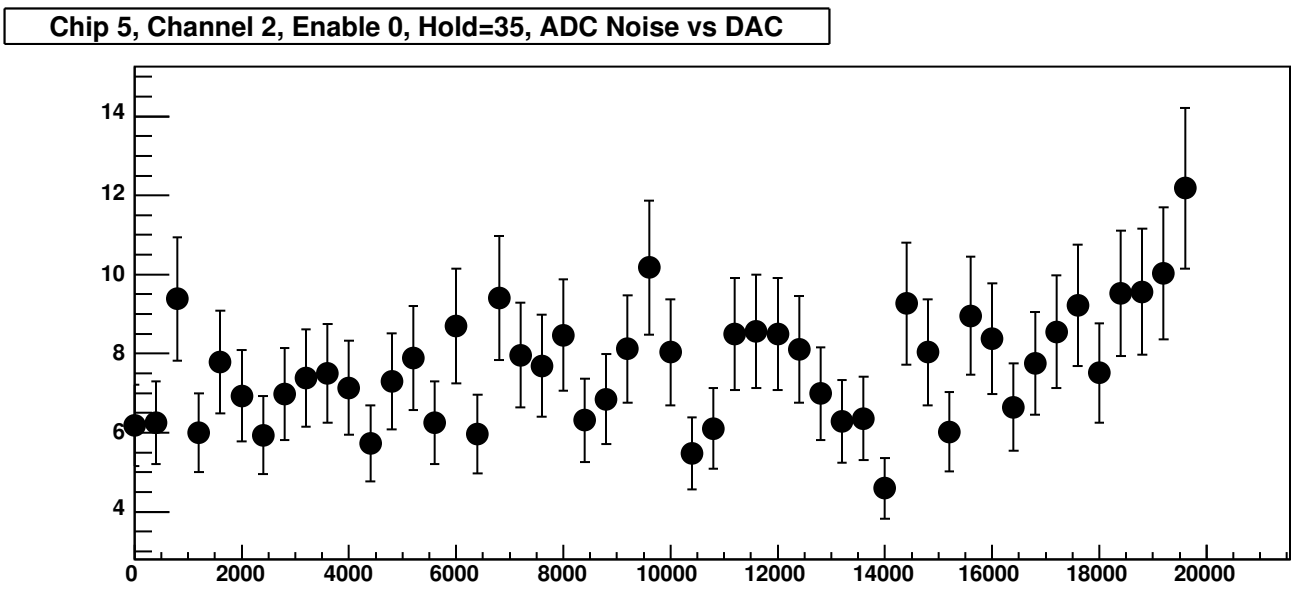
Chip 5, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

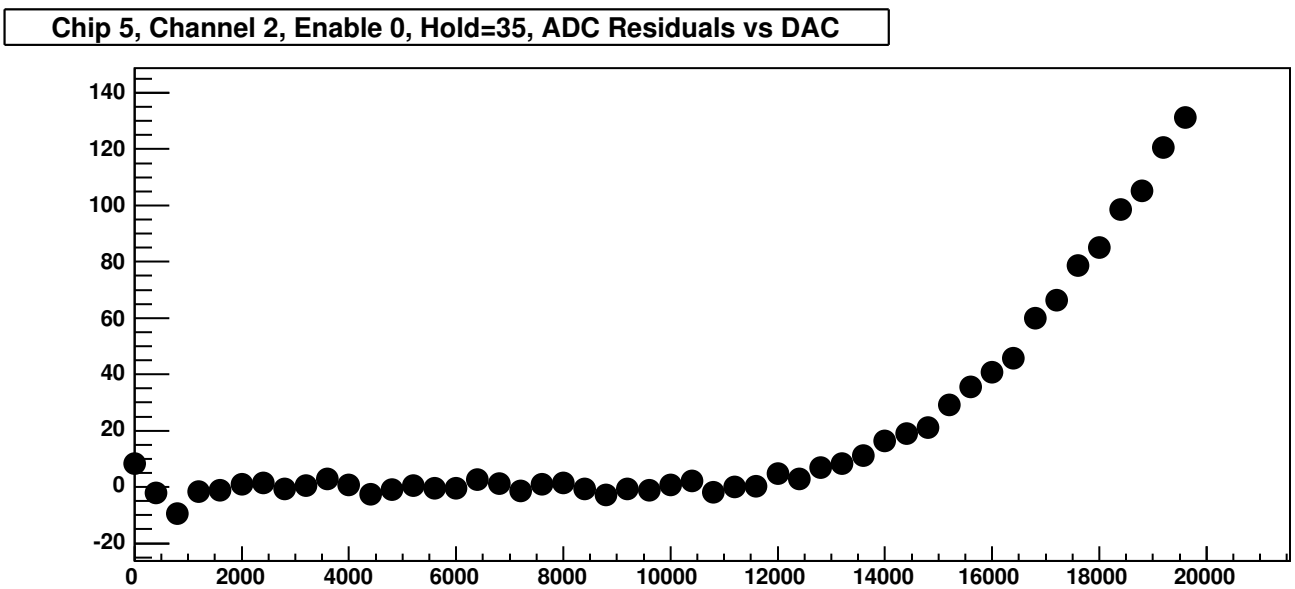


Chip 5, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

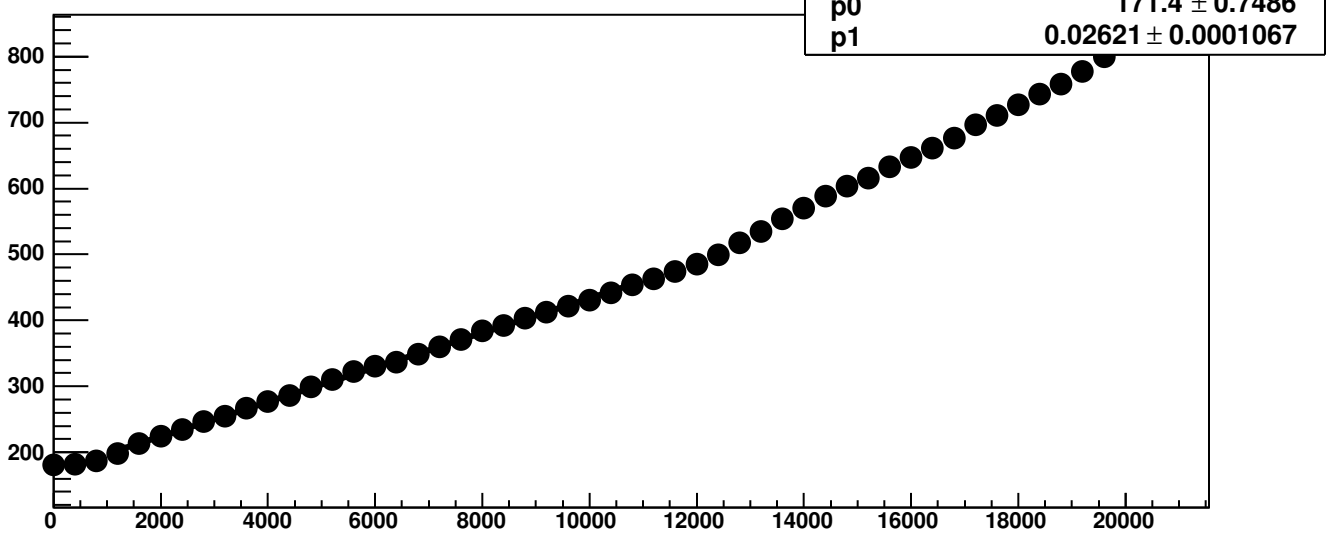



Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 24.05 / 23
p0 169.9 ± 0.7121
p1 0.02372 ± 0.0001076
800
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

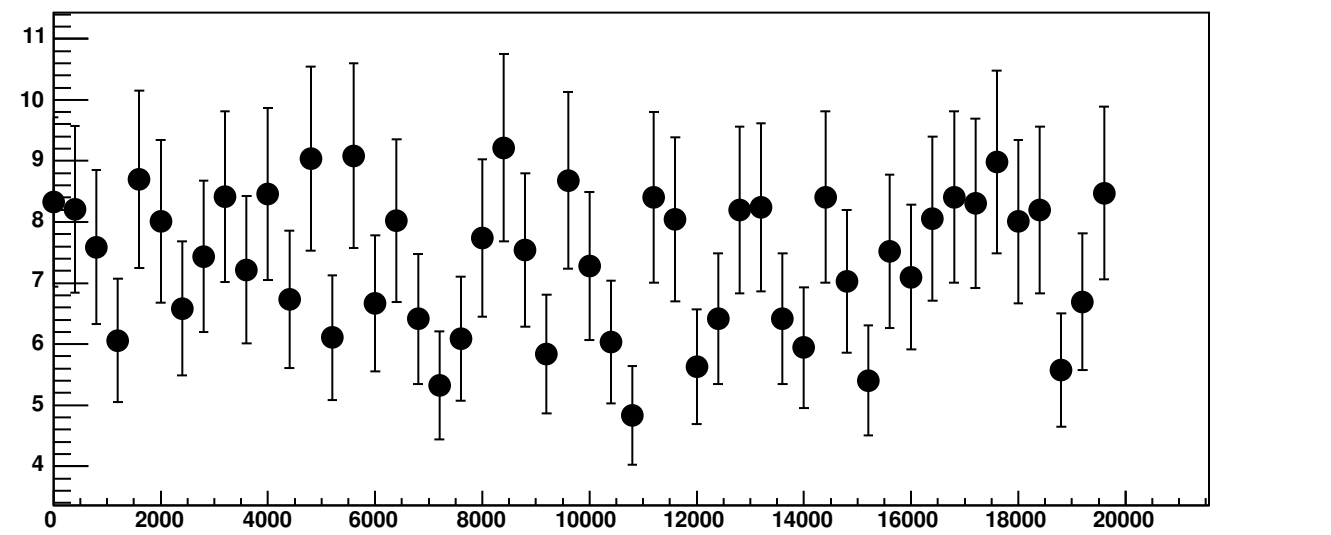

Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

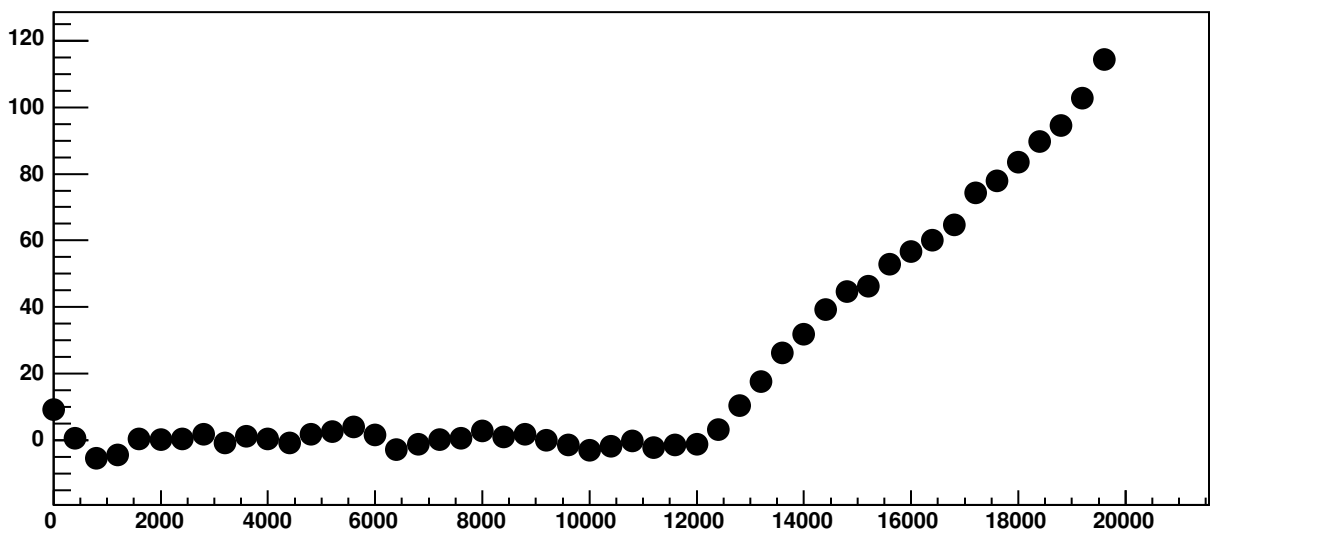
Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



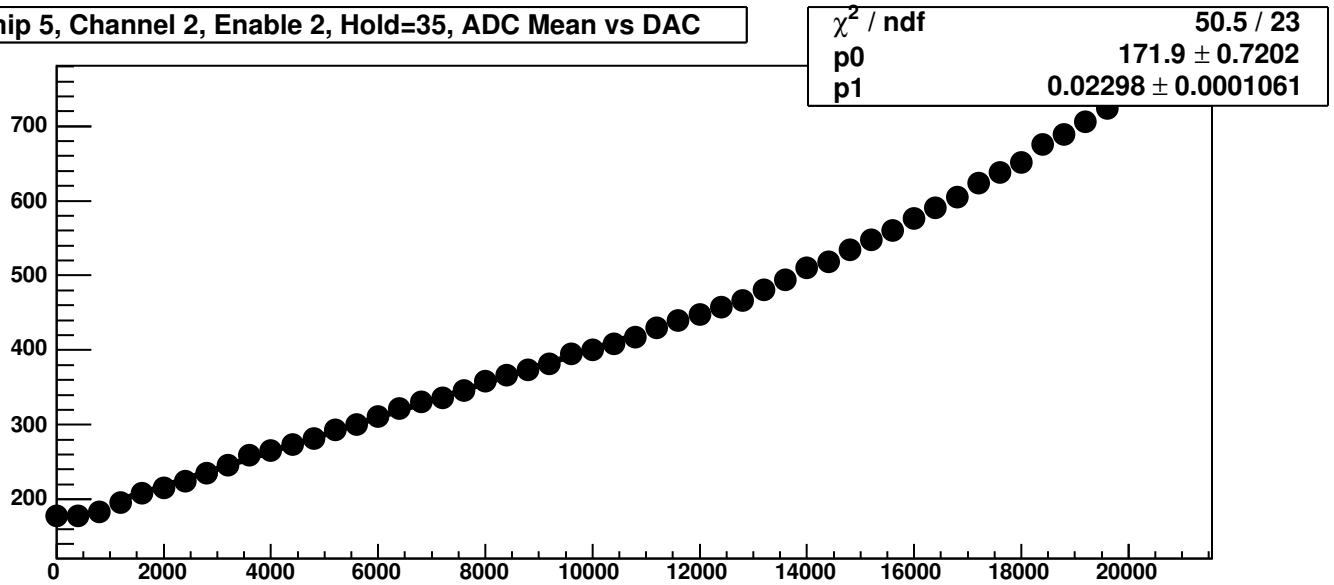
Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



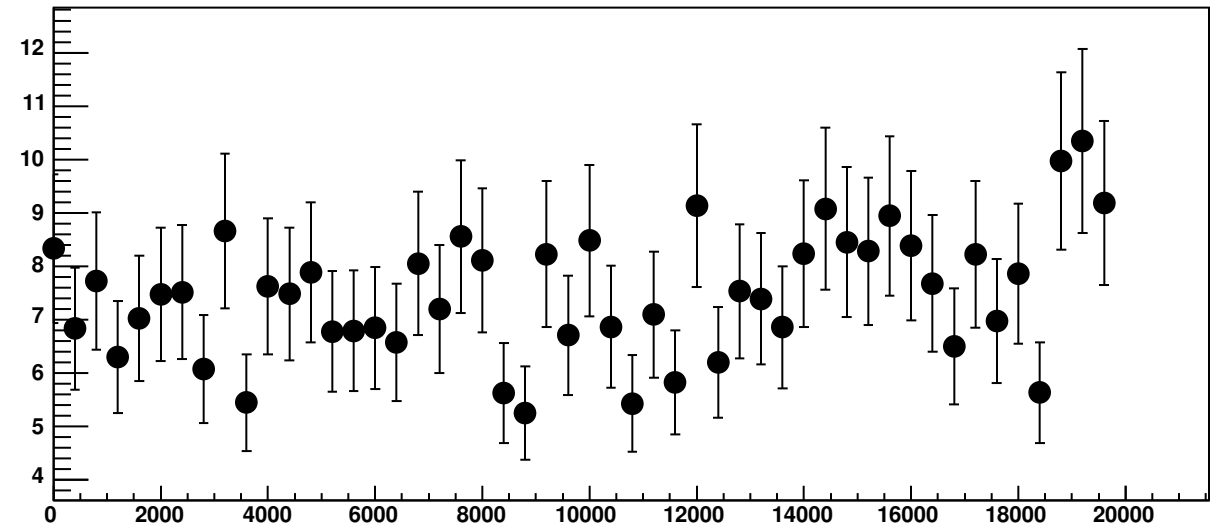
Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



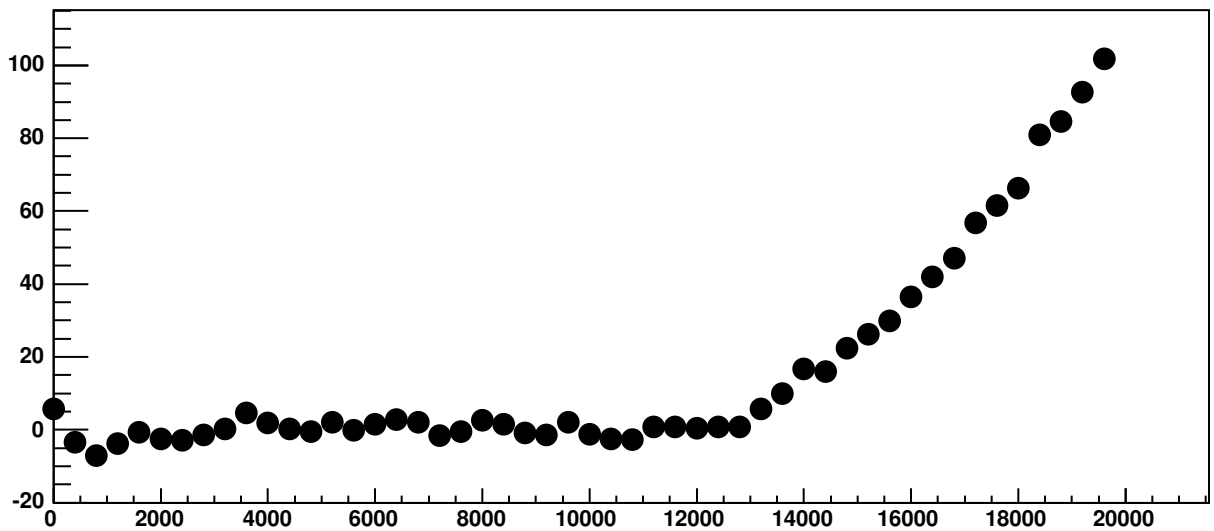
Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

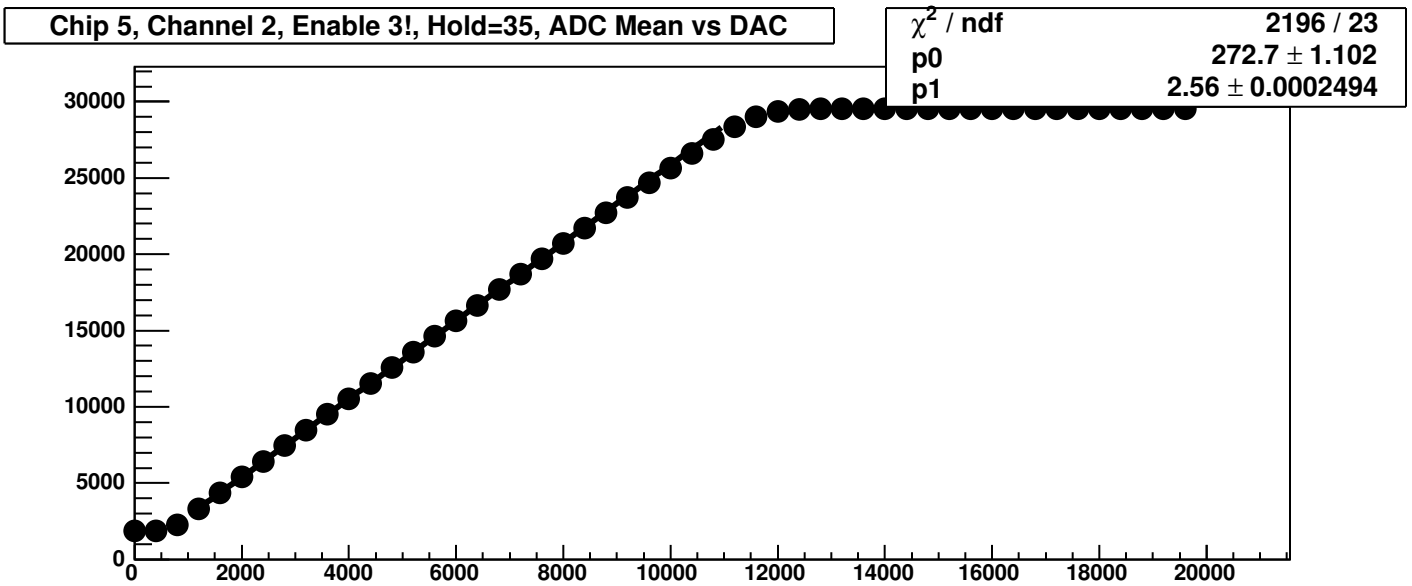


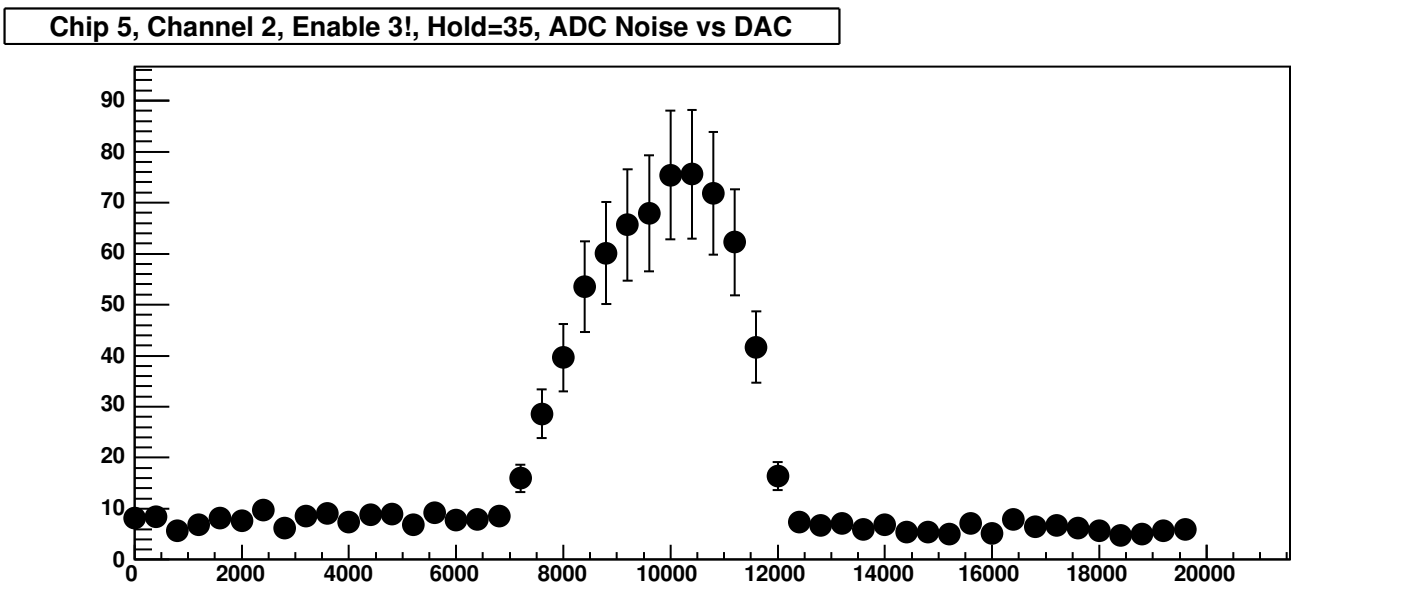
Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

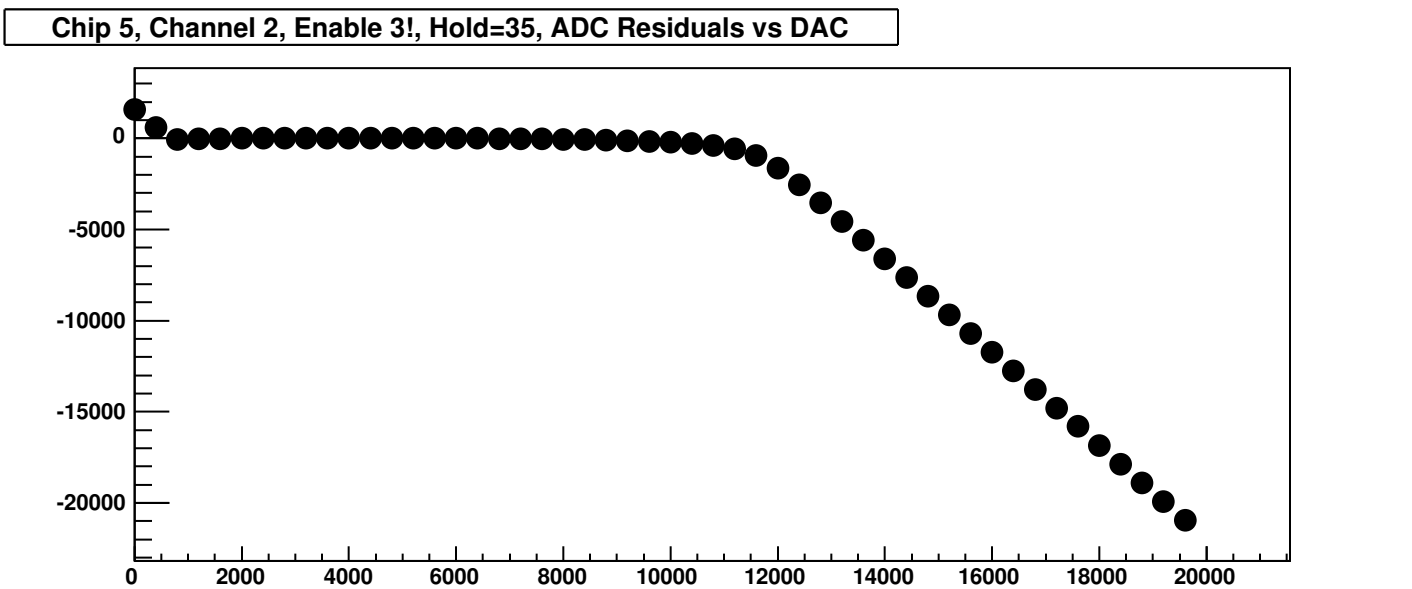


Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

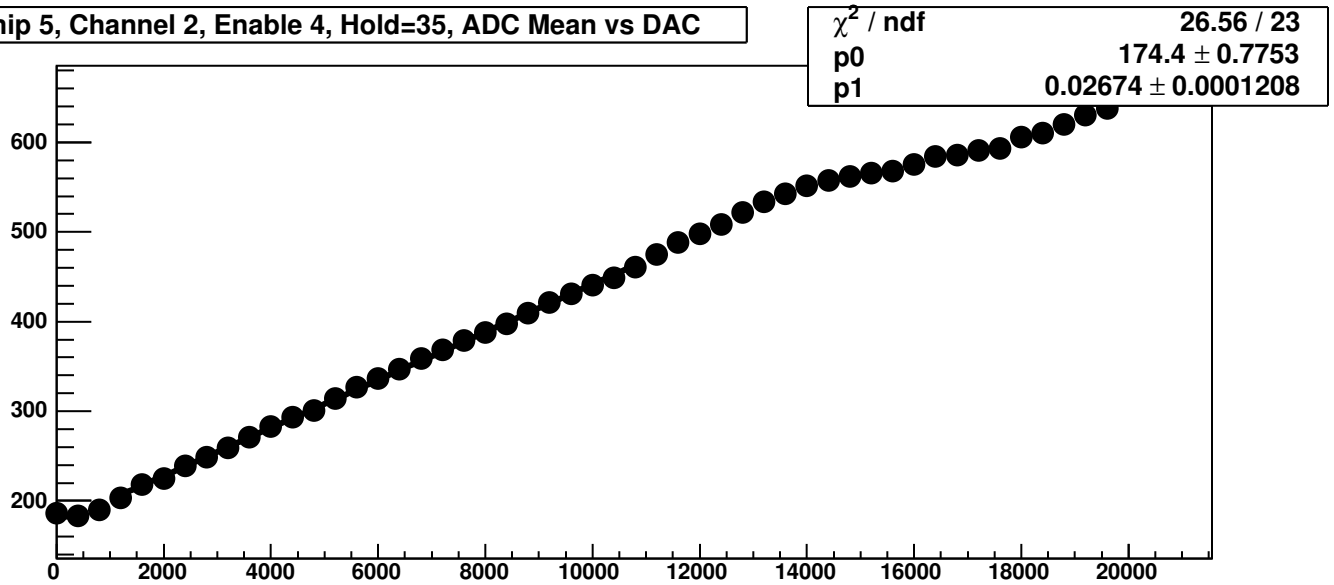



Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 2196 / 23
p0 272.7 ± 1.102
p1 2.56 ± 0.0002494
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

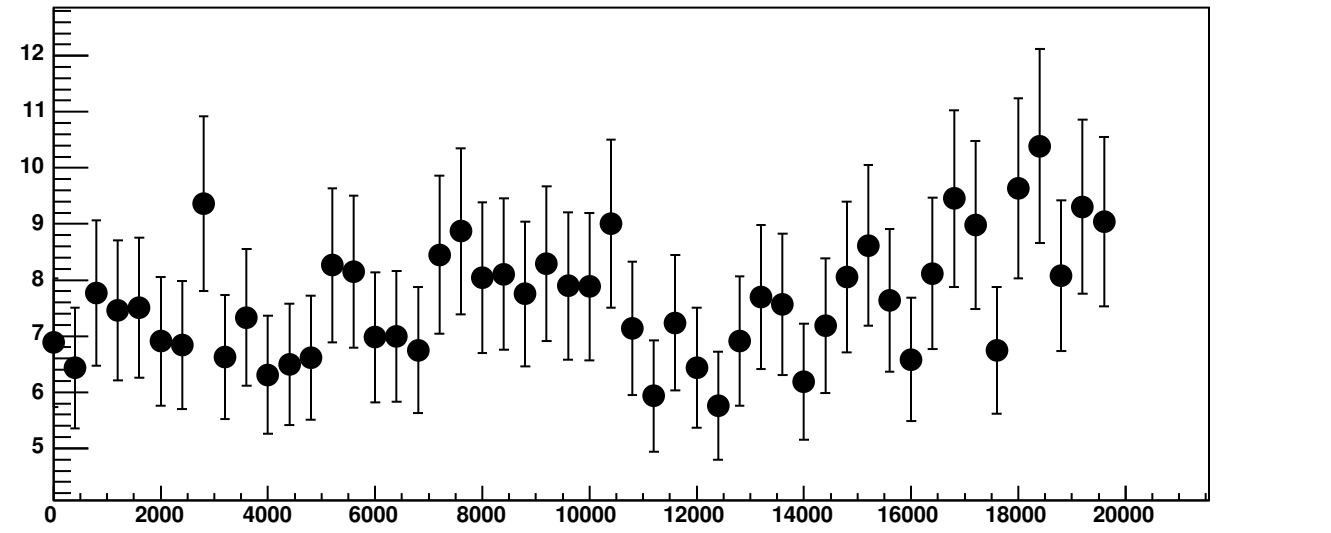

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

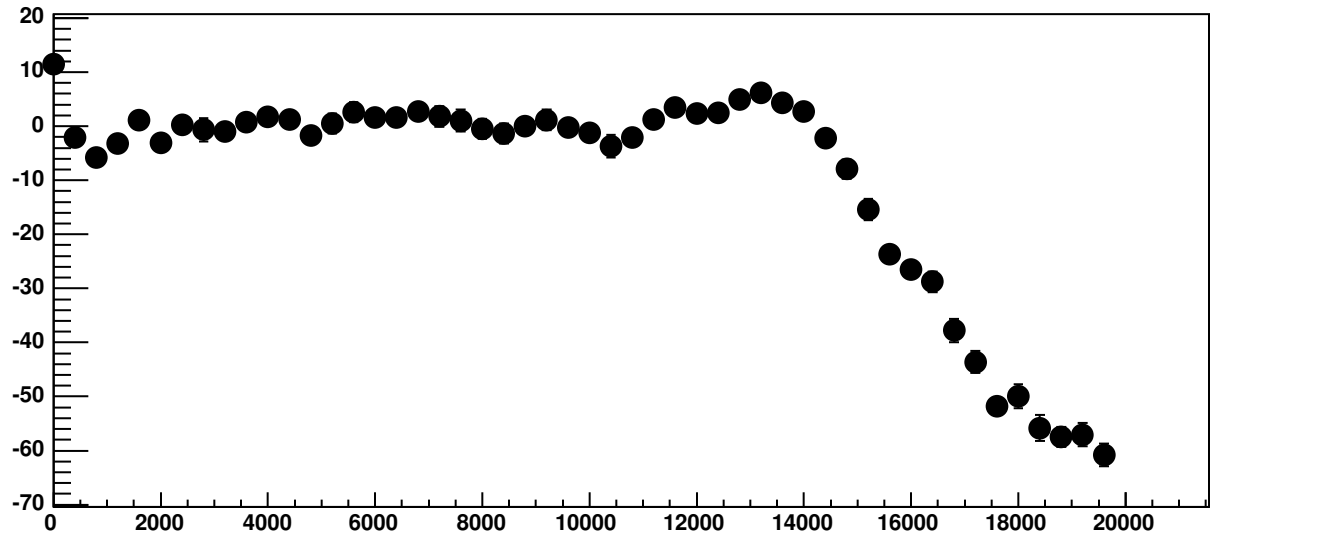
Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

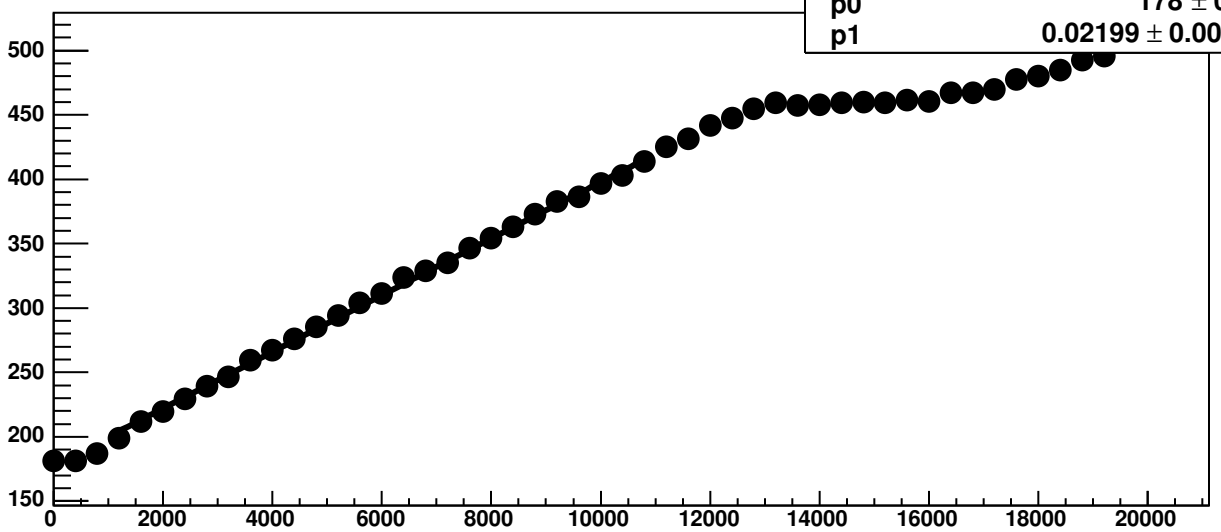


Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

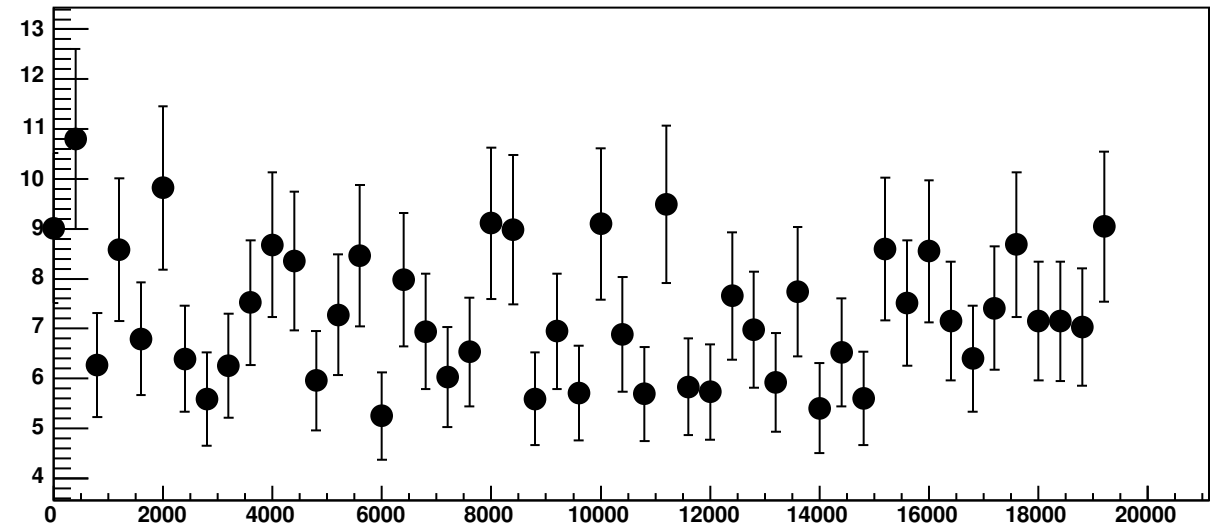


Chip 5, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

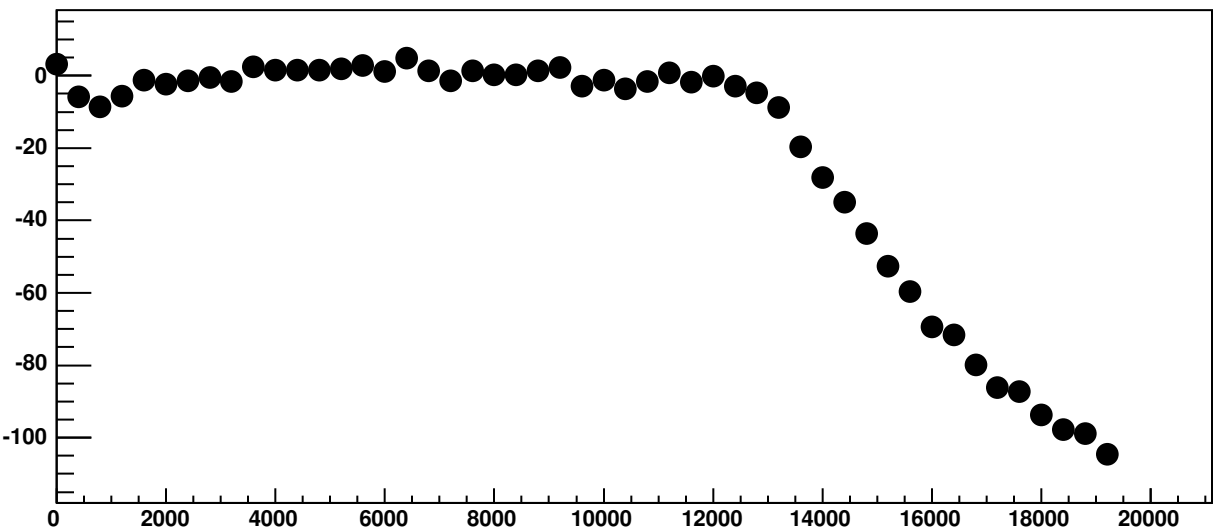
χ^2 / ndf	46.12 / 23
p0	178 ± 0.7464
p1	0.02199 ± 0.0001096

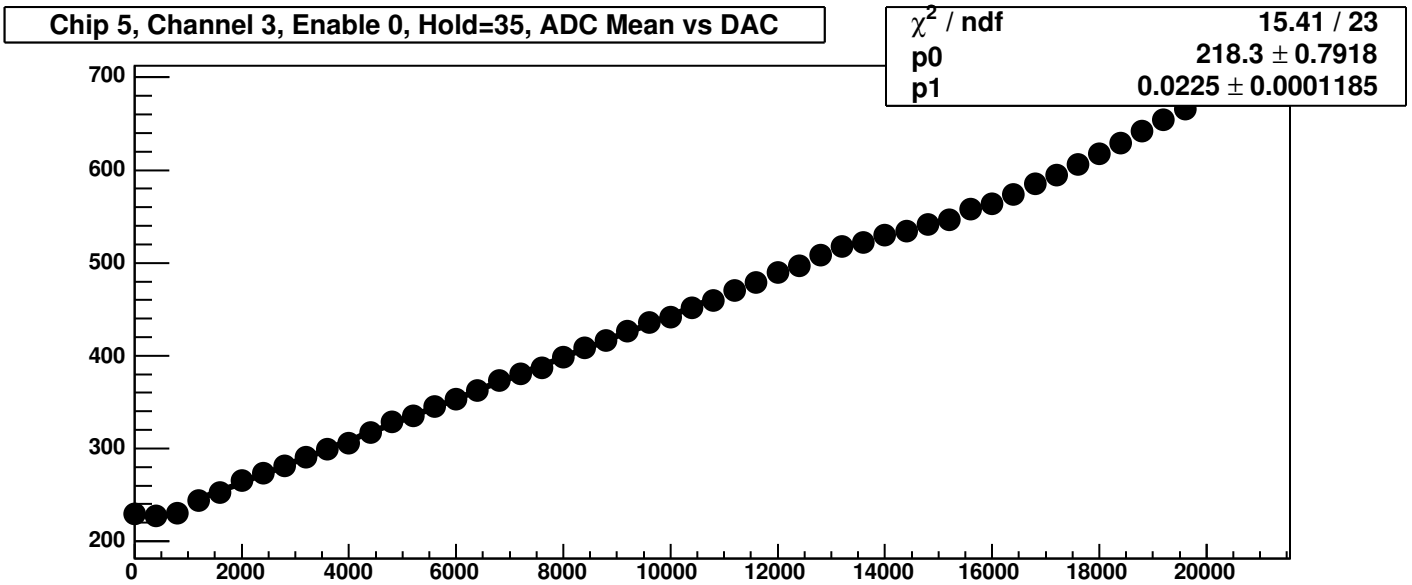


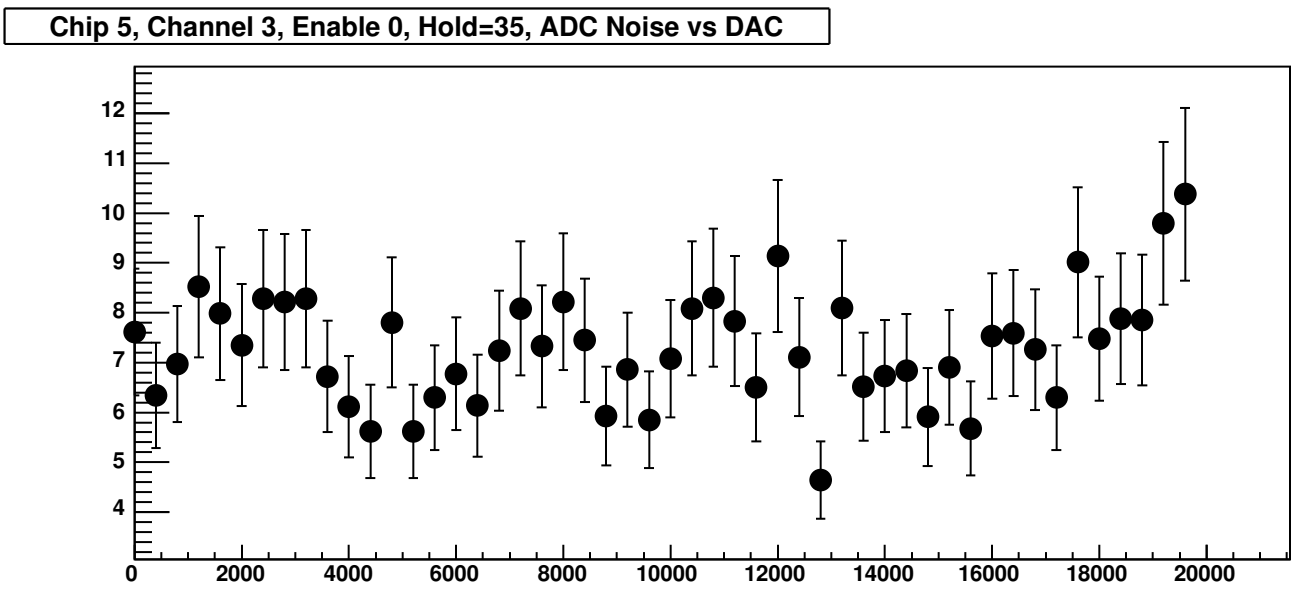
Chip 5, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

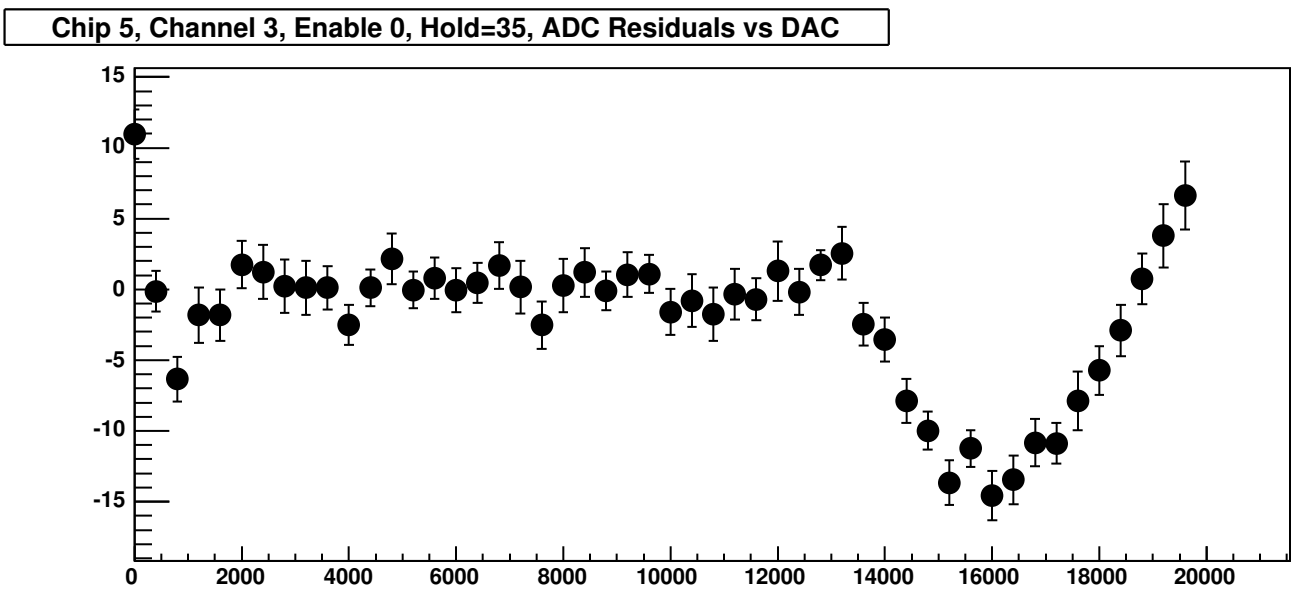


Chip 5, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

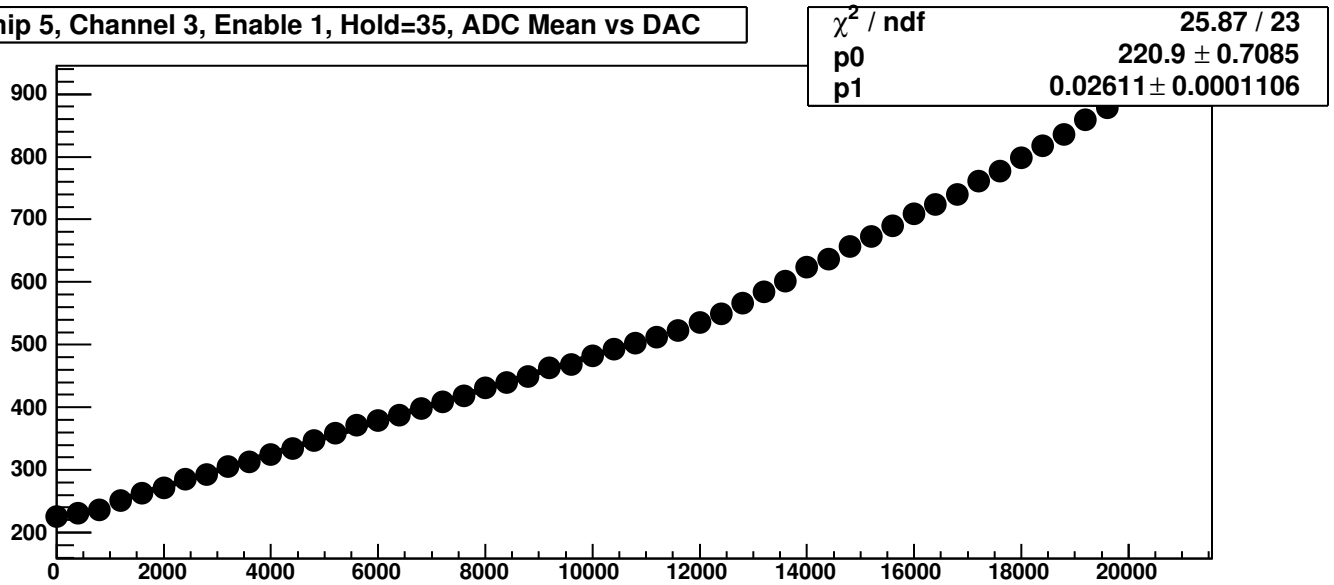



Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 15.41 / 23
p0 218.3 ± 0.7918
p1 0.0225 ± 0.0001185
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

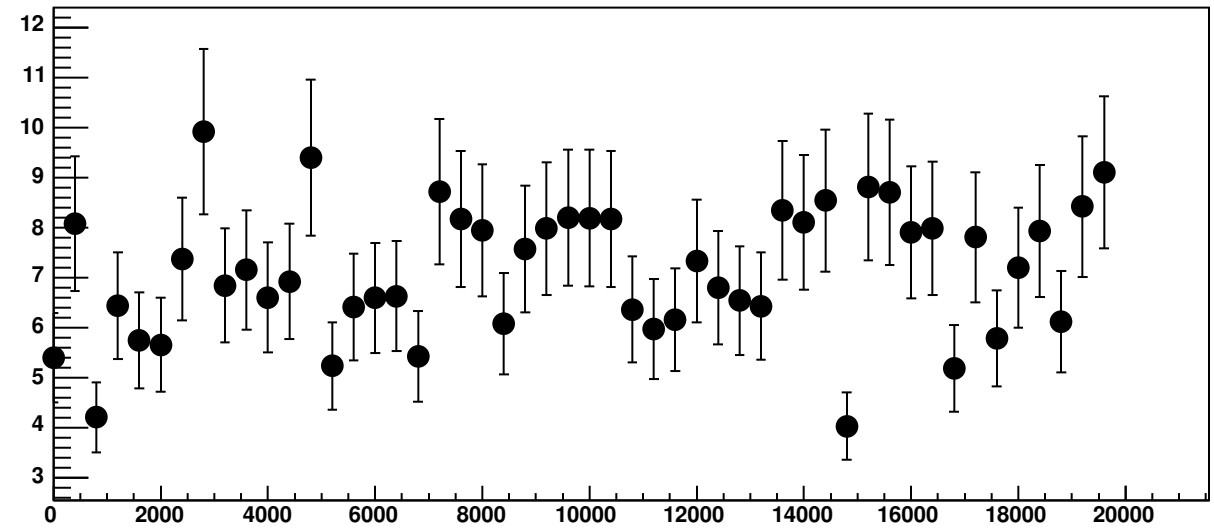

Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

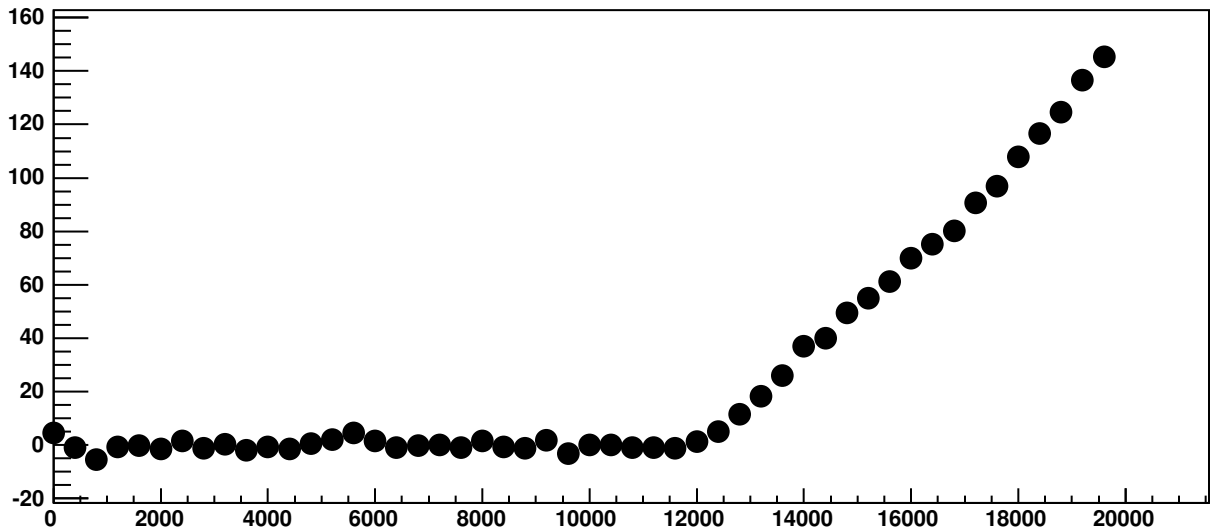
Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



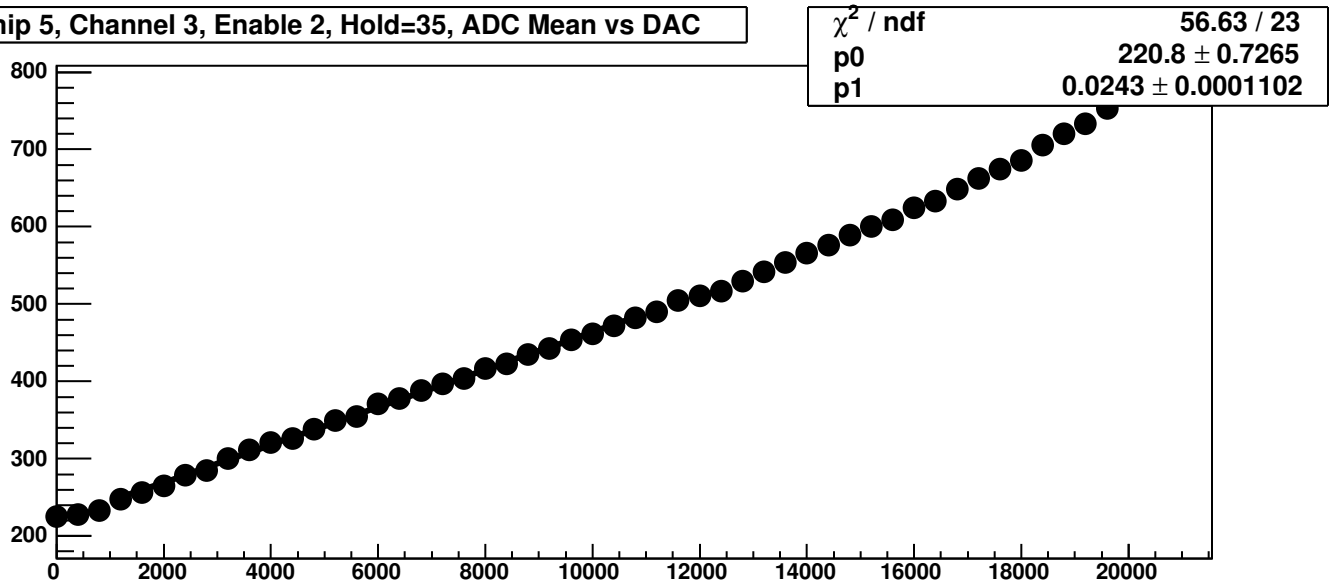
Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



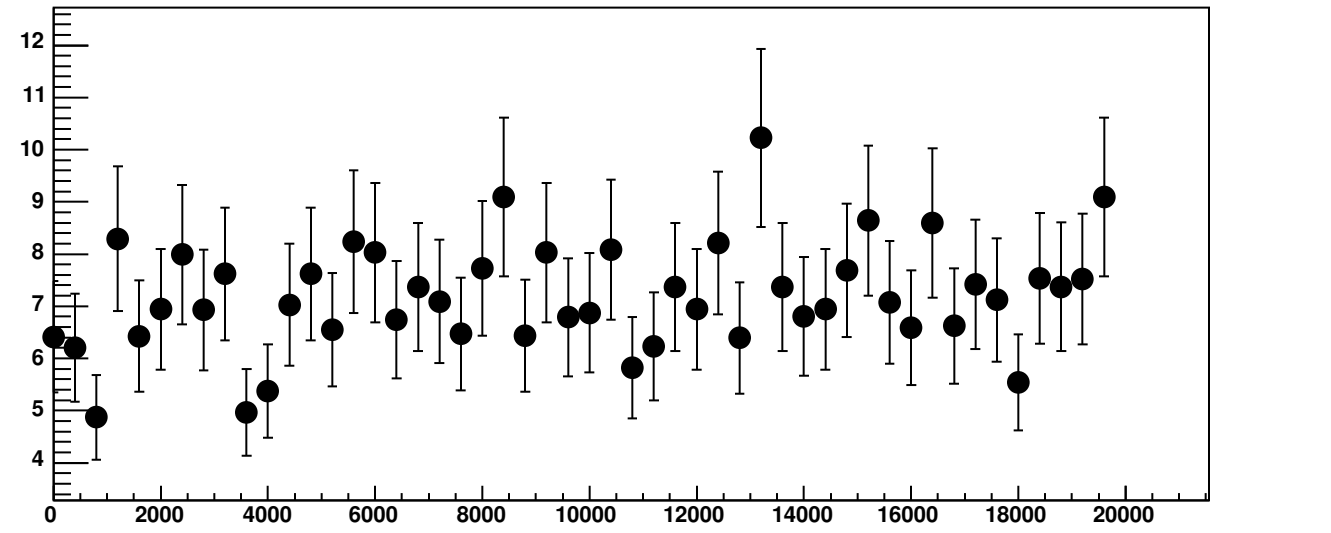
Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



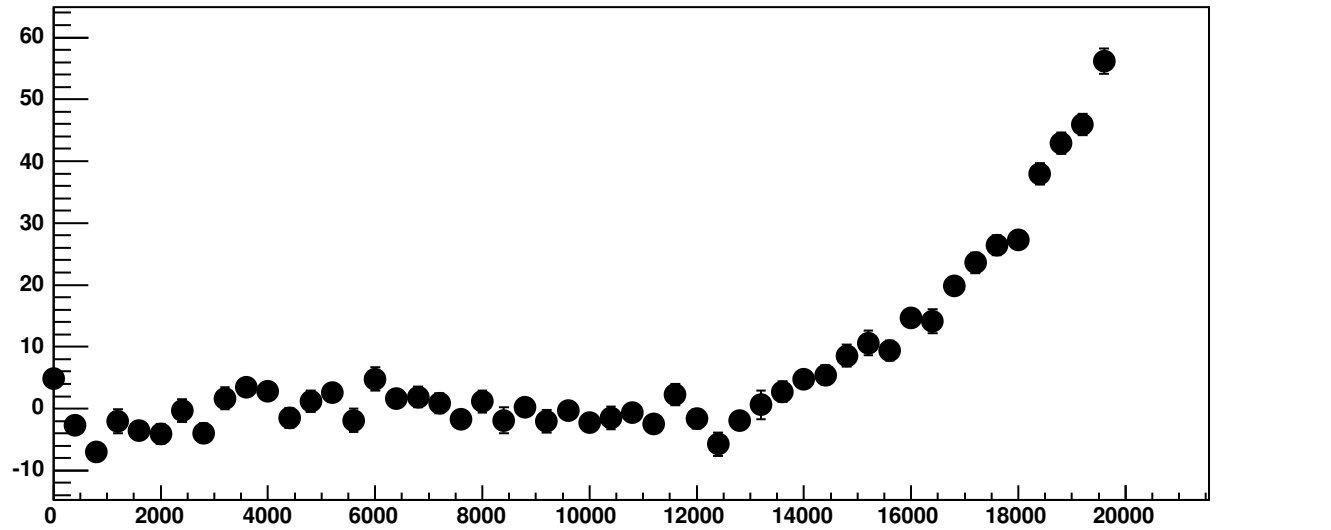
Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



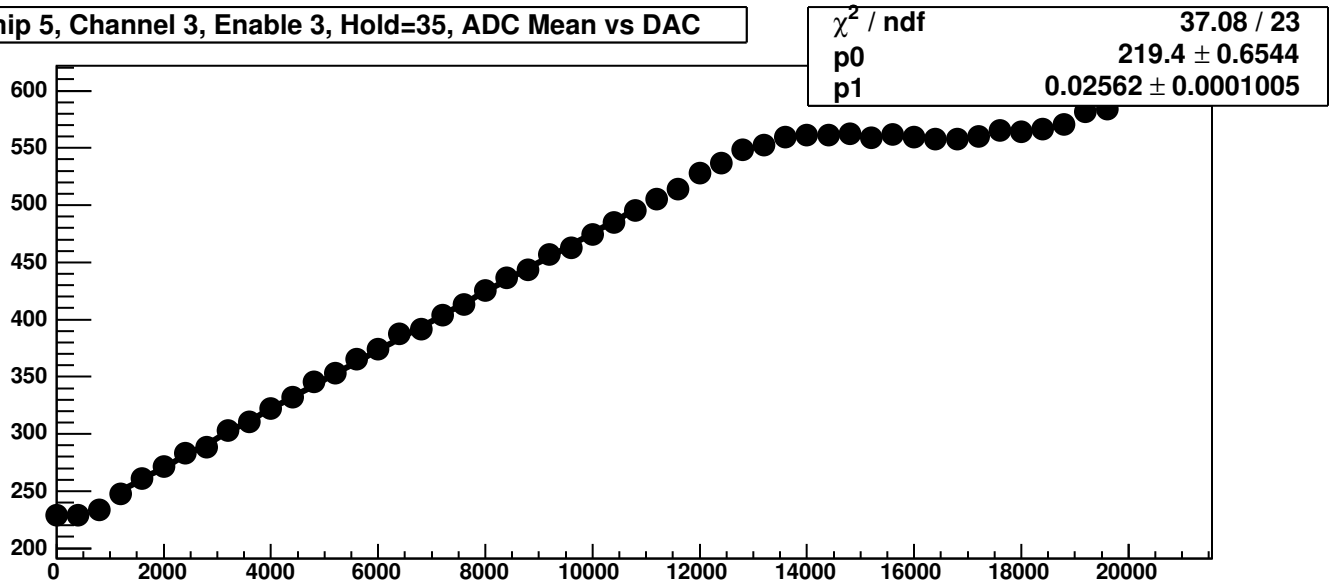
Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



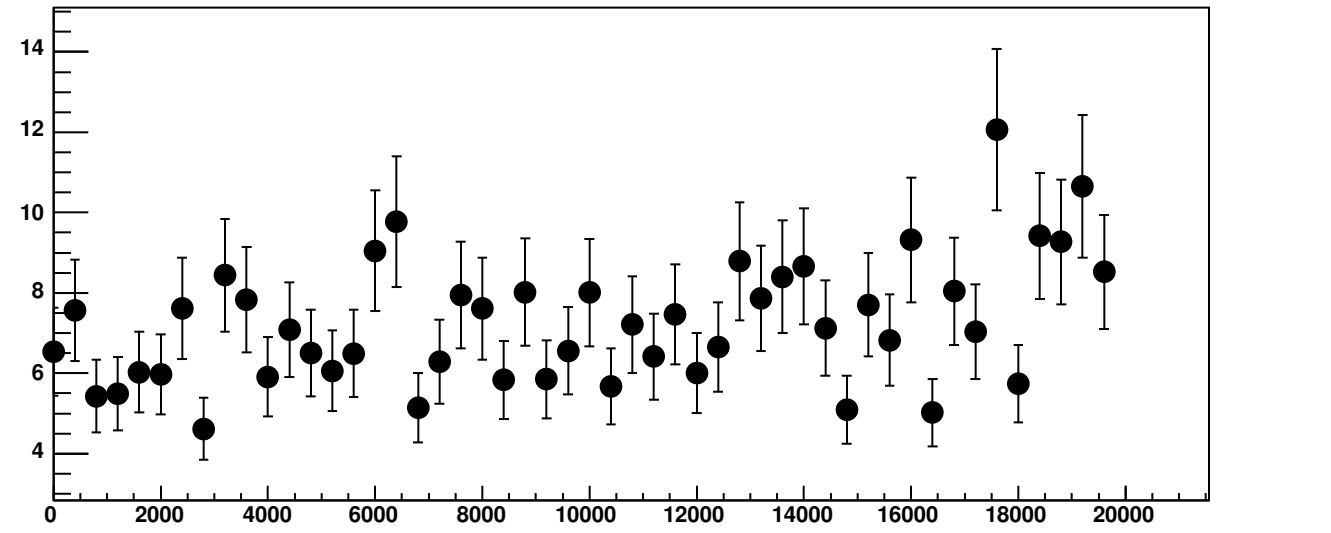
Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



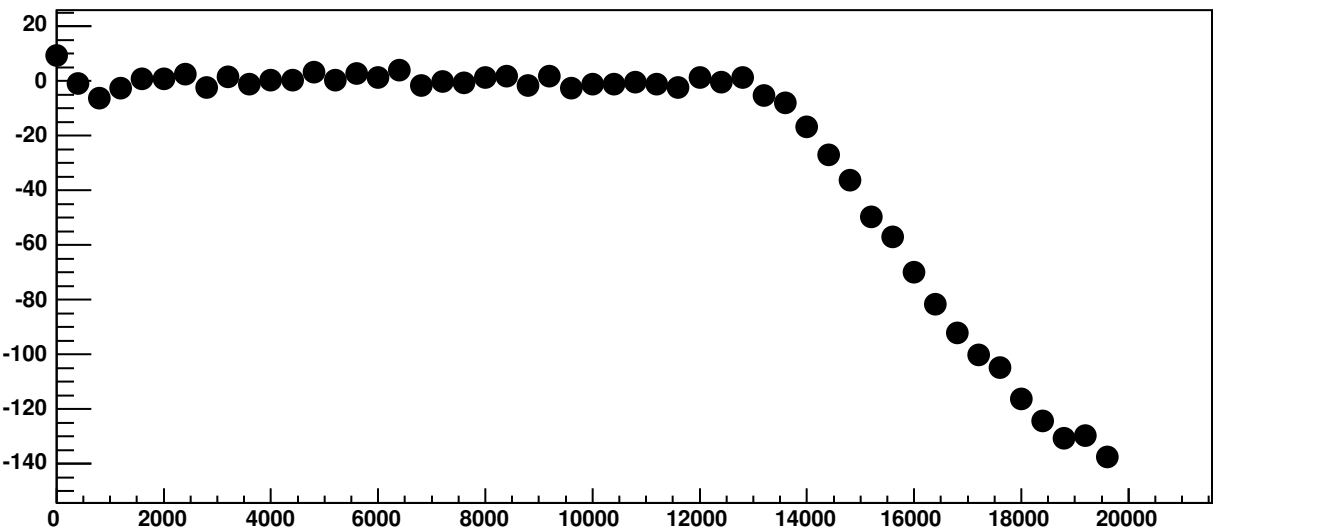
Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

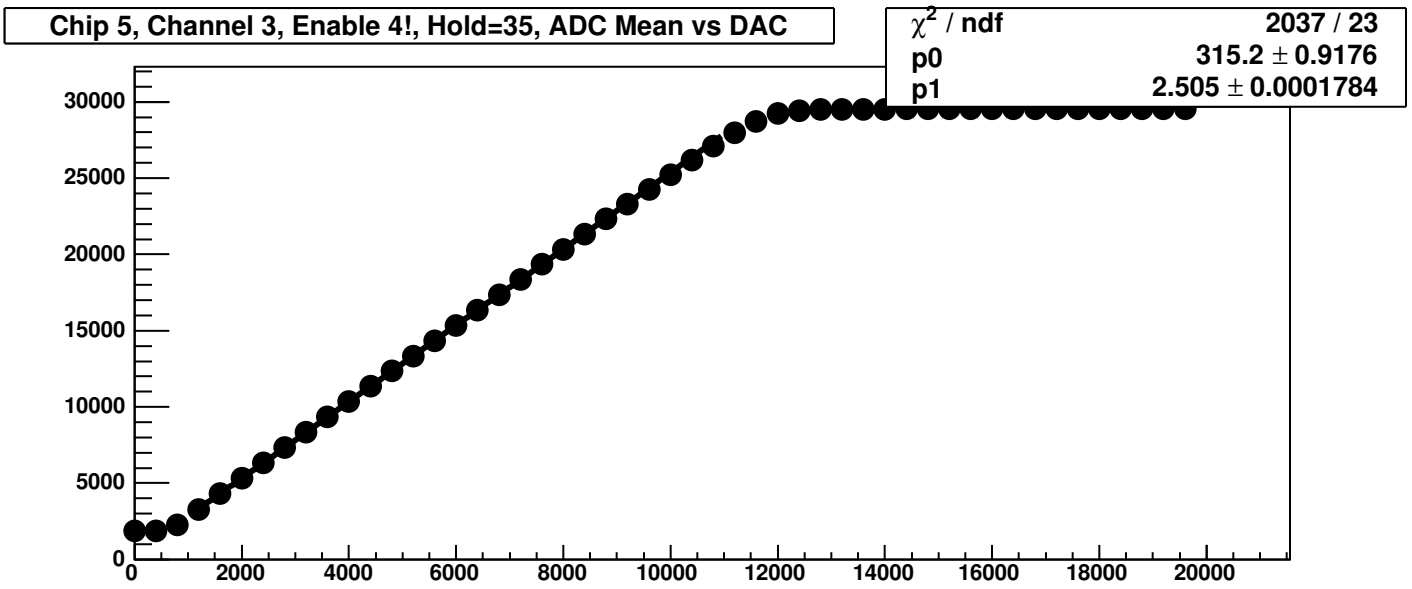


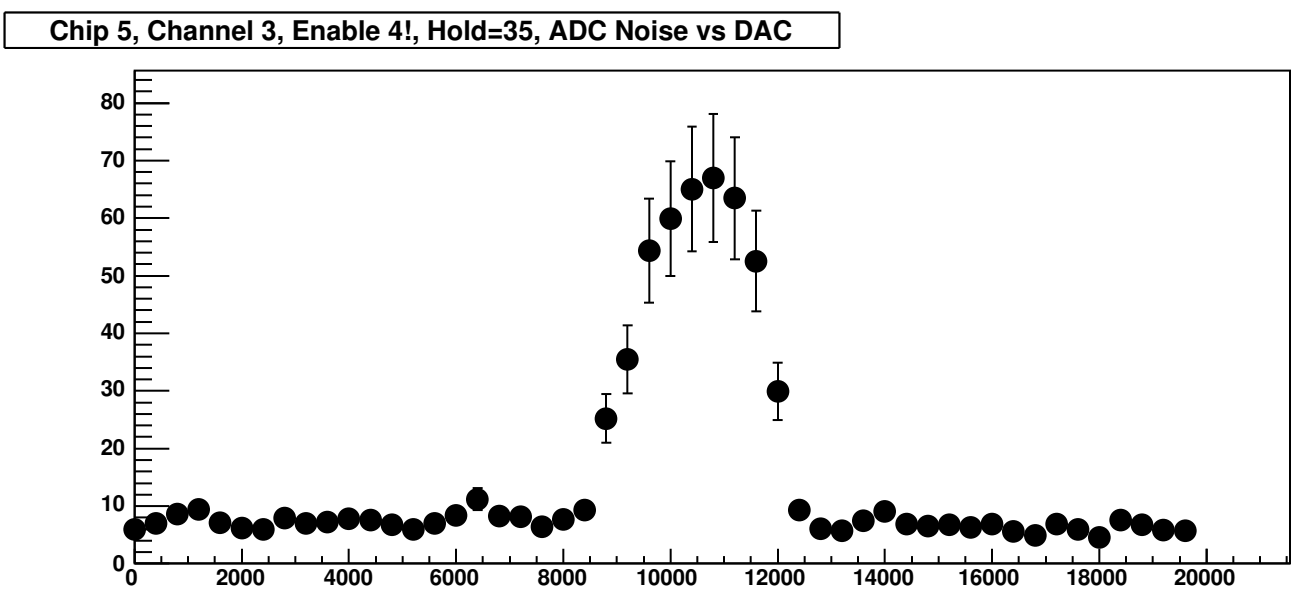
Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

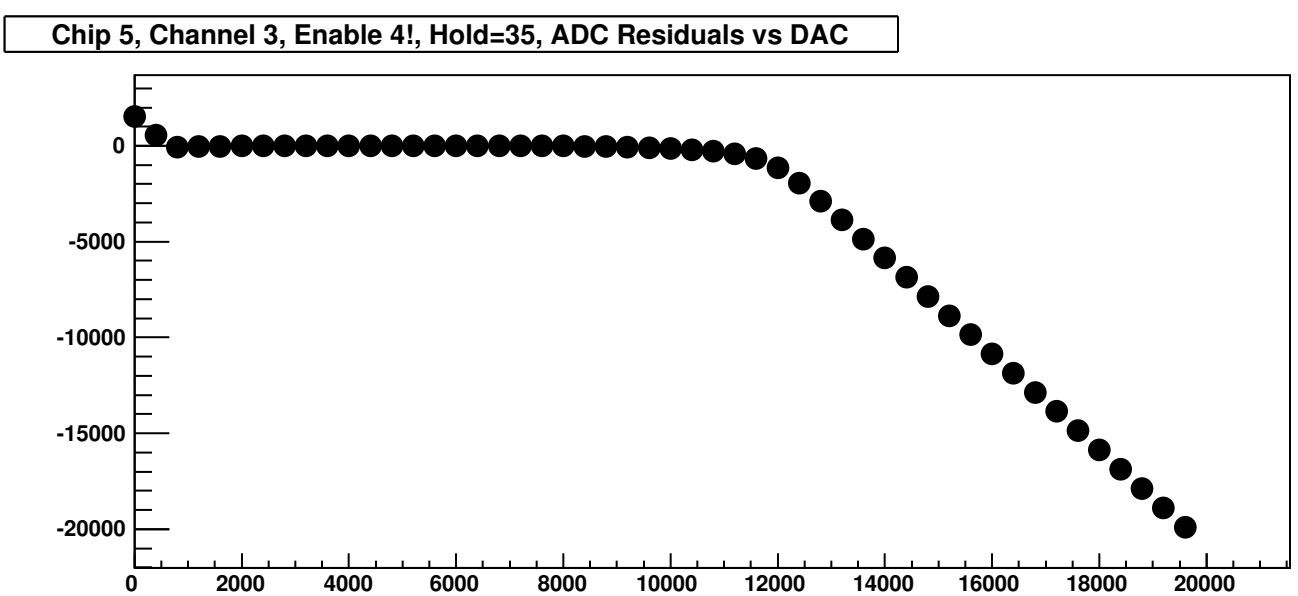


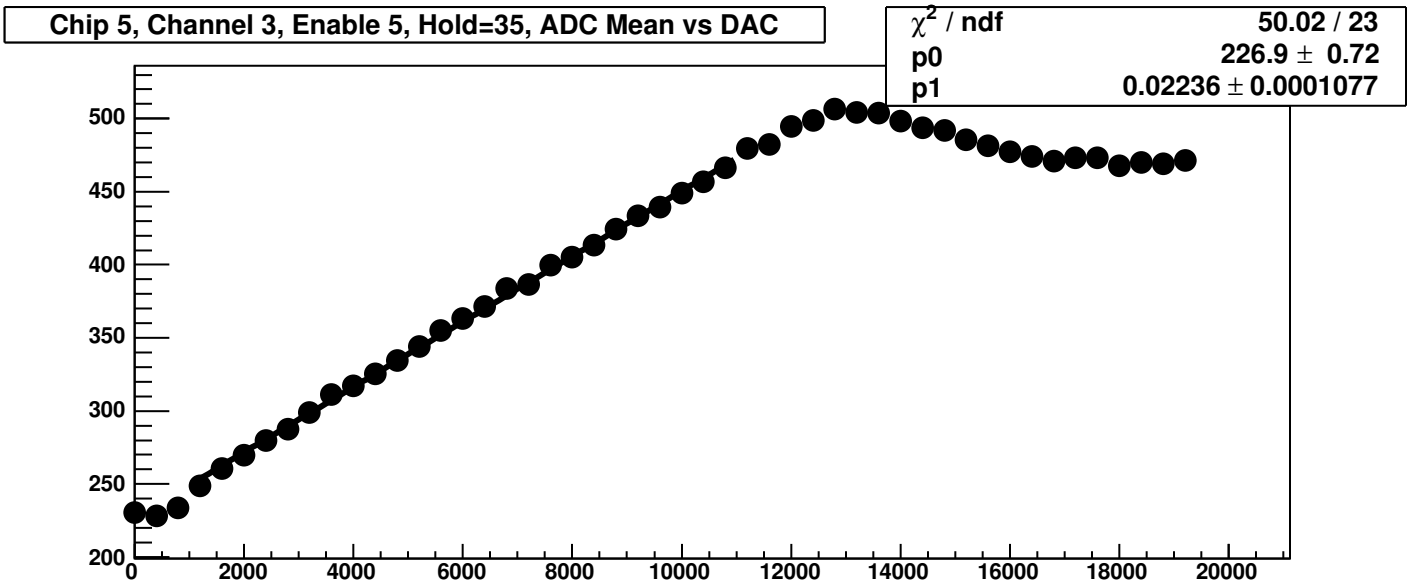
Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

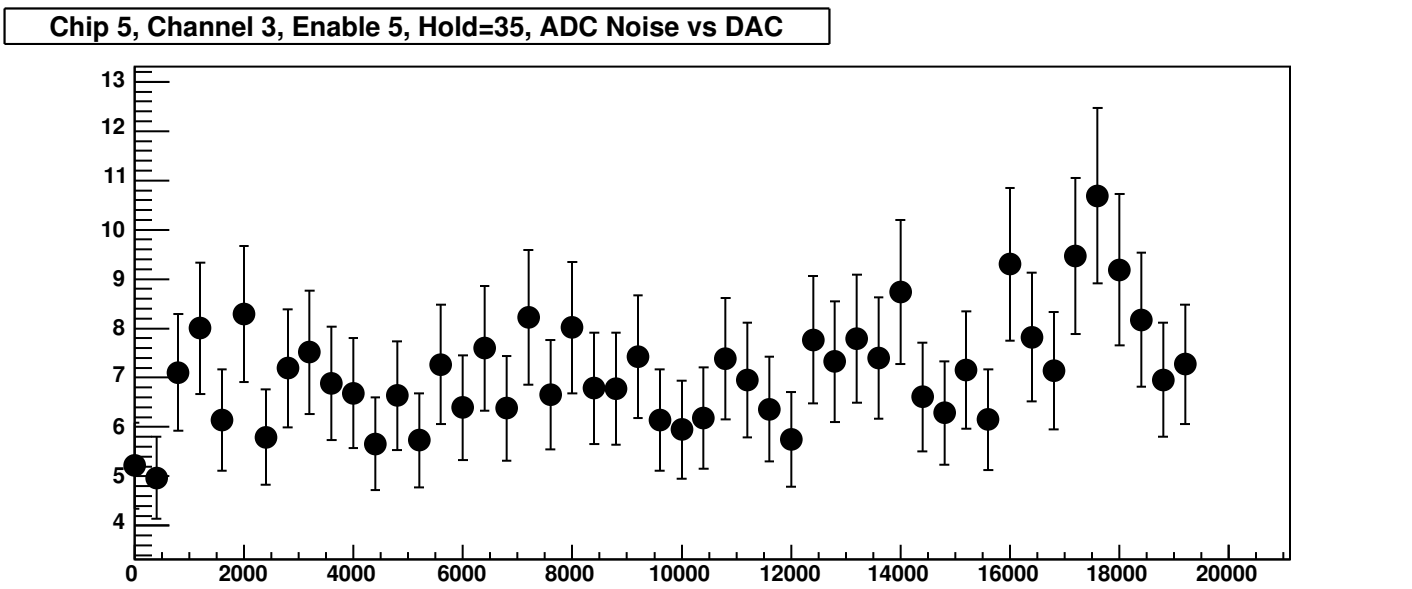


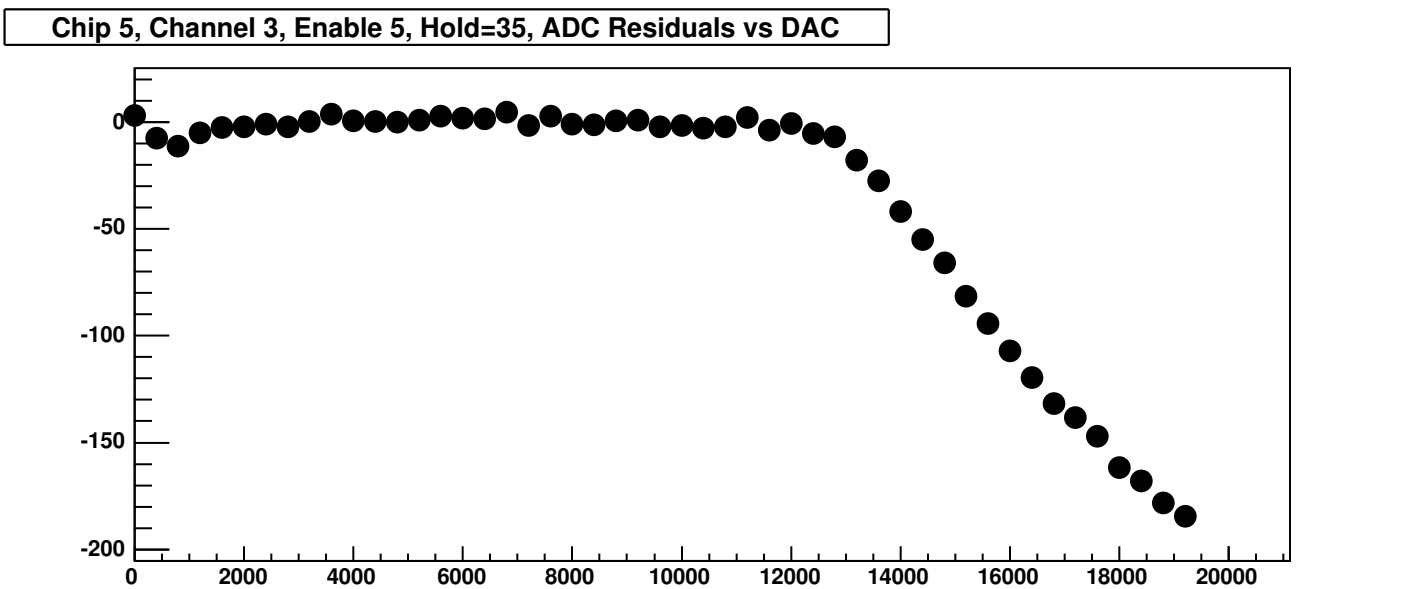

Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 2037 / 23
p0 315.2 ± 0.9176
p1 2.505 ± 0.0001784
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

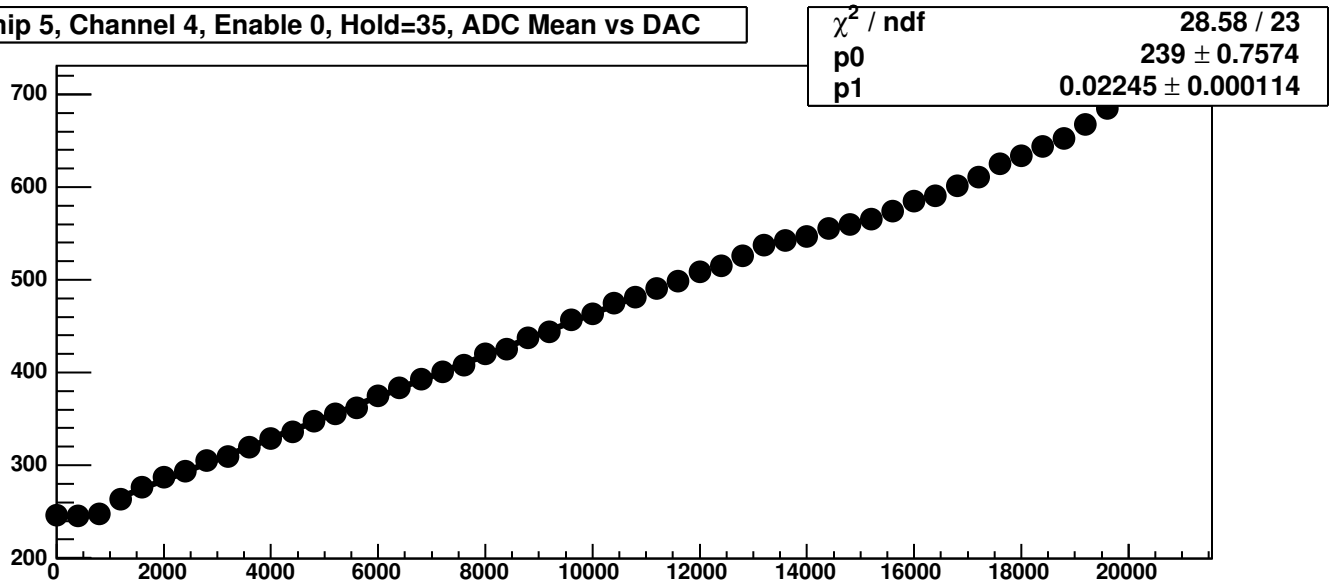

Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 50.02 / 23
p0 226.9 ± 0.72
p1 0.02236 ± 0.0001077
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

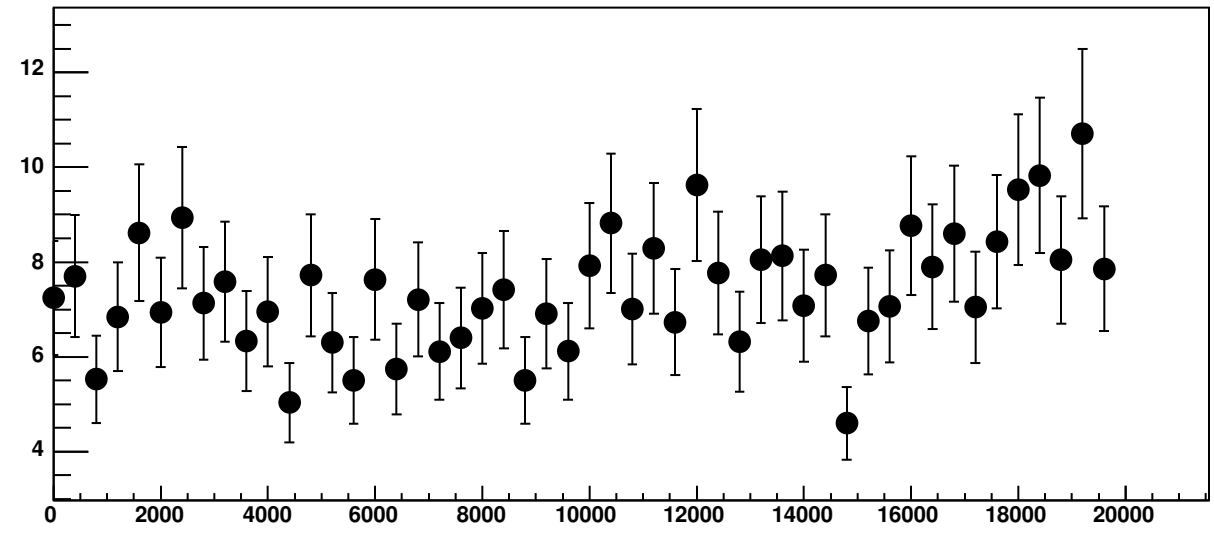

Chip 5, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

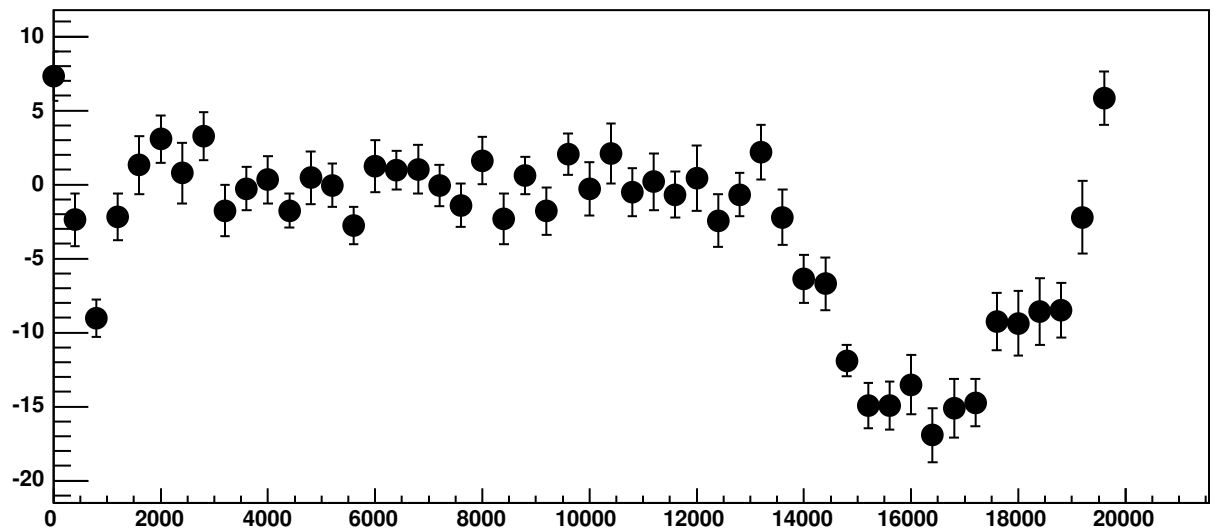
Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

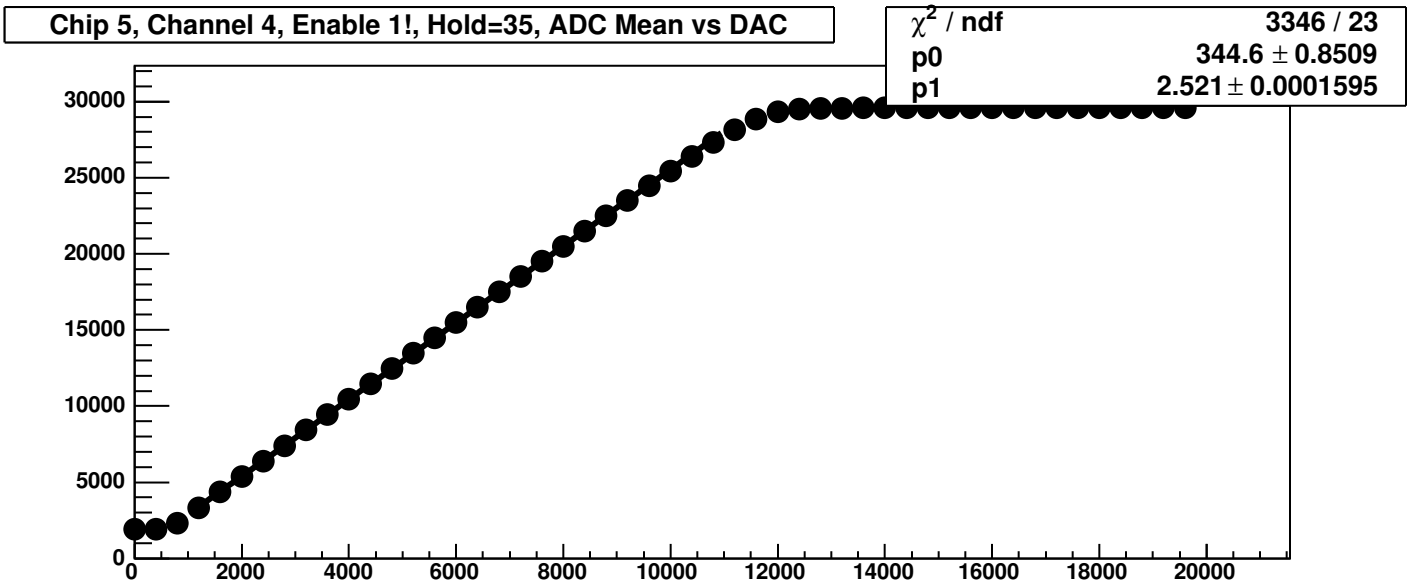


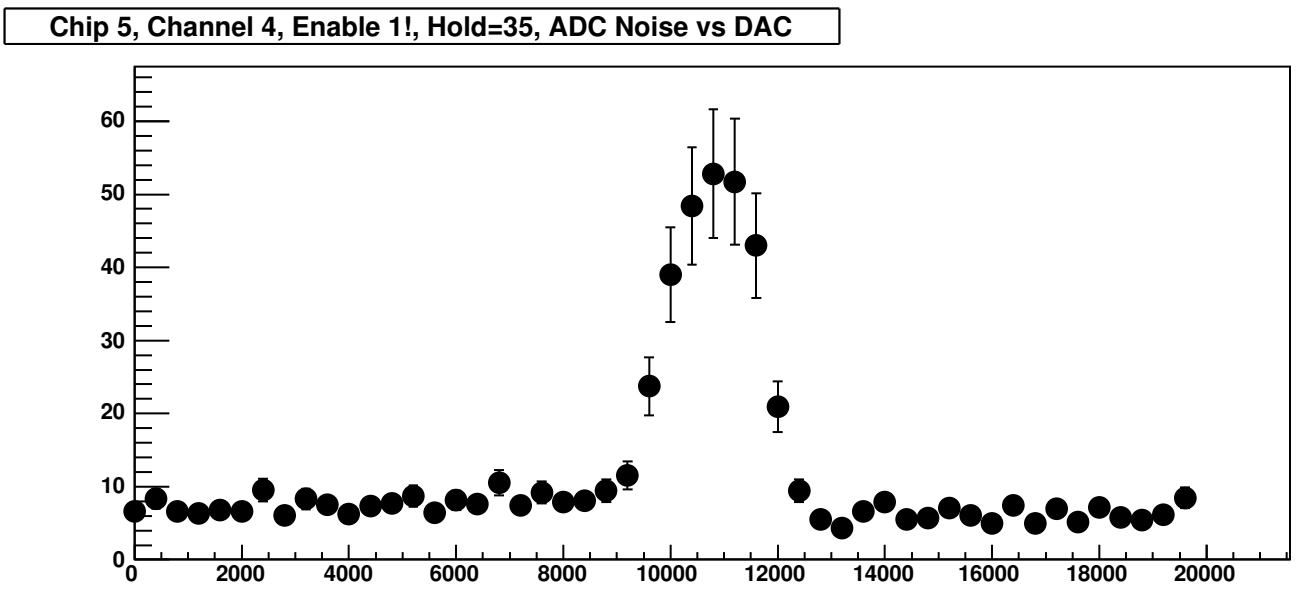
Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

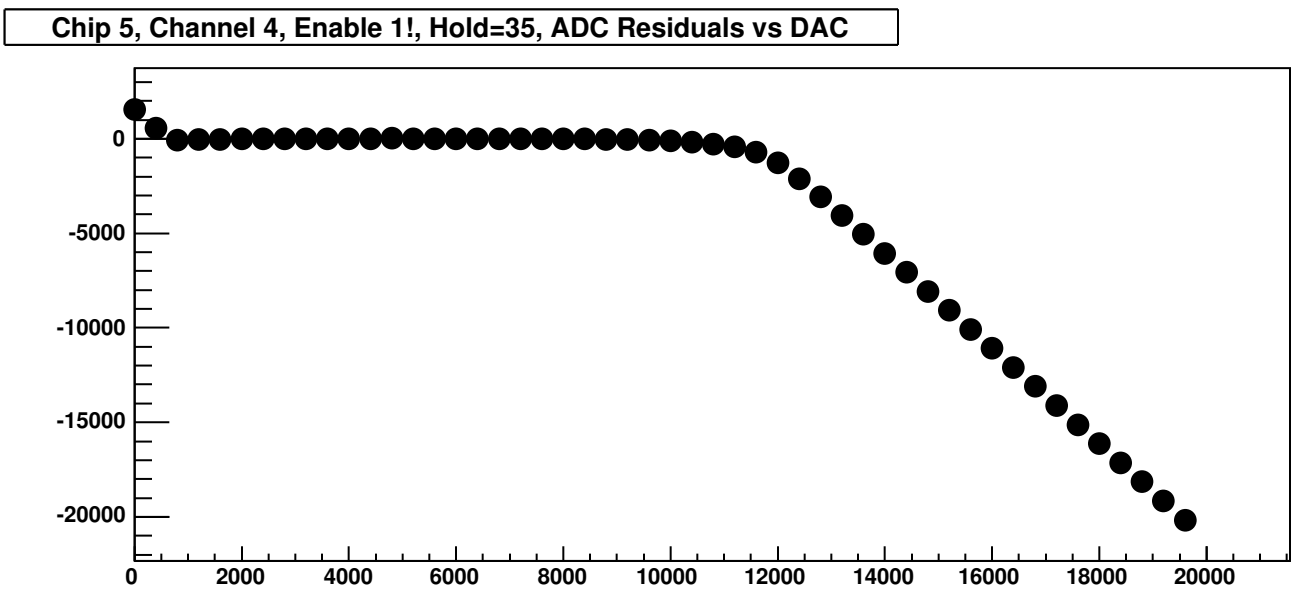


Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

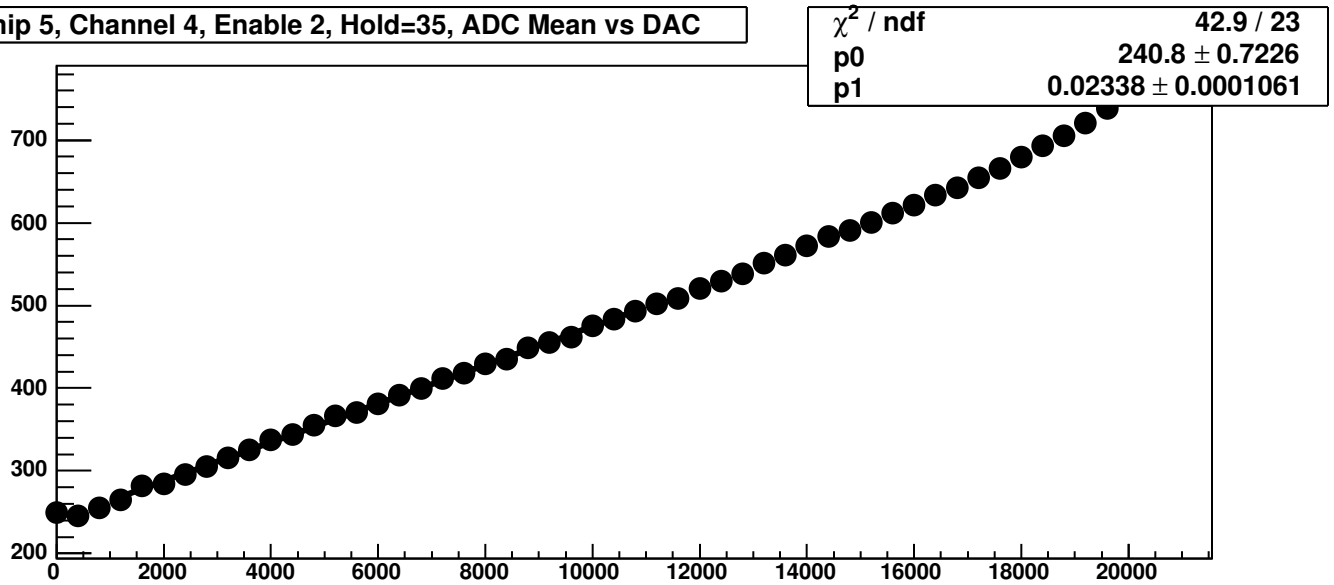



Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 3346 / 23
p0 344.6 ± 0.8509
p1 2.521 ± 0.0001595
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

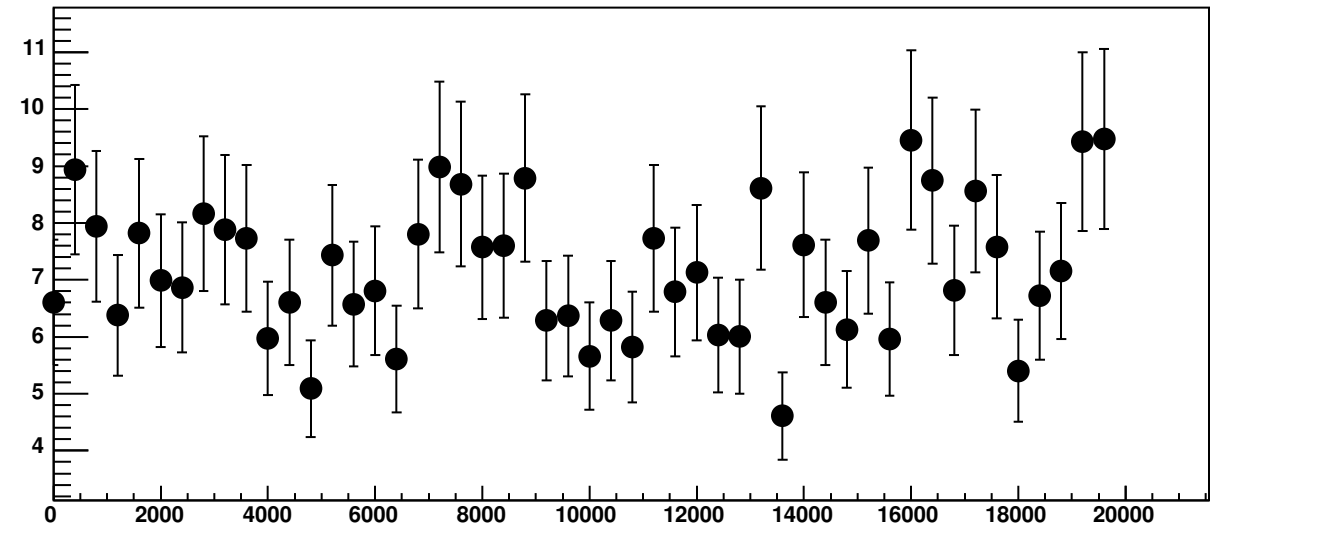

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

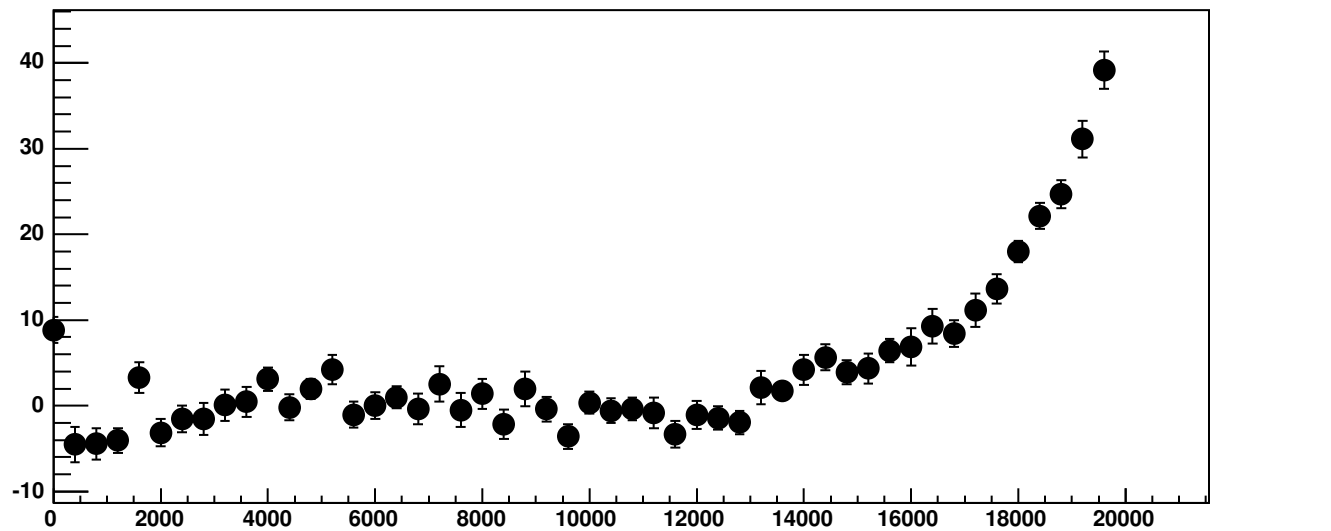
Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



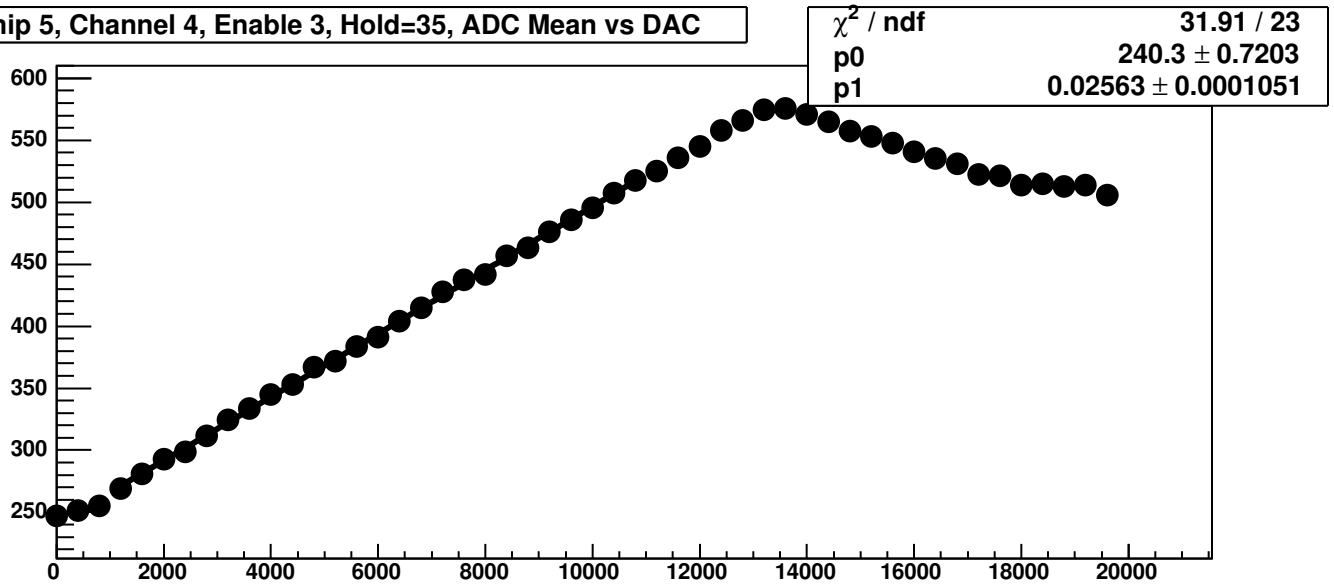
Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



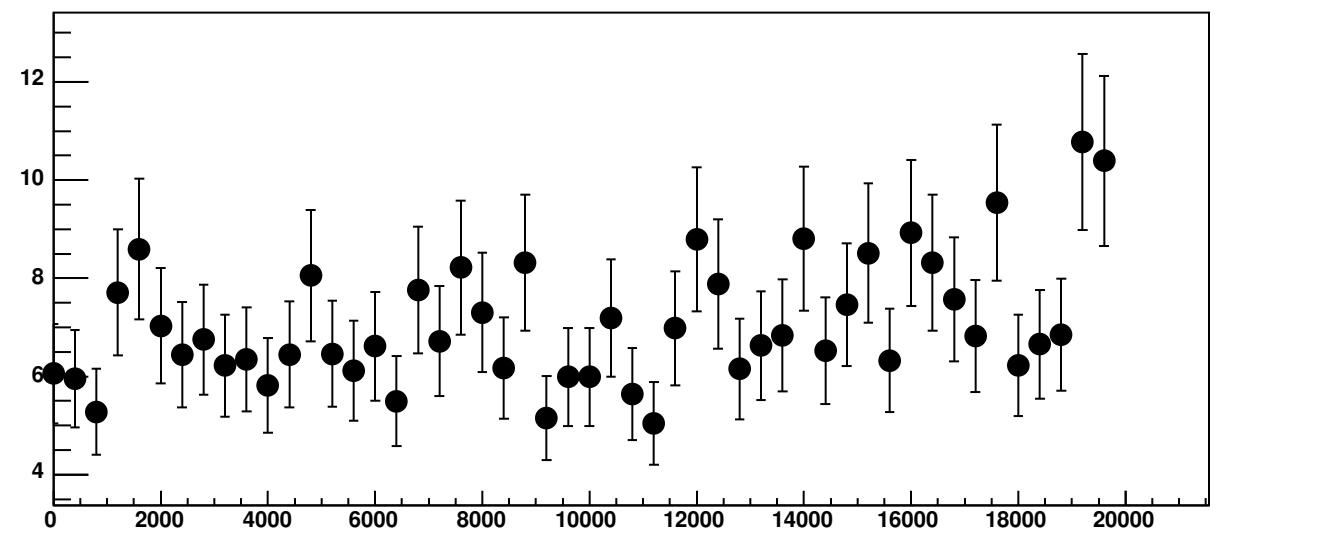
Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



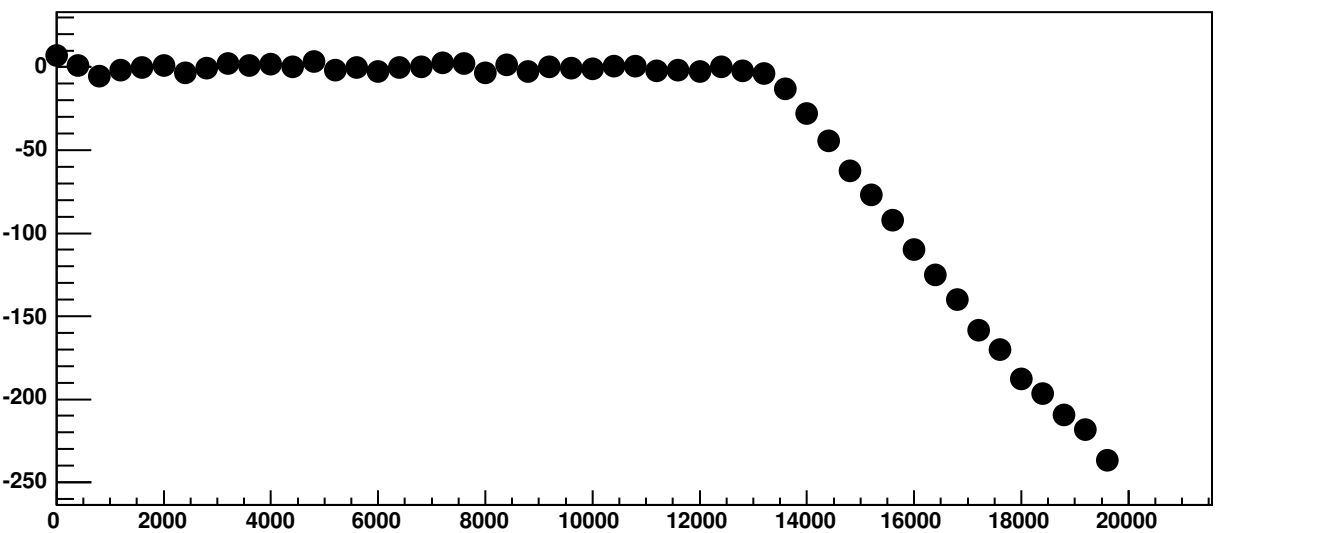
Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

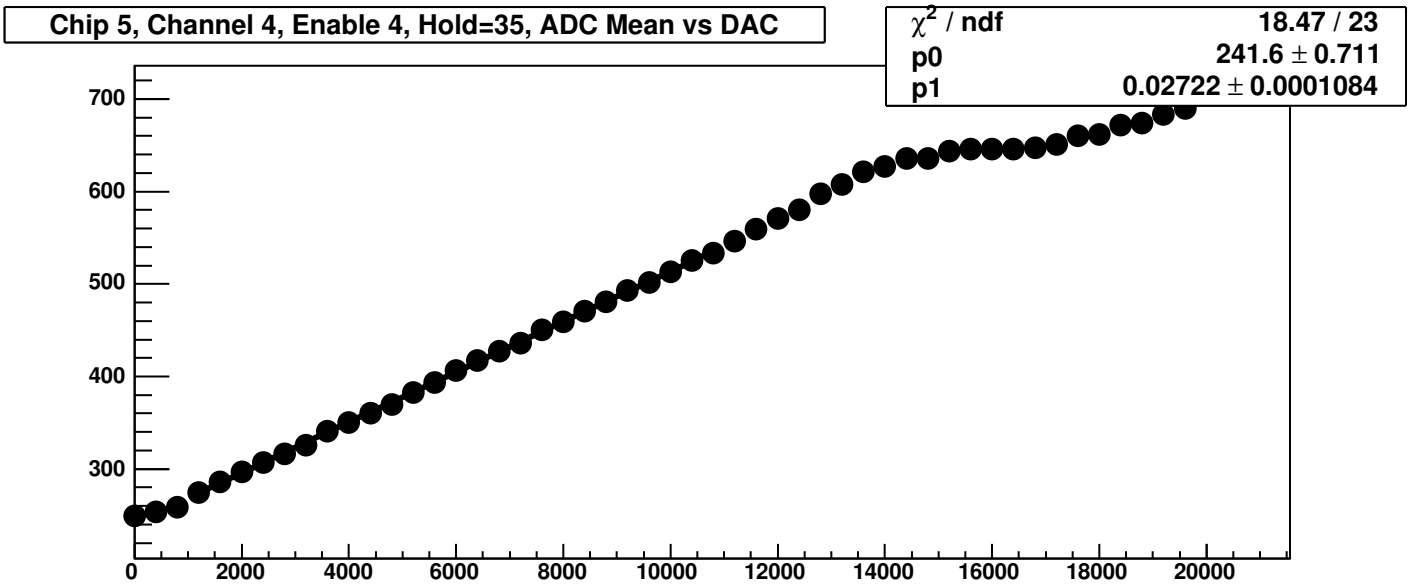


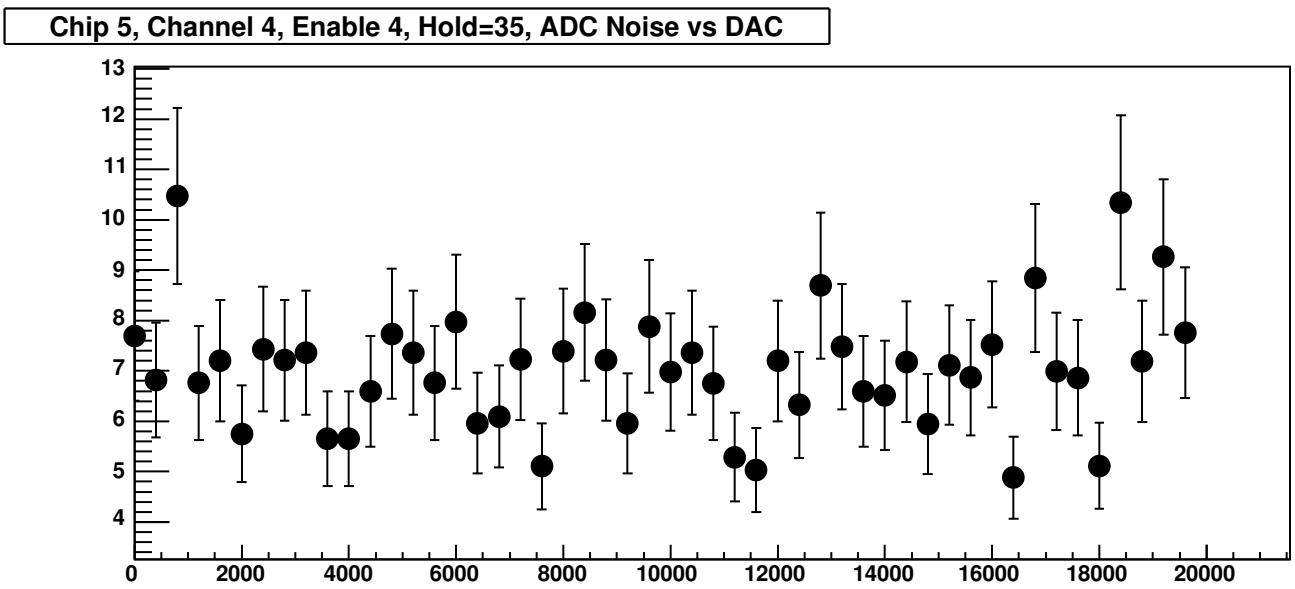
Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

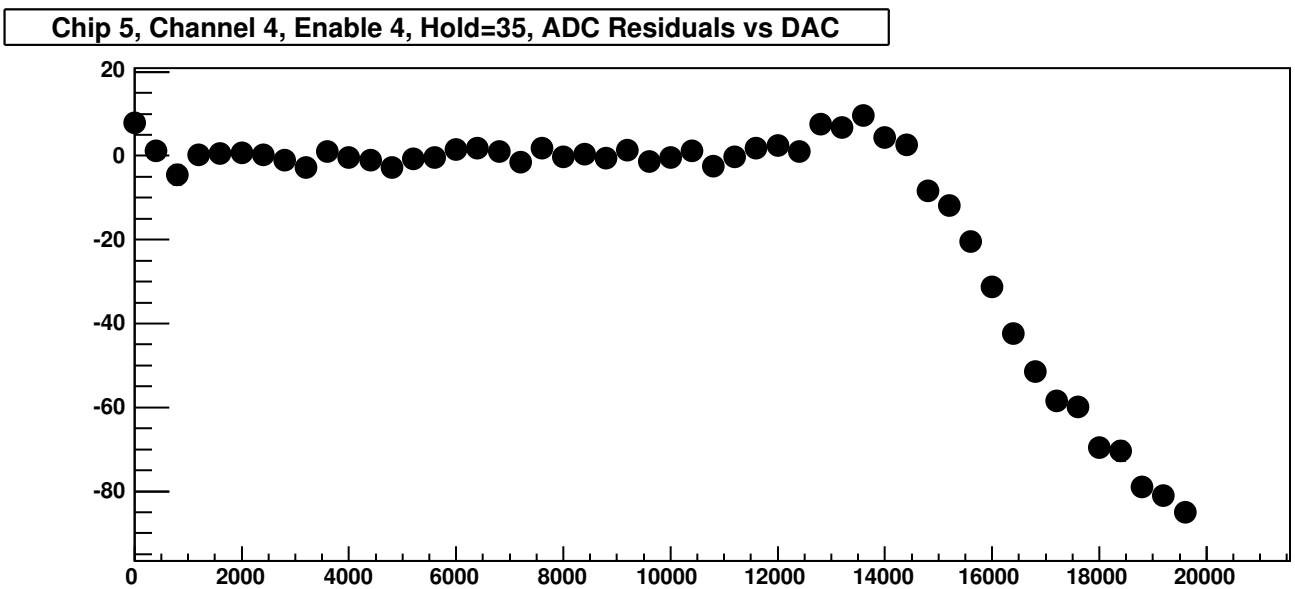


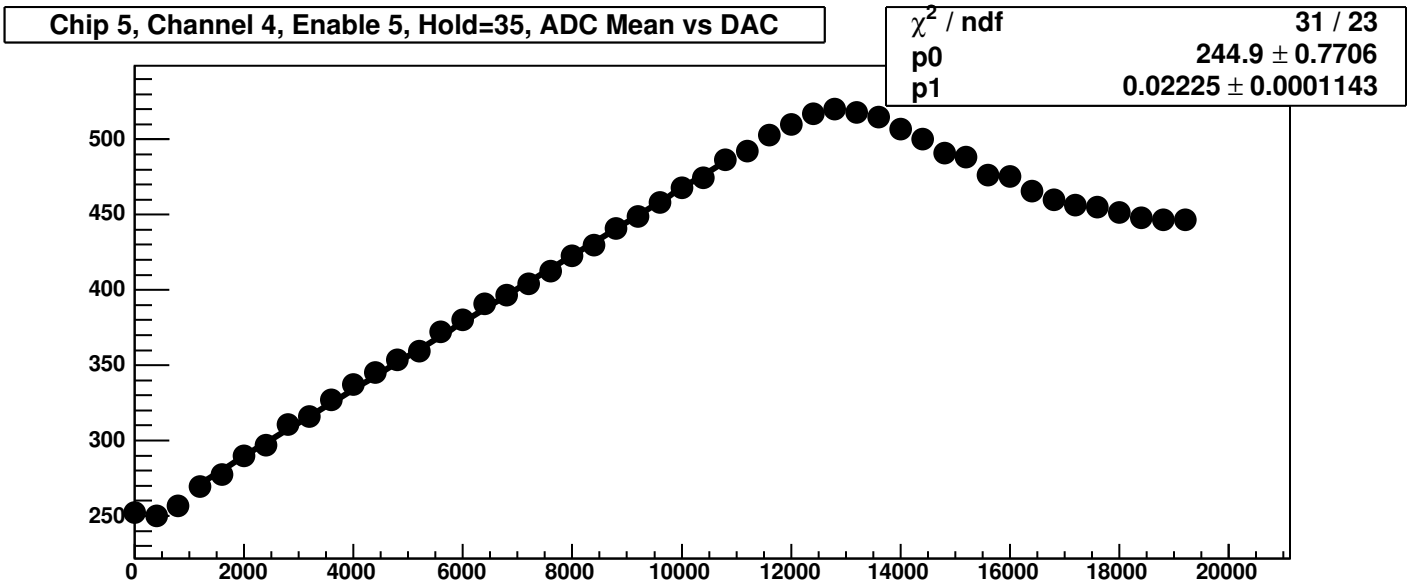
Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

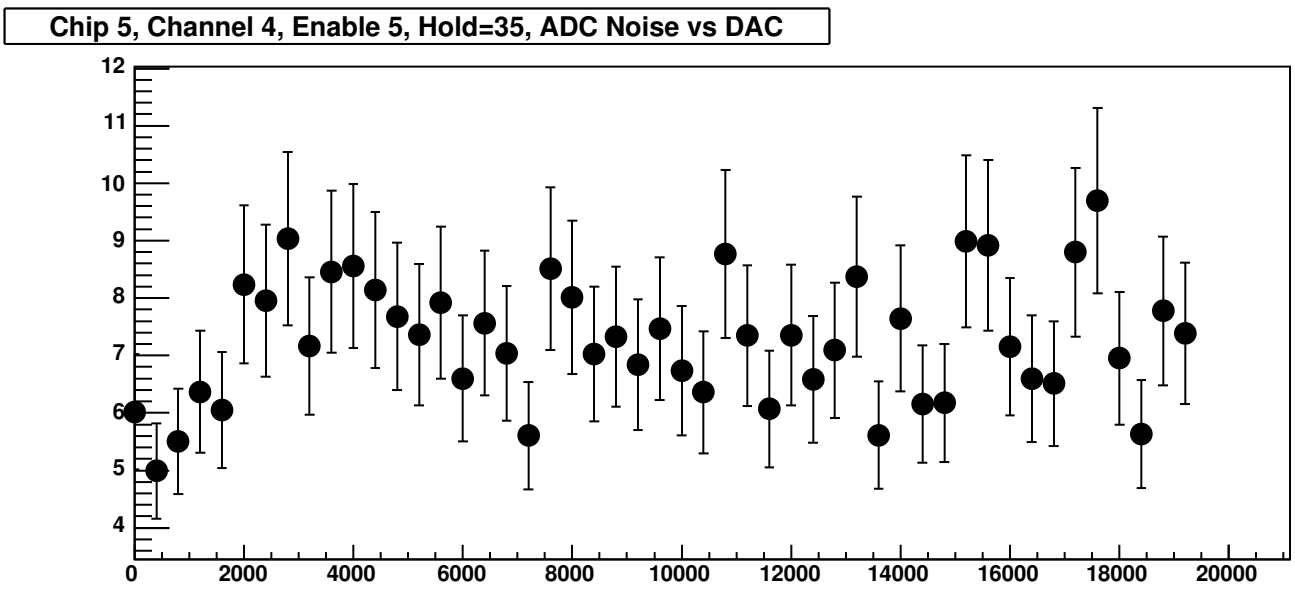


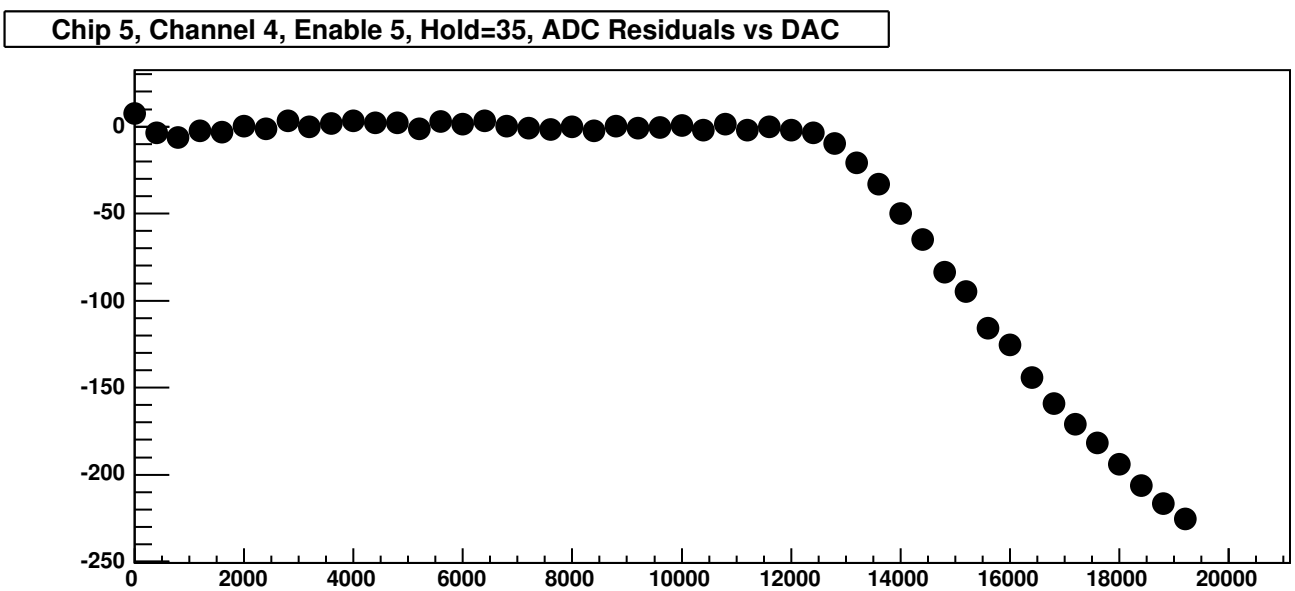

Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 18.47 / 23
p0 241.6 ± 0.711
p1 0.02722 ± 0.0001084
700
600
500
400
300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

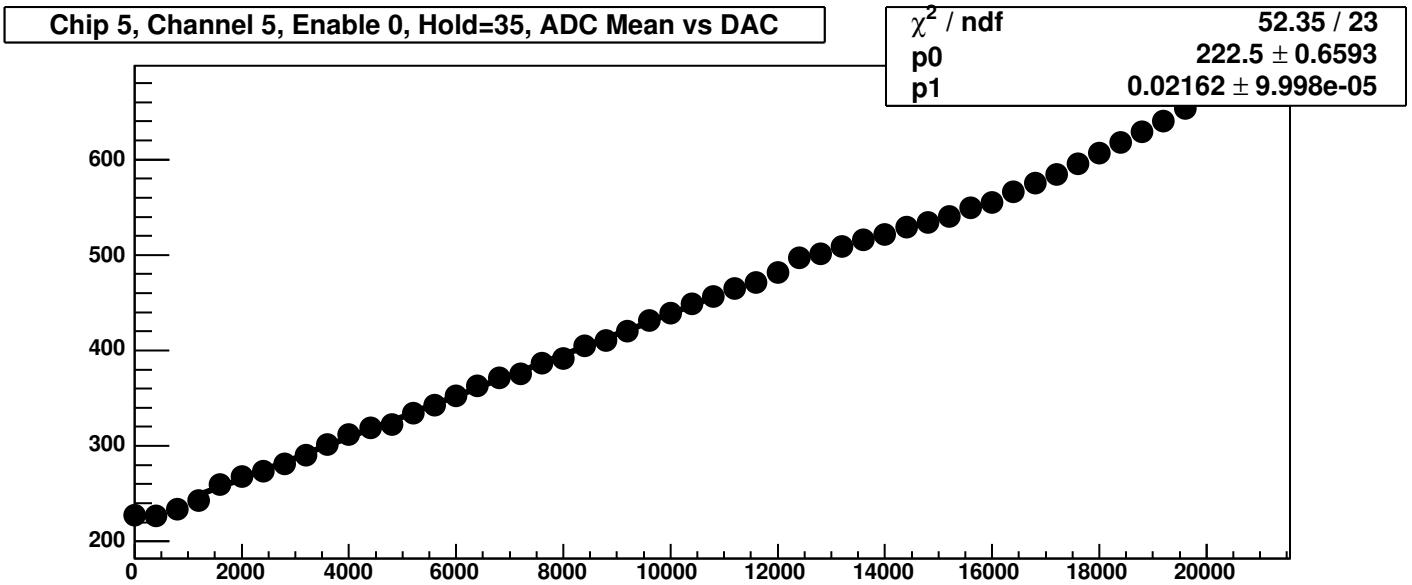

Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

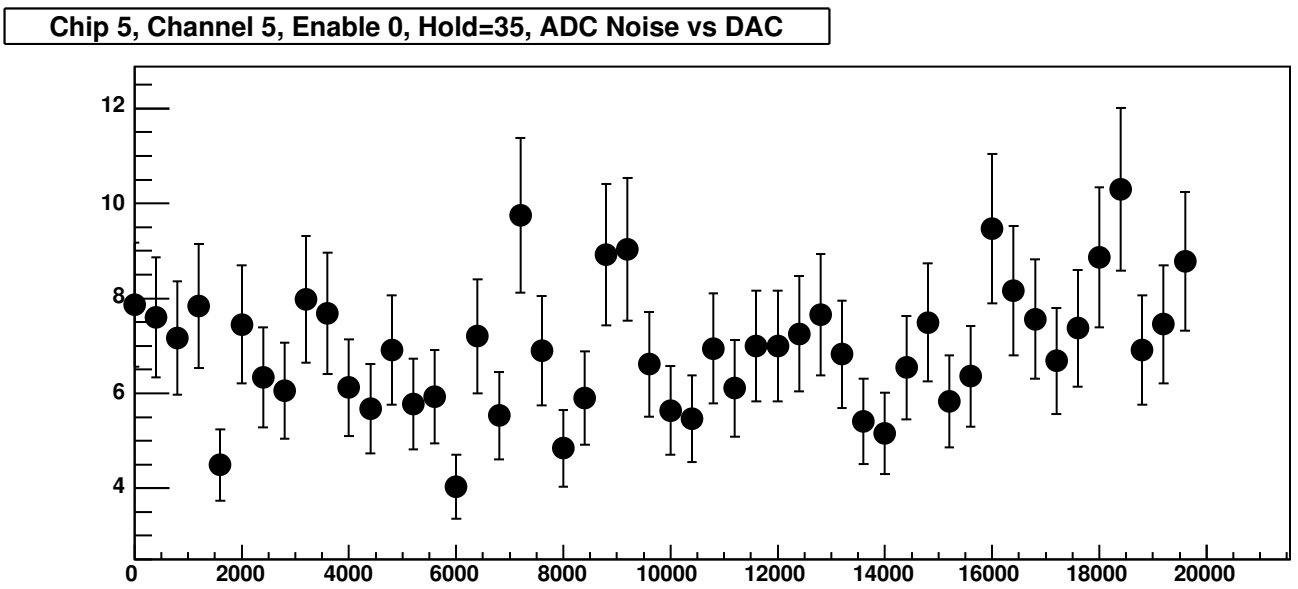

Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

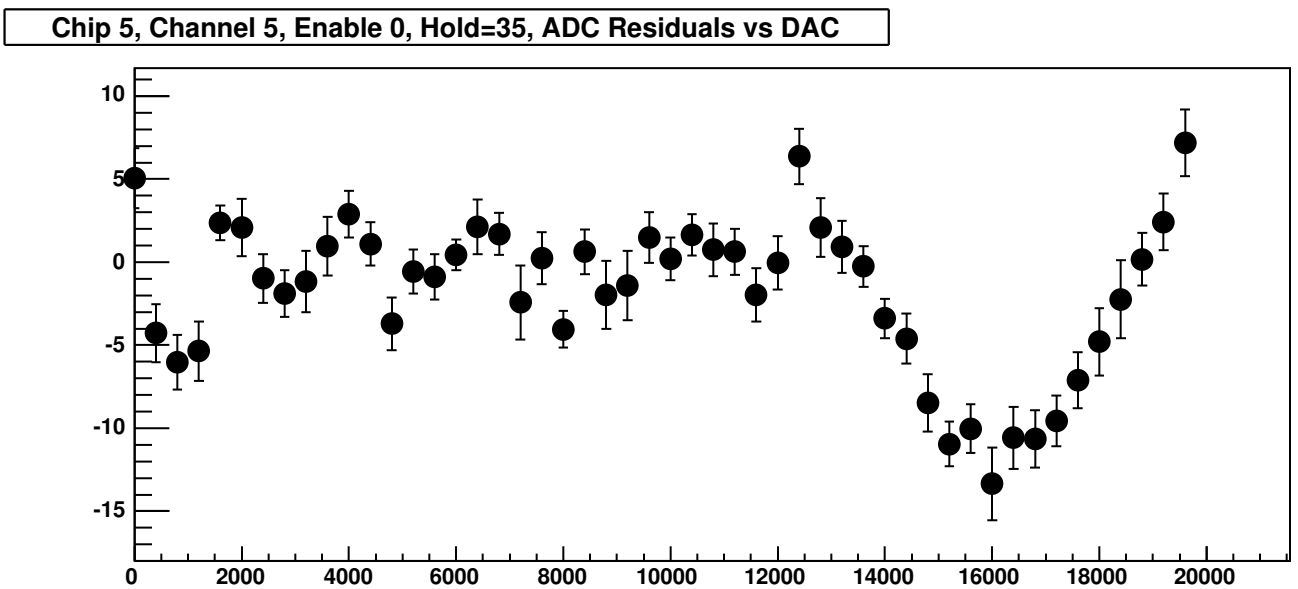

Chip 5, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 31 / 23
p0 244.9 ± 0.7706
p1 0.02225 ± 0.0001143
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

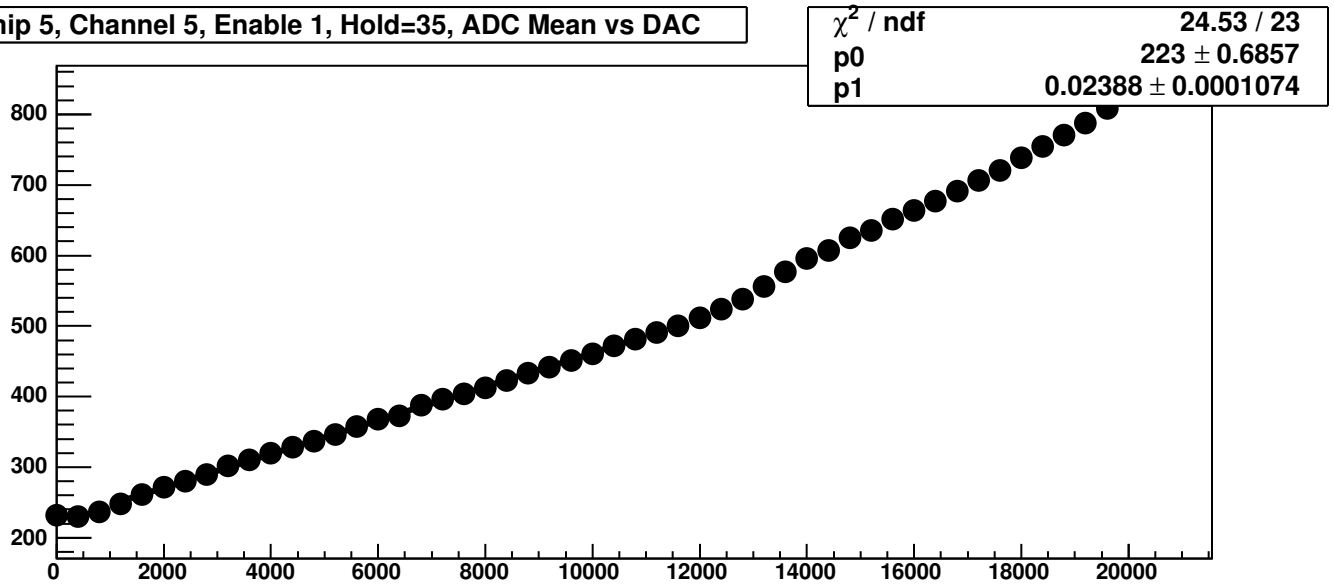

Chip 5, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 52.35 / 23
p0 222.5 ± 0.6593
p1 0.02162 ± 9.998e-05
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

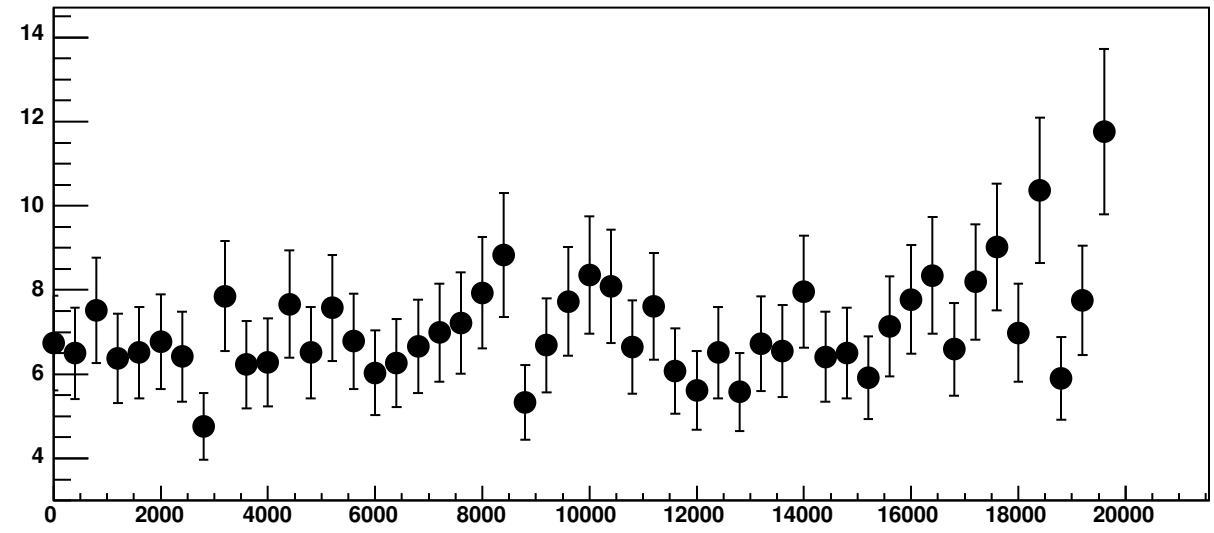

Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

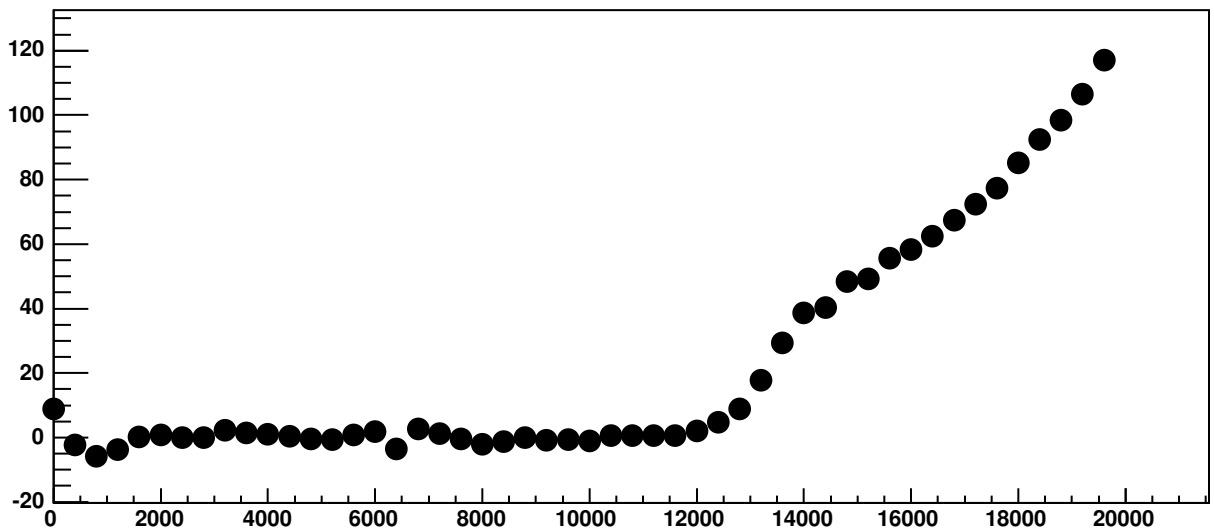
Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

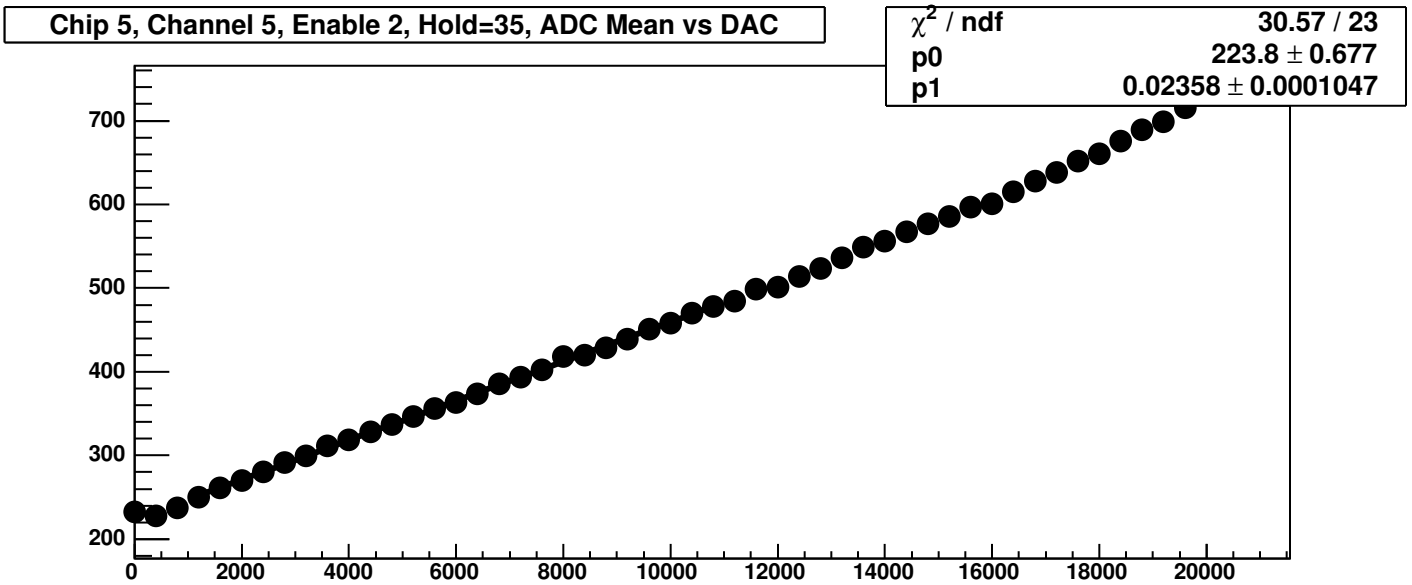


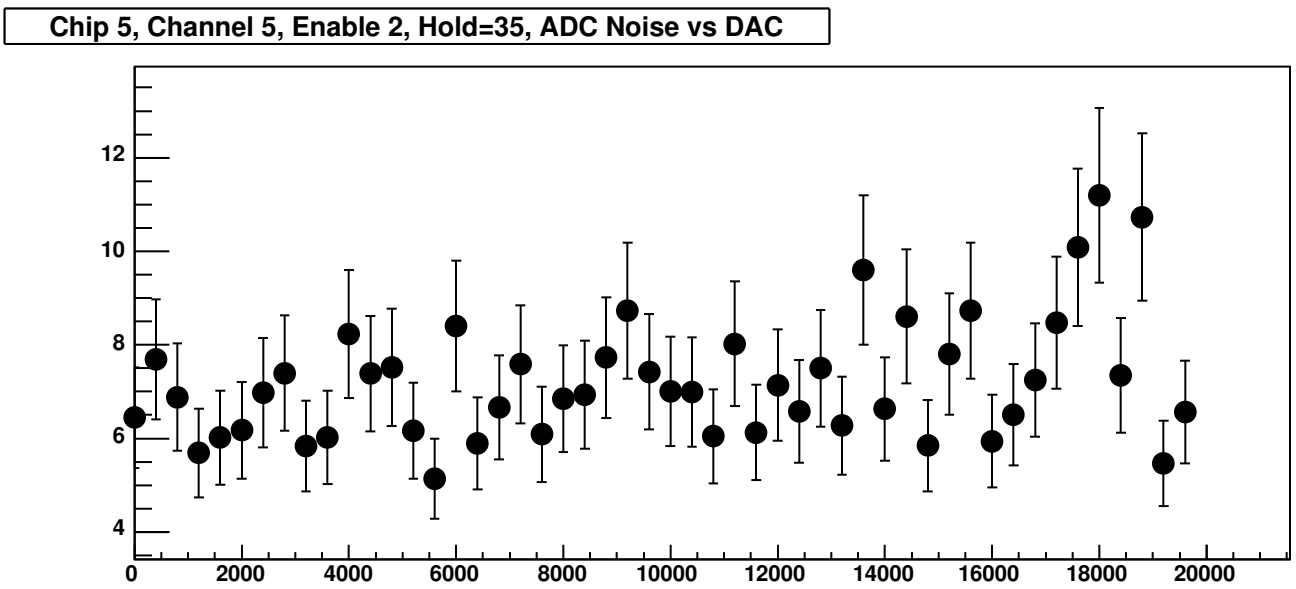
Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC

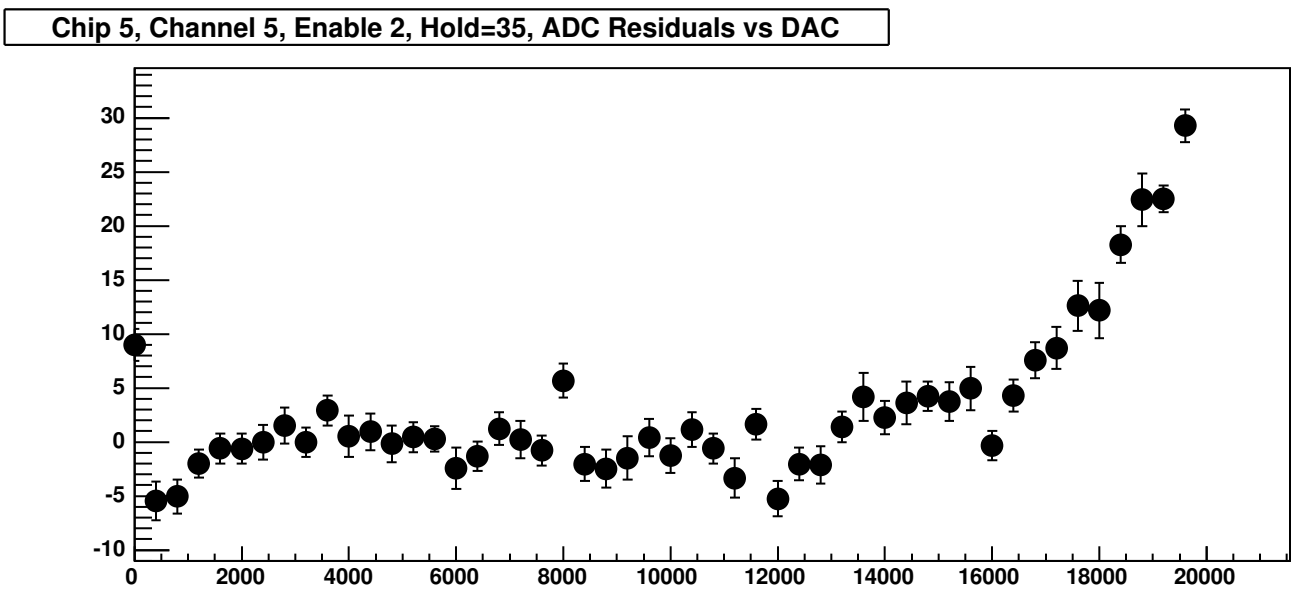


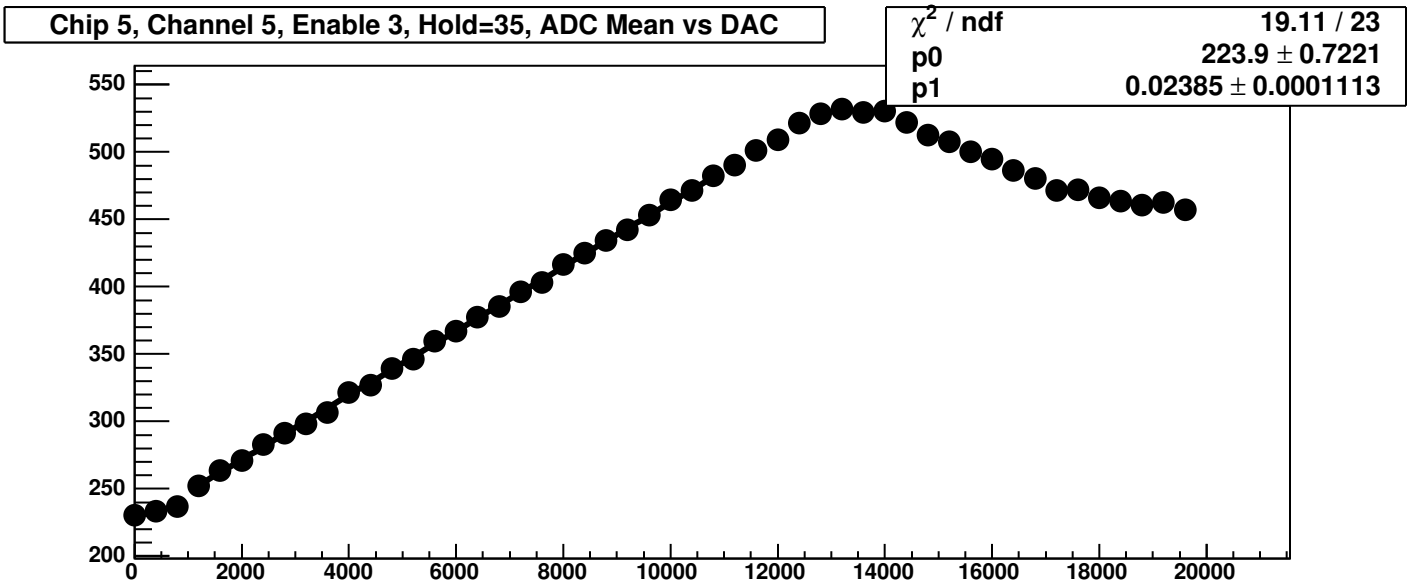
Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

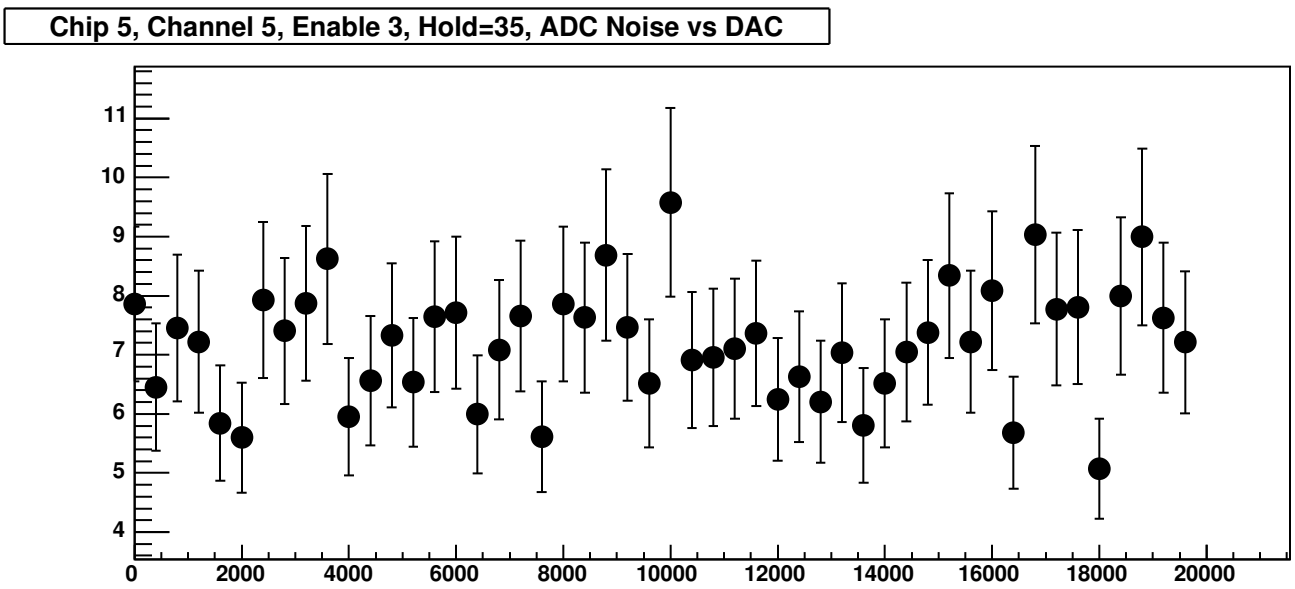


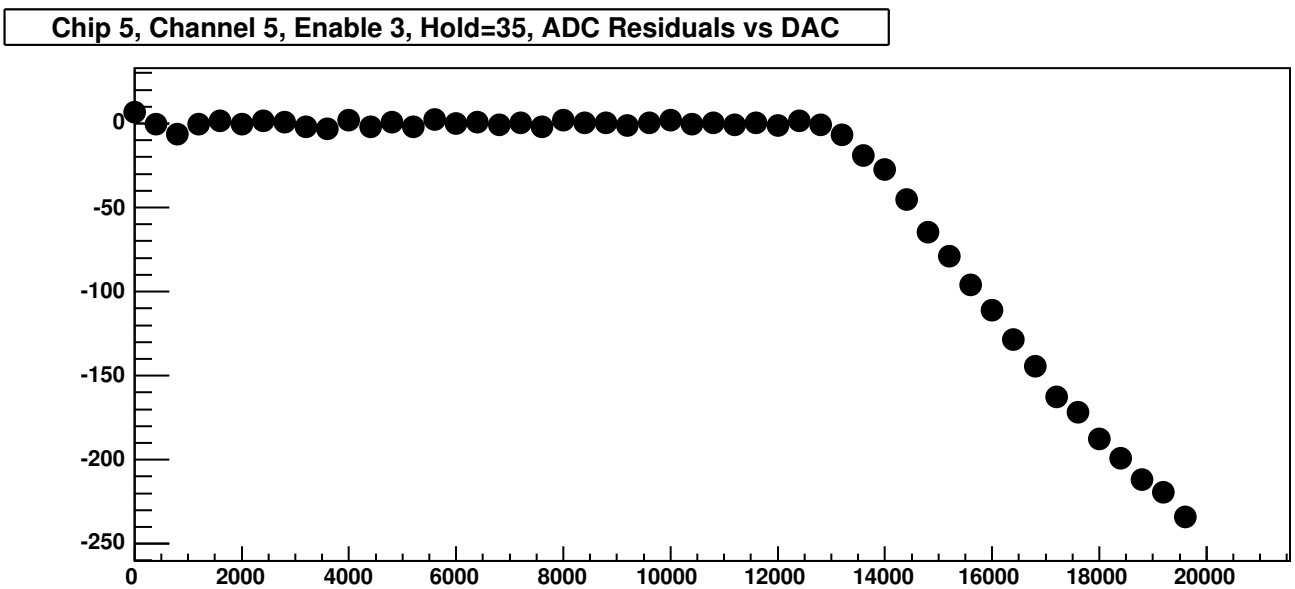

Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.57 / 23
p0 223.8 ± 0.677
p1 0.02358 ± 0.0001047
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

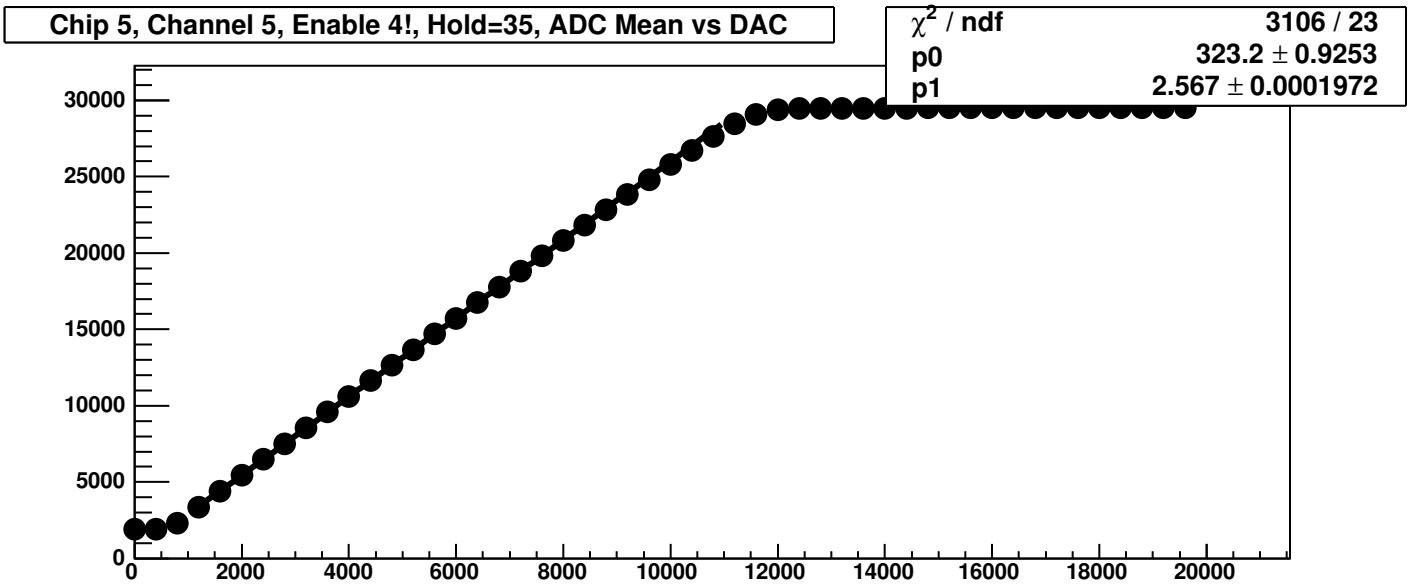

Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

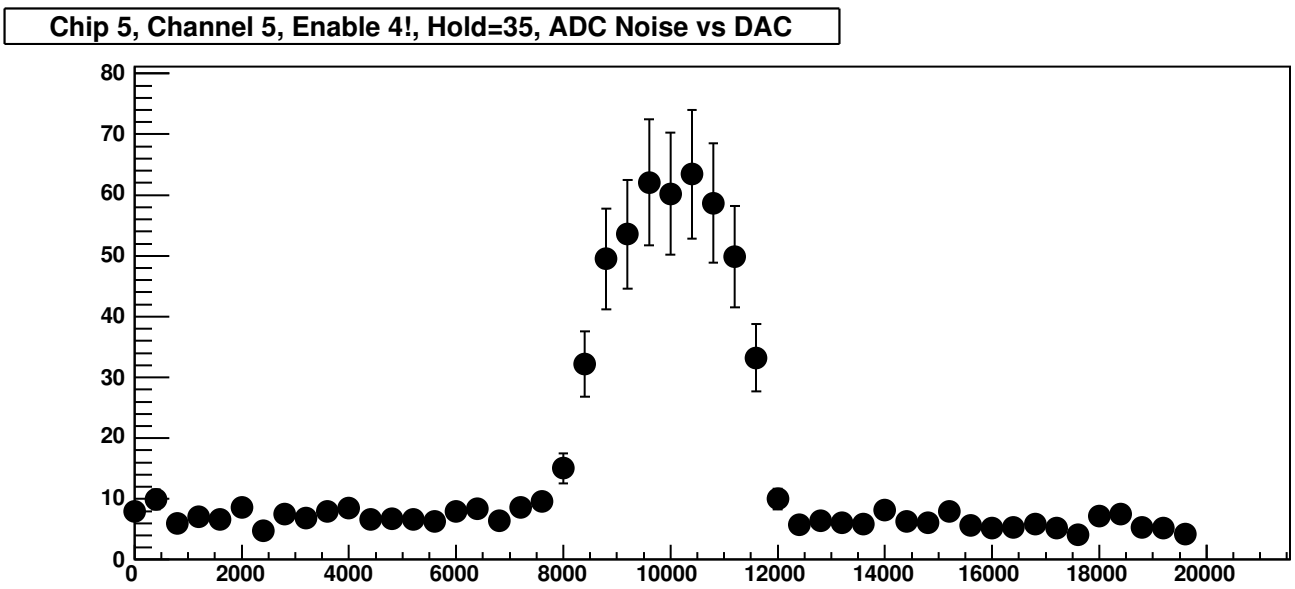

Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

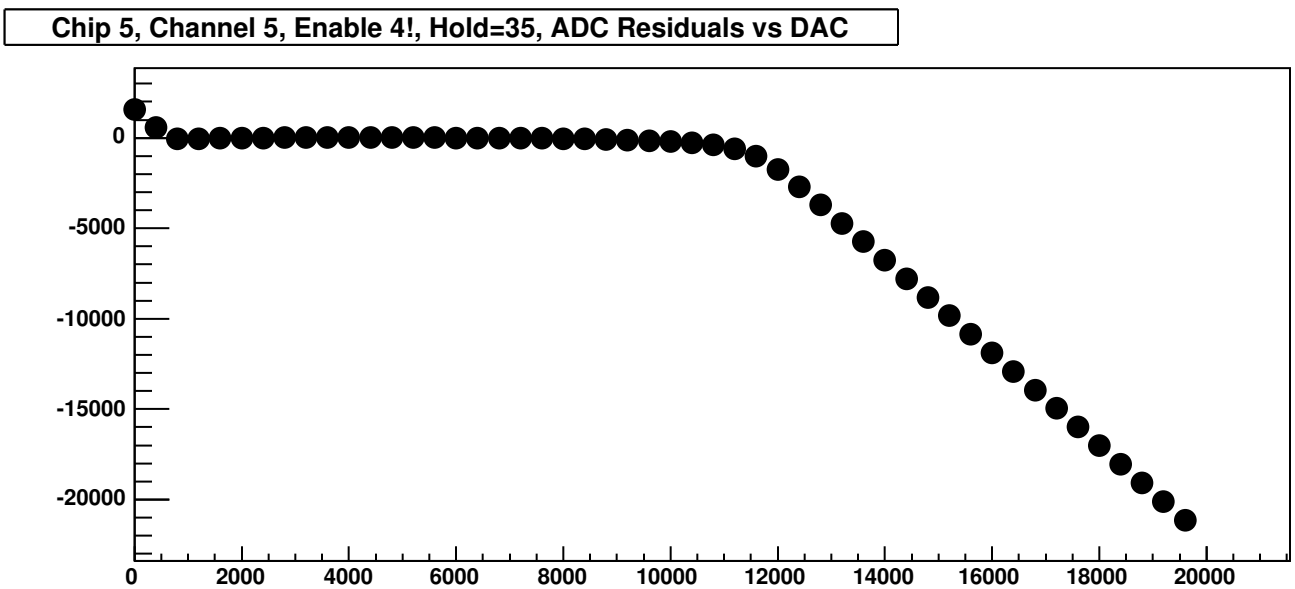

Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.11 / 23
p0 223.9 ± 0.7221
p1 0.02385 ± 0.0001113
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

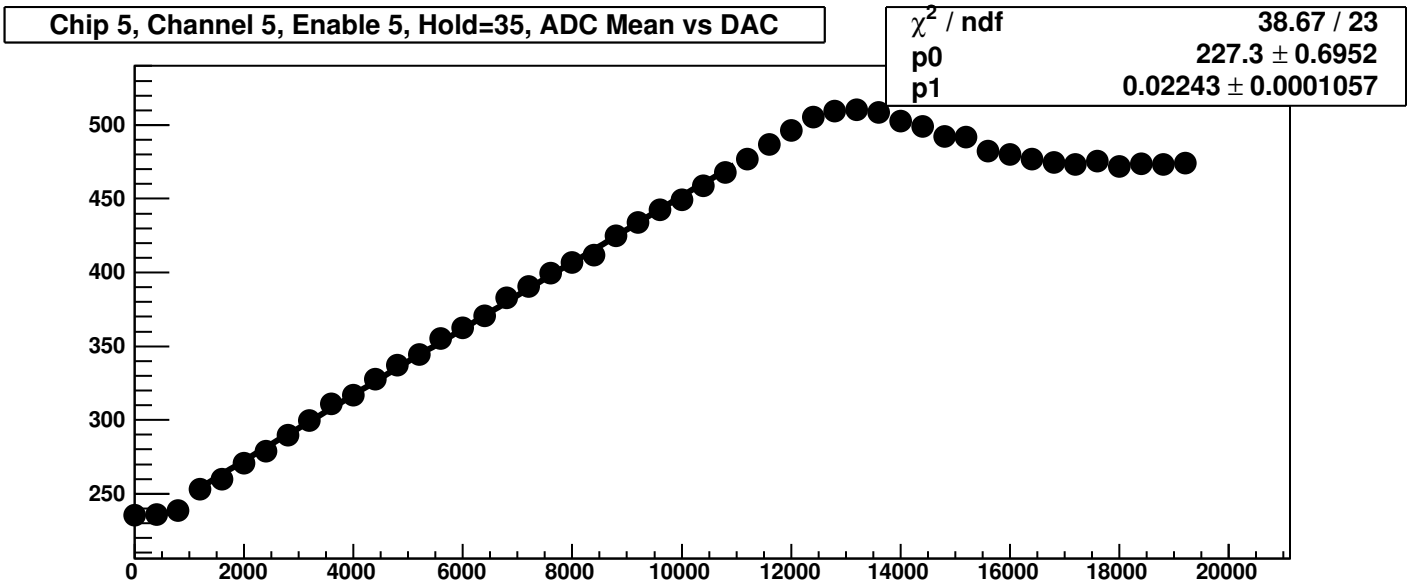

Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

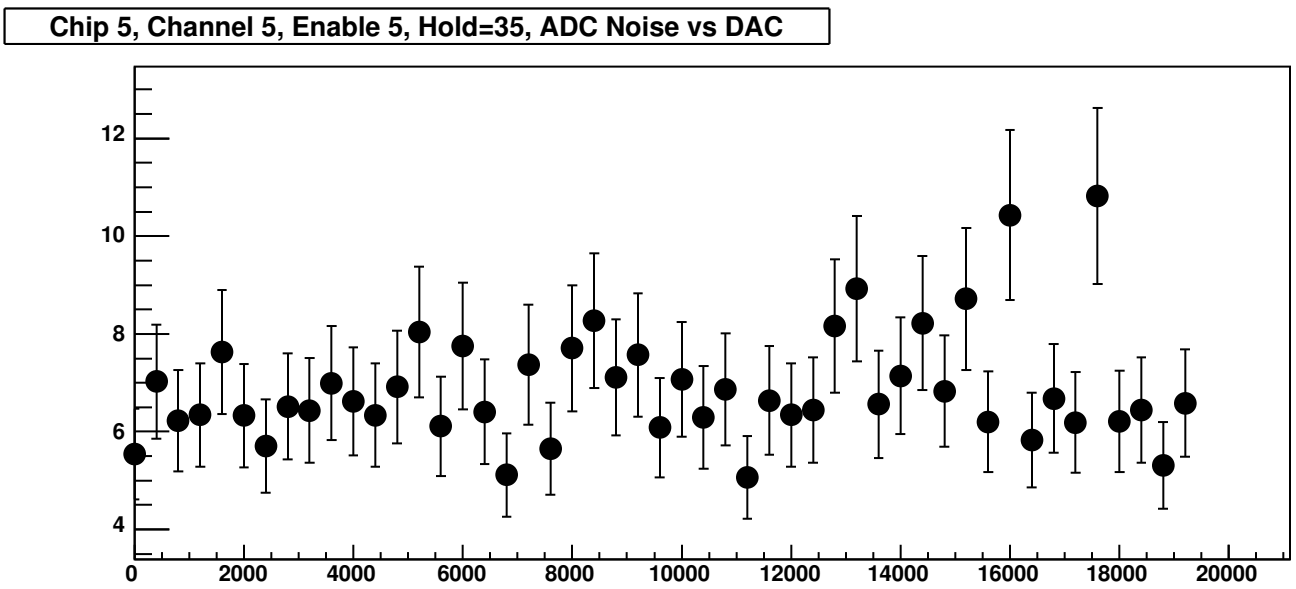

Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

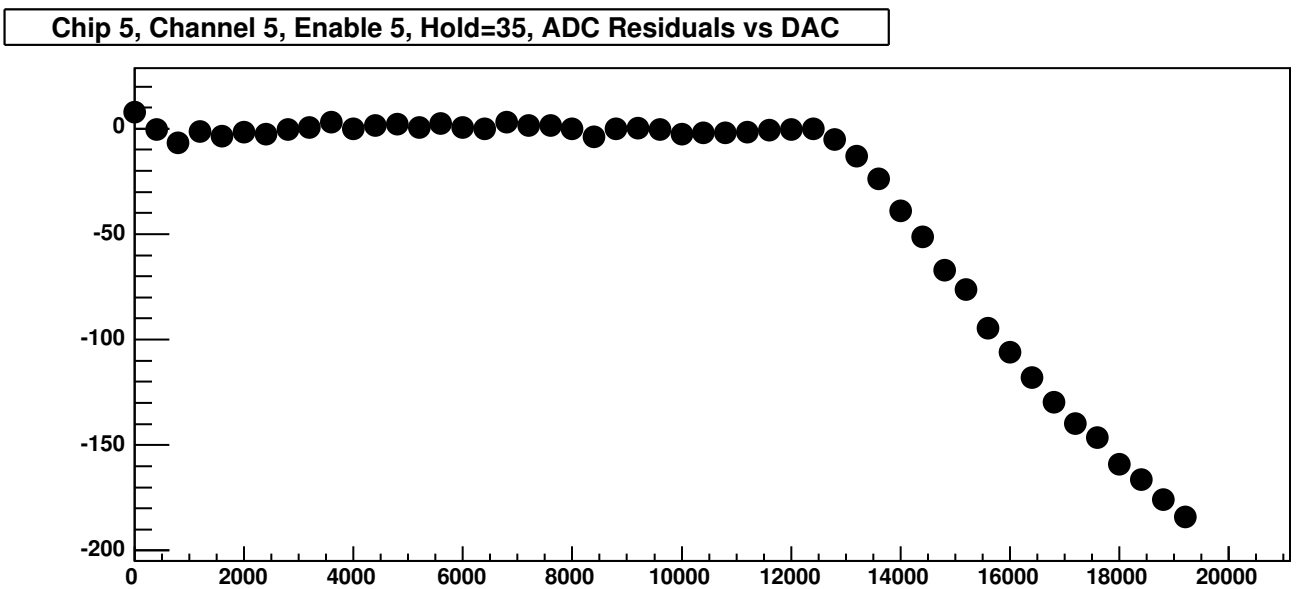

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 3106 / 23
p0 323.2 ± 0.9253
p1 2.567 ± 0.0001972
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

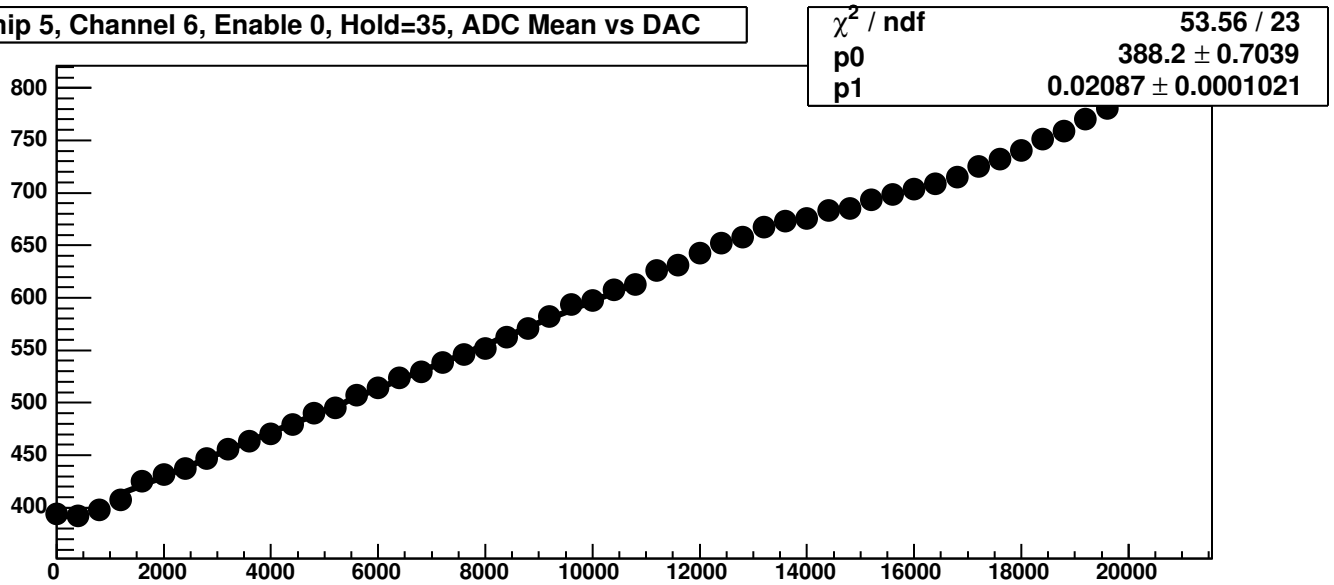

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 38.67 / 23
p0 227.3 ± 0.6952
p1 0.02243 ± 0.0001057
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

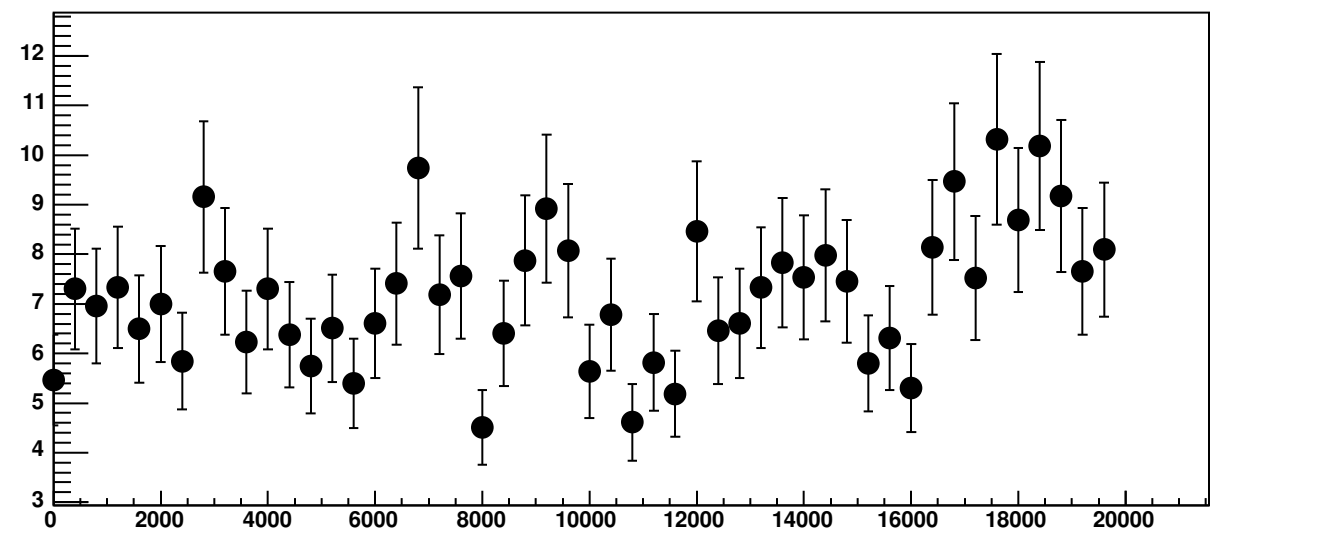

Chip 5, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

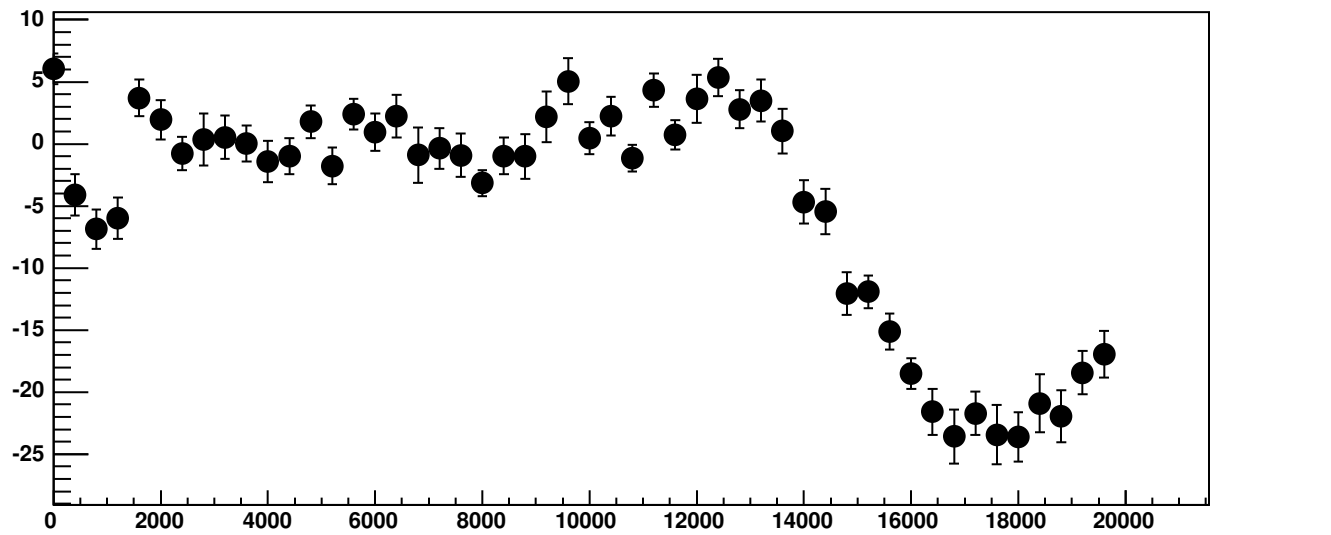
Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



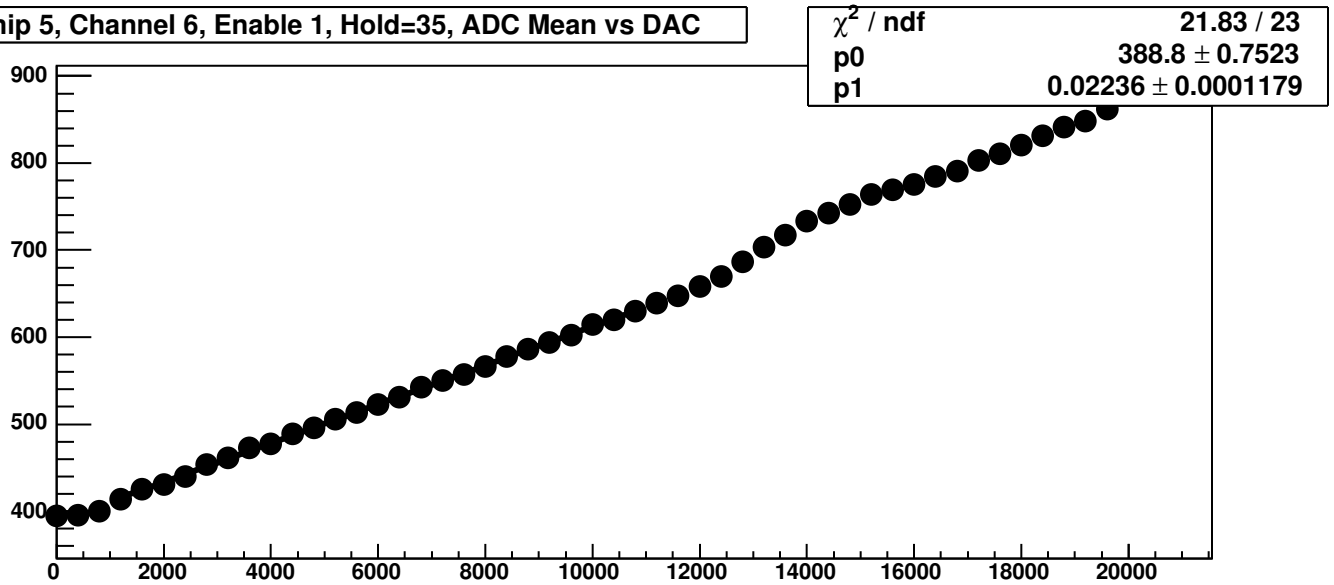
Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



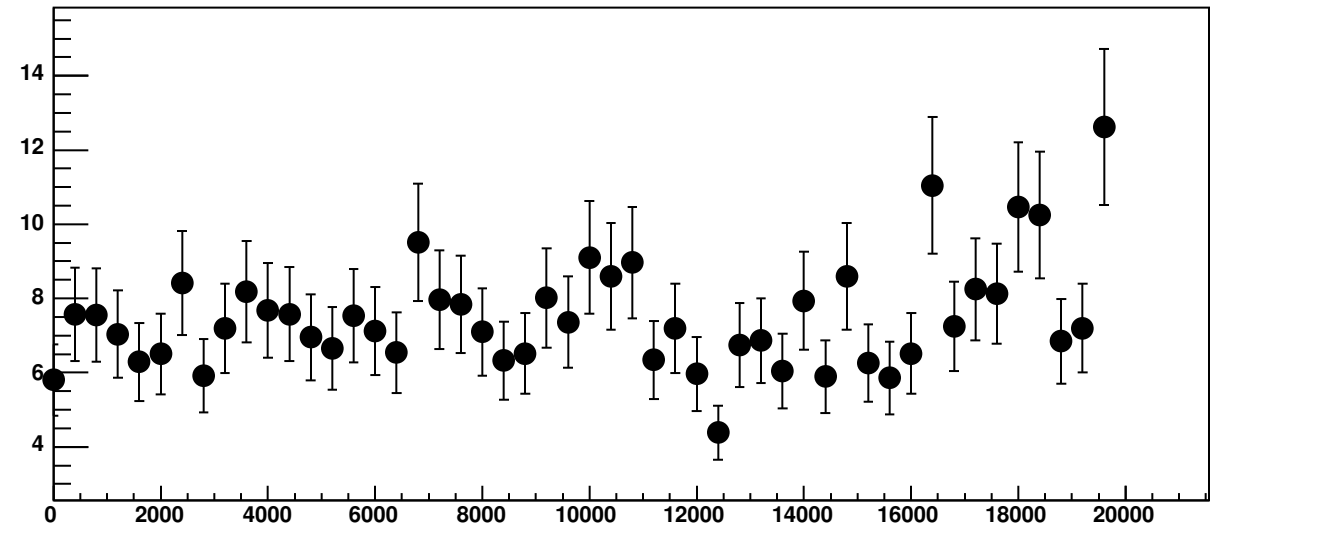
Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



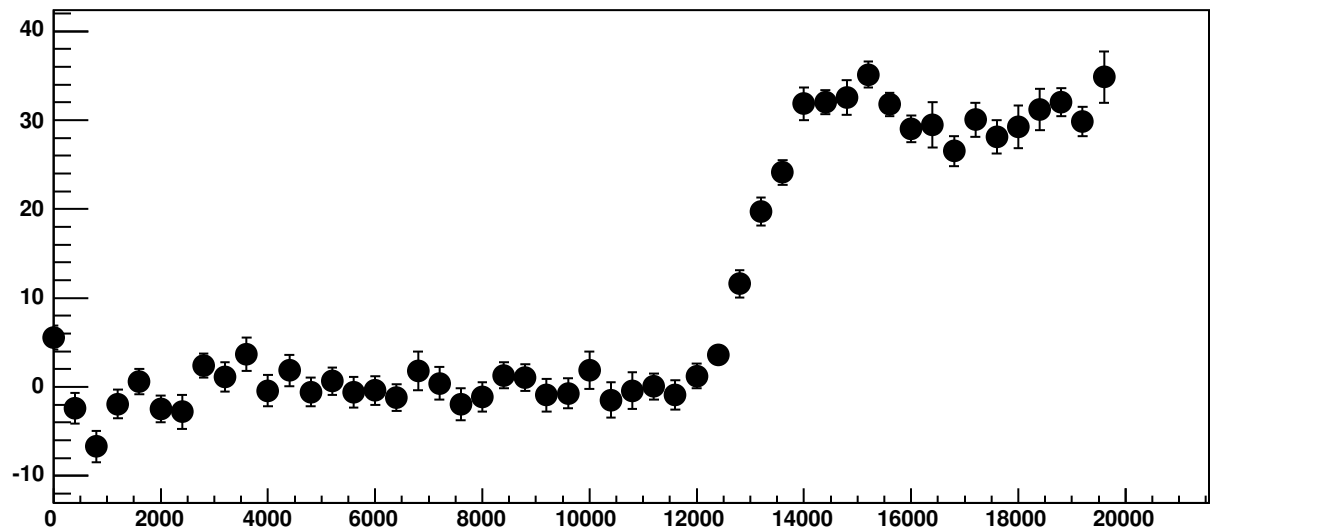
Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



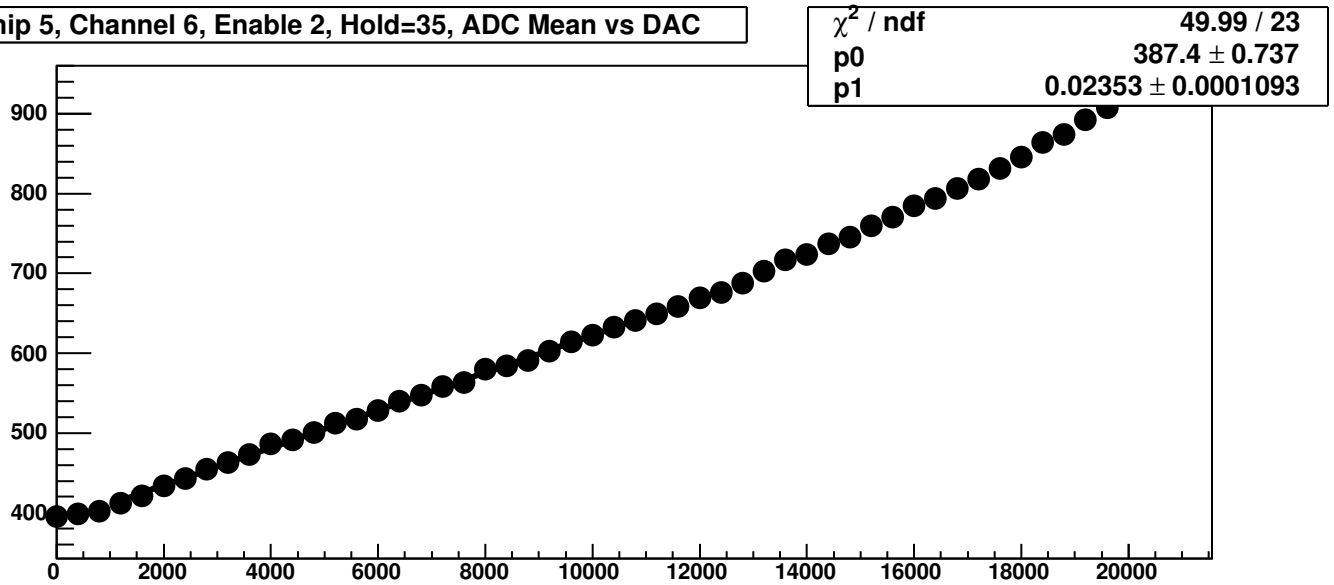
Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



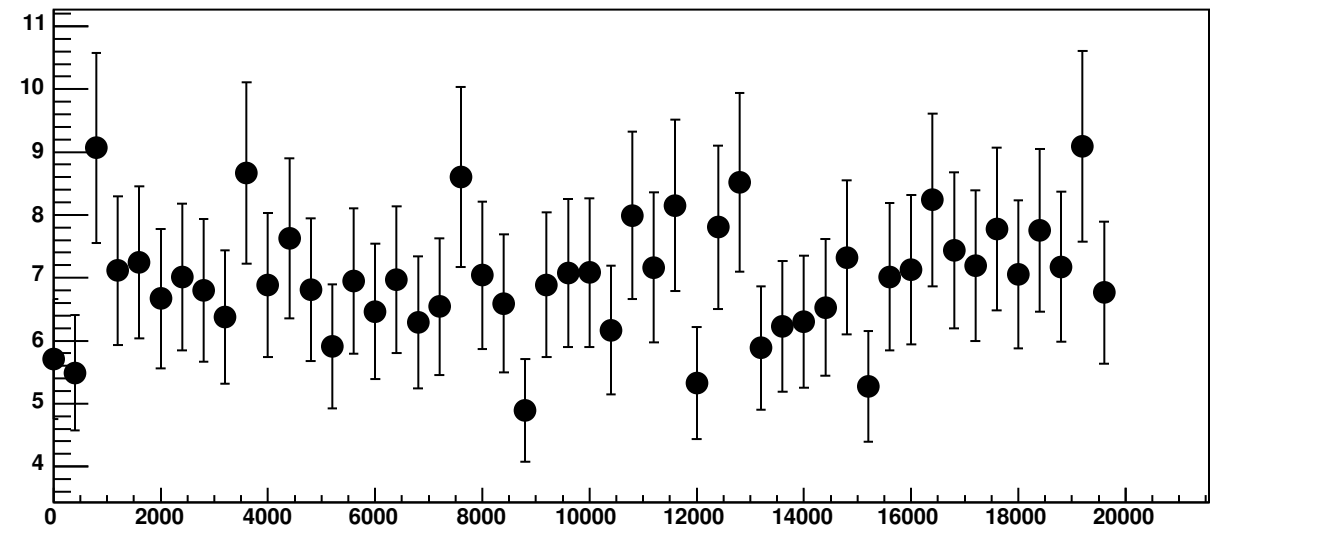
Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



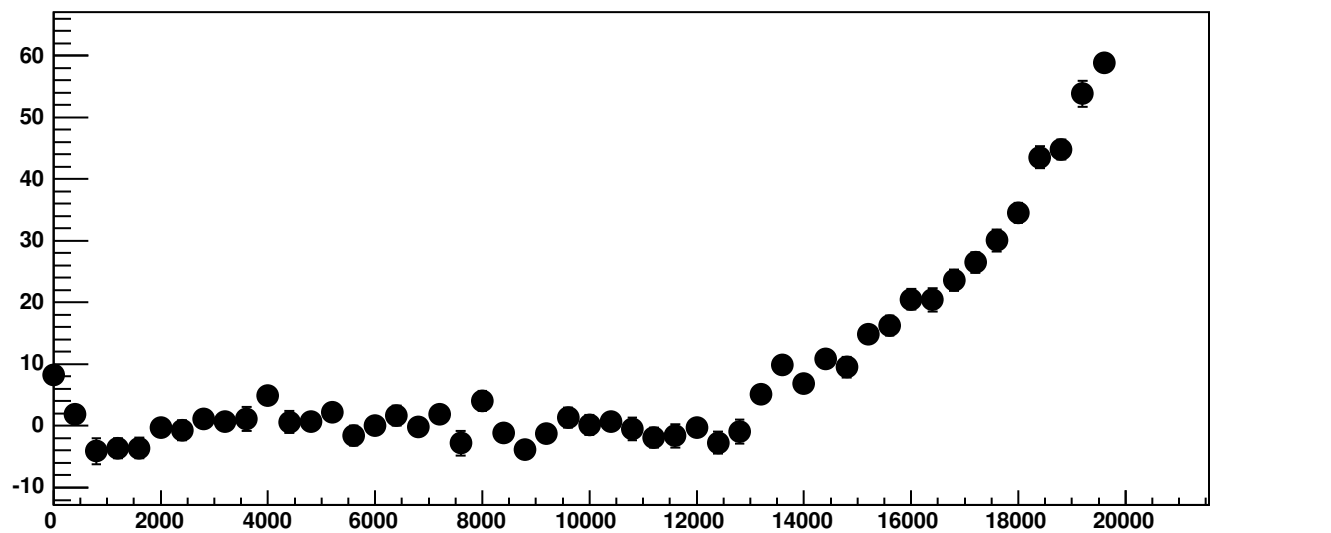
Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

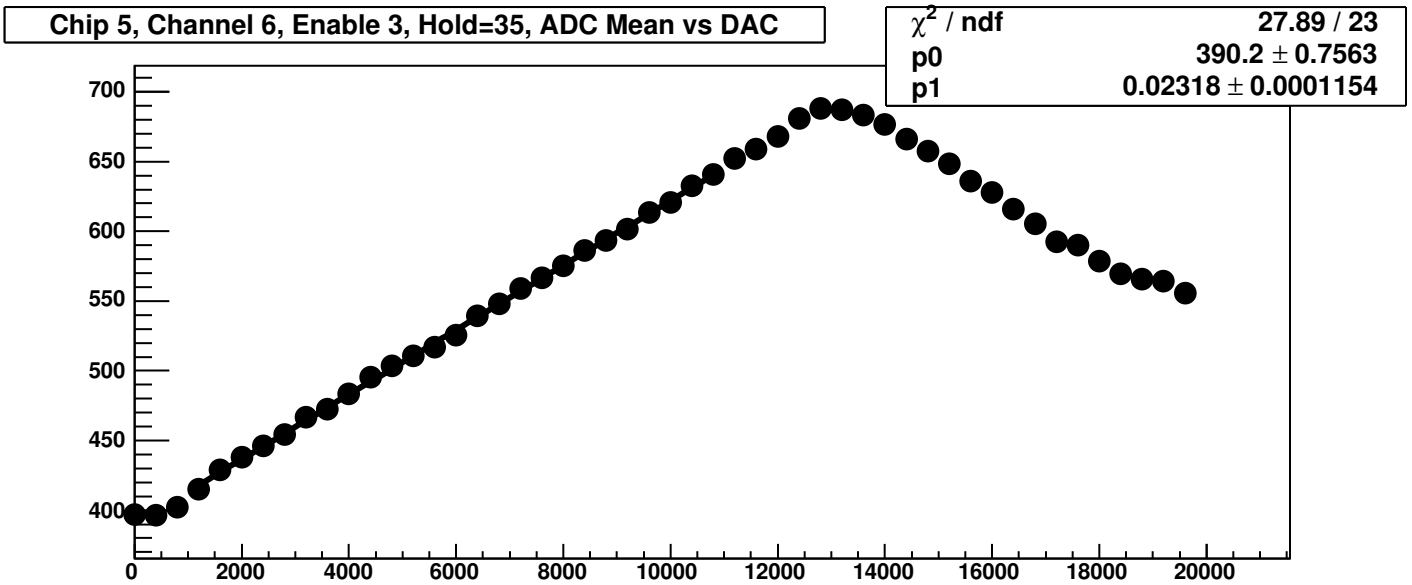


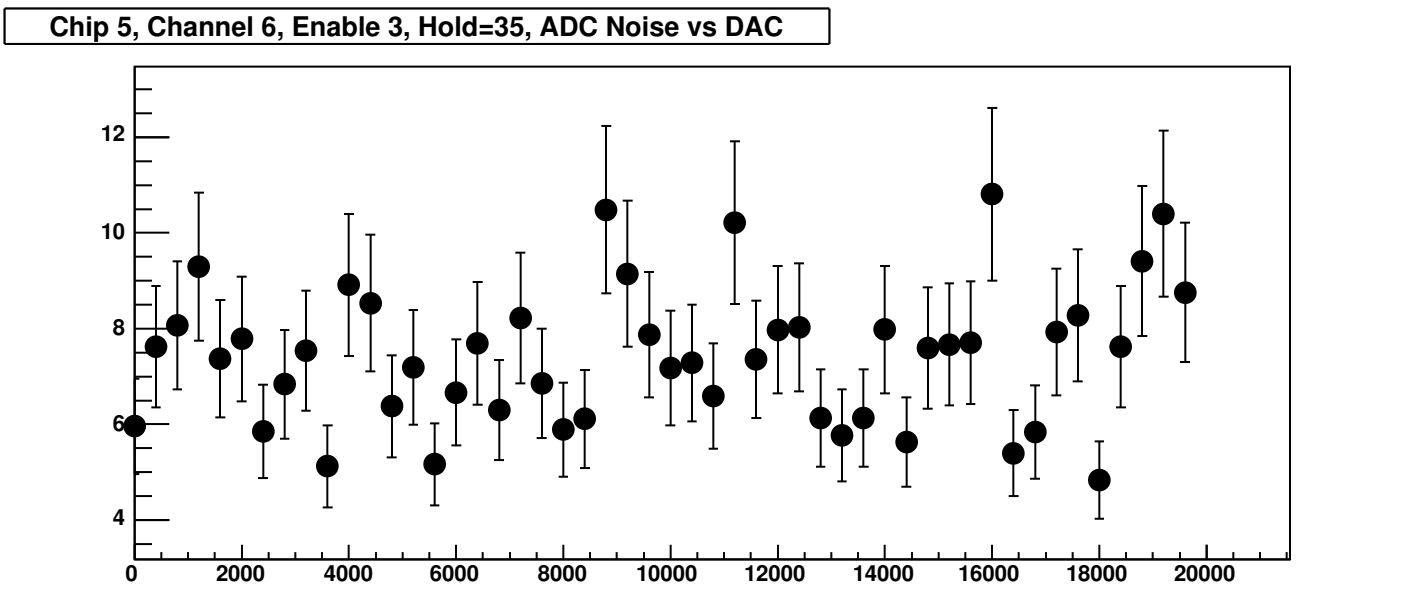
Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC

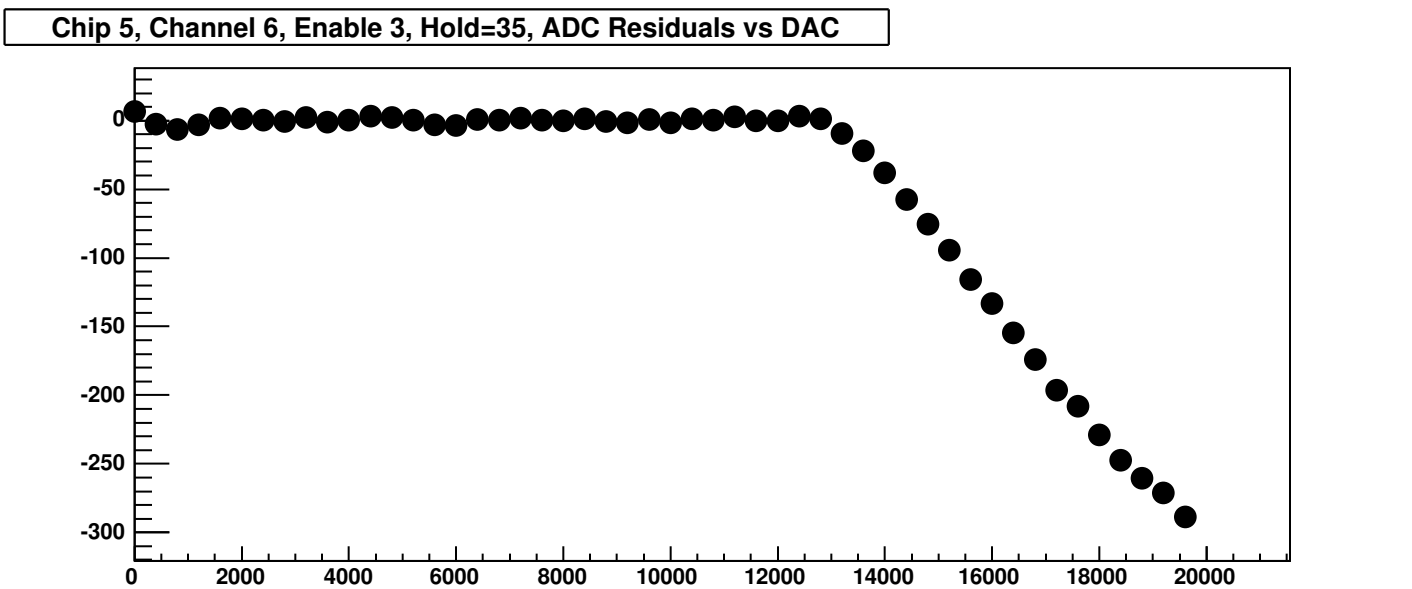


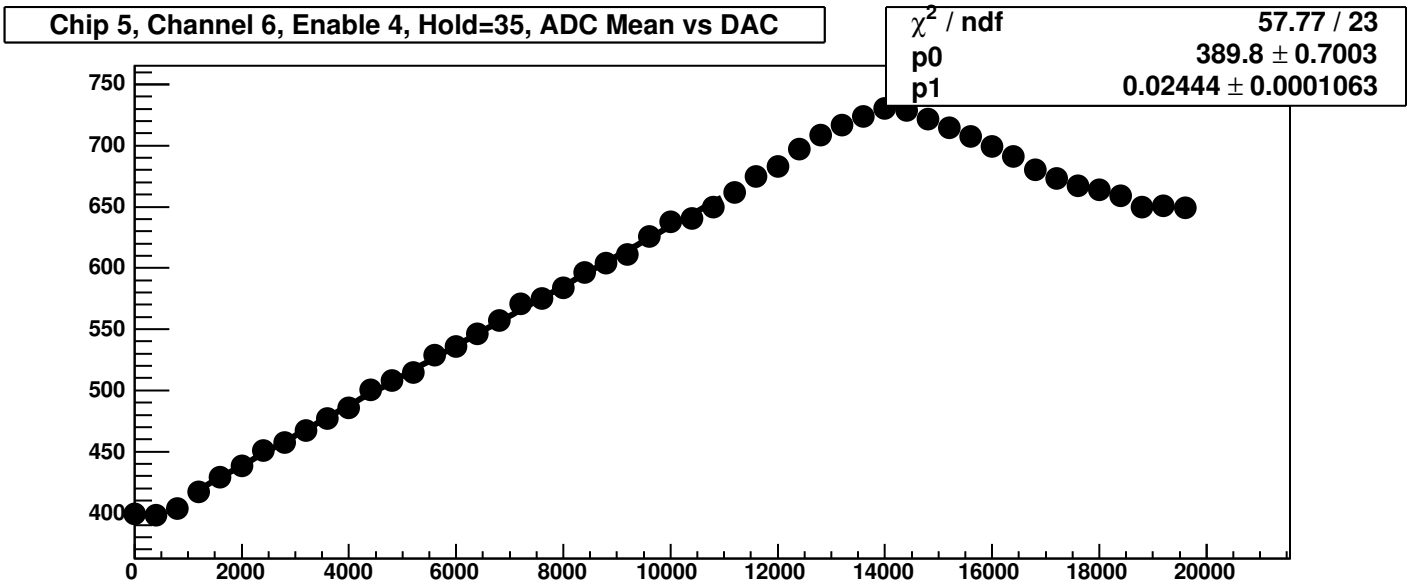
Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

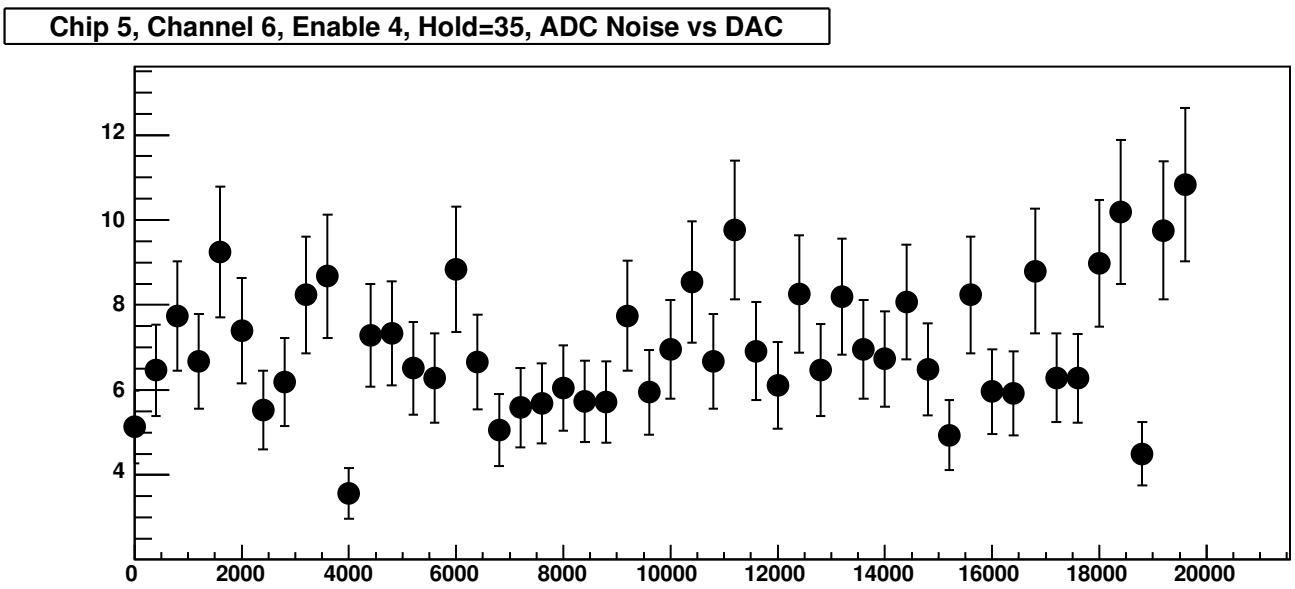


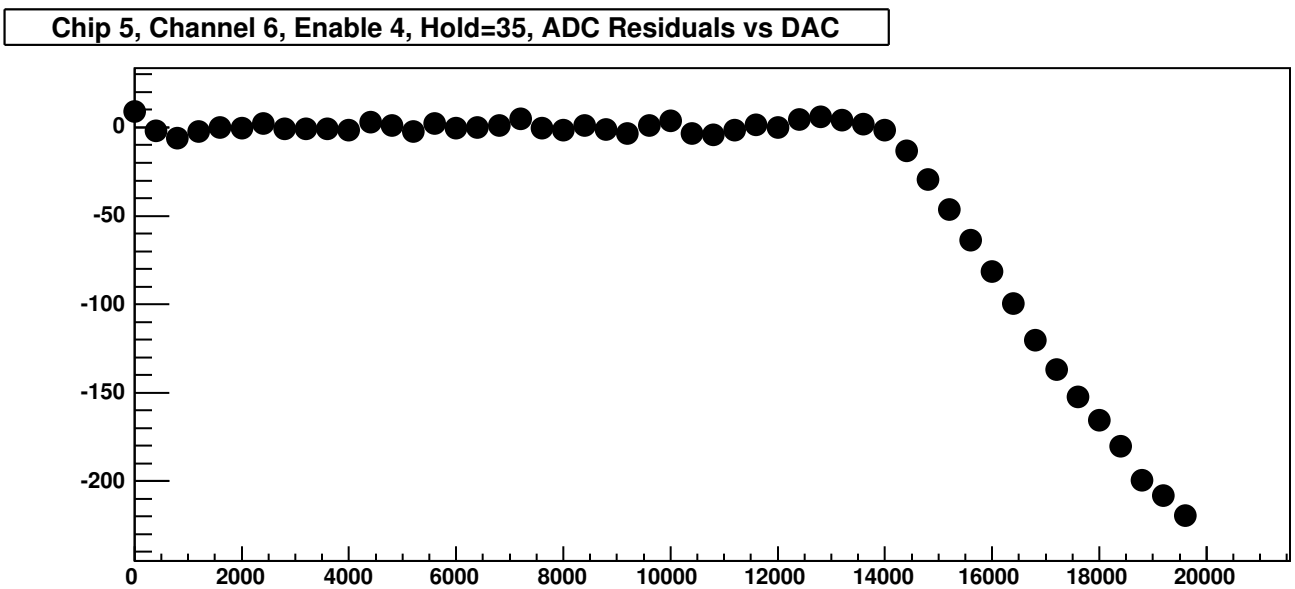

Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.89 / 23
p0 390.2 ± 0.7563
p1 0.02318 ± 0.0001154
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

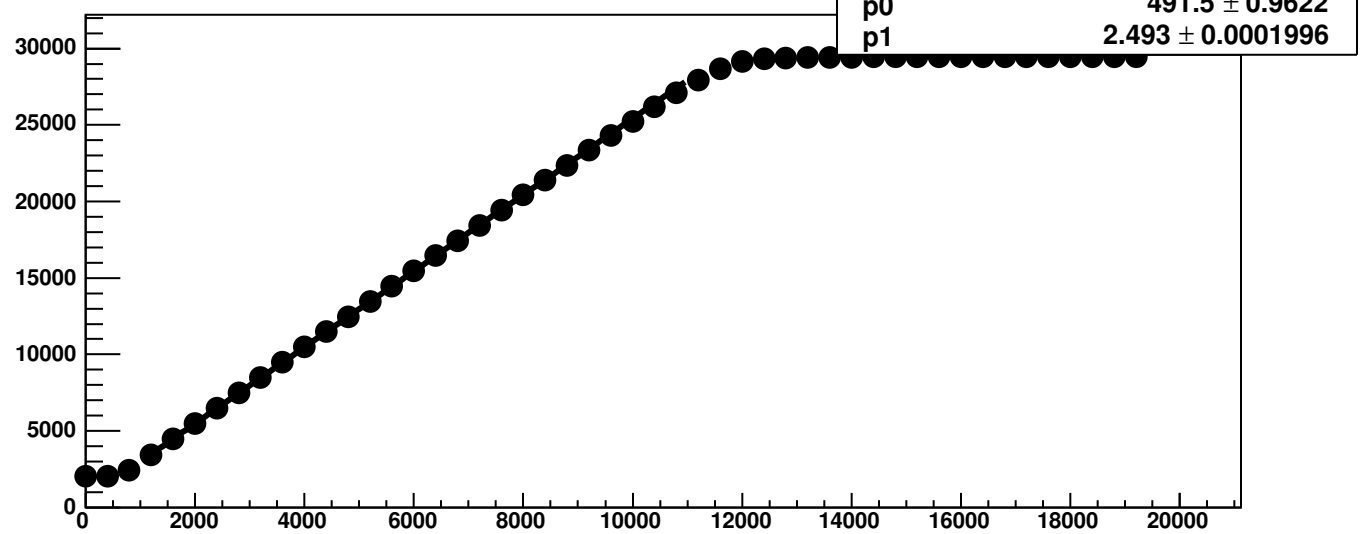

Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 57.77 / 23
p0 389.8 ± 0.7003
p1 0.02444 ± 0.0001063
750
700
650
600
550
500
450
400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

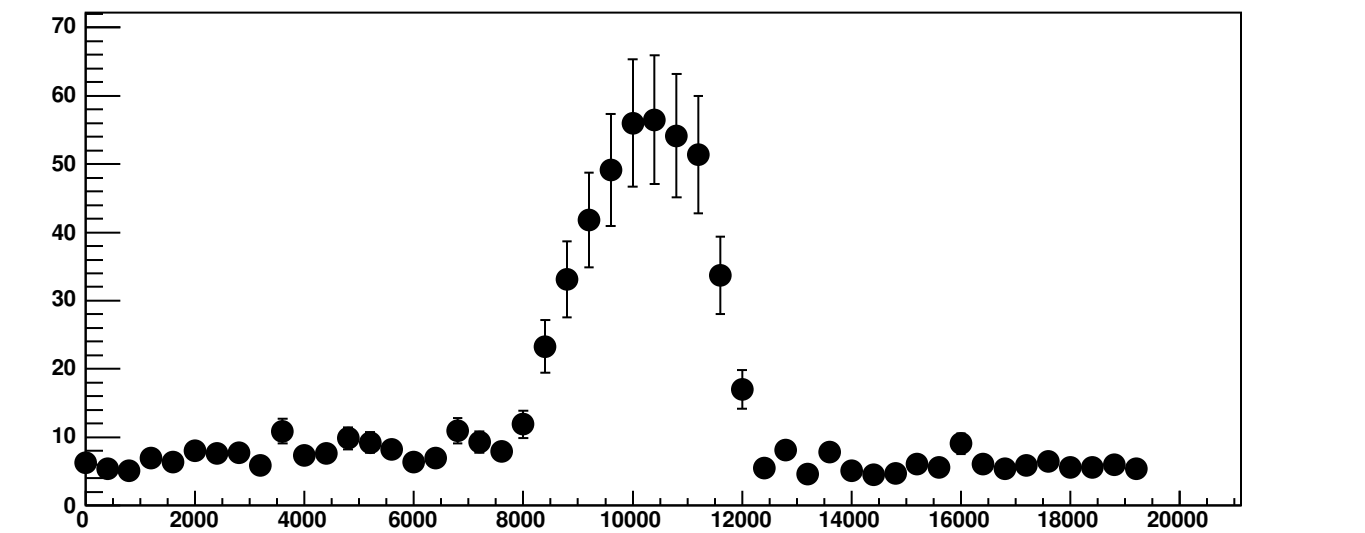

Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

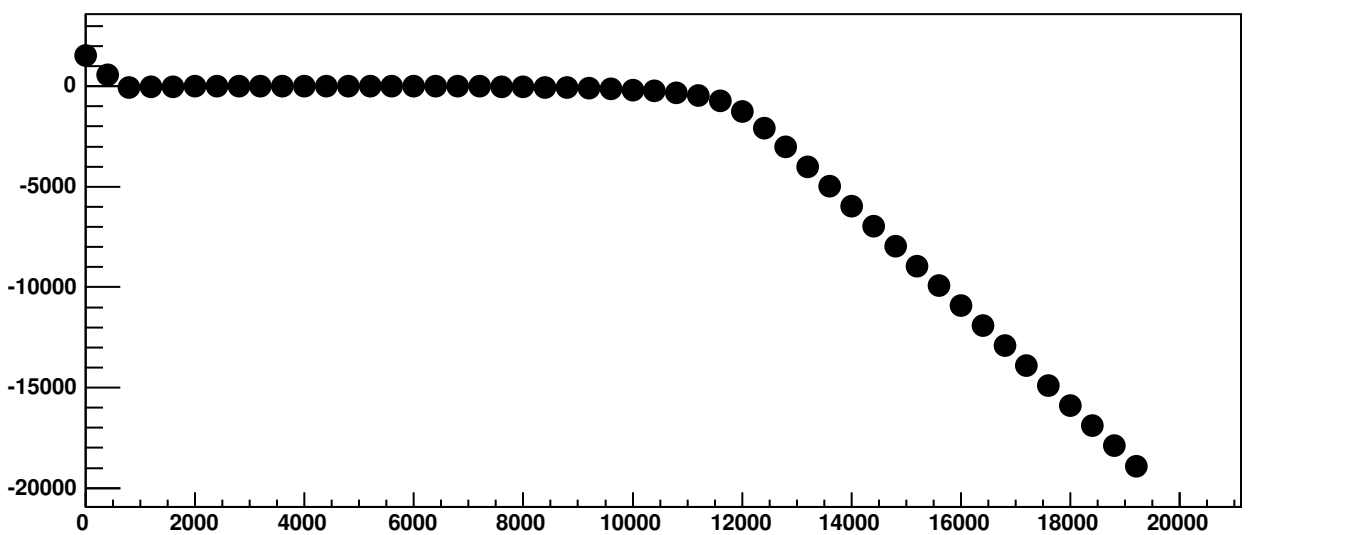
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC

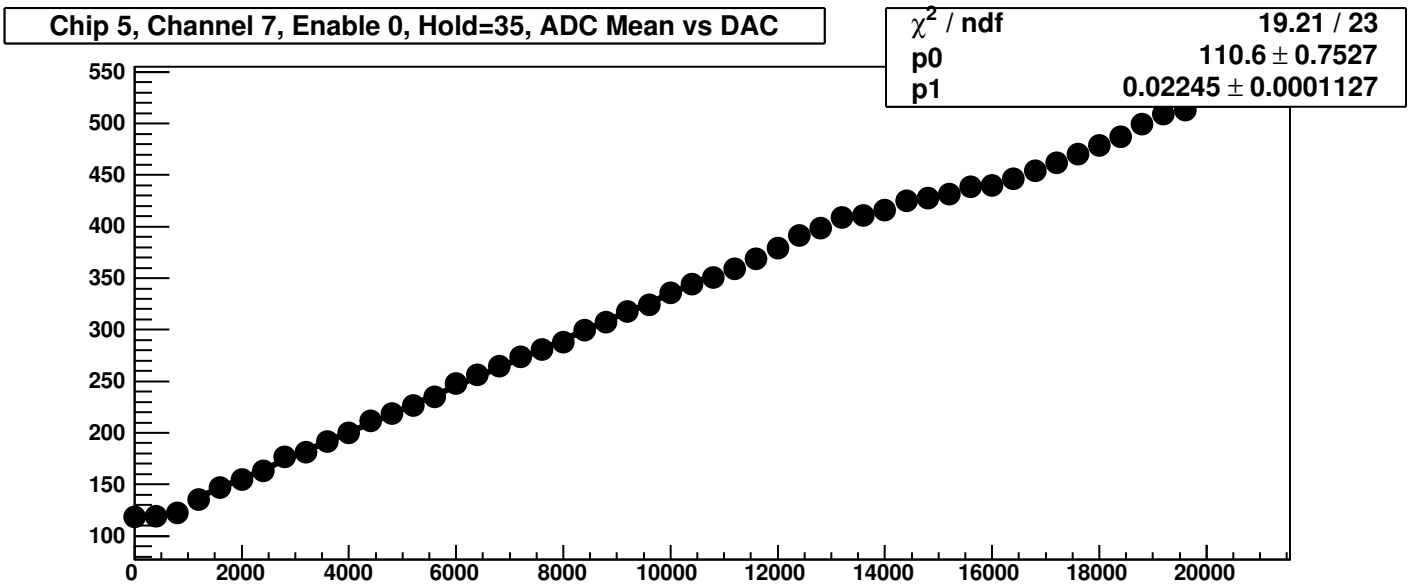


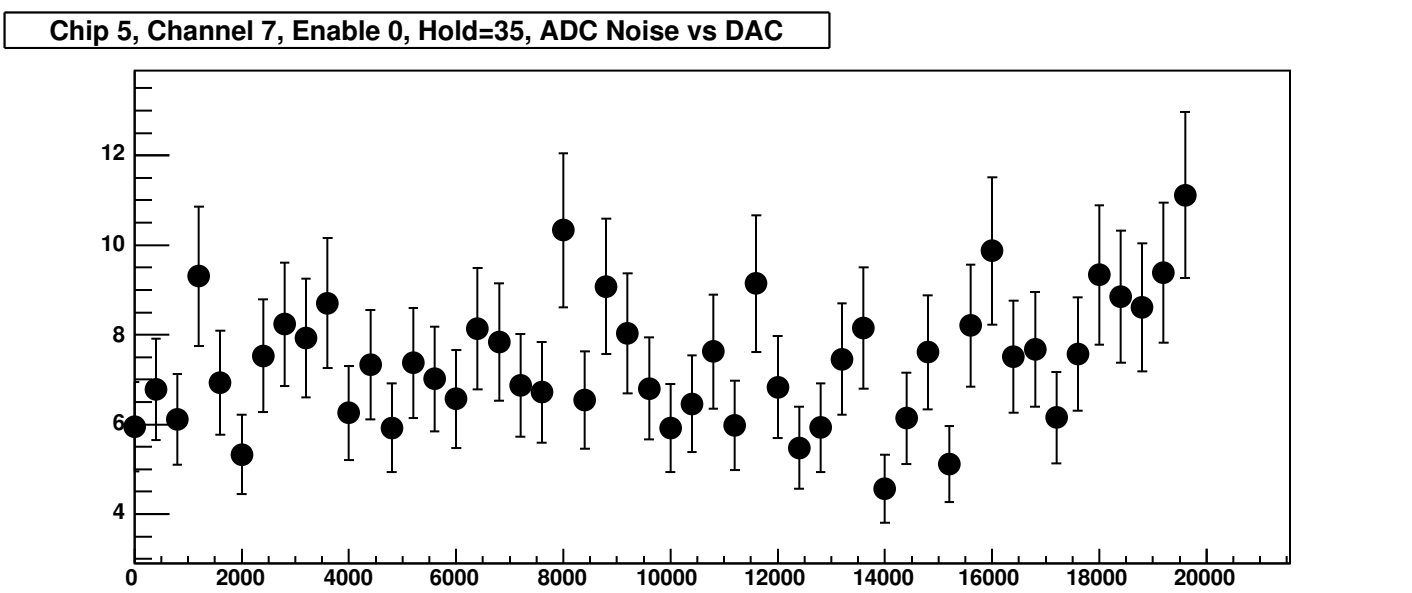
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC

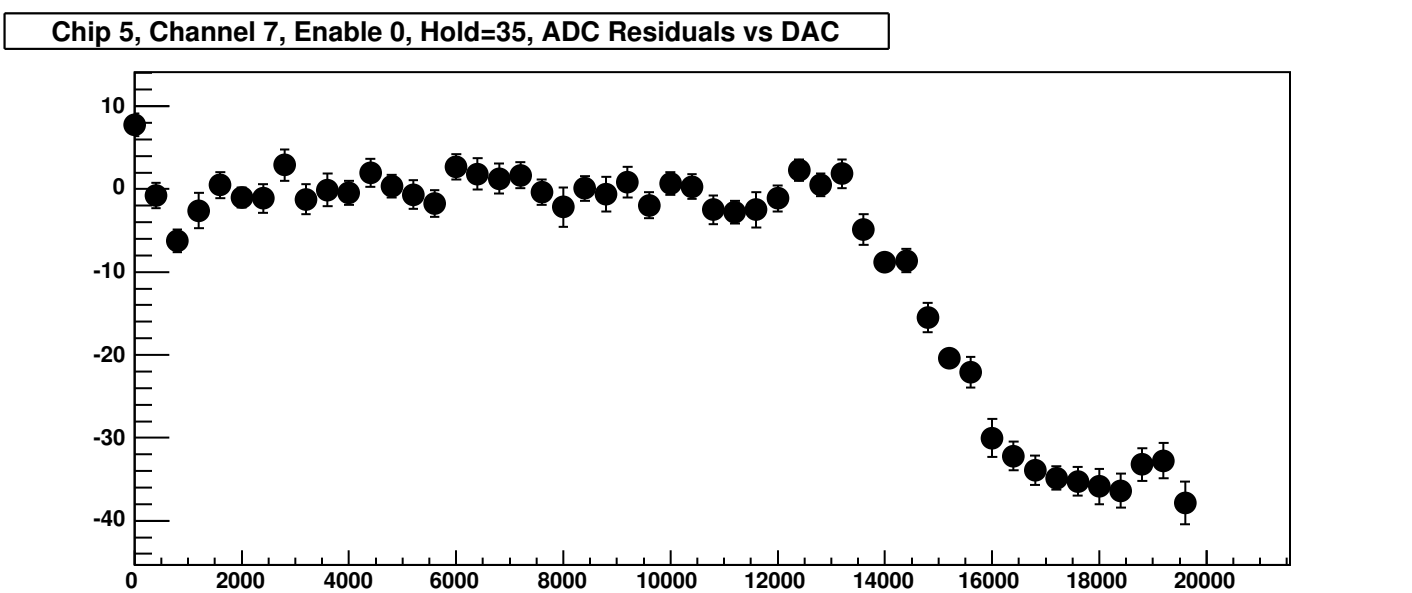


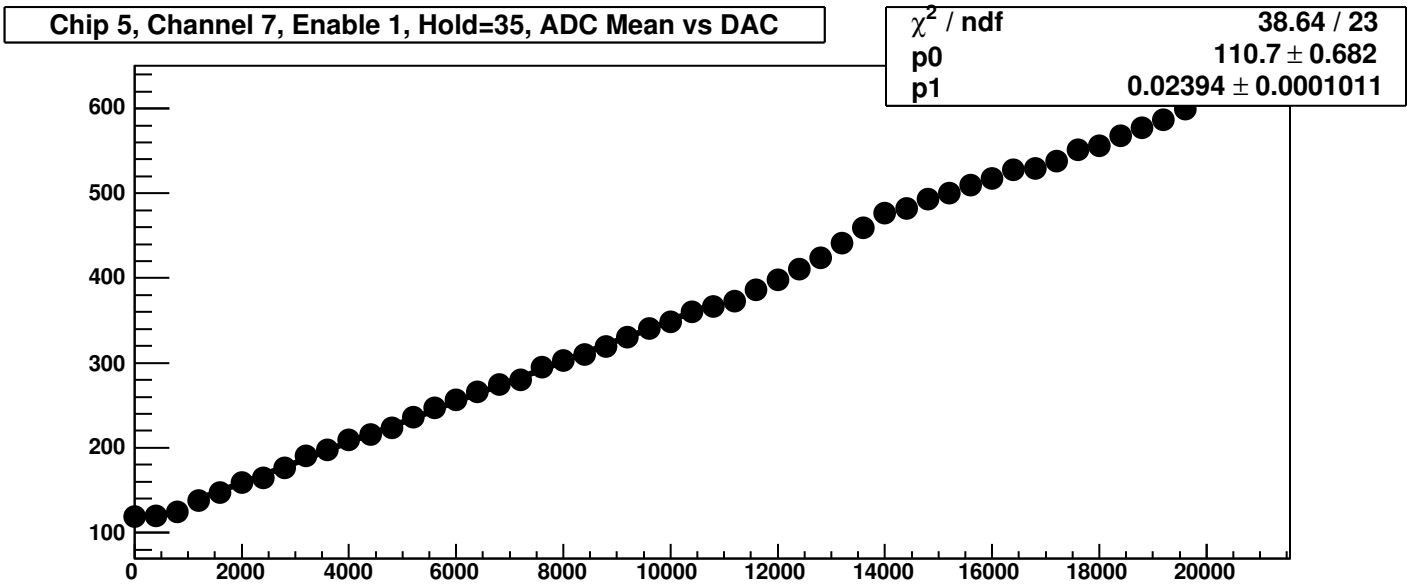
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

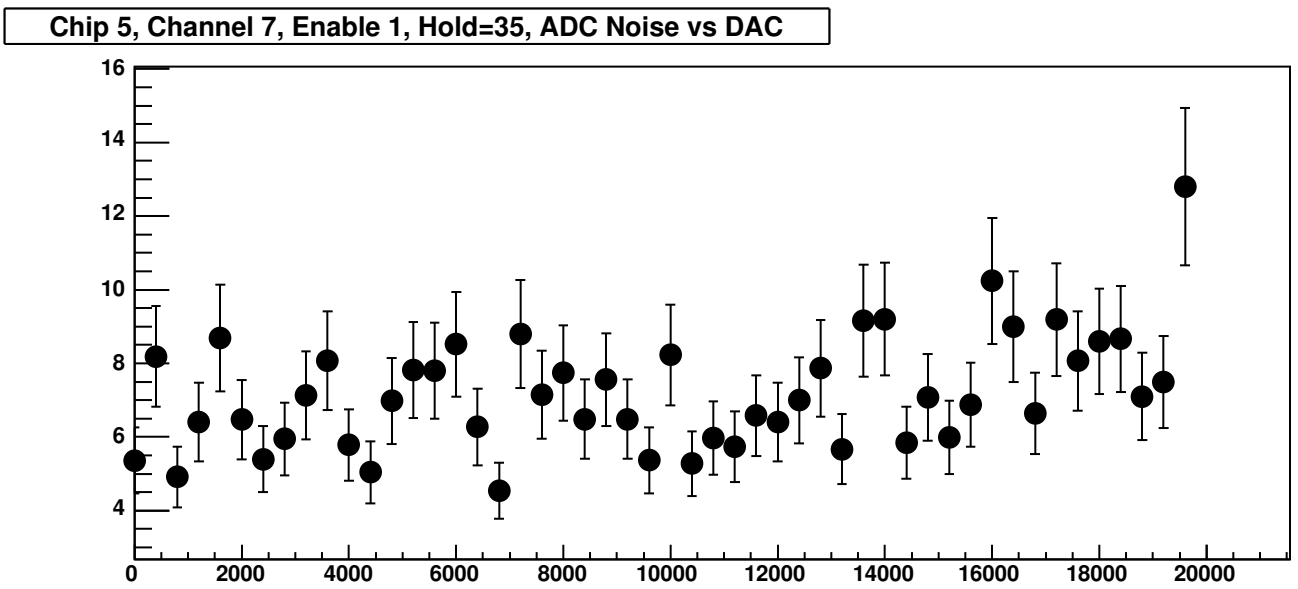


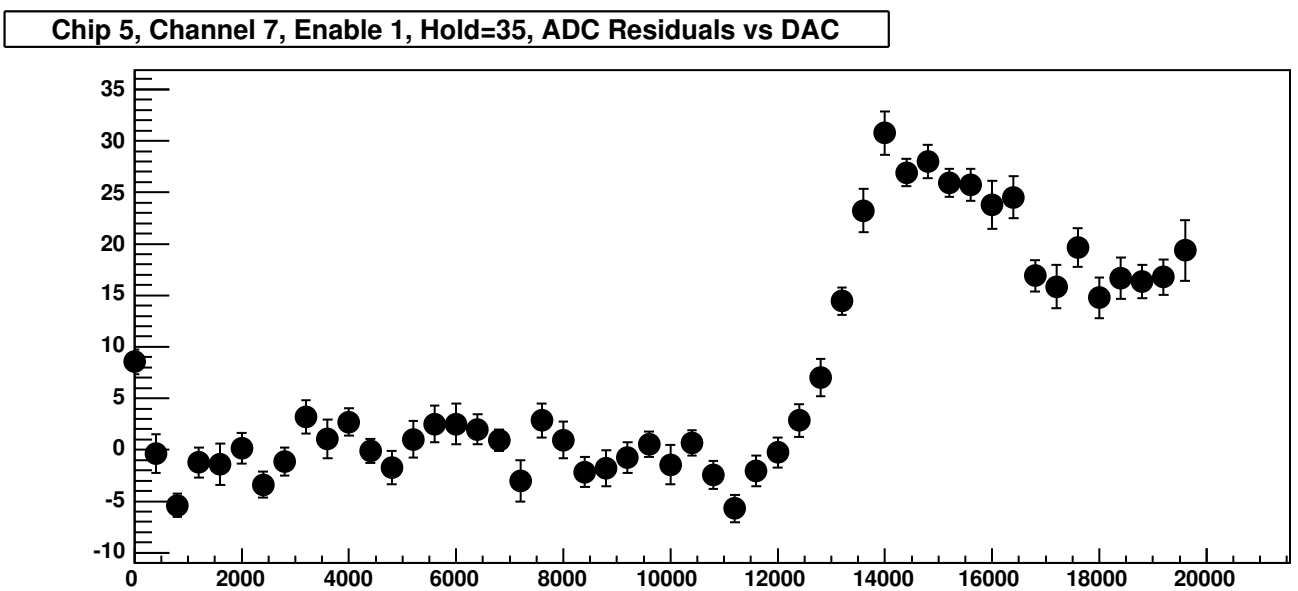

Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 19.21 / 23
p0 110.6 ± 0.7527
p1 0.02245 ± 0.0001127
550
500
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

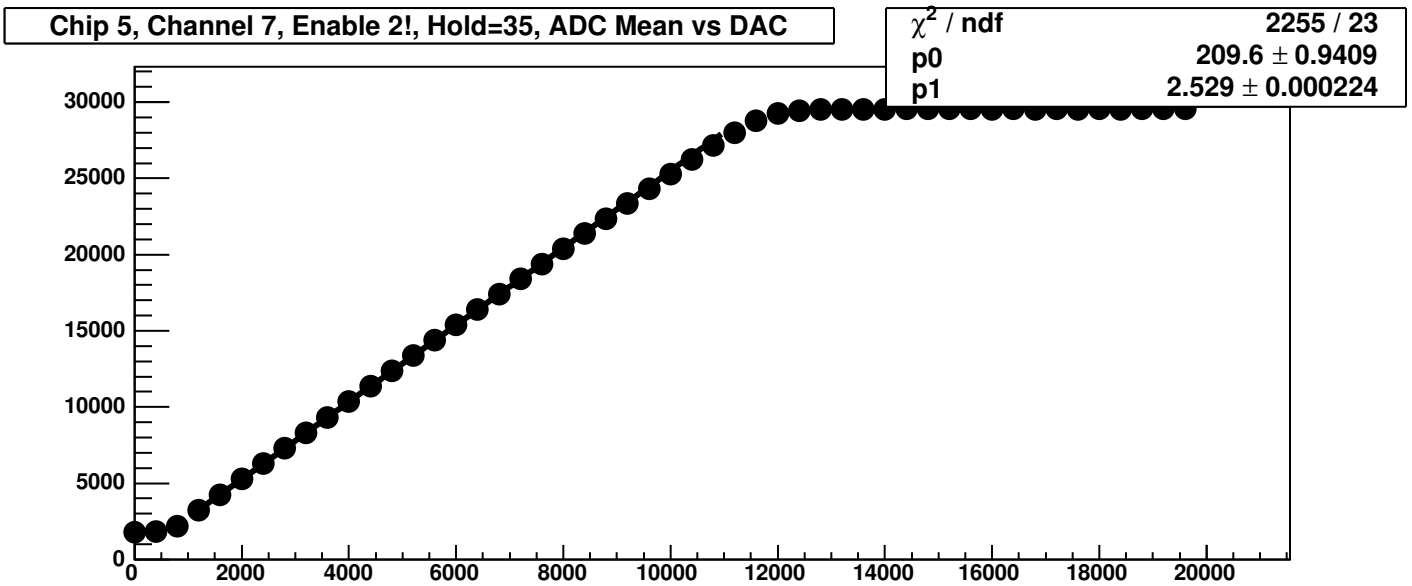

Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

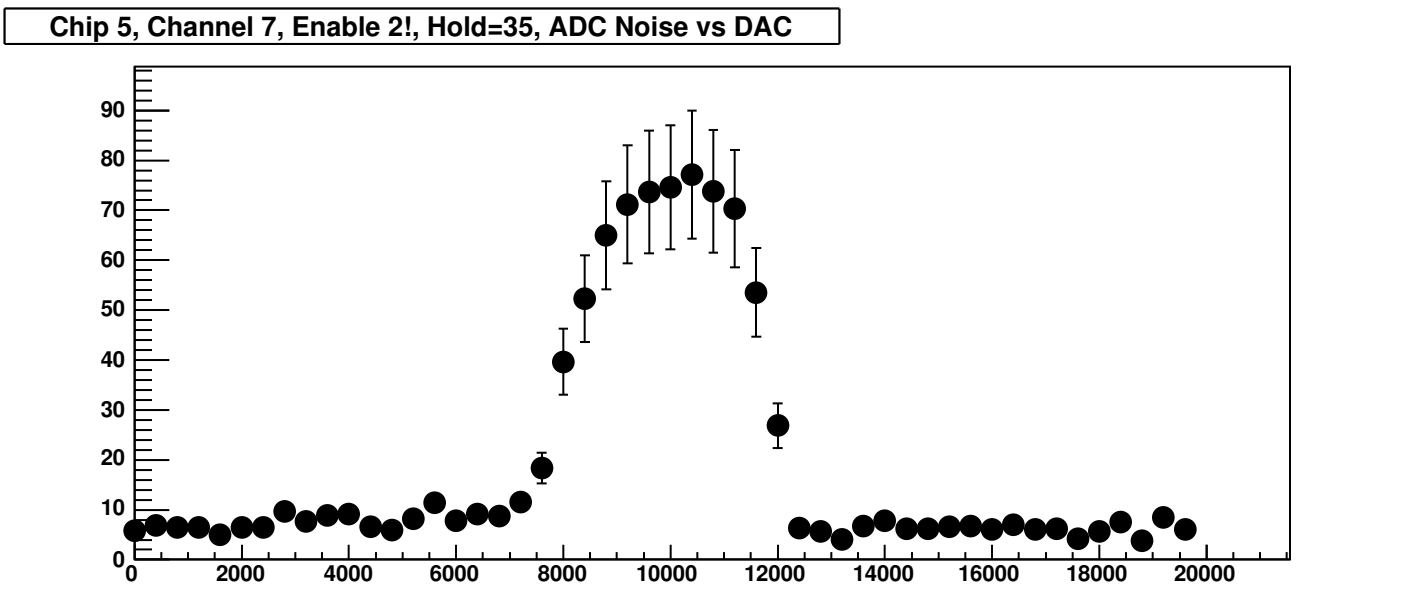

Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

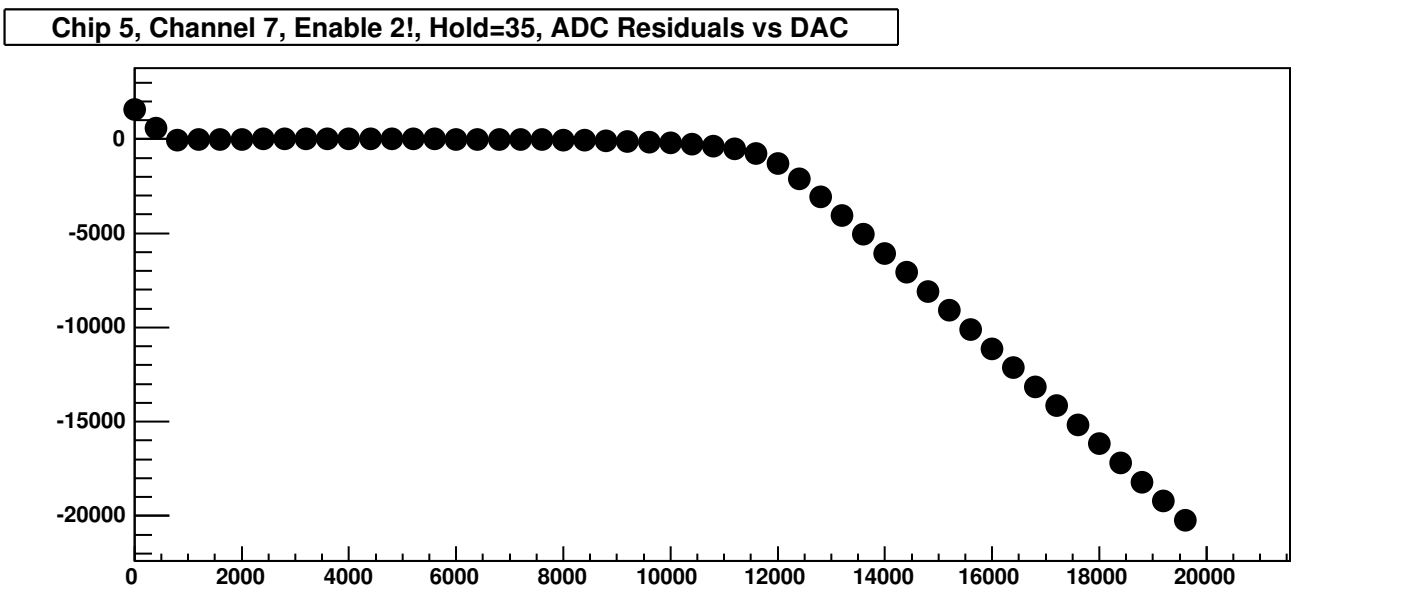

Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.64 / 23
p0 110.7 ± 0.682
p1 0.02394 ± 0.0001011
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

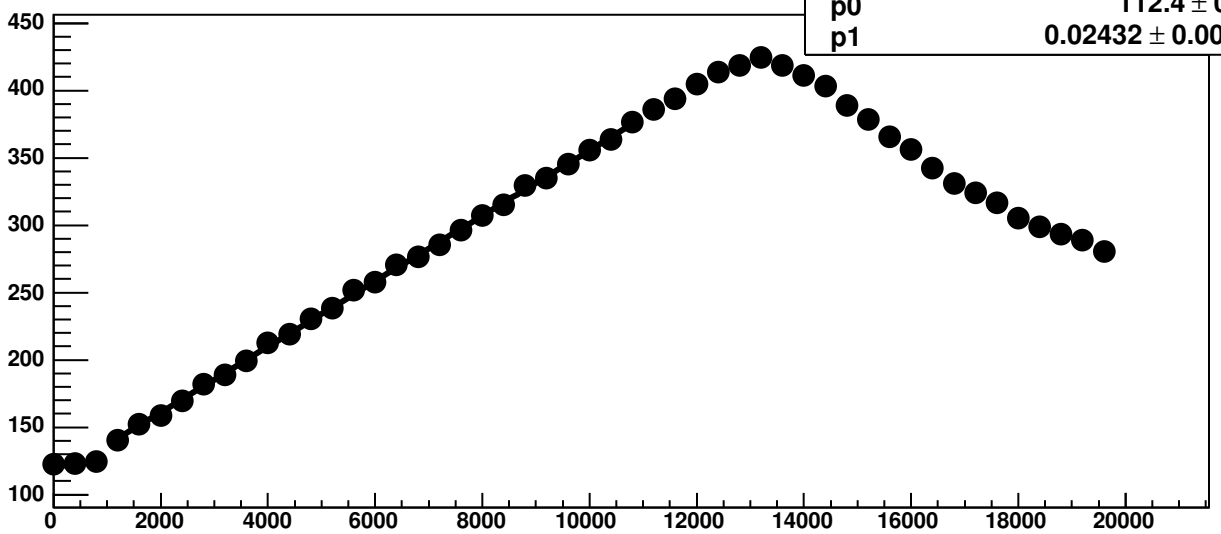

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 2255 / 23
p0 209.6 ± 0.9409
p1 2.529 ± 0.000224
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

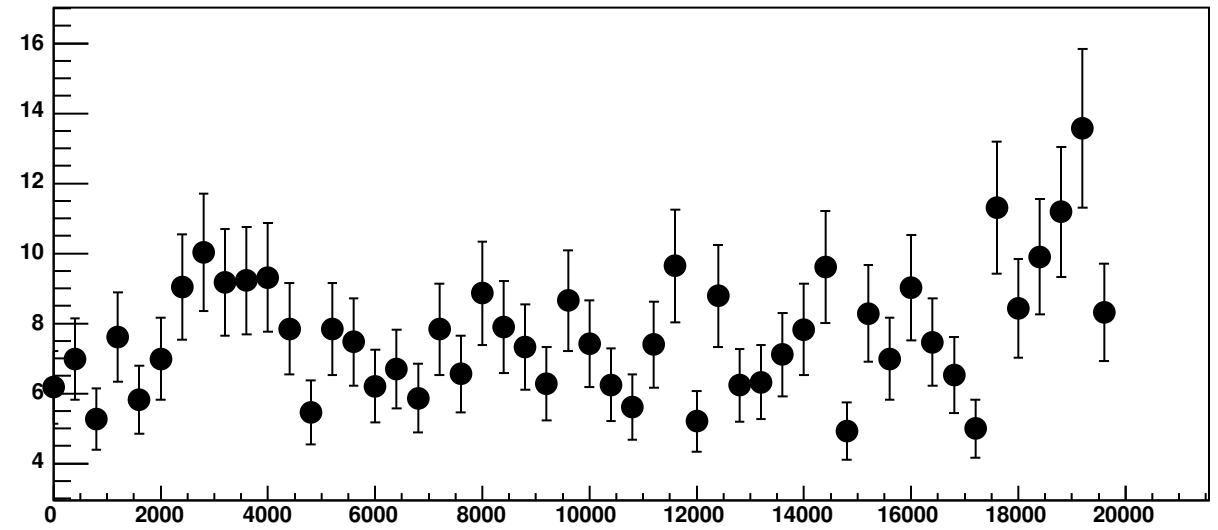

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

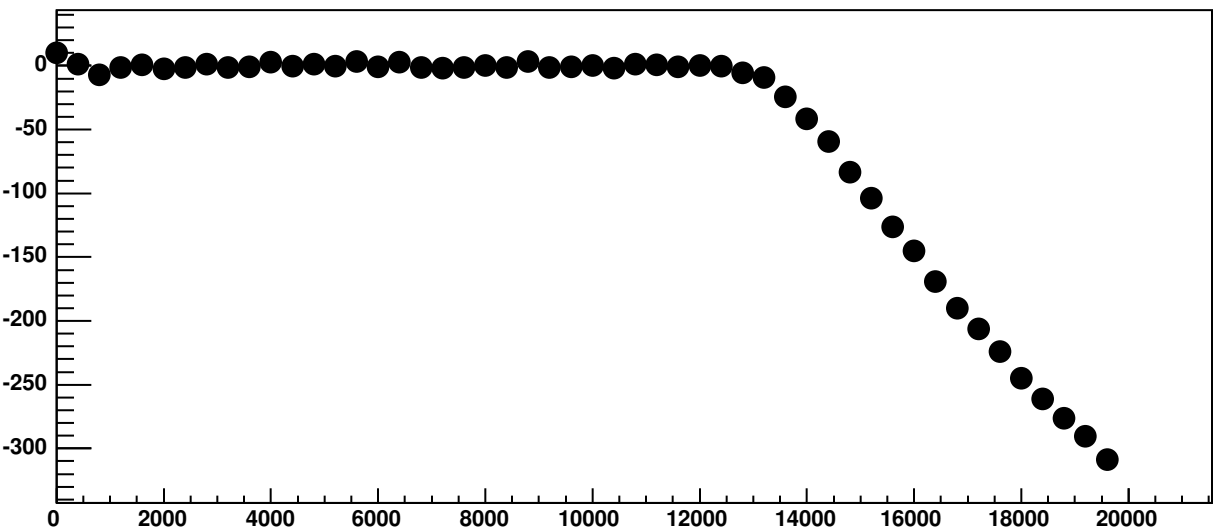
χ^2 / ndf	25.12 / 23
p0	112.4 ± 0.7766
p1	0.02432 ± 0.0001122

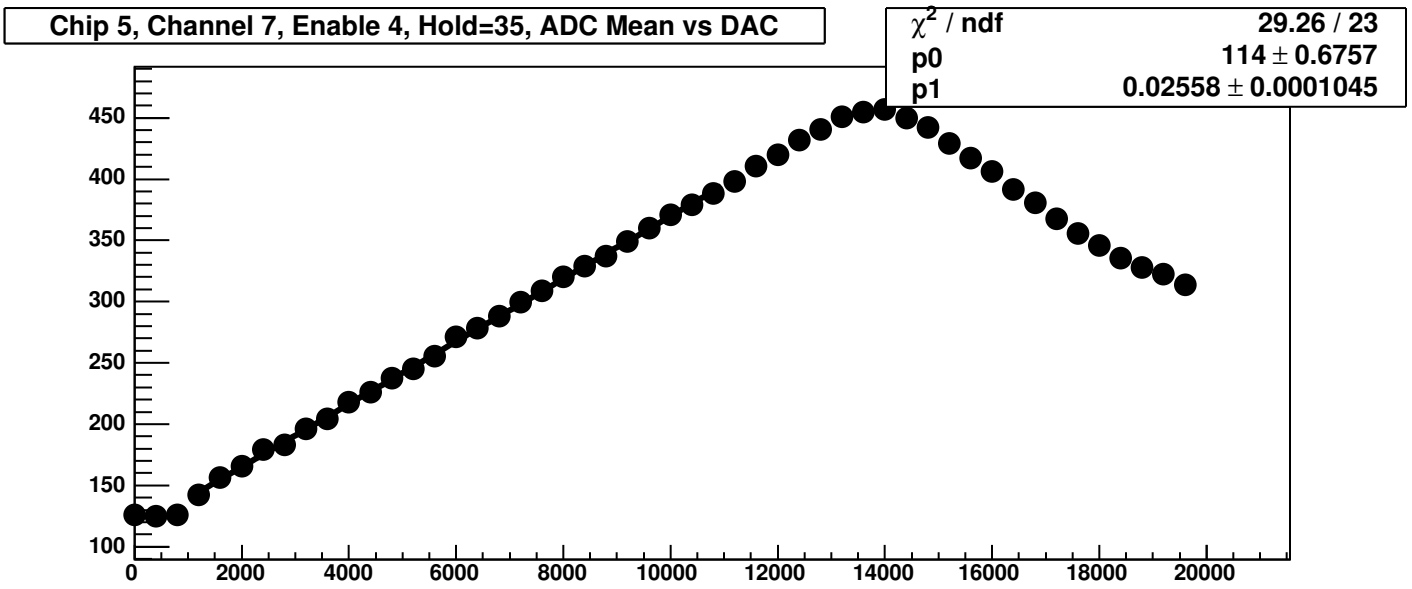


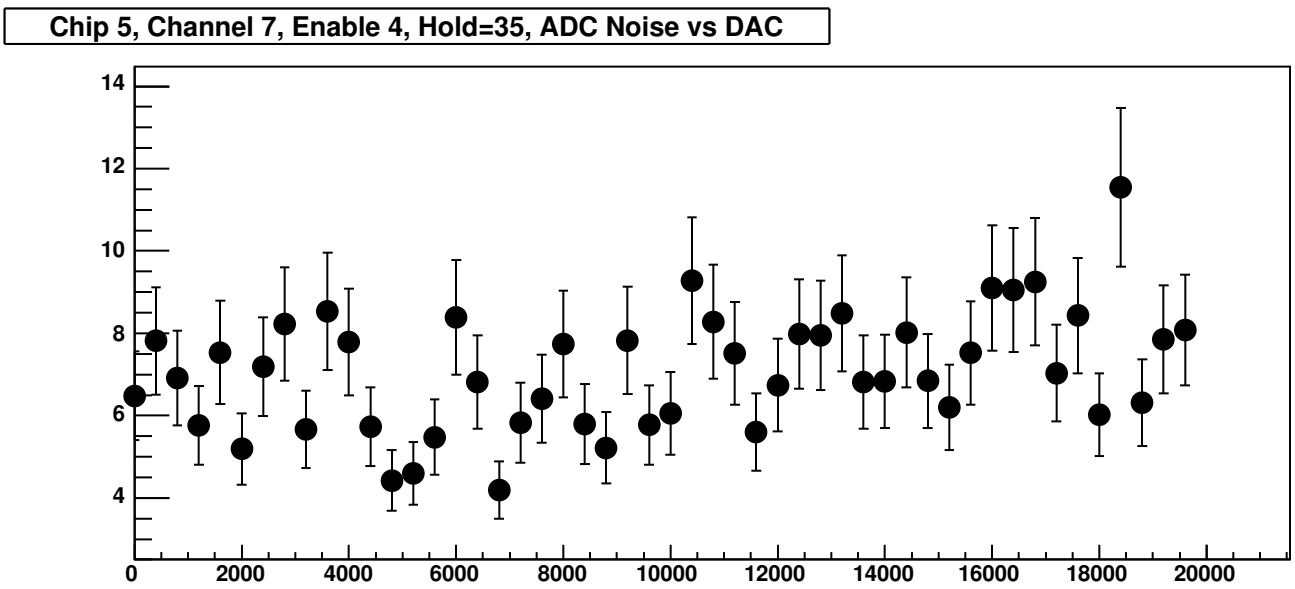
Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC

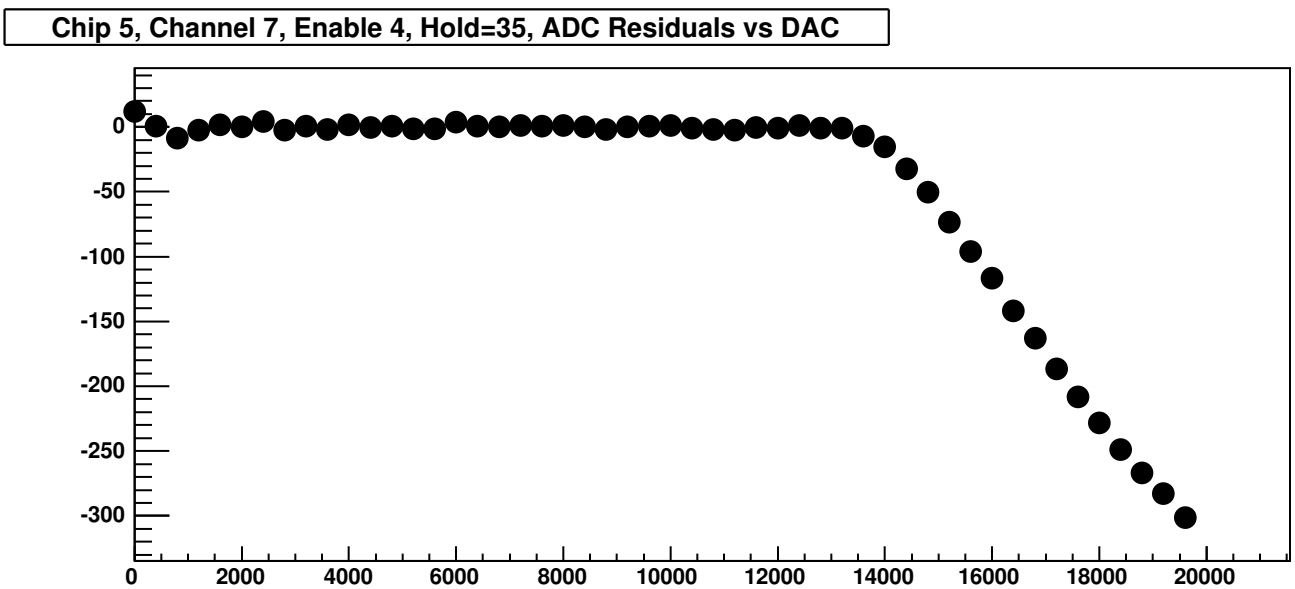


Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



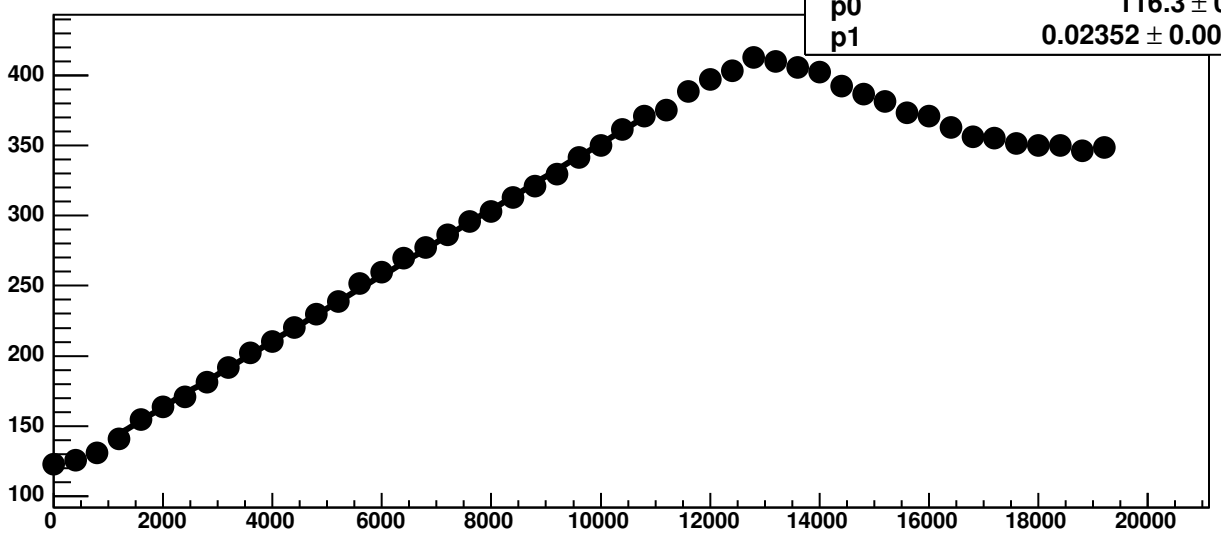

Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.26 / 23
p0 114 ± 0.6757
p1 0.02558 ± 0.0001045
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

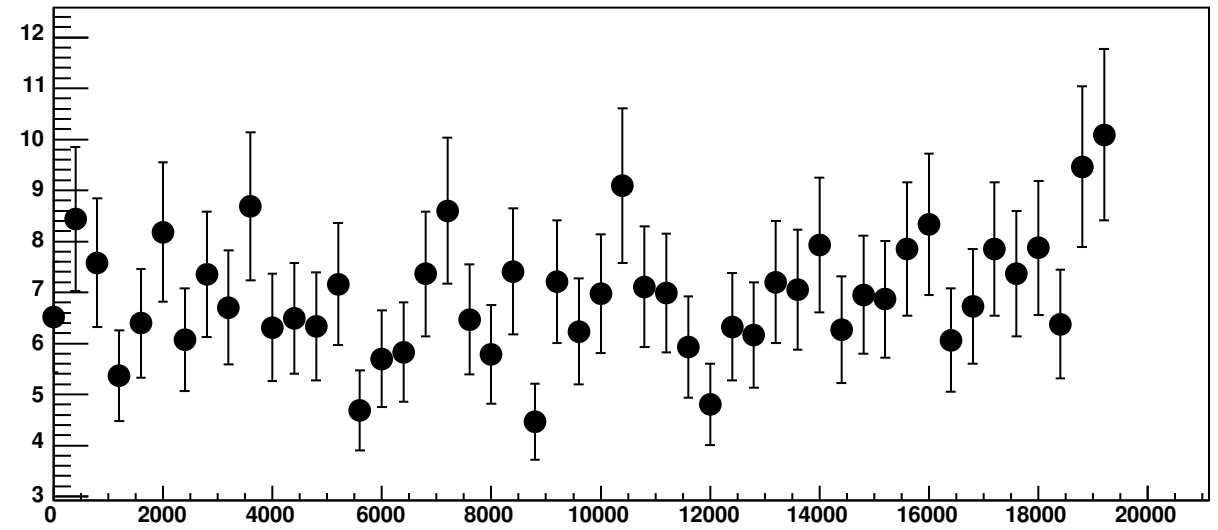

Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 5, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

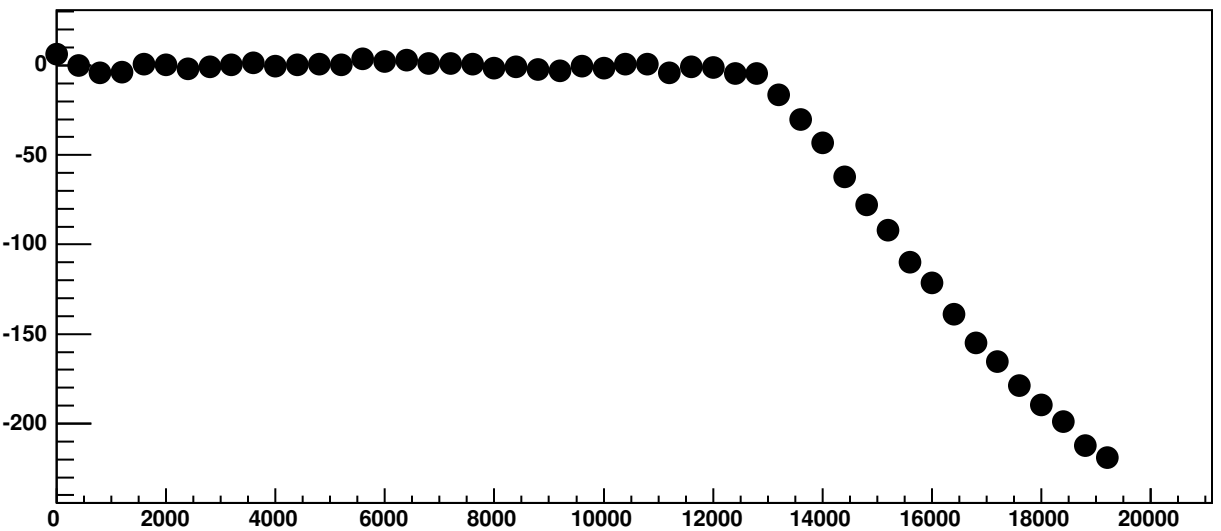
χ^2 / ndf	44.13 / 23
p0	116.3 ± 0.6907
p1	0.02352 ± 0.0001049

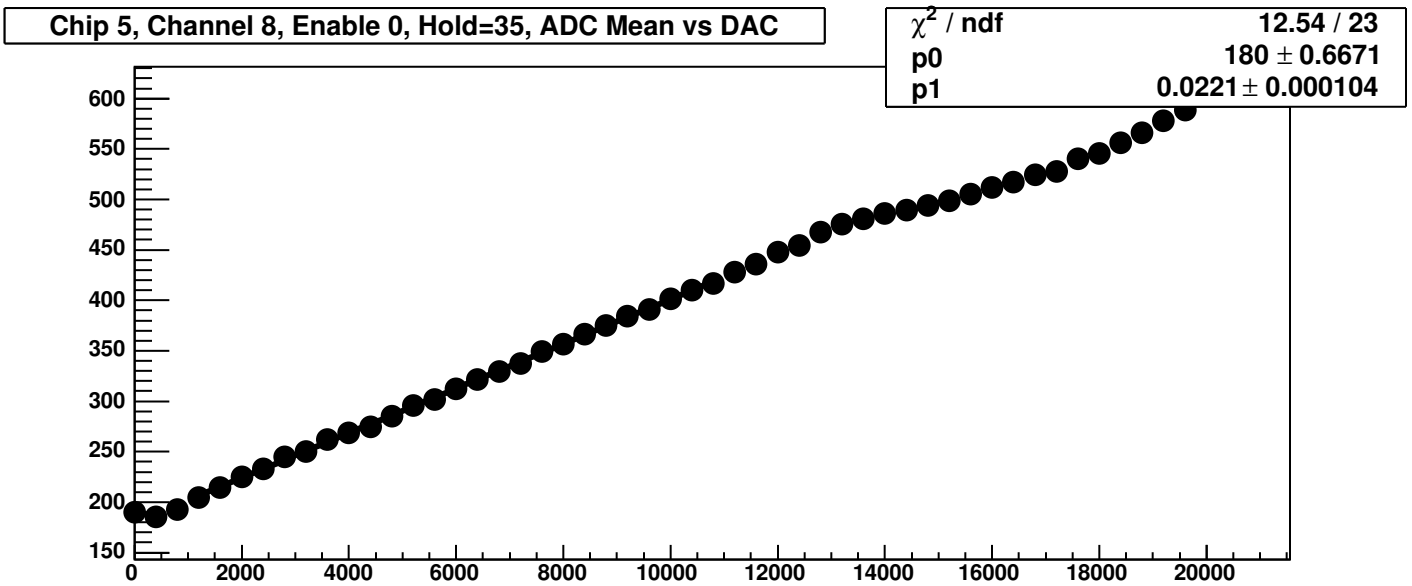


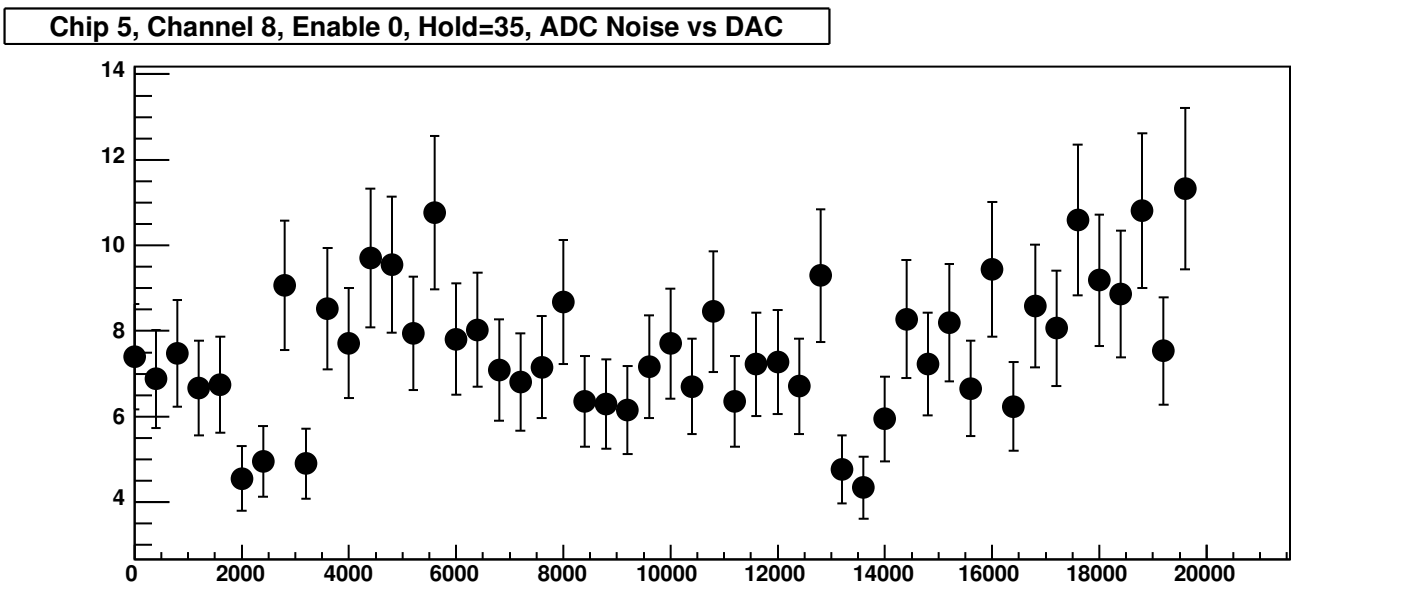
Chip 5, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC

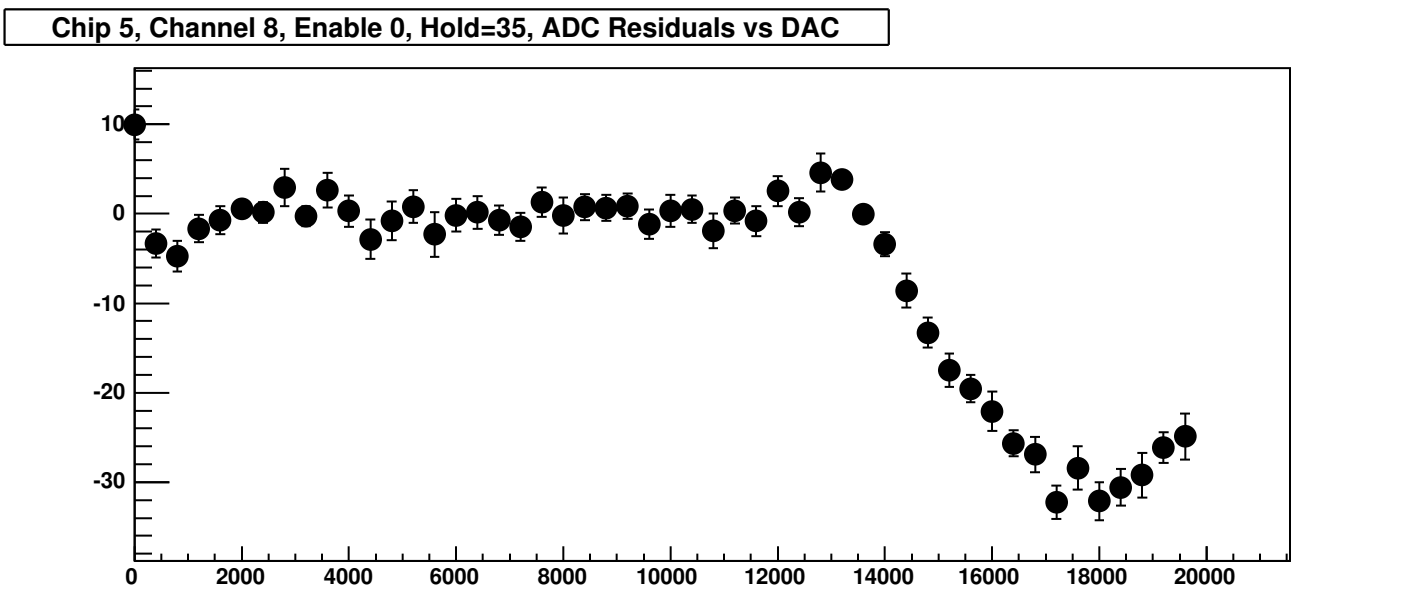


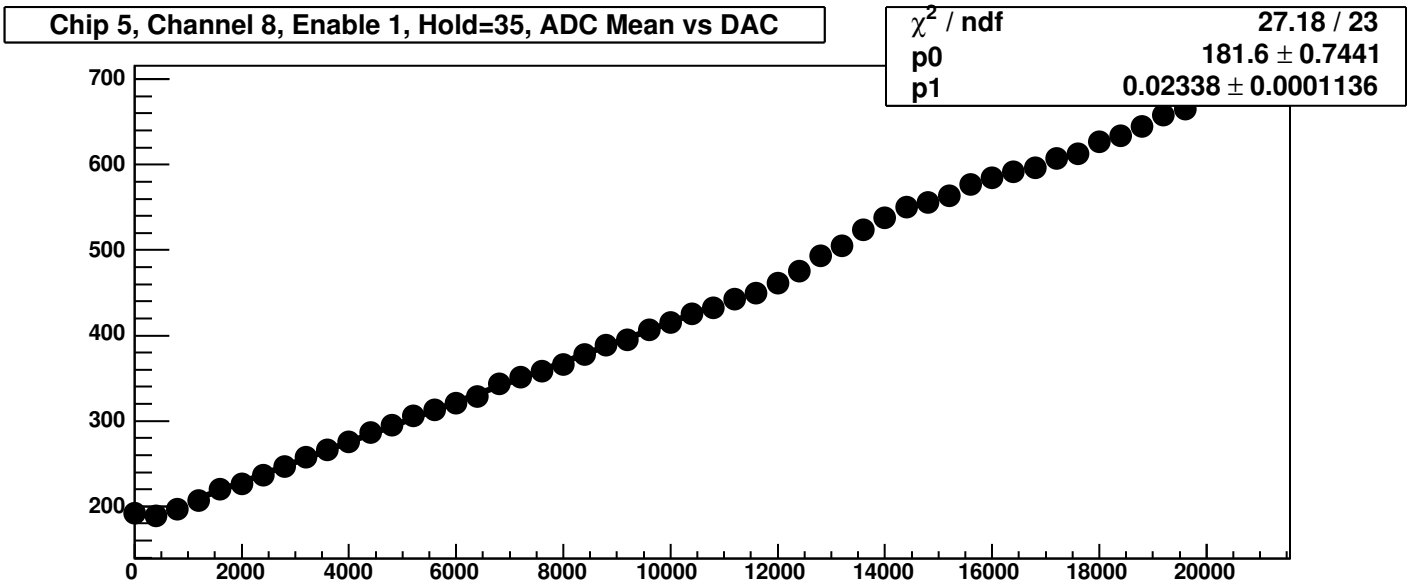
Chip 5, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

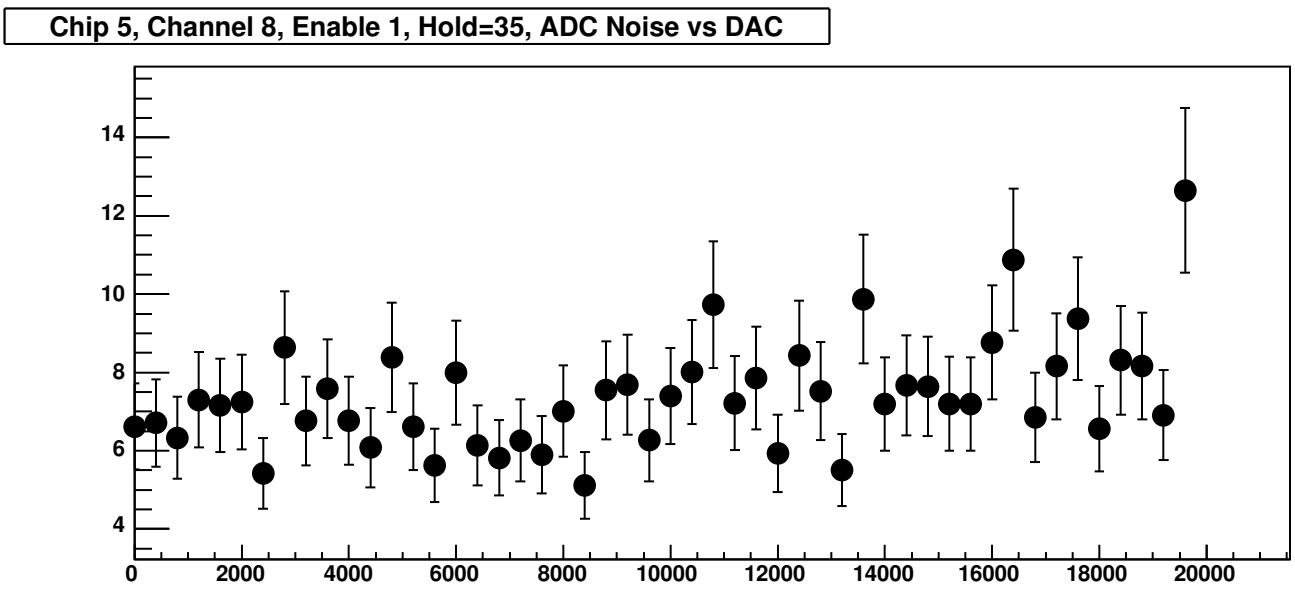


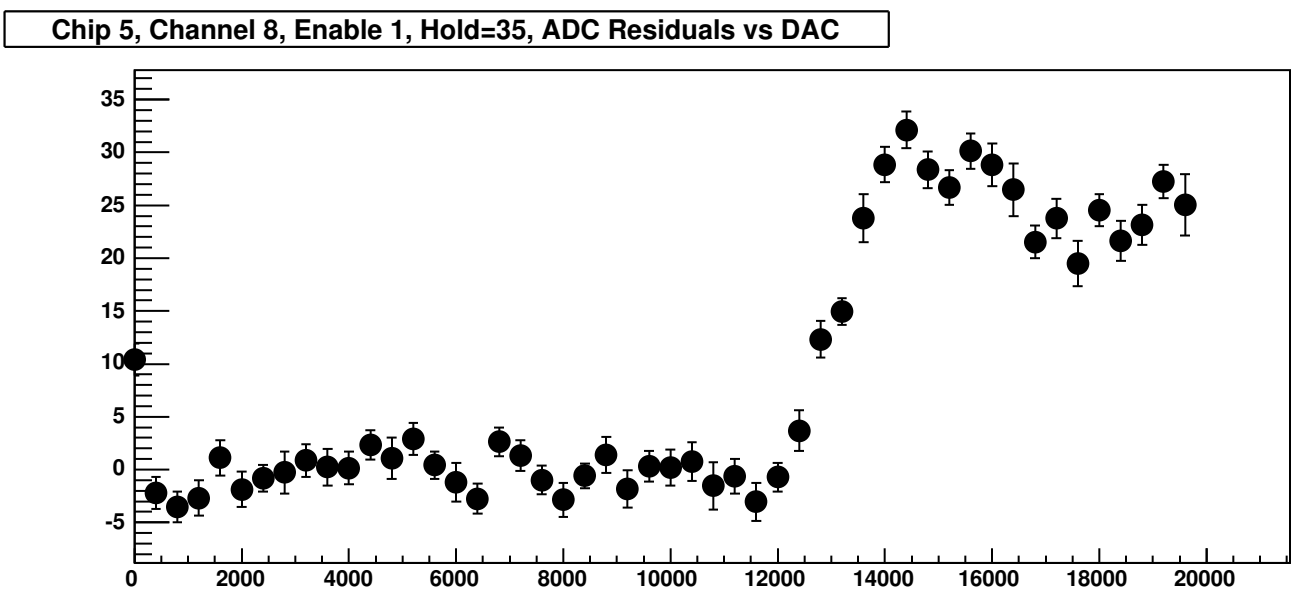

Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 12.54 / 23
p0 180 ± 0.6671
p1 0.0221 ± 0.000104
600
550
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

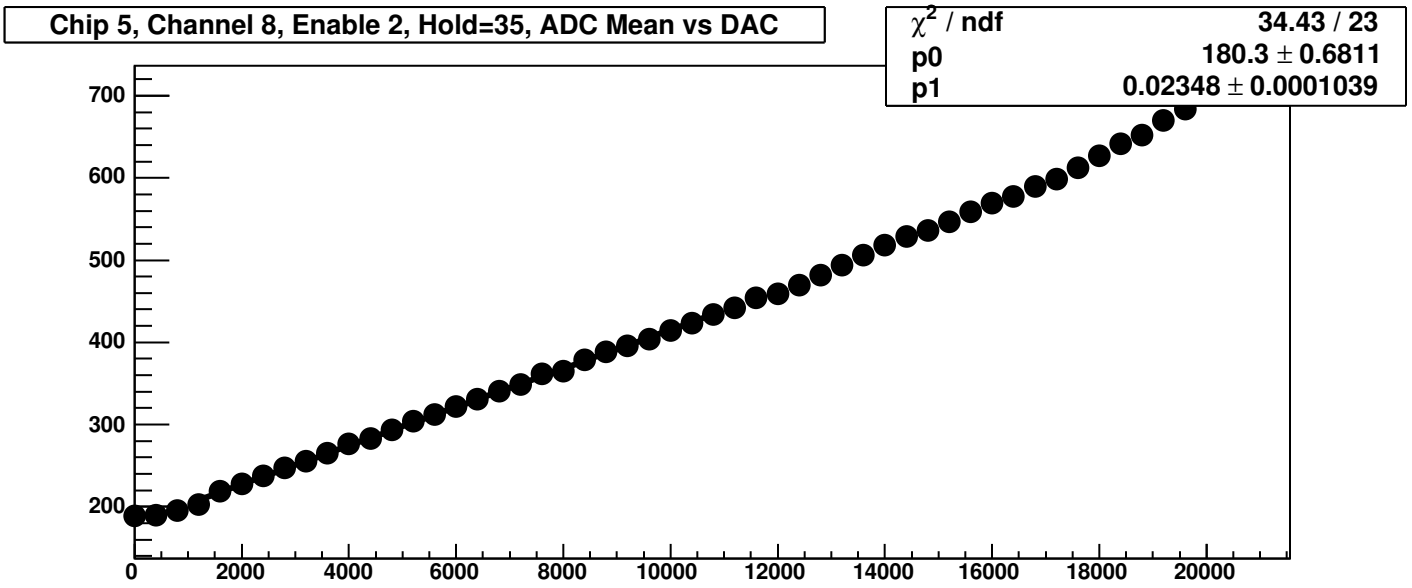

Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

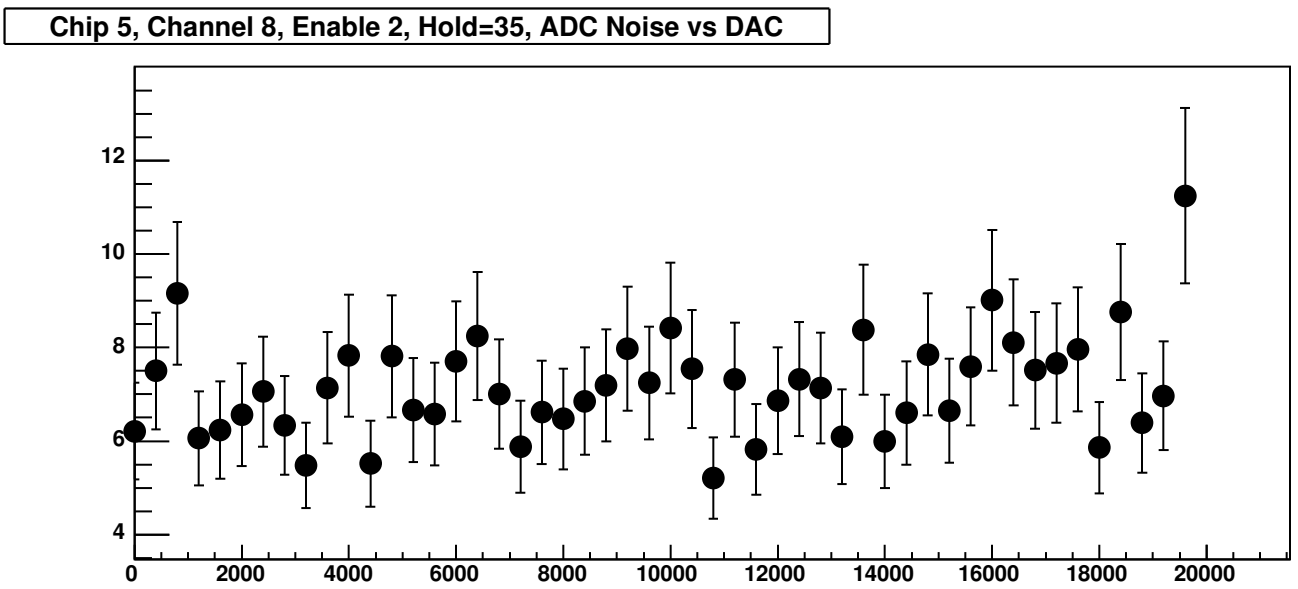

Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

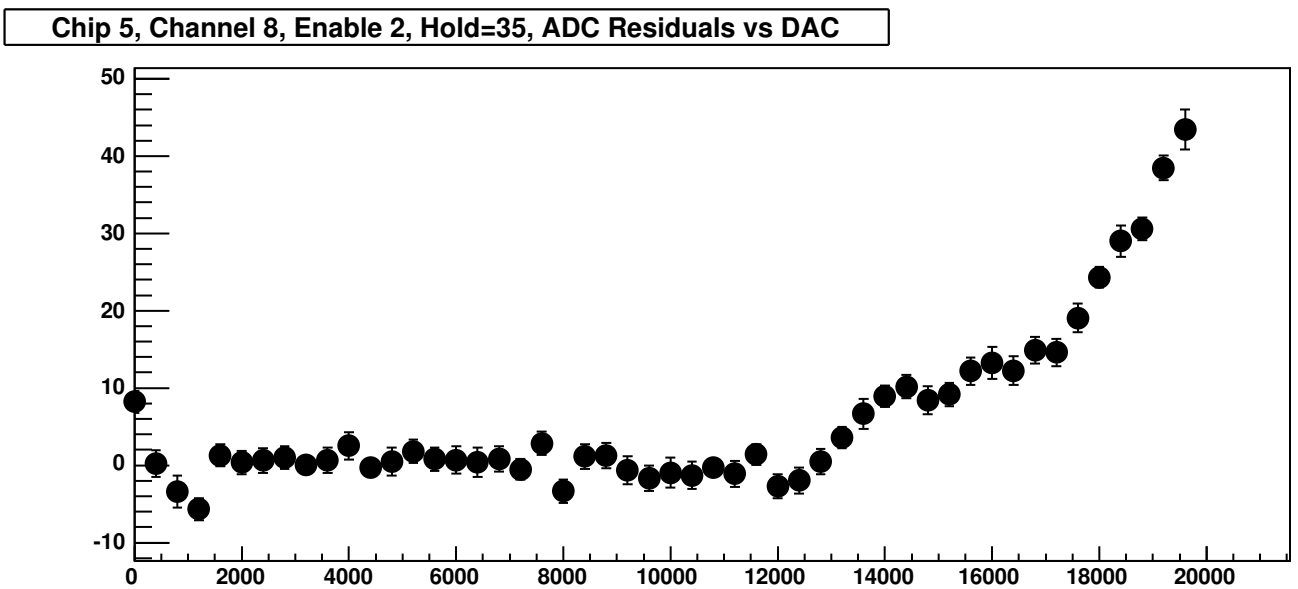

Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.18 / 23
p0 181.6 ± 0.7441
p1 0.02338 ± 0.0001136
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

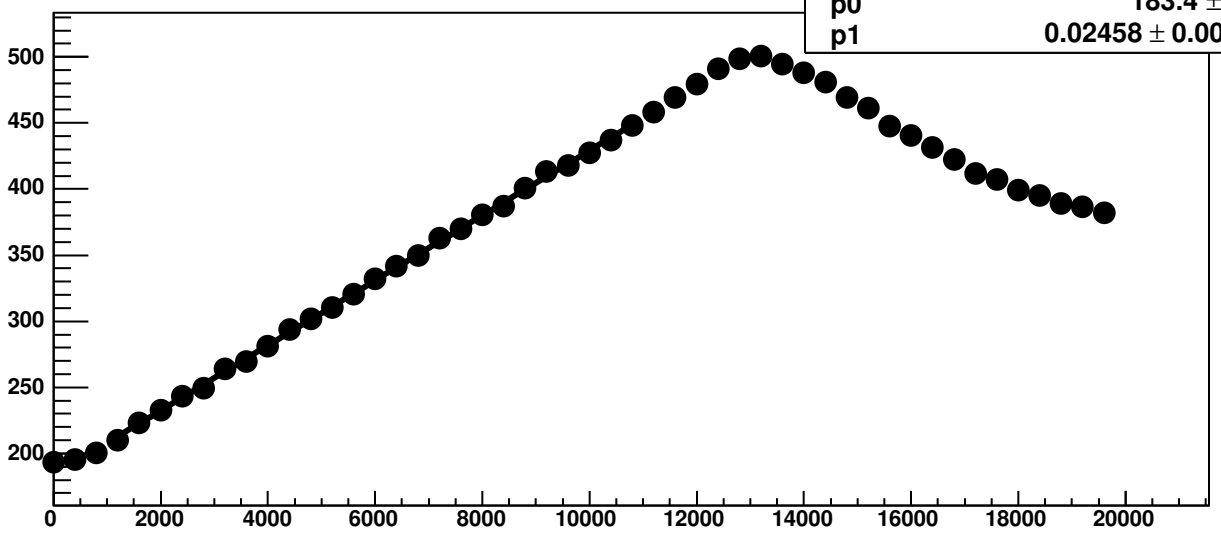

Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 34.43 / 23
p0 180.3 ± 0.6811
p1 0.02348 ± 0.0001039
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

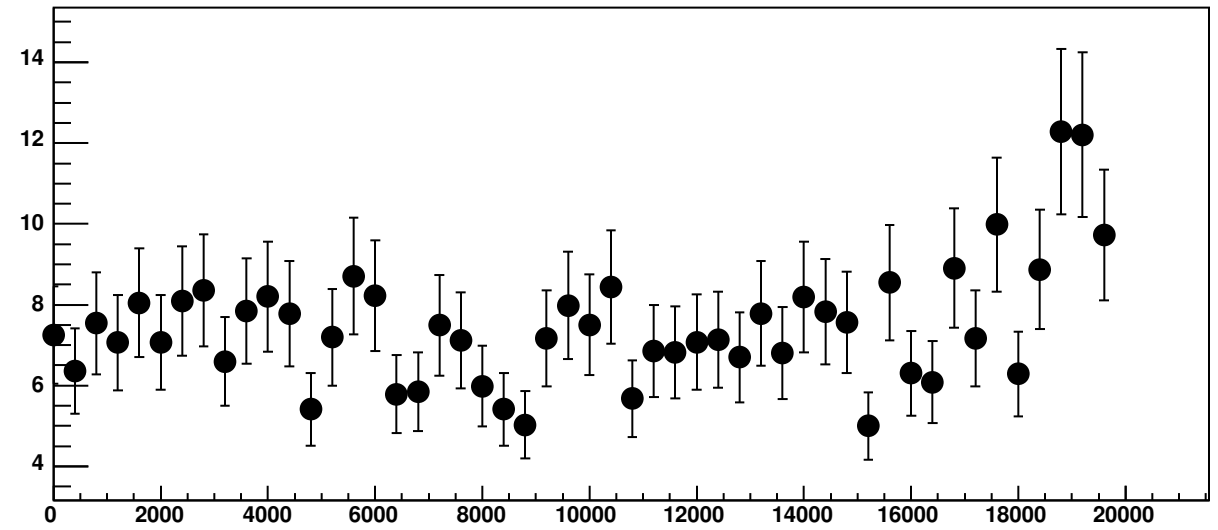

Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

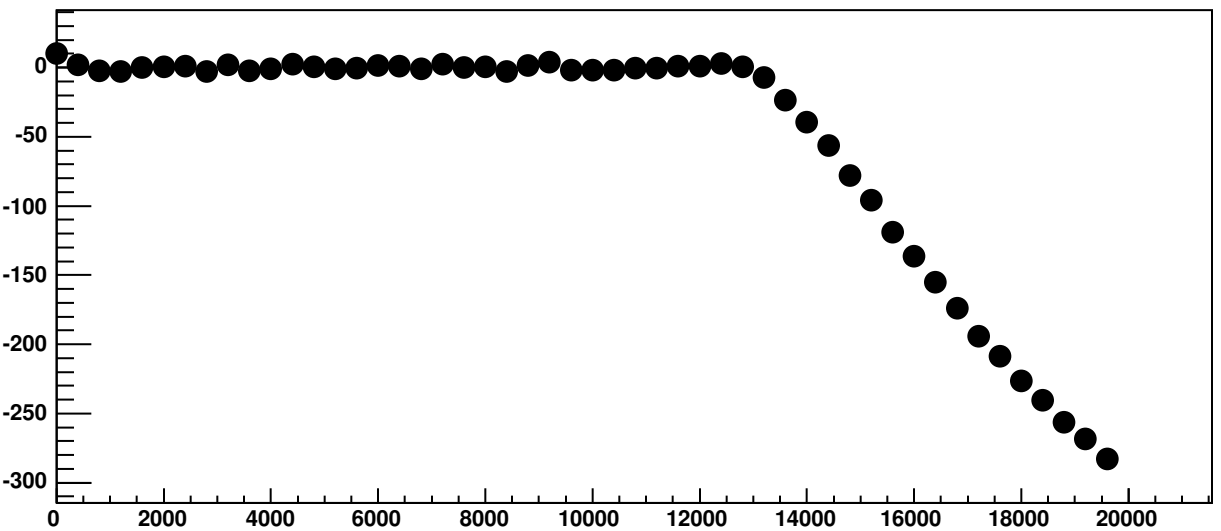
χ^2 / ndf	30.94 / 23
p0	183.4 ± 0.774
p1	0.02458 ± 0.0001119

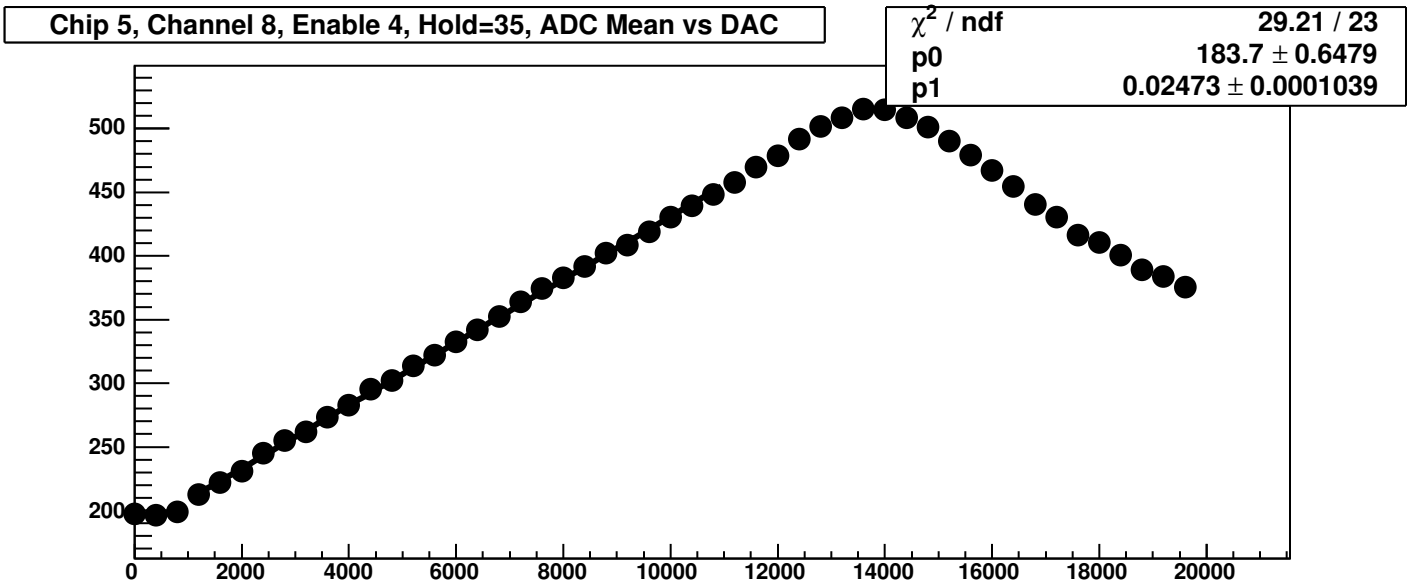


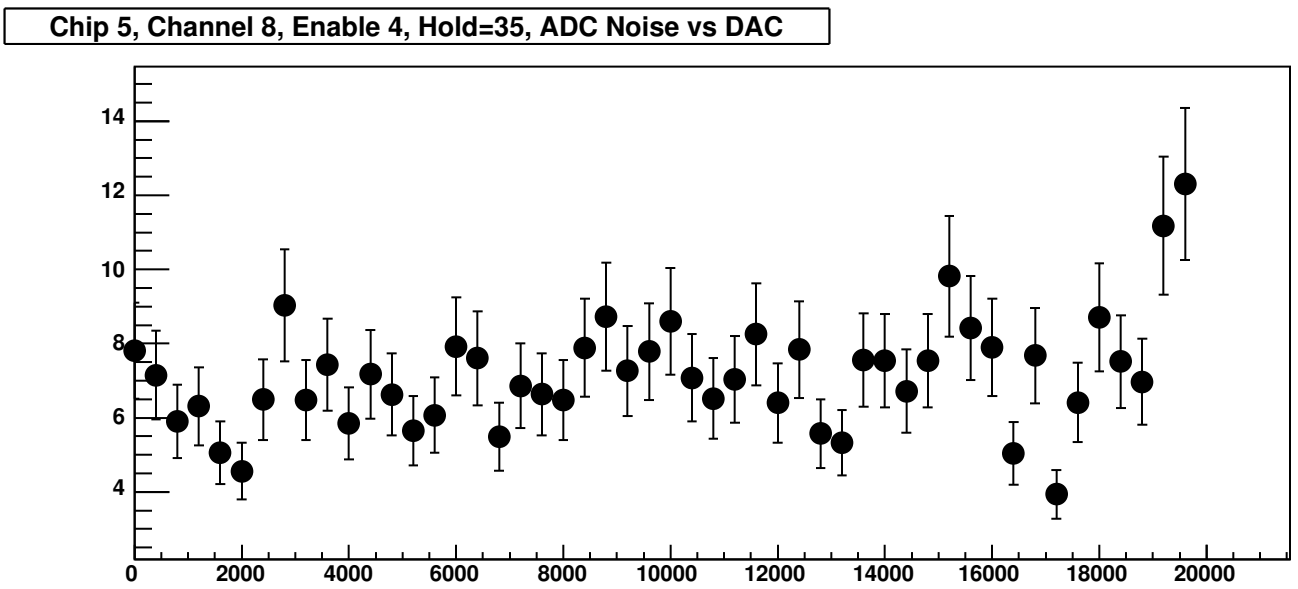
Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC

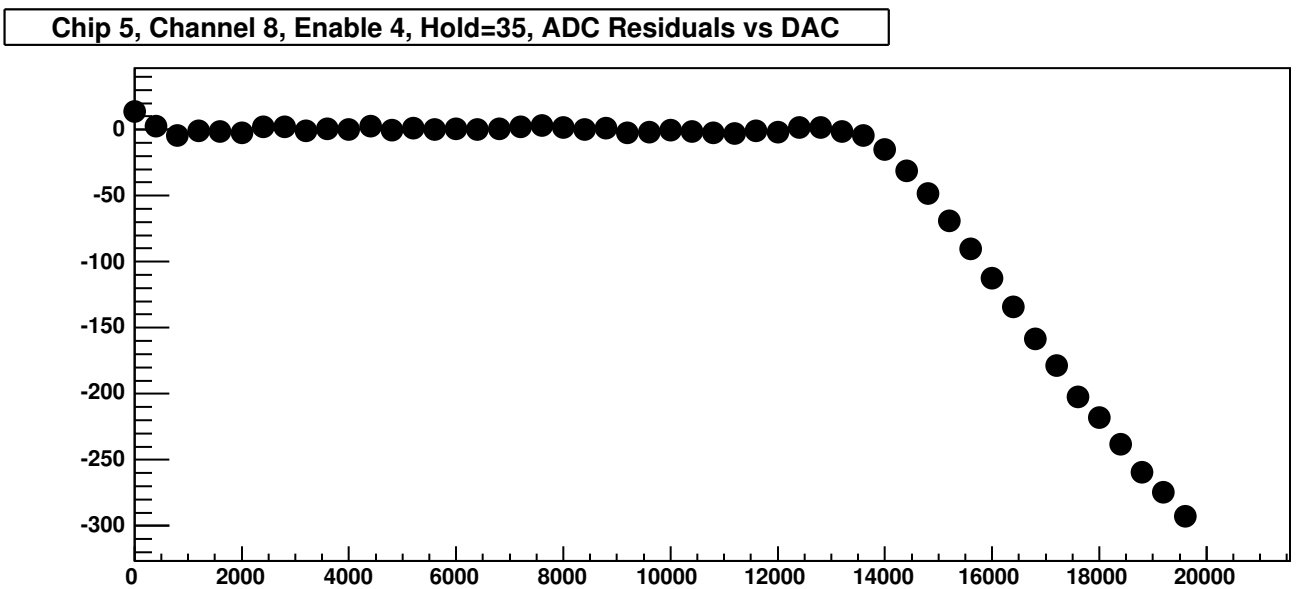


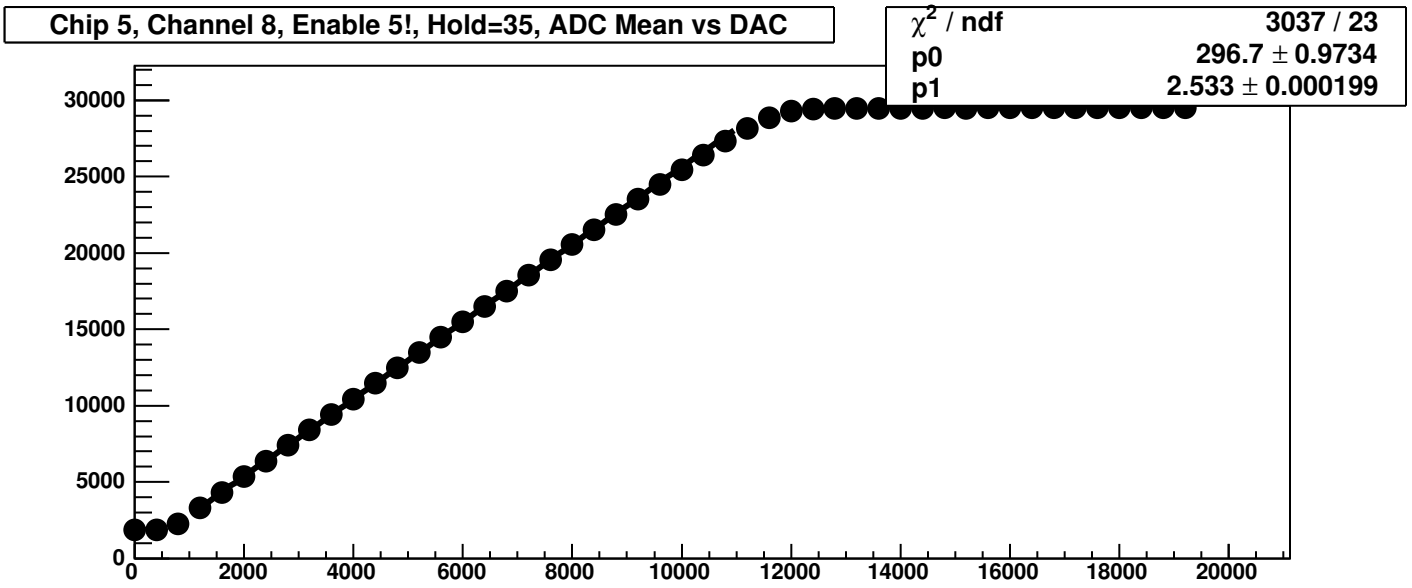
Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

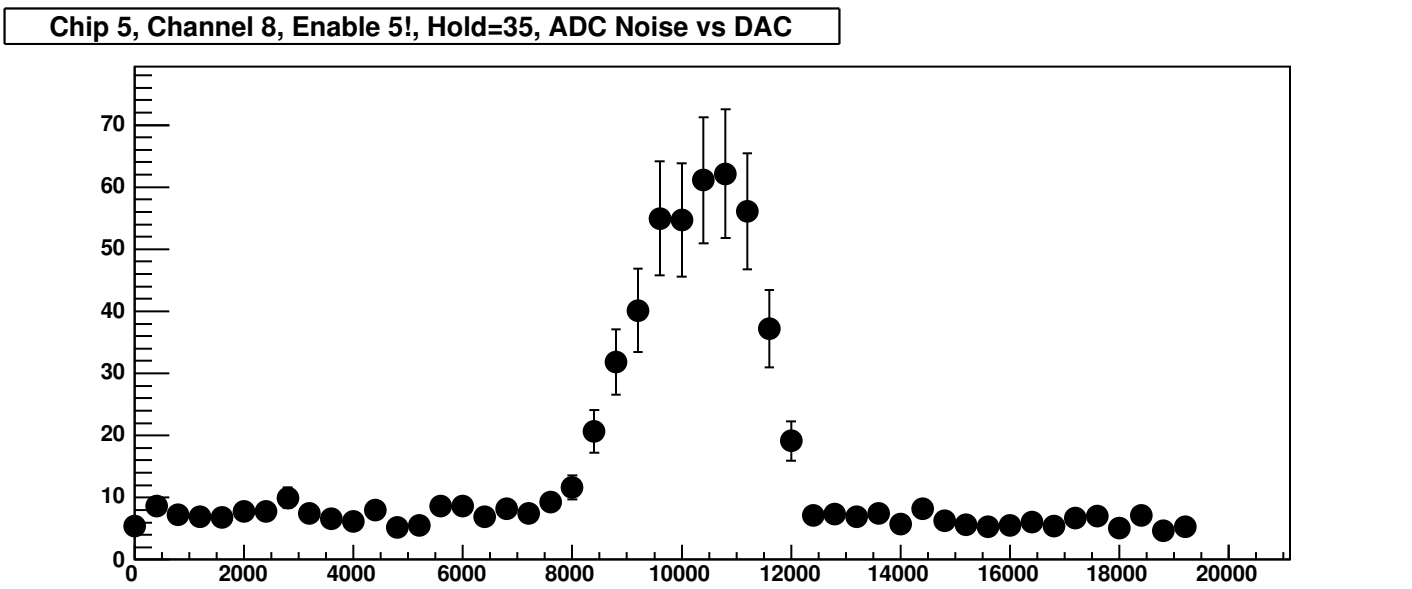


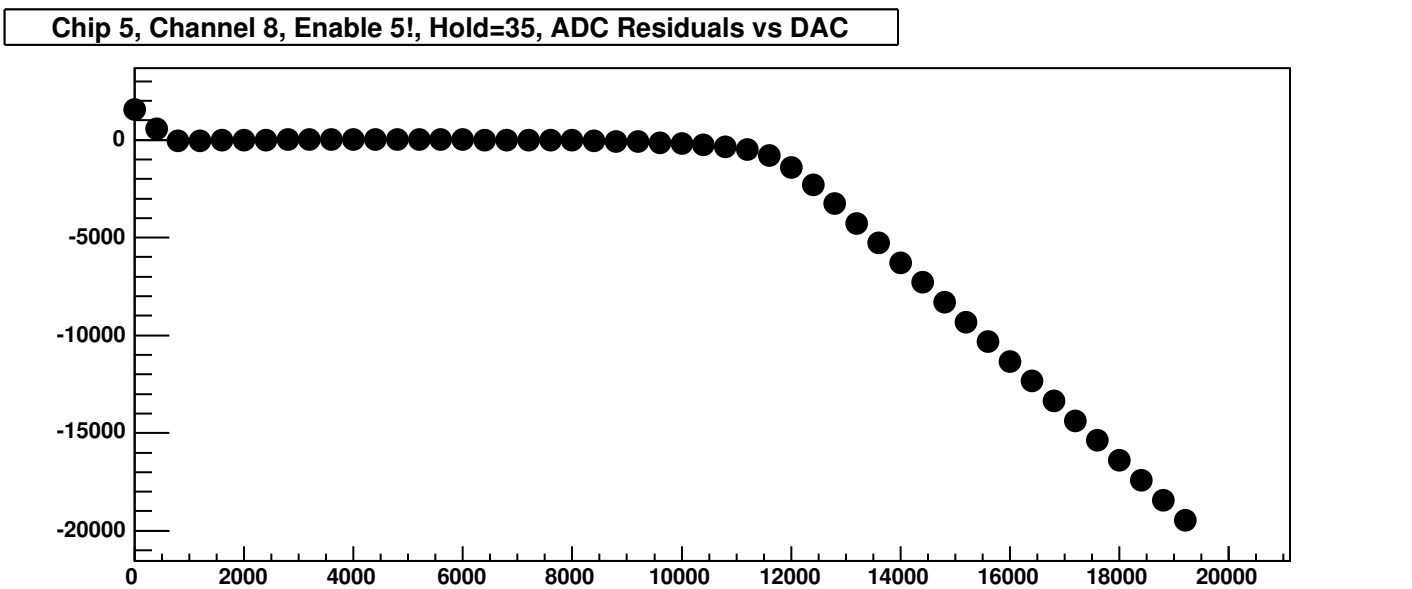

Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.21 / 23
p0 183.7 ± 0.6479
p1 0.02473 ± 0.0001039
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

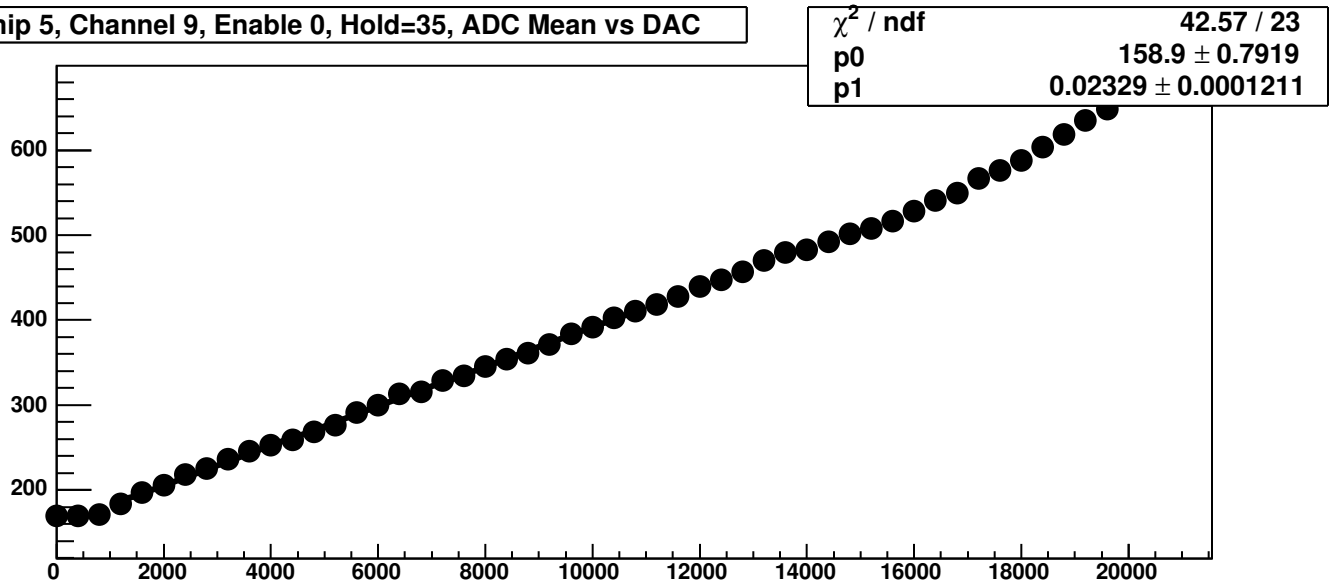

Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3037 / 23
p0 296.7 ± 0.9734
p1 2.533 ± 0.000199
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

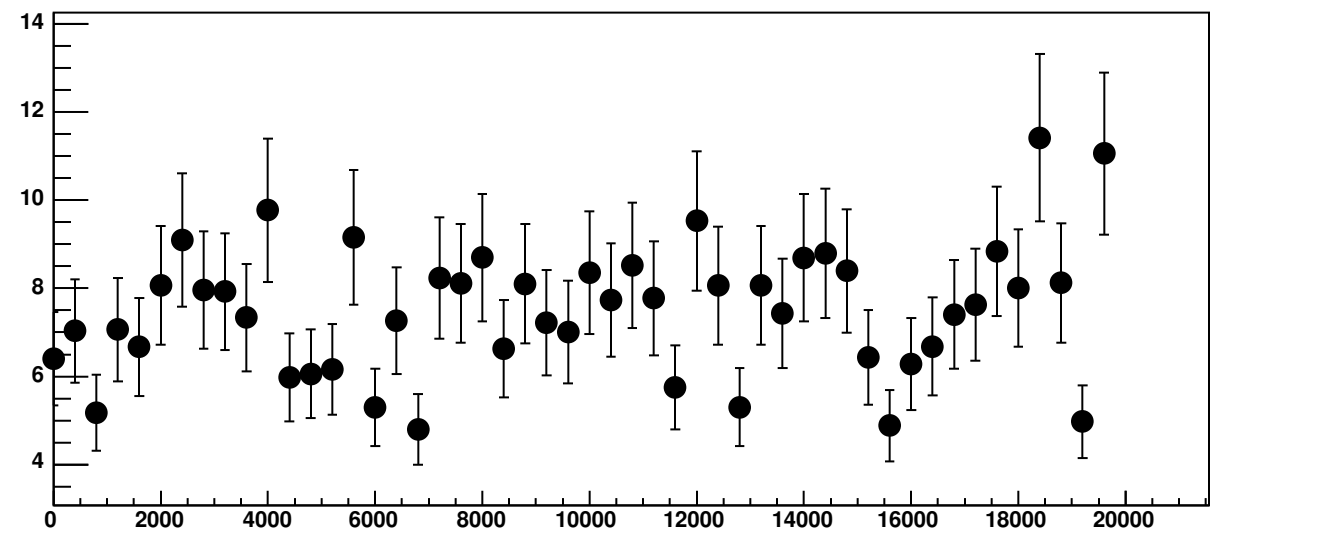

Chip 5, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

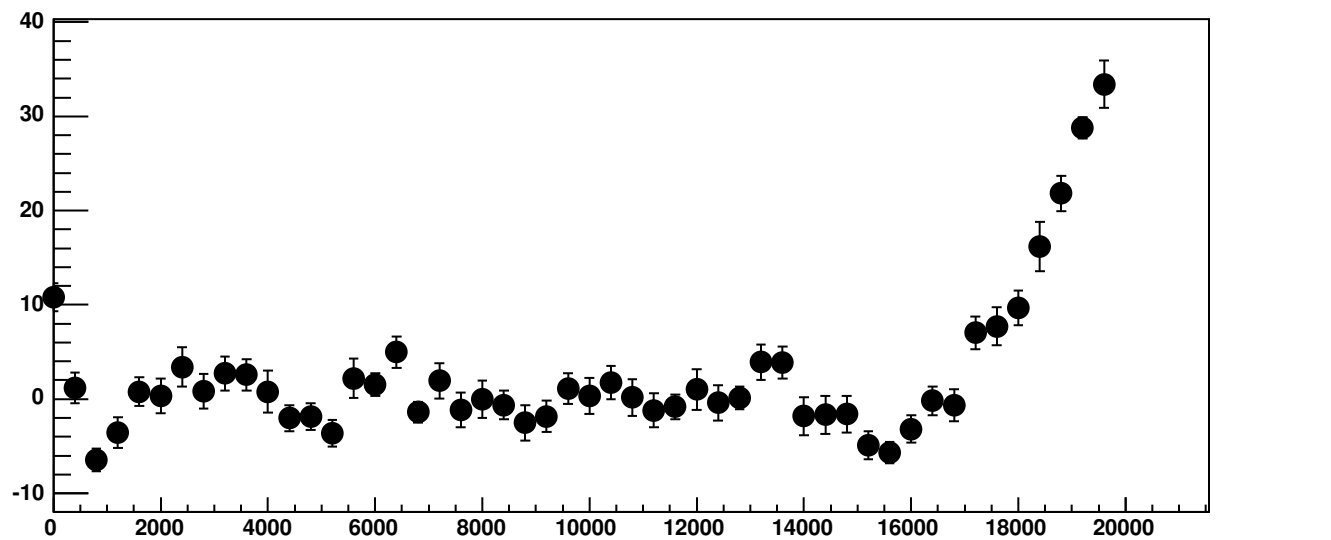
Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



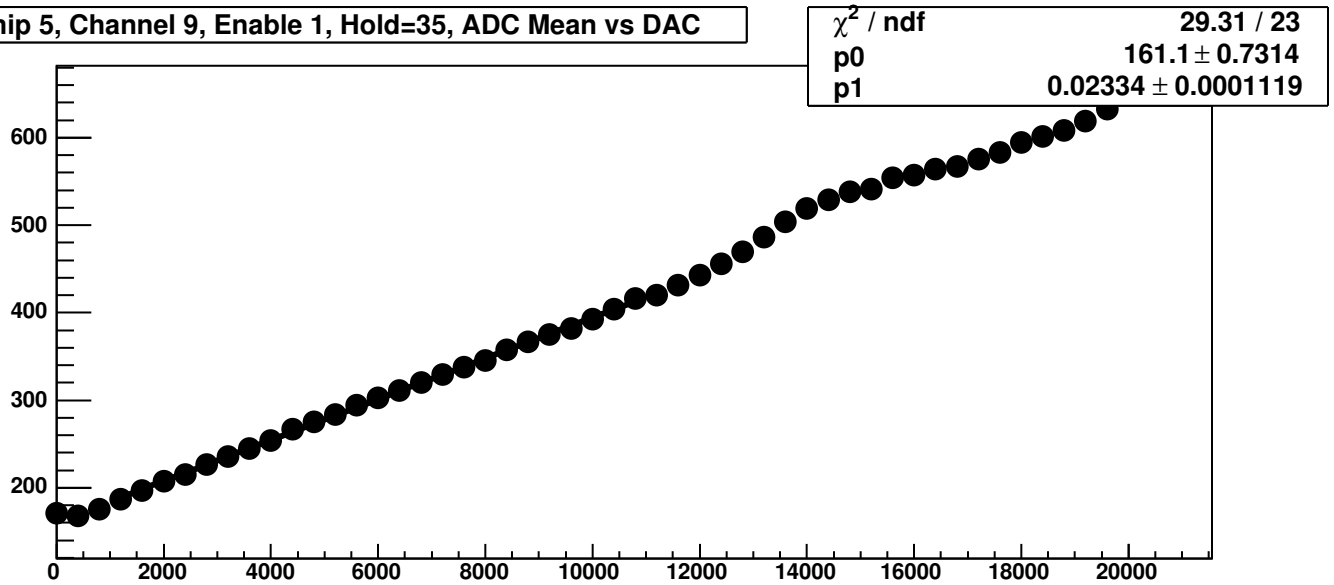
Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



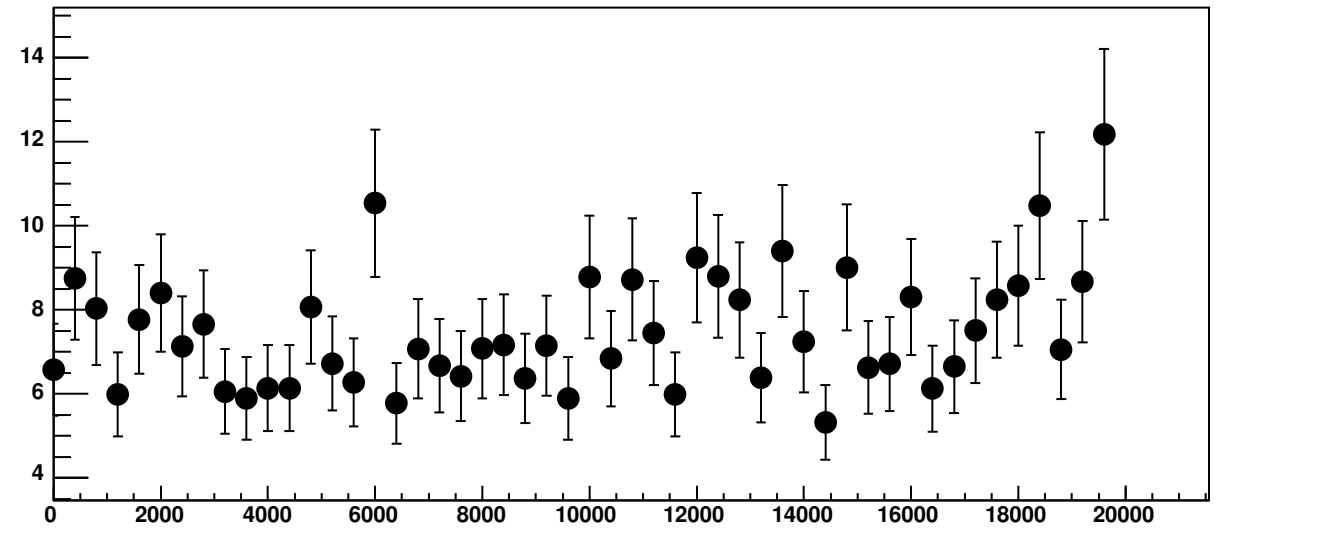
Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC



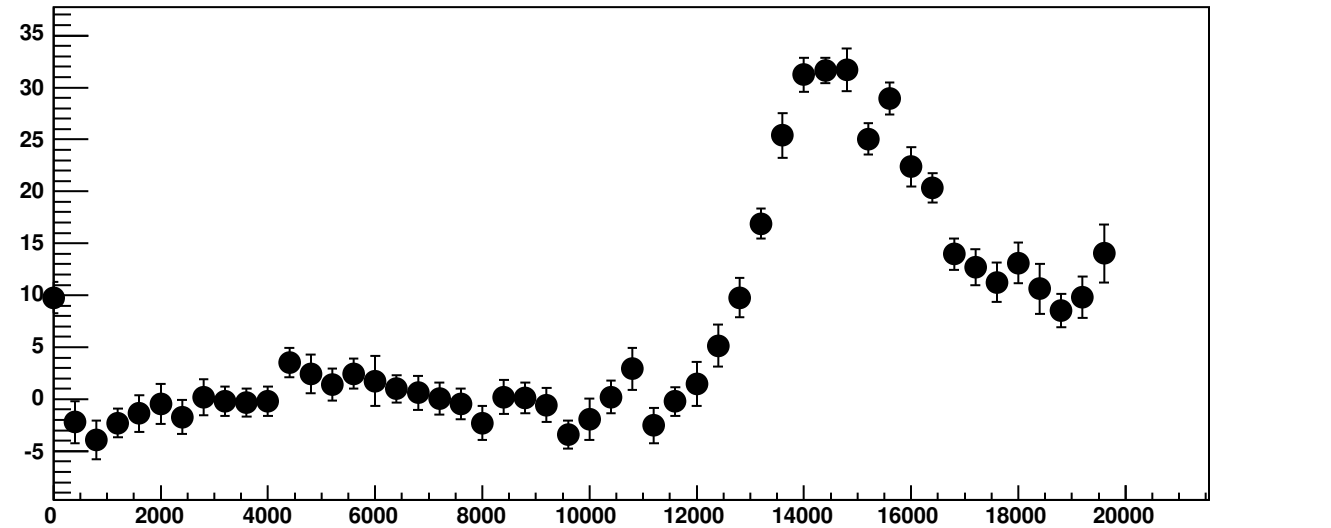
Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



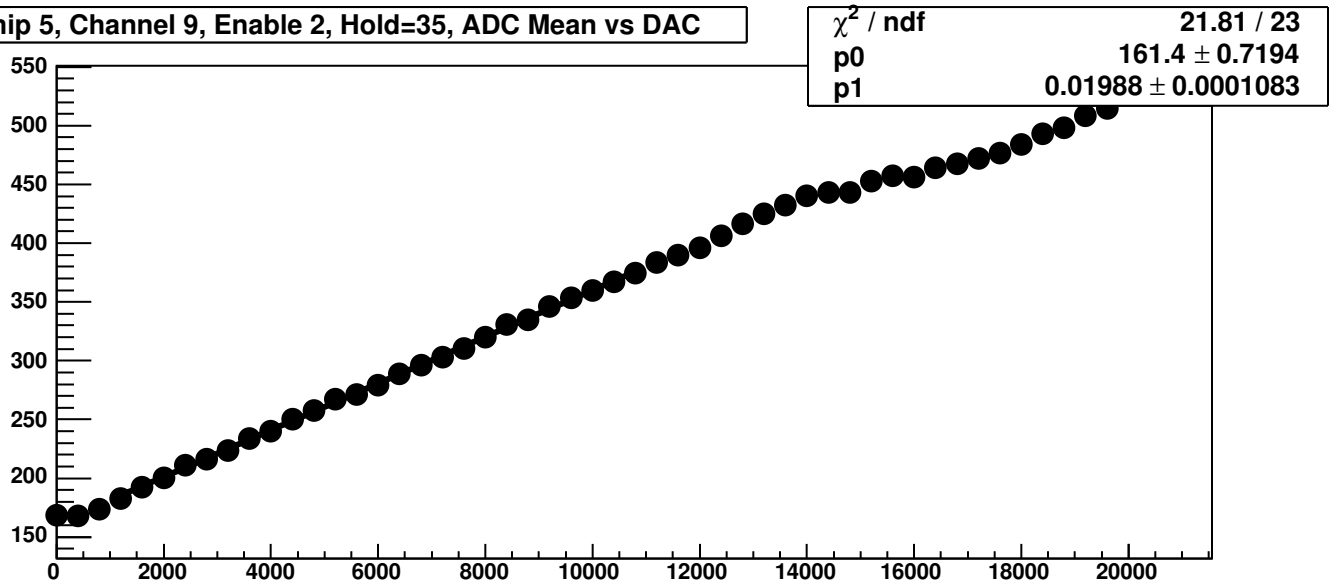
Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



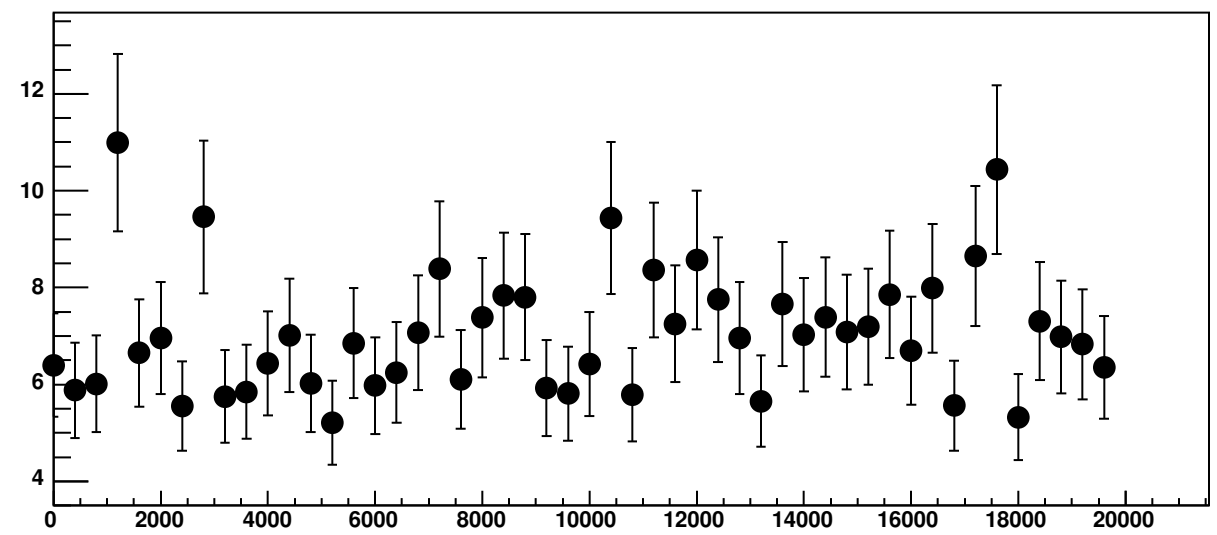
Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



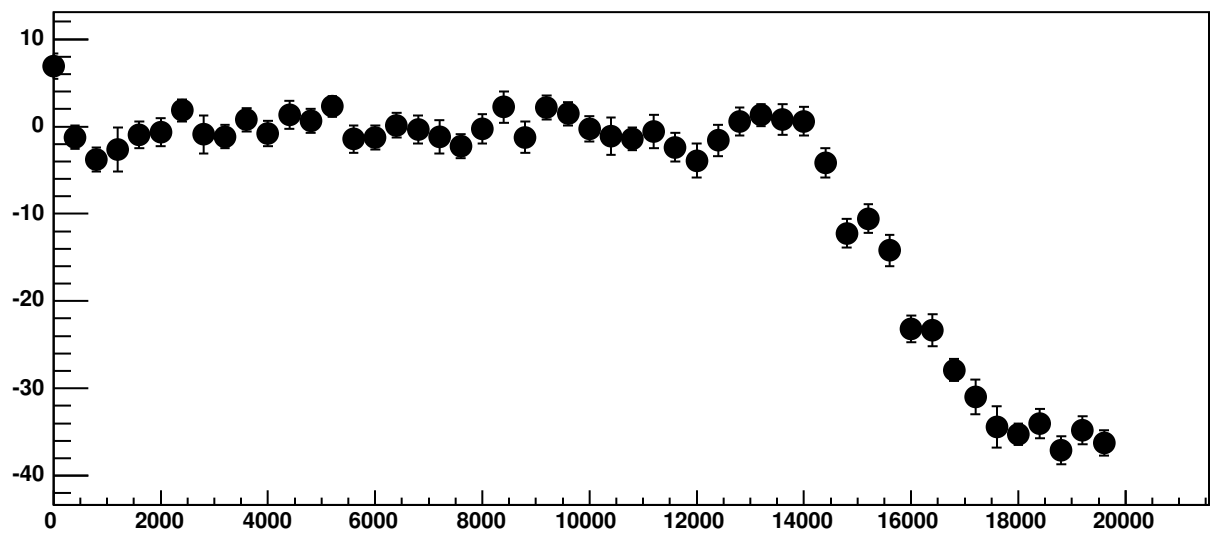
Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



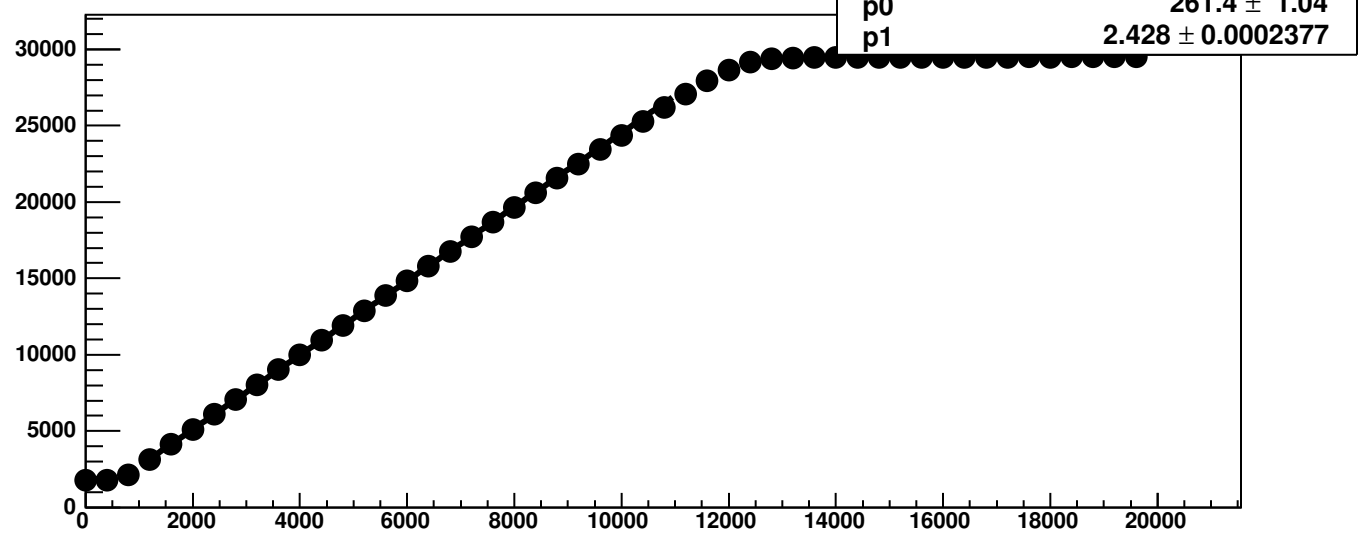
Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



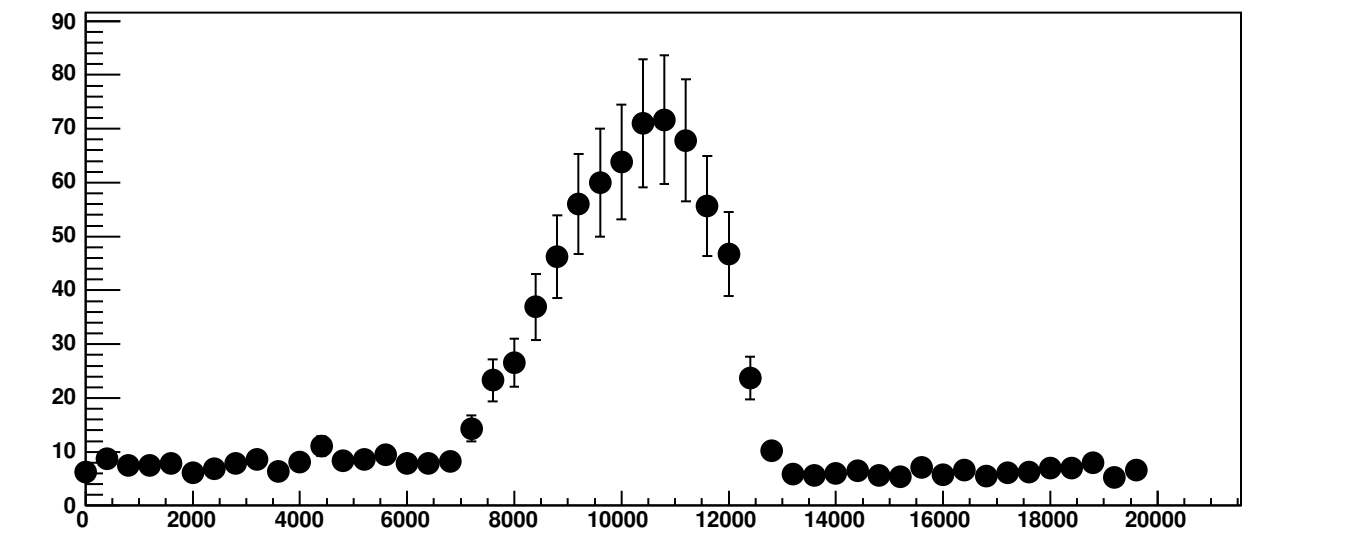
Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



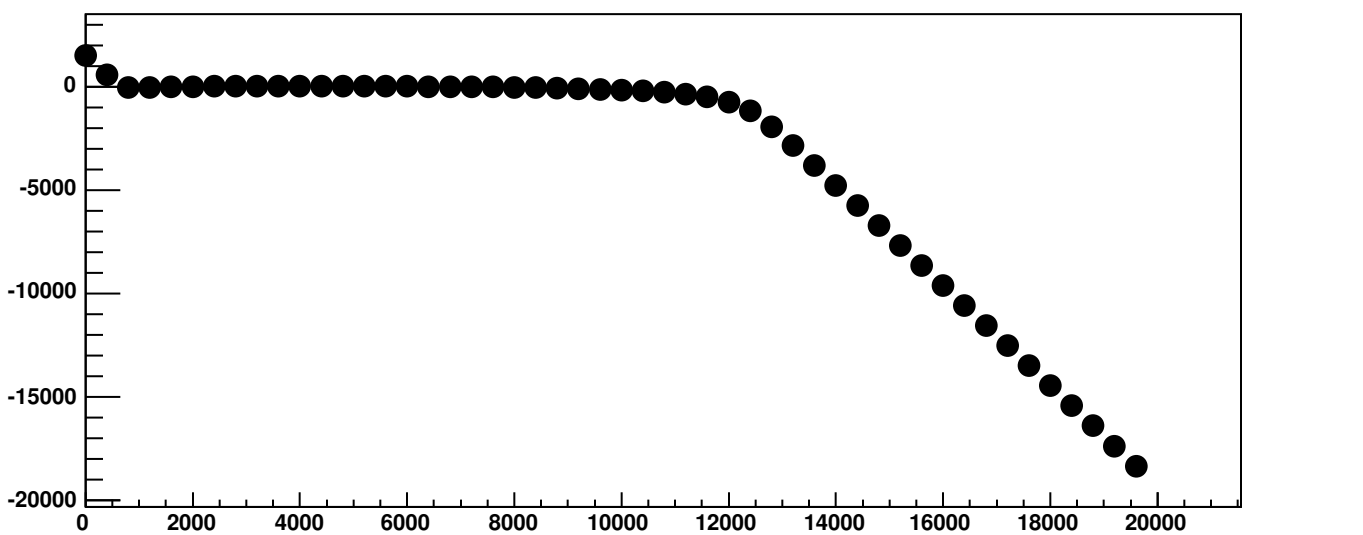
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



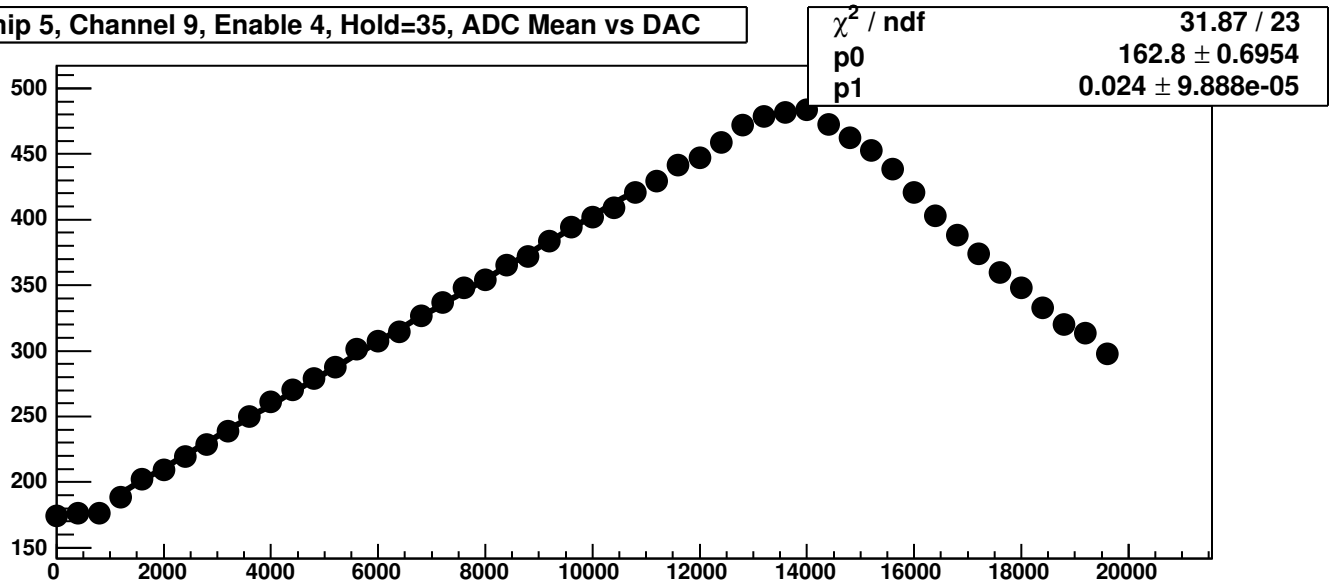
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



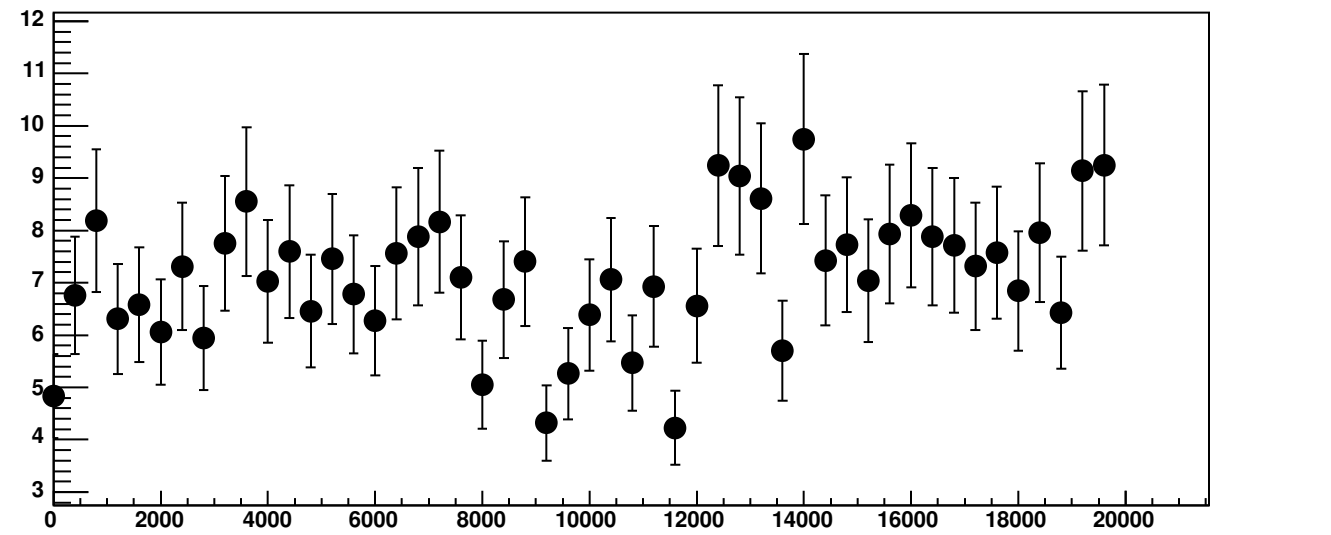
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC



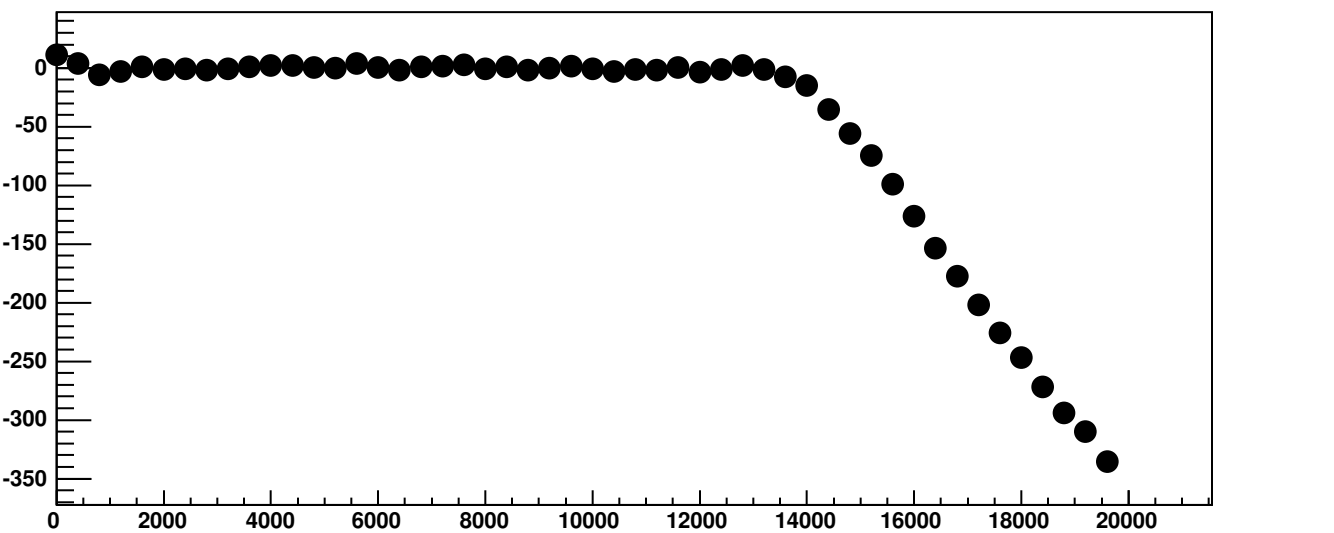
Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



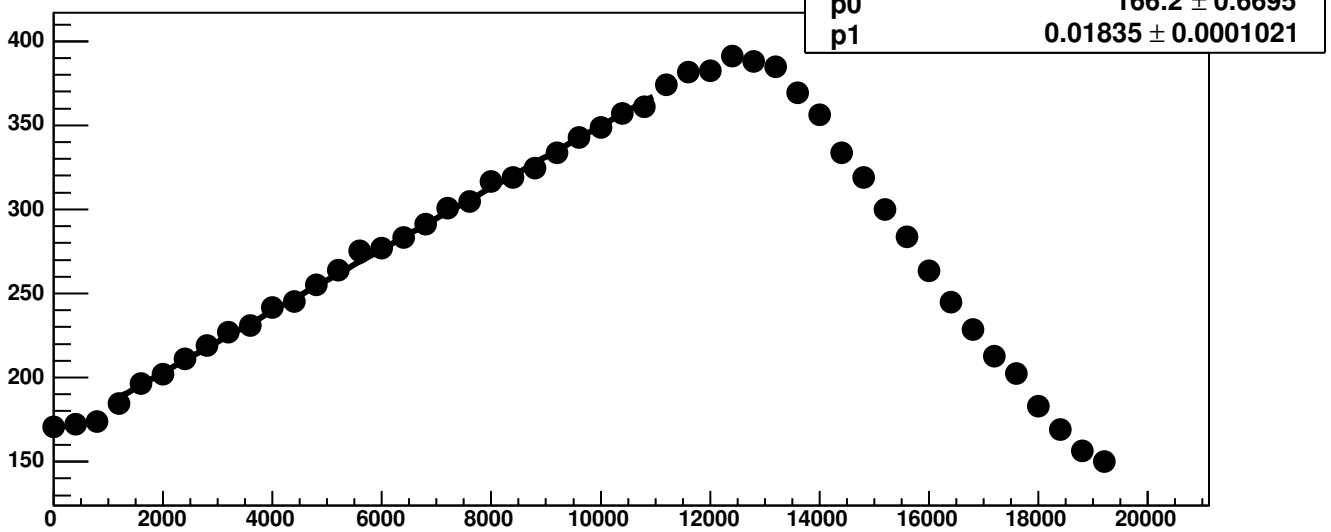
Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



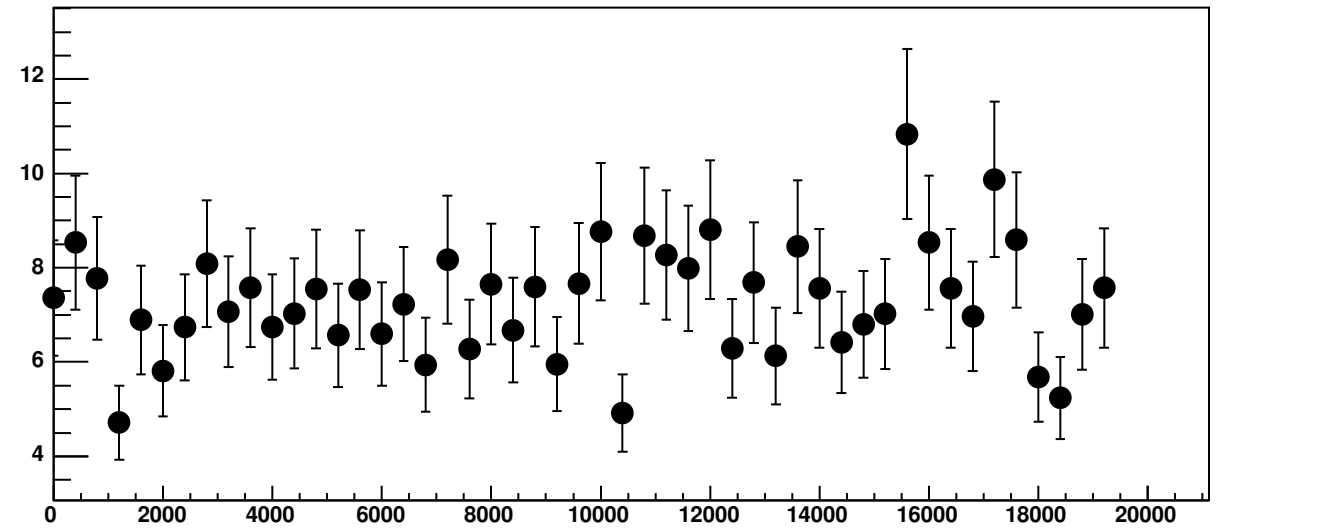
Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



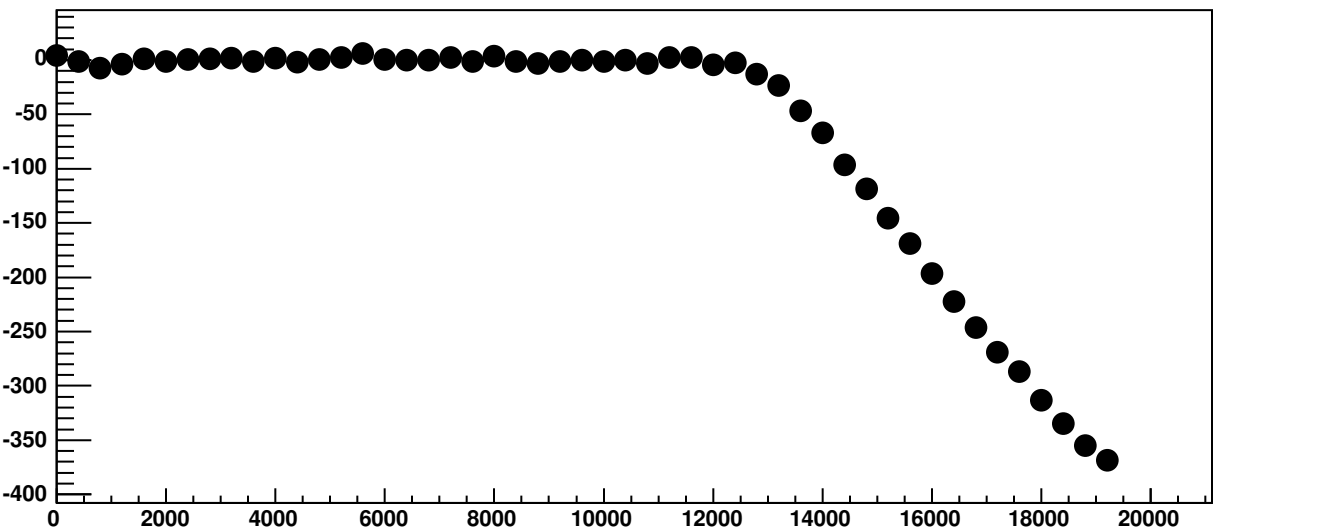
Chip 5, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

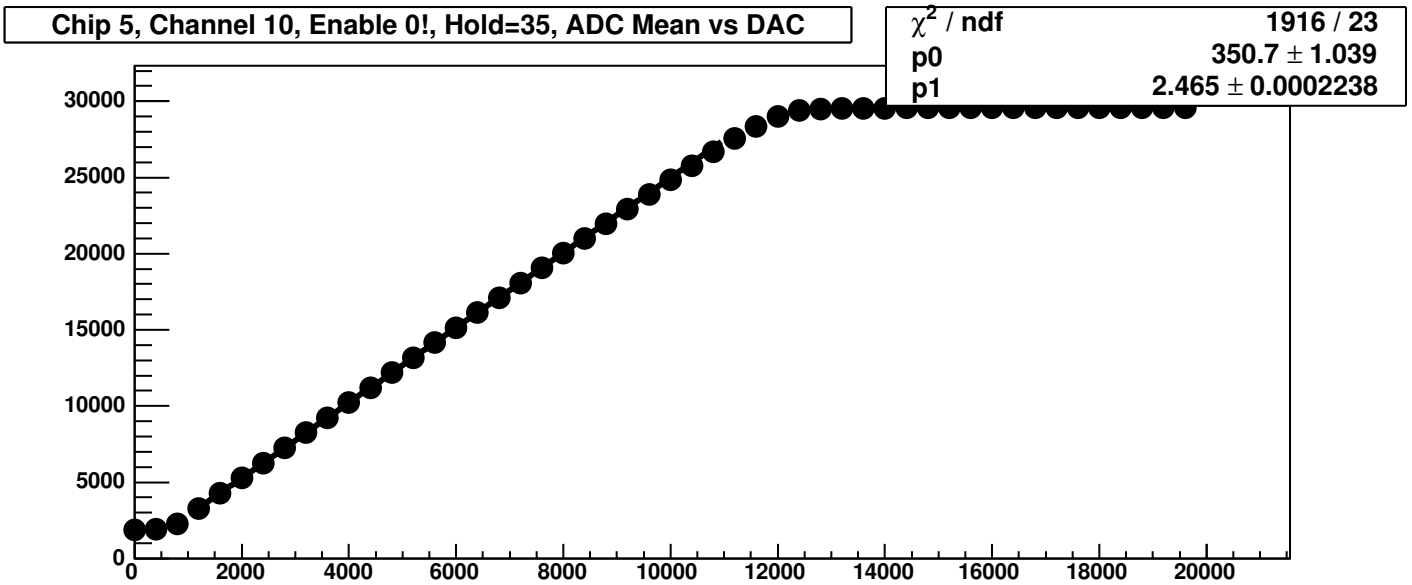


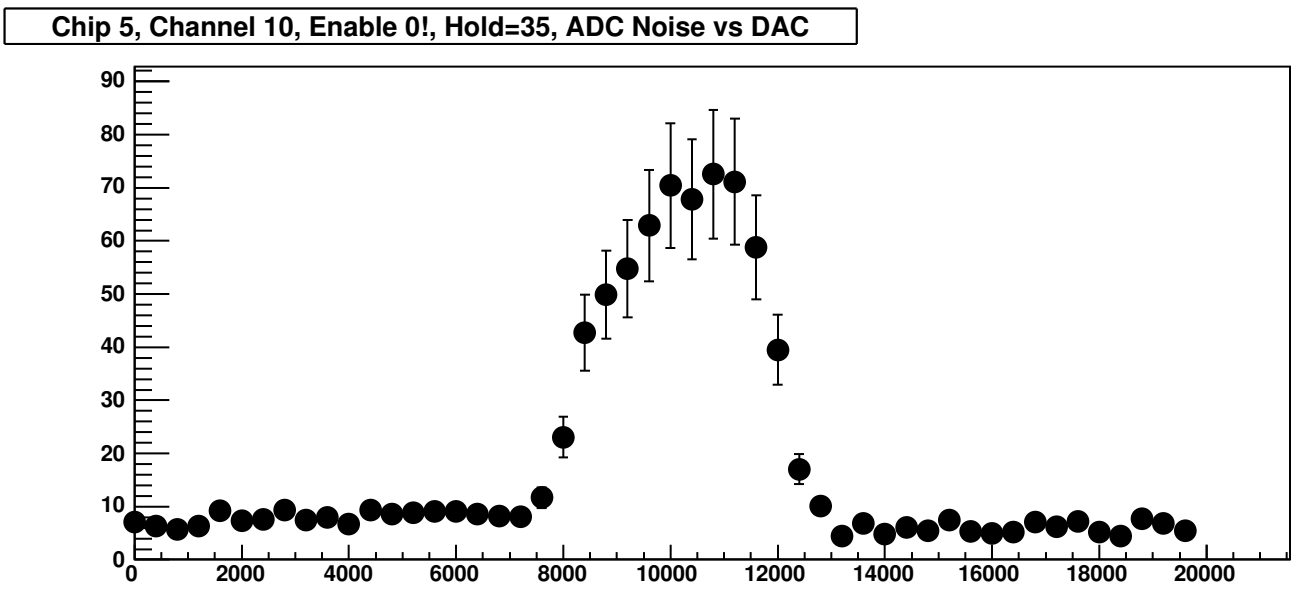
Chip 5, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

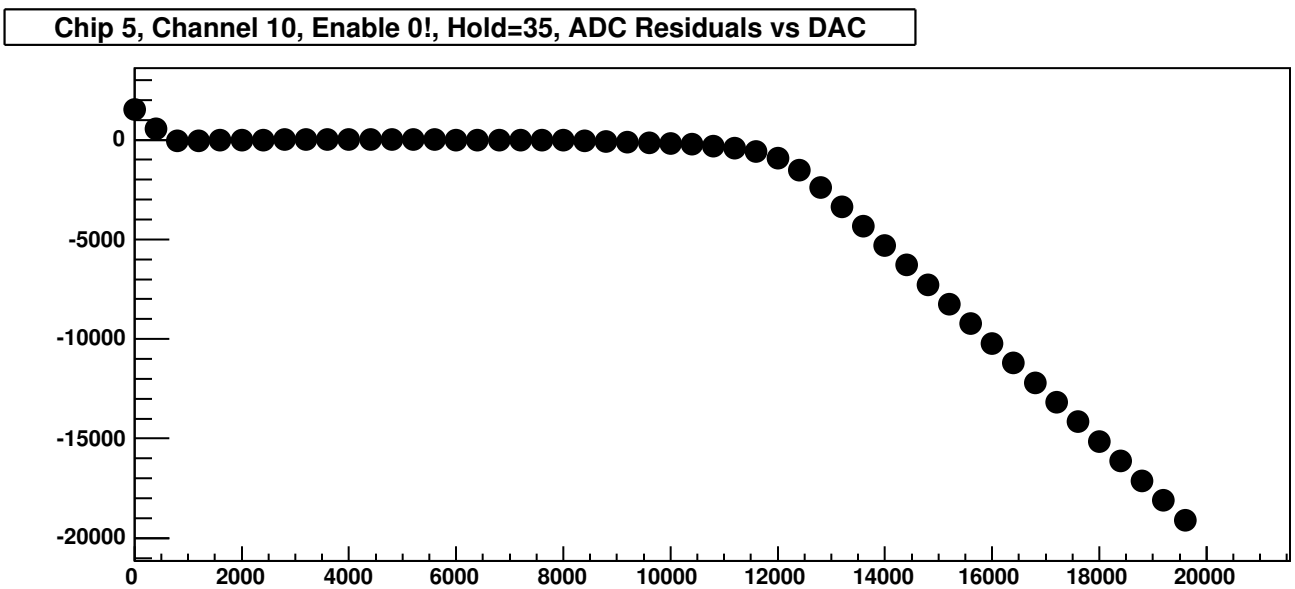


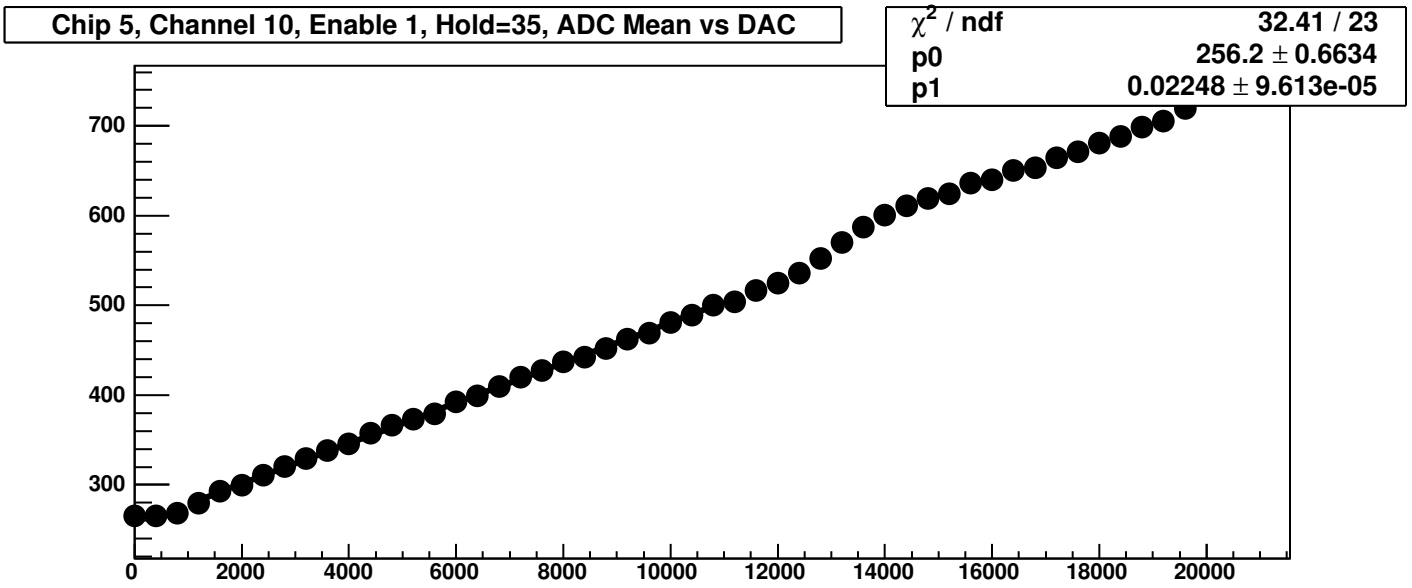
Chip 5, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

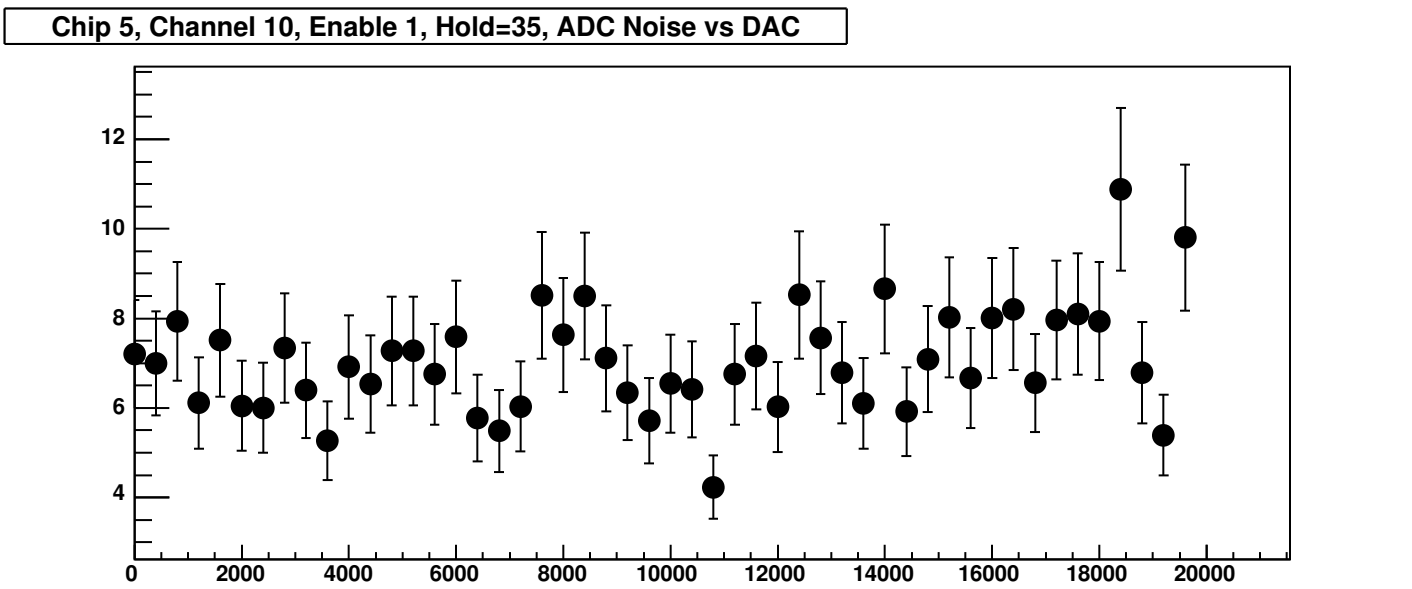


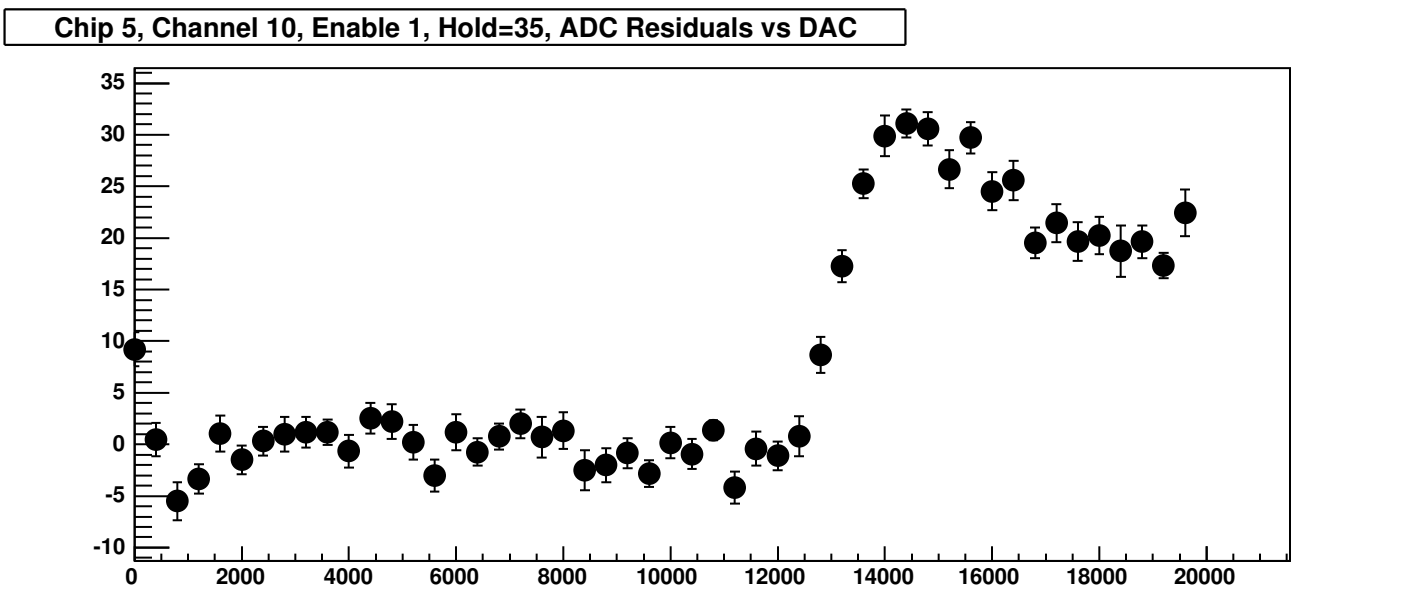

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1916 / 23
p0 350.7 ± 1.039
p1 2.465 ± 0.0002238
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

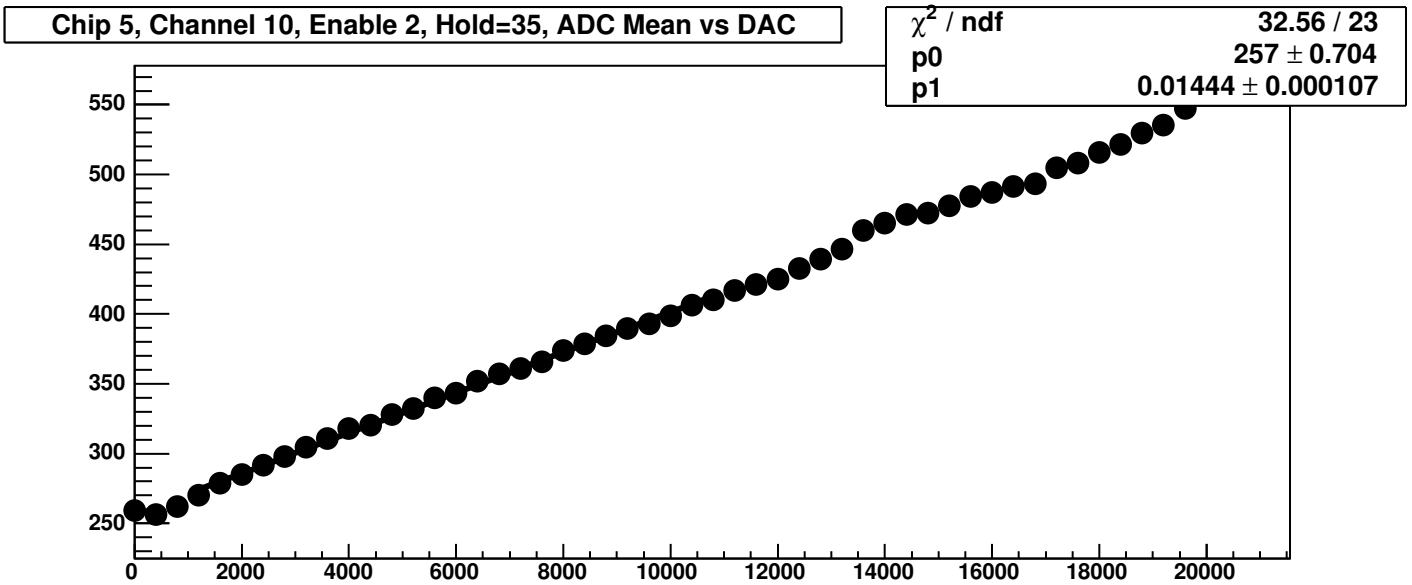

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

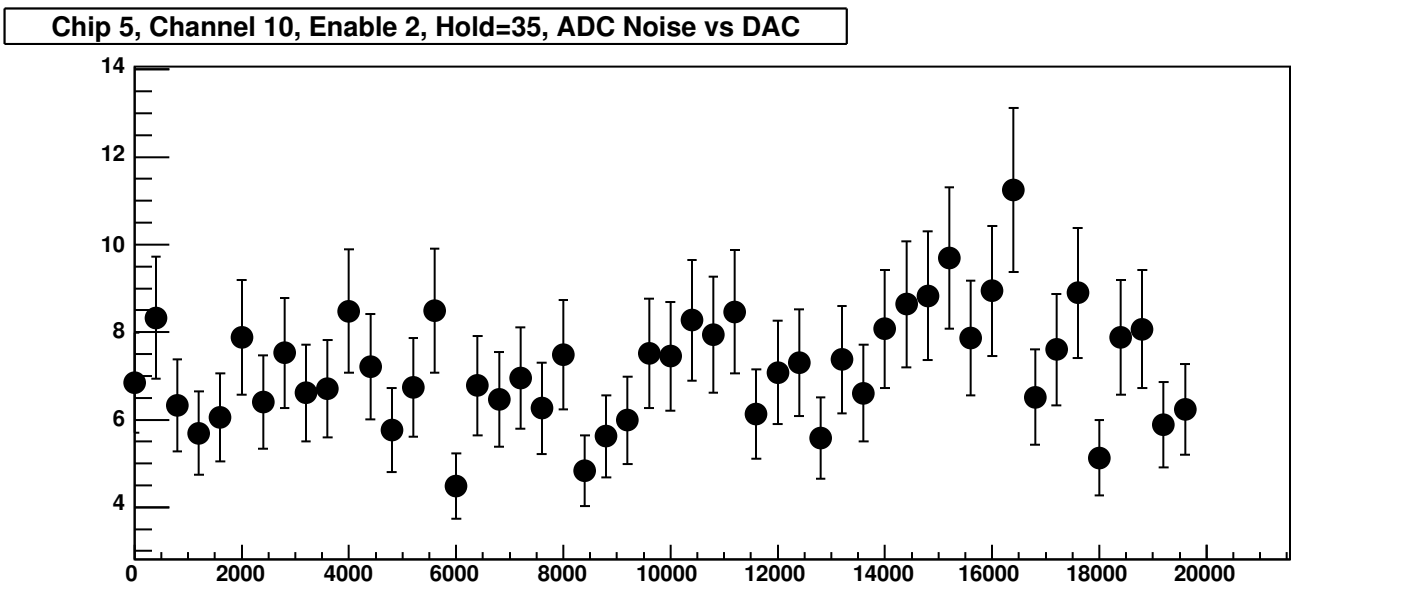

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

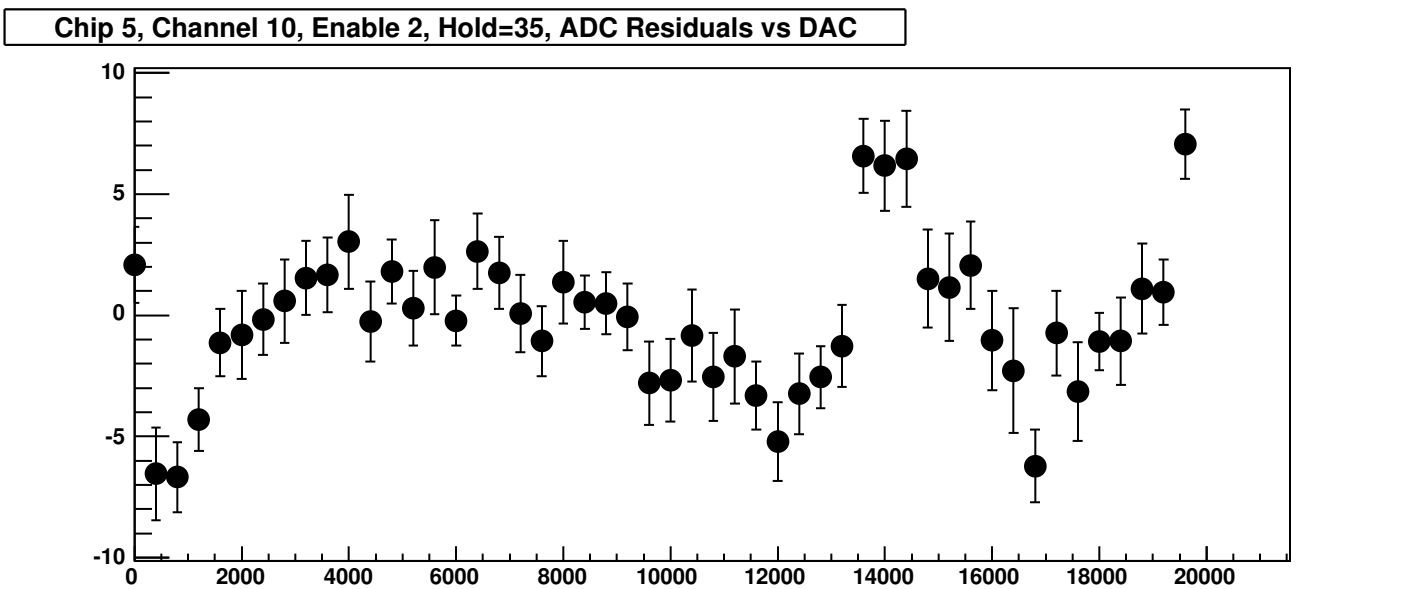

Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.41 / 23
p0 256.2 ± 0.6634
p1 0.02248 ± 9.613e-05
700
600
500
400
300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

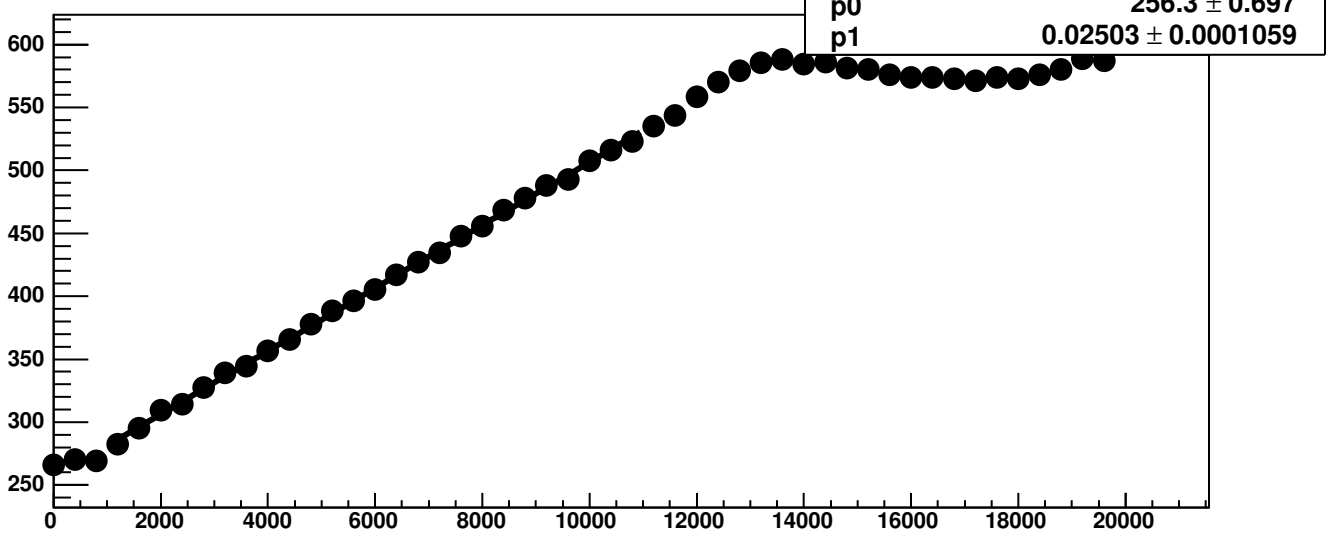

Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.56 / 23
p0 257 ± 0.704
p1 0.01444 ± 0.000107
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

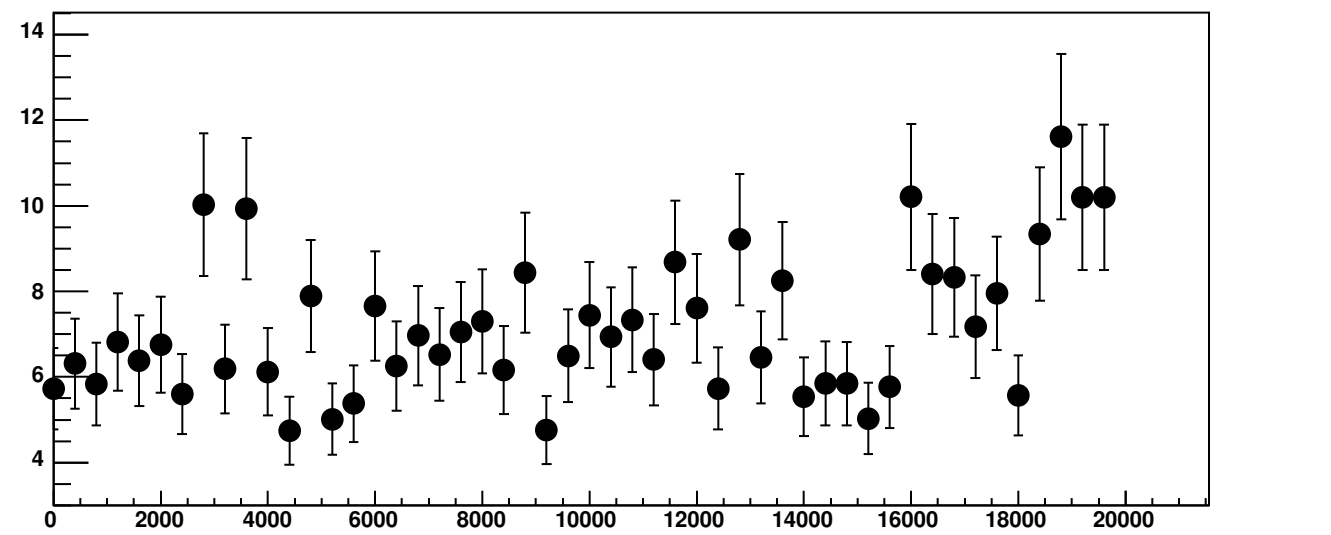

Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

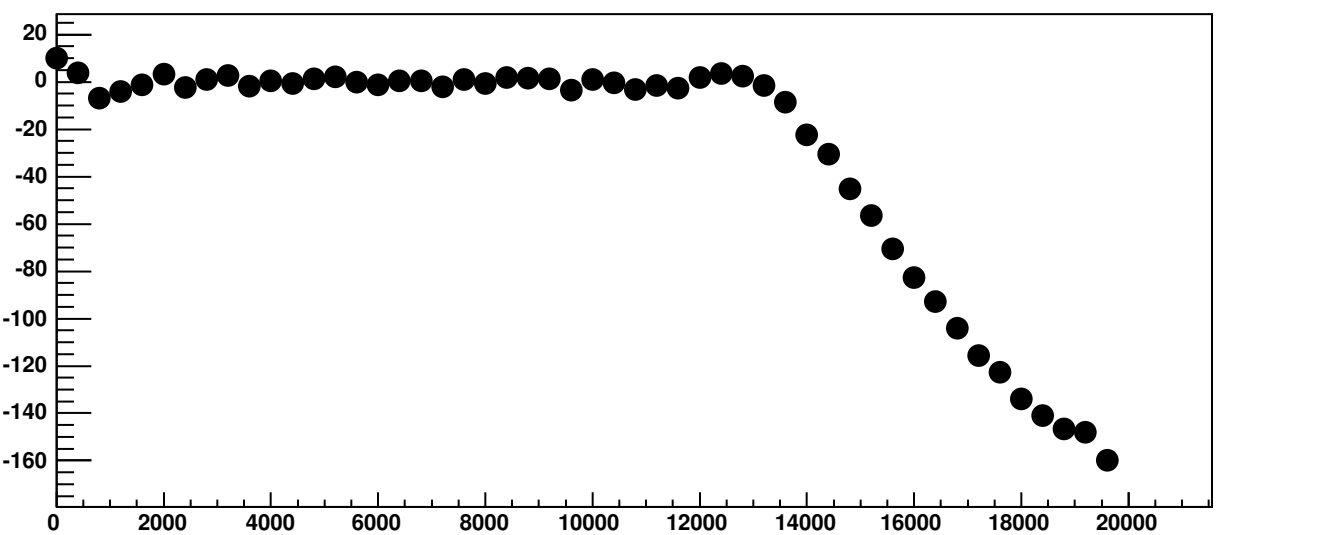
Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



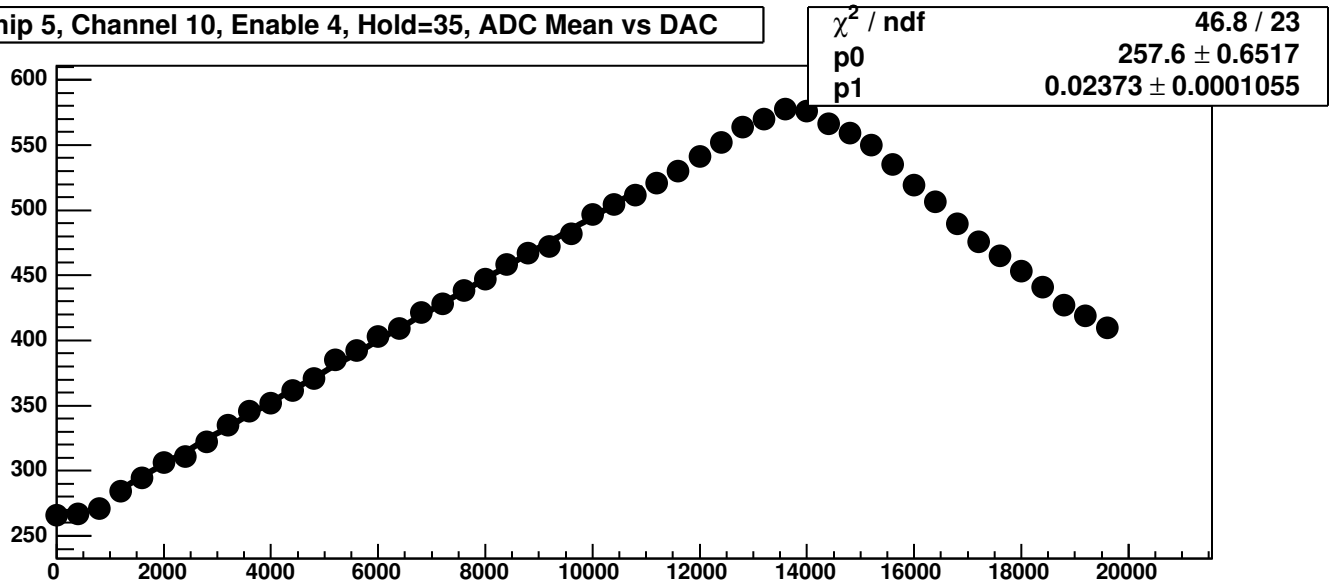
Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



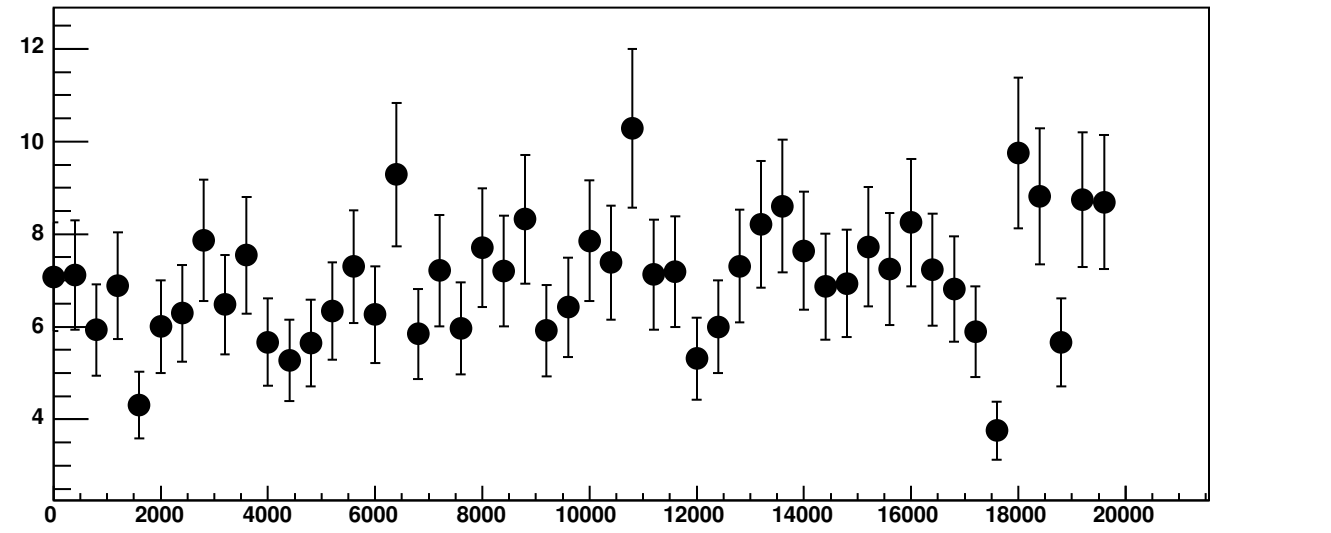
Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC



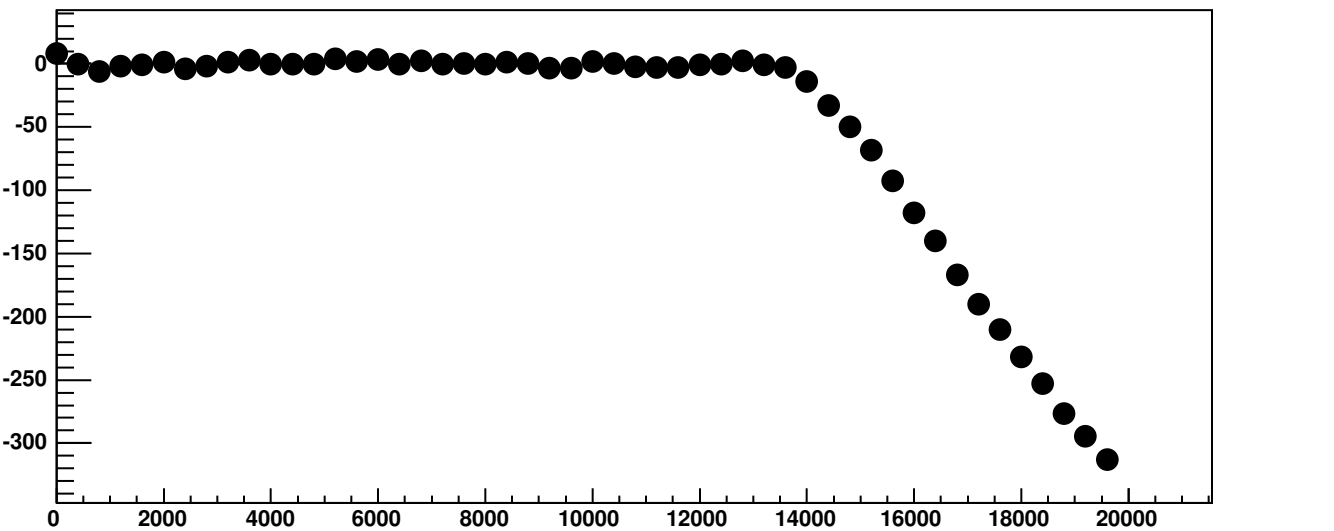
Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

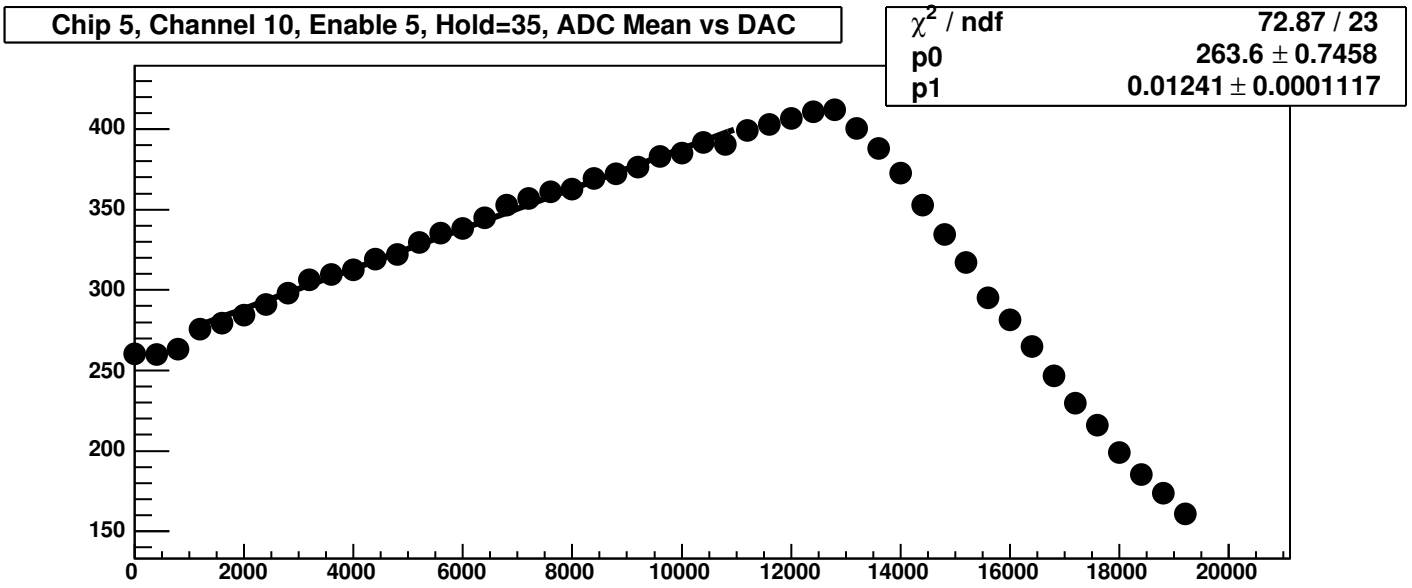


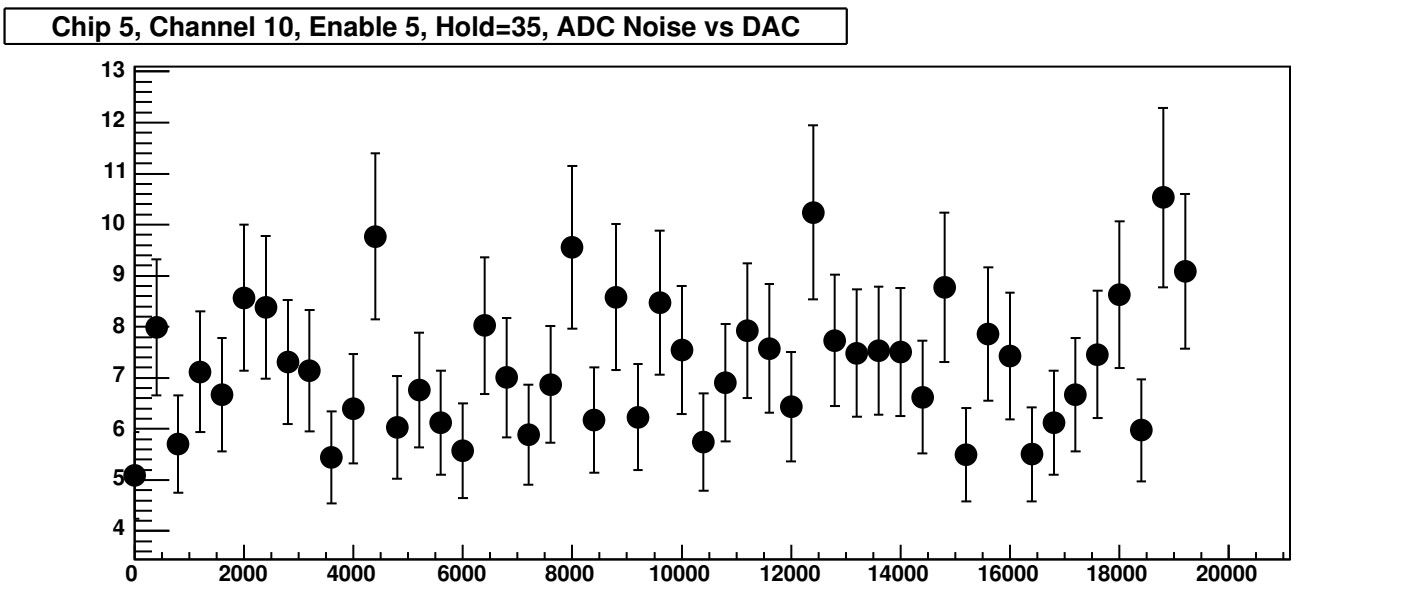
Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

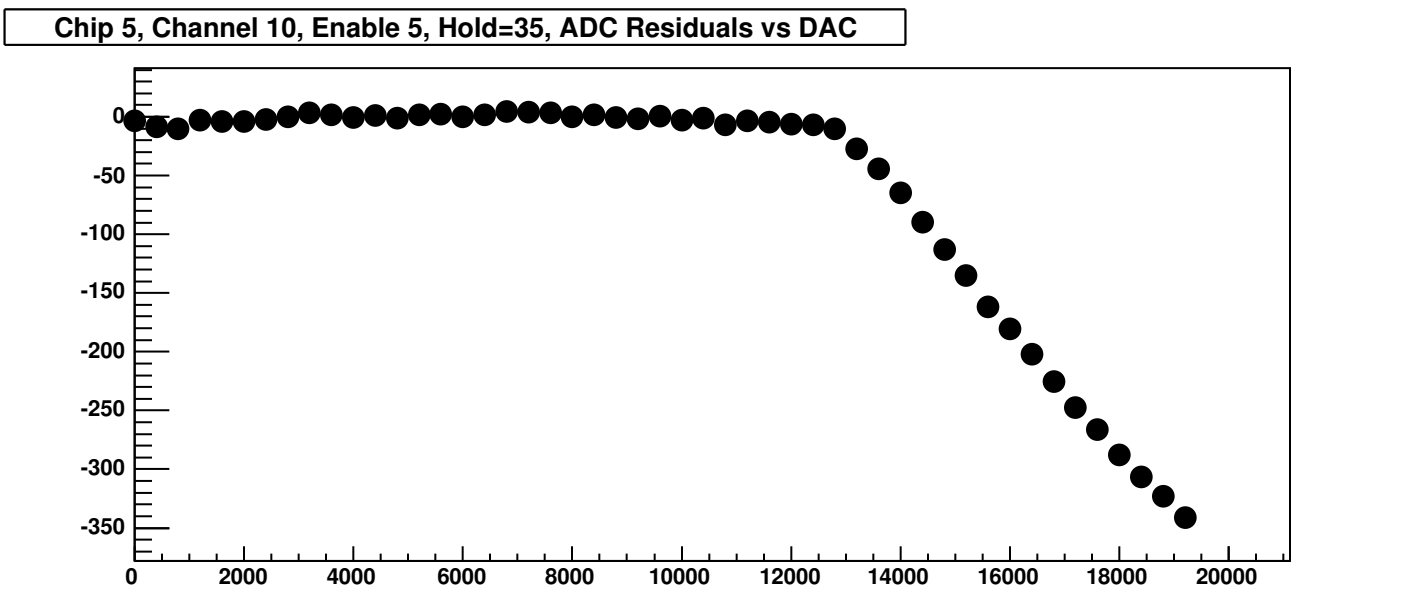


Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



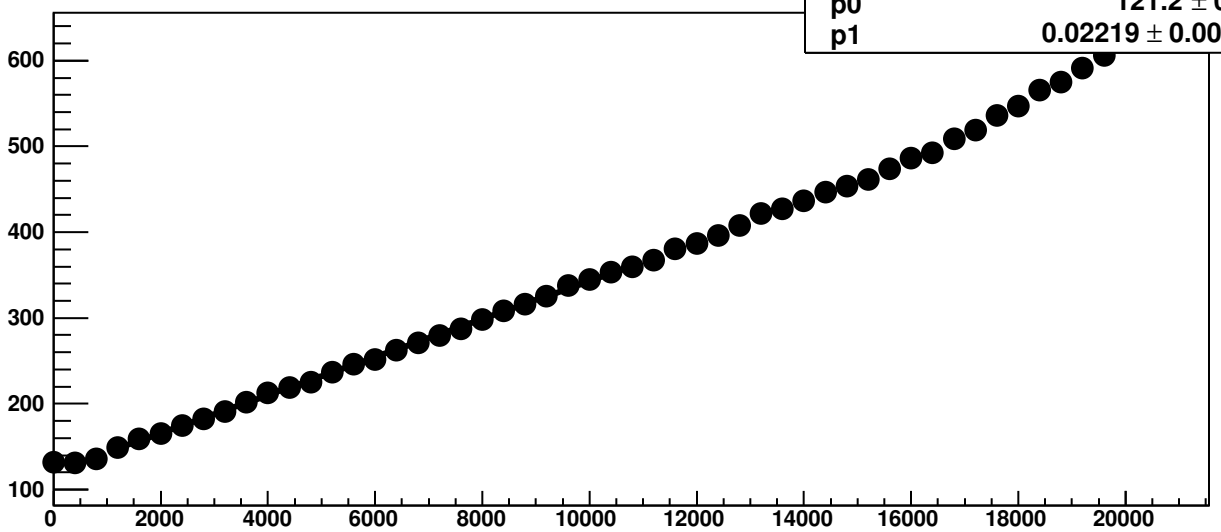

Chip 5, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 72.87 / 23
p0 263.6 ± 0.7458
p1 0.01241 ± 0.0001117
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

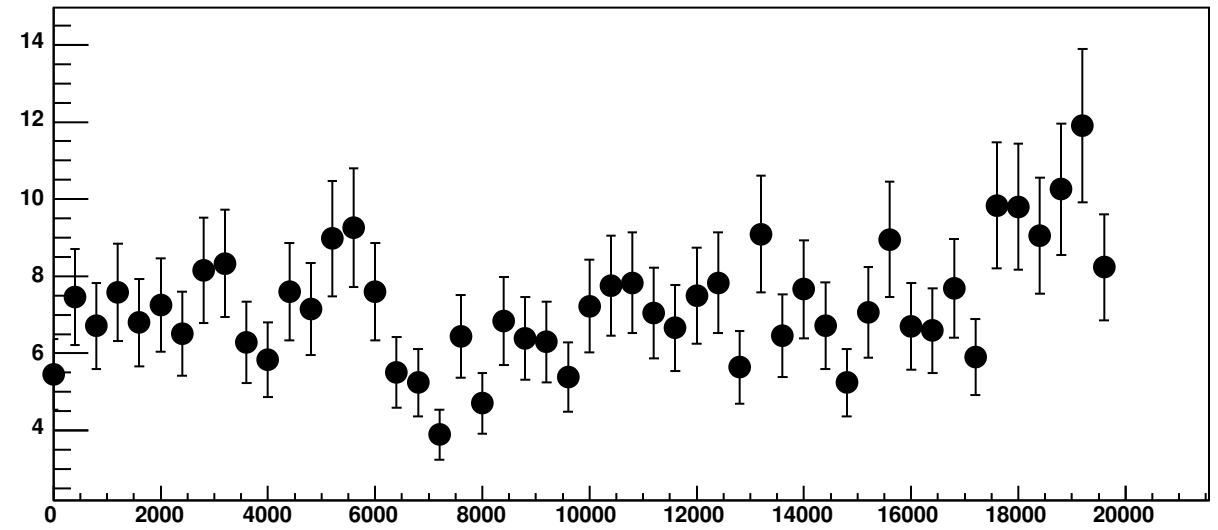

Chip 5, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

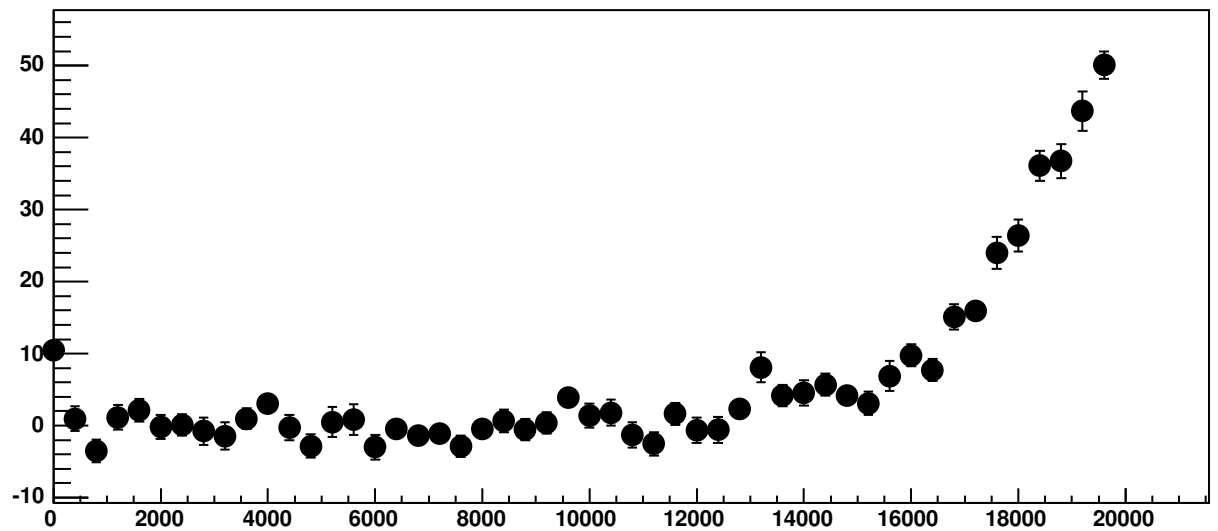
χ^2 / ndf	34.44 / 23
p0	121.2 ± 0.7526
p1	0.02219 ± 0.0001097

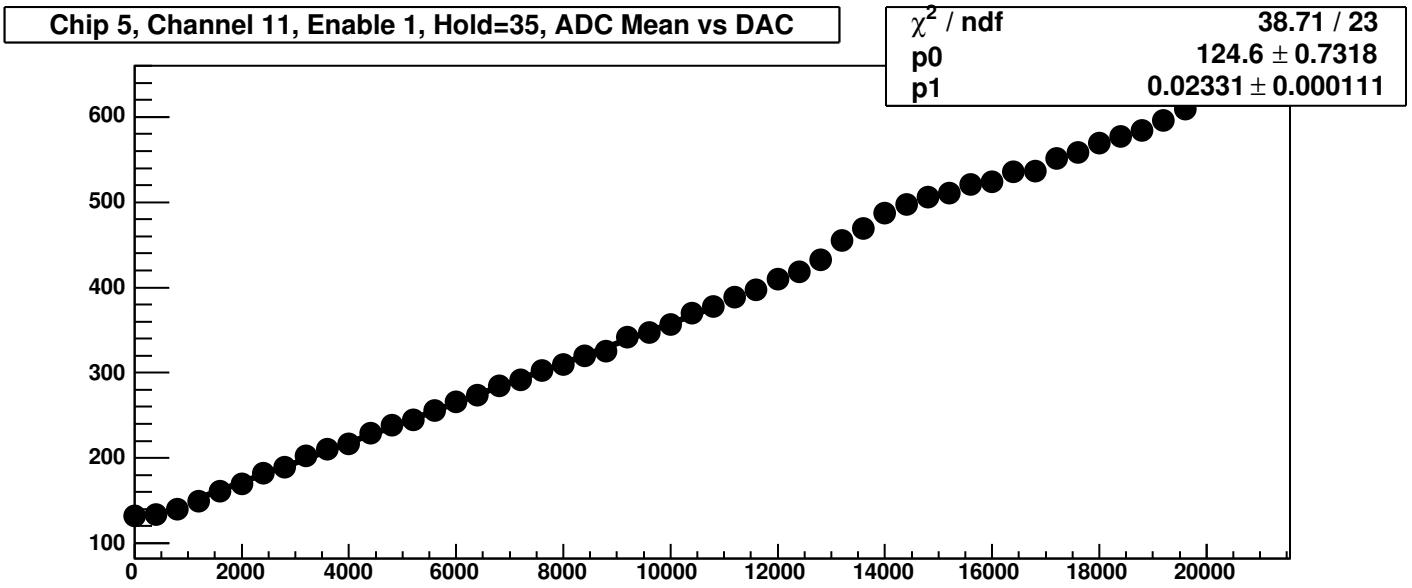


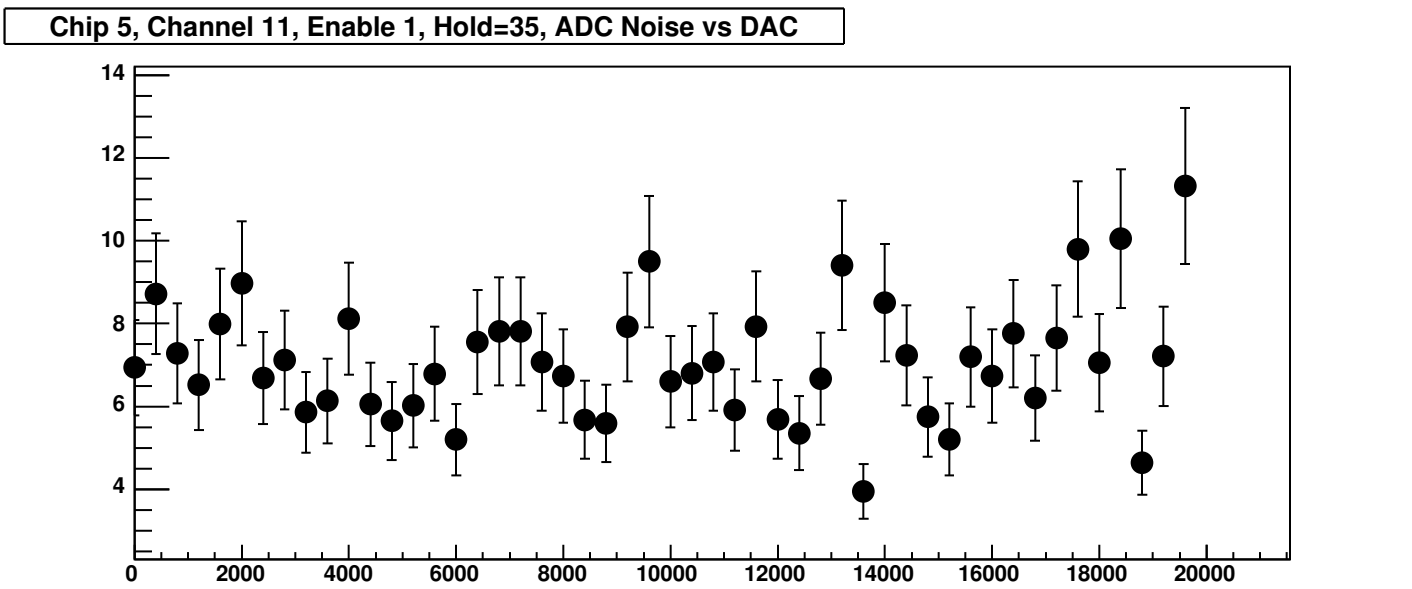
Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC

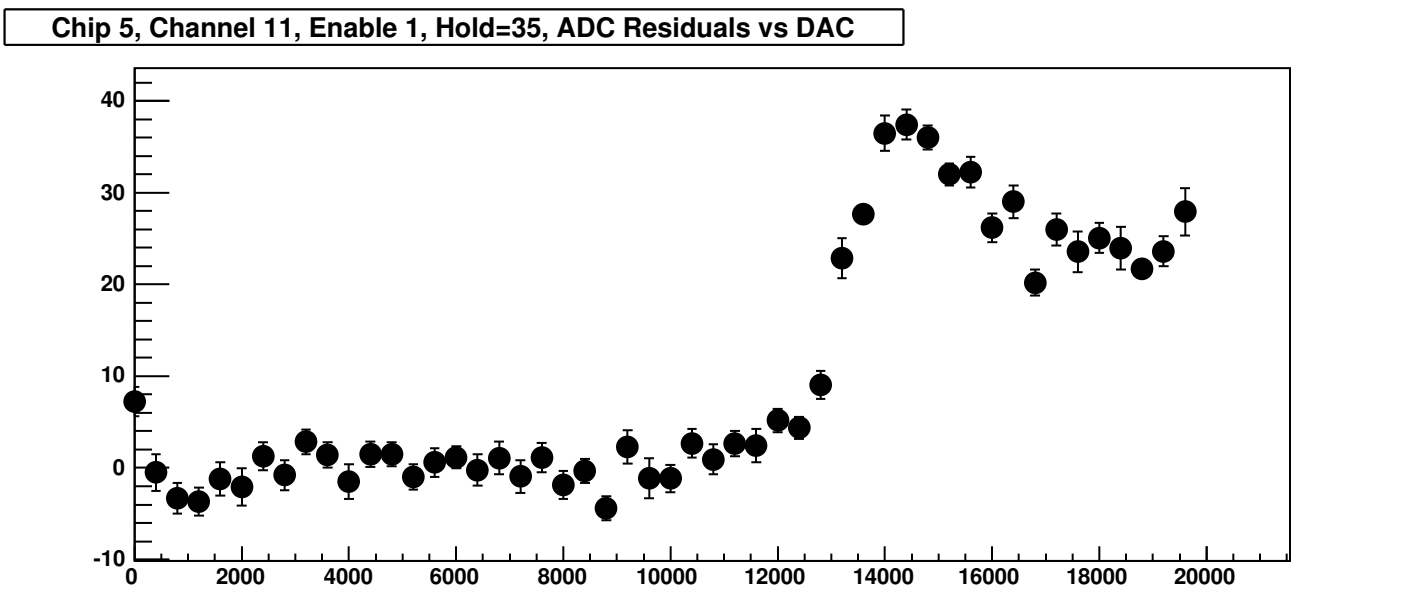


Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

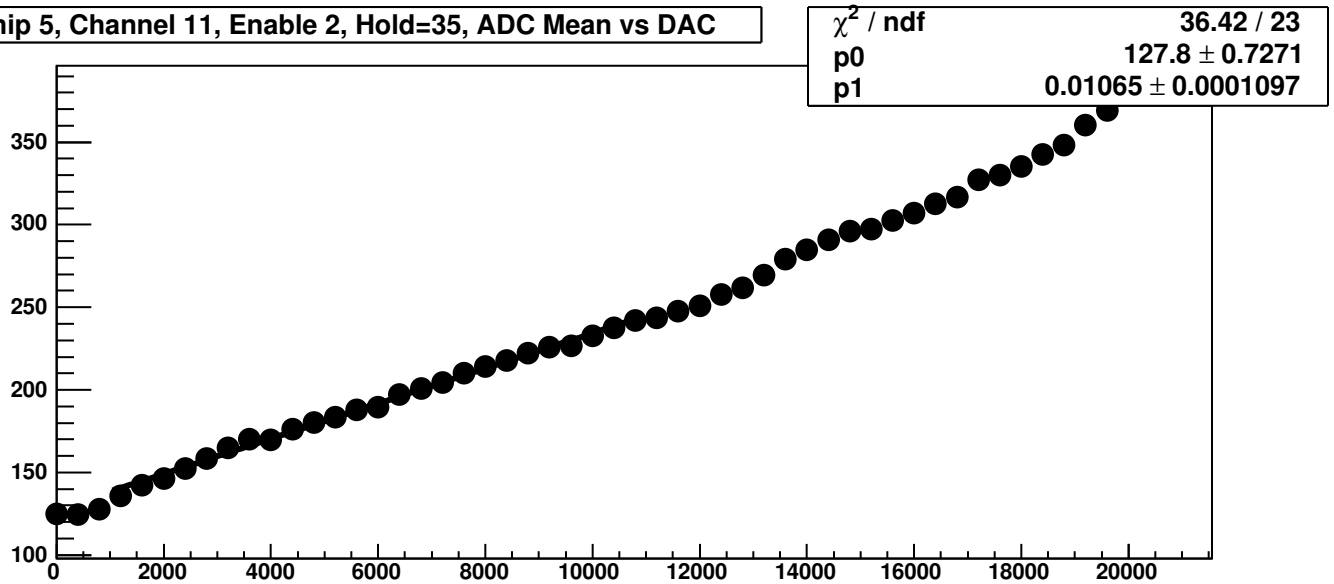



Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.71 / 23
p0 124.6 ± 0.7318
p1 0.02331 ± 0.000111
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

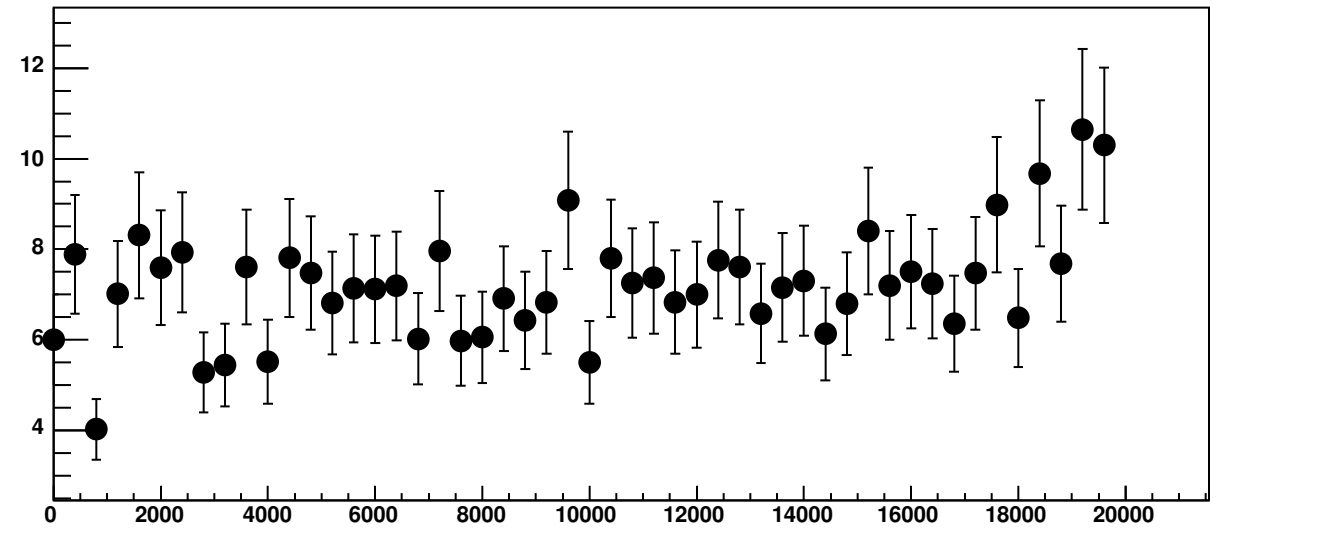

Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

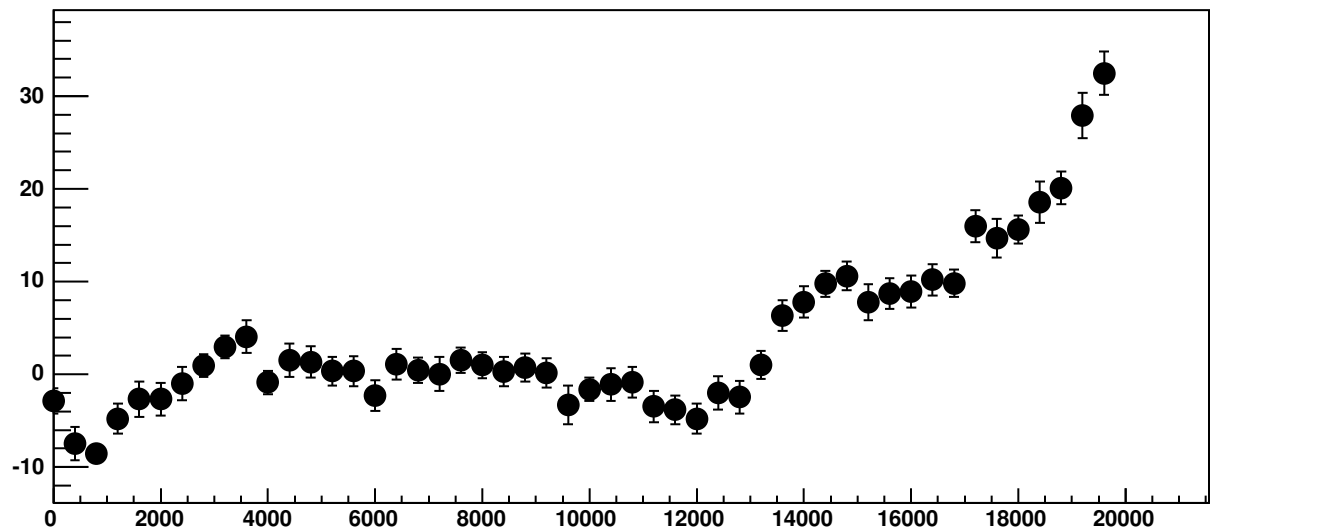
Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

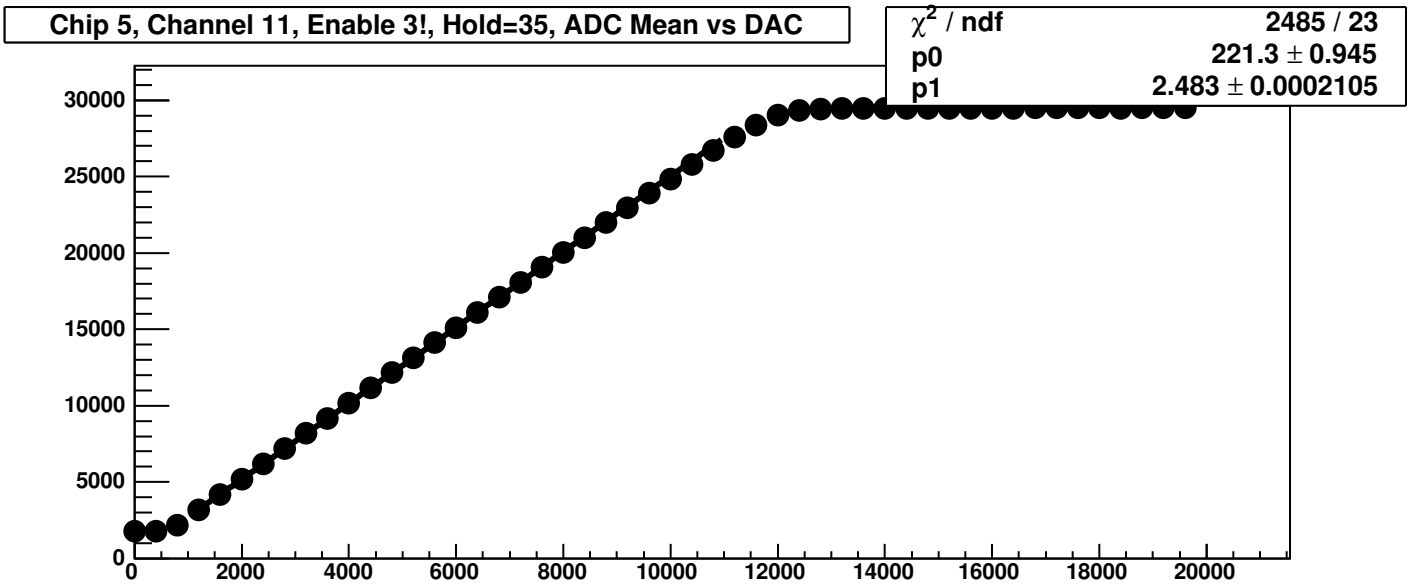


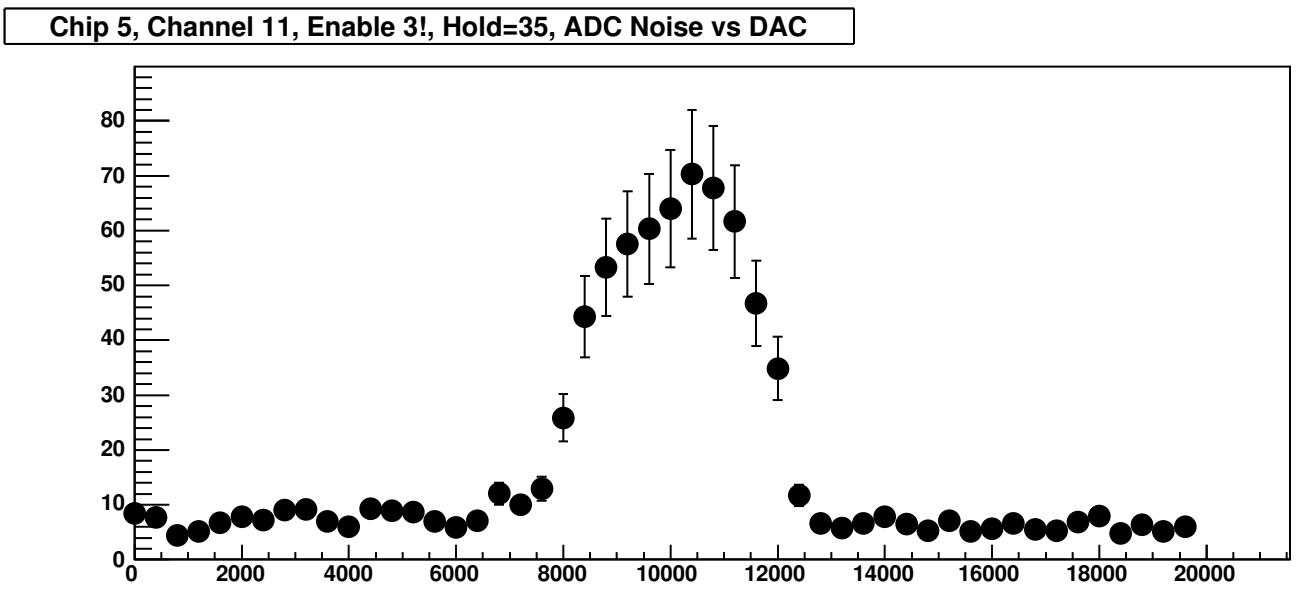
Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

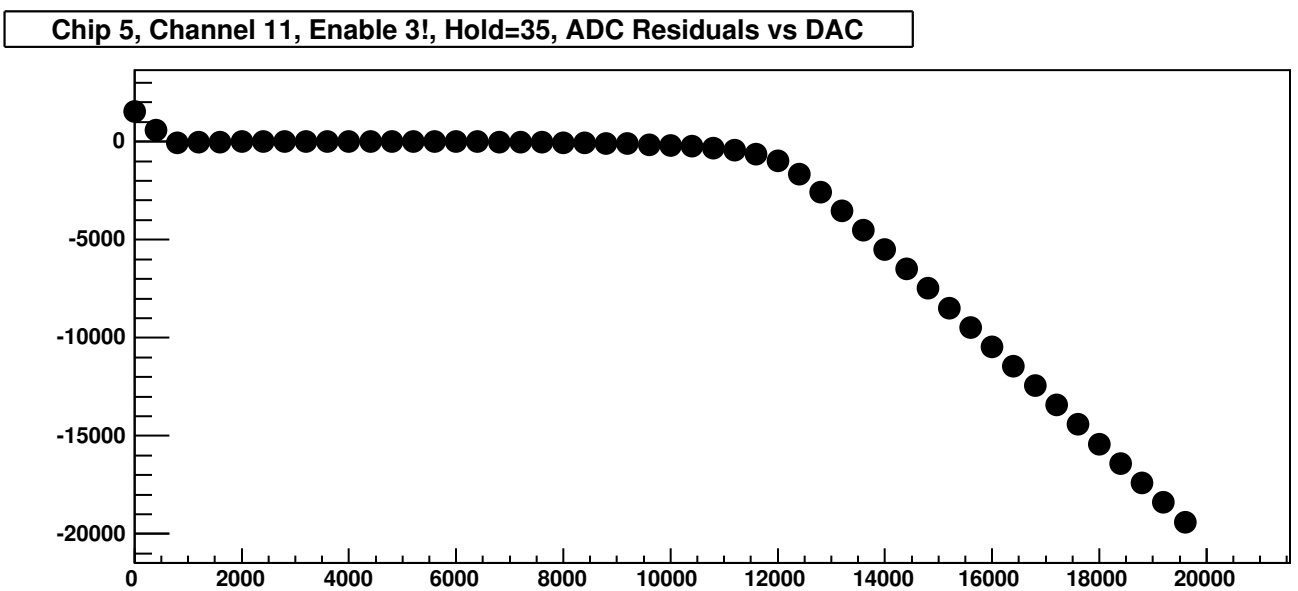


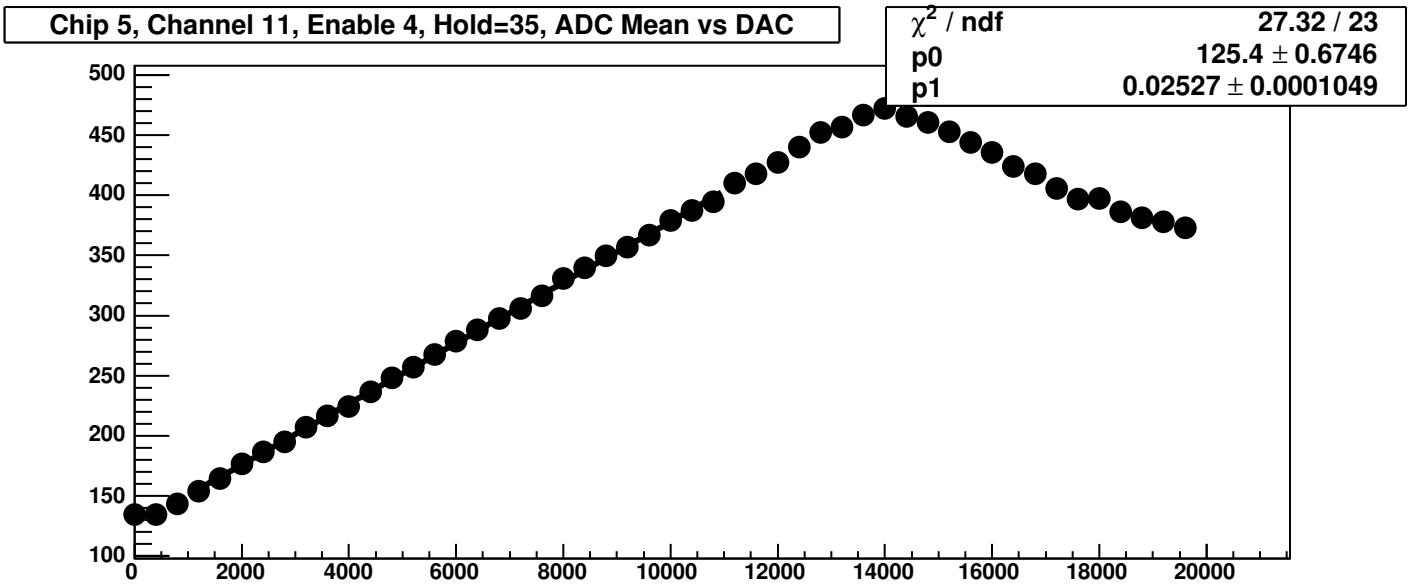
Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

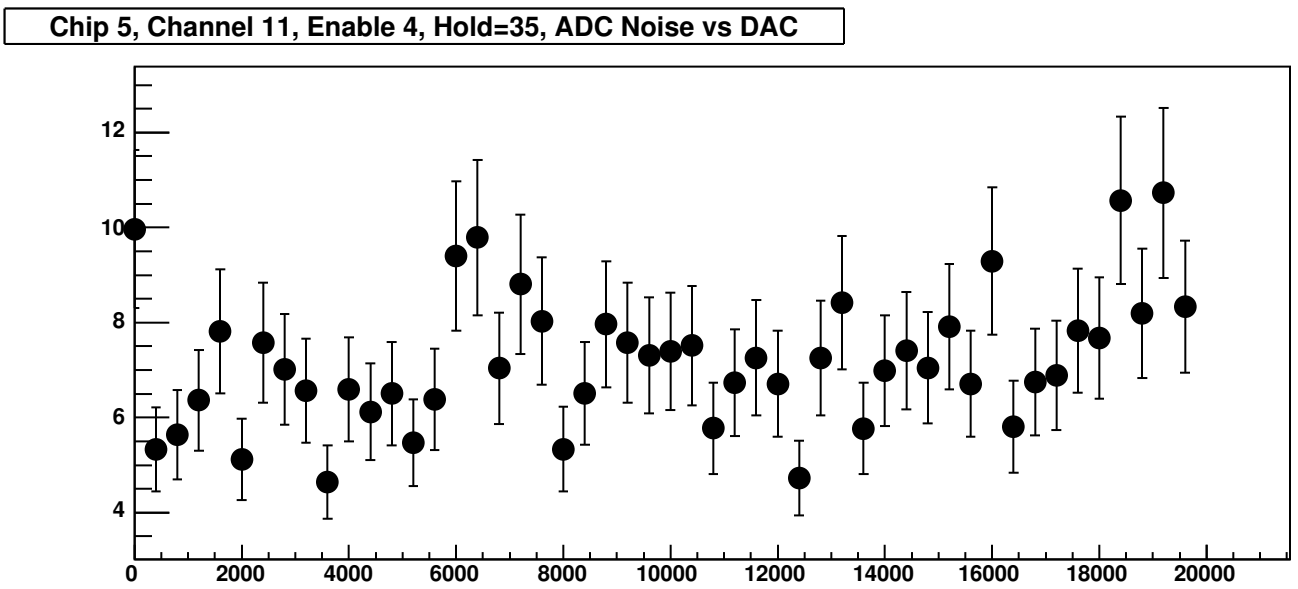


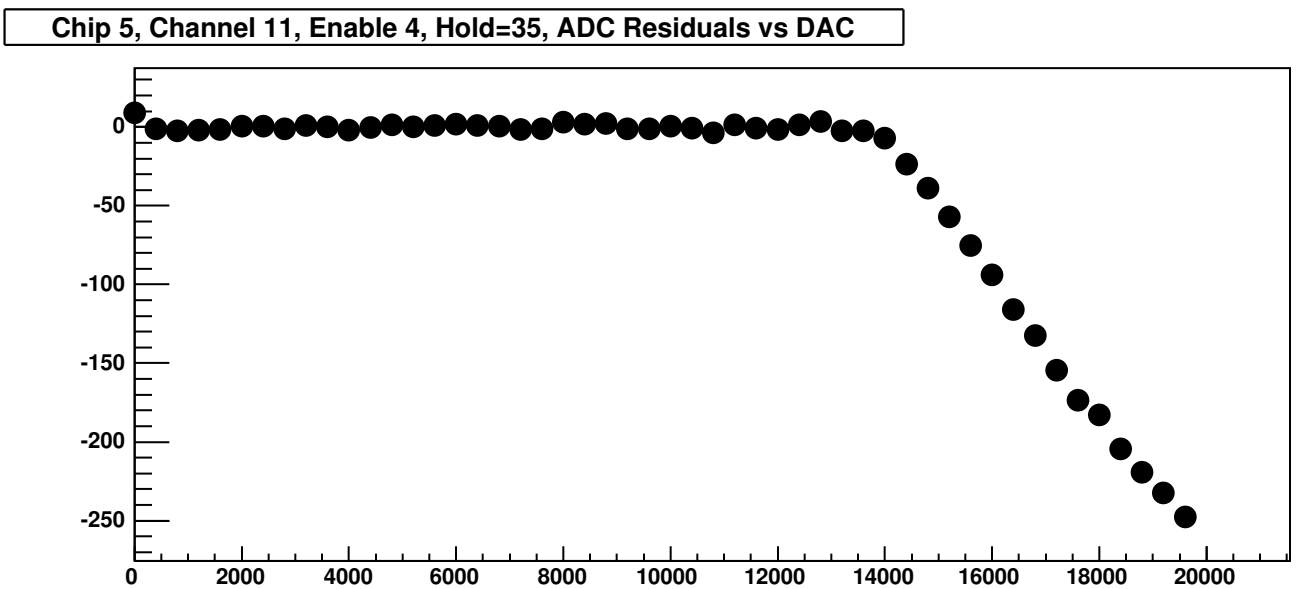

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 2485 / 23
p0 221.3 ± 0.945
p1 2.483 ± 0.0002105
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

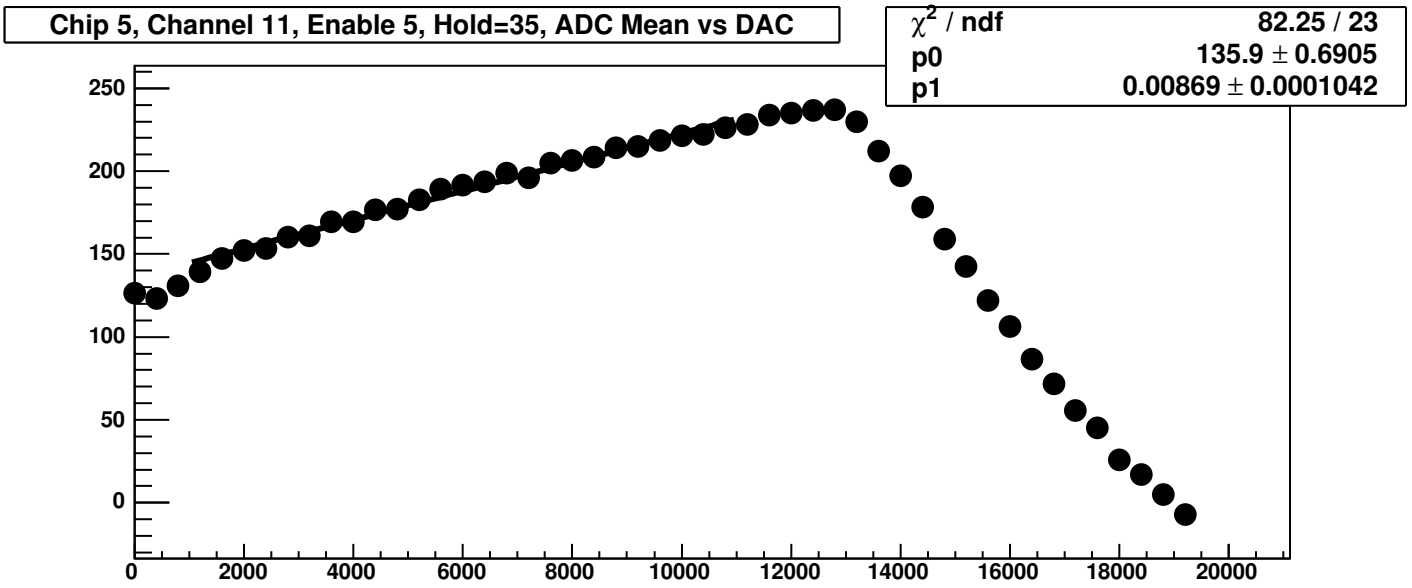

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

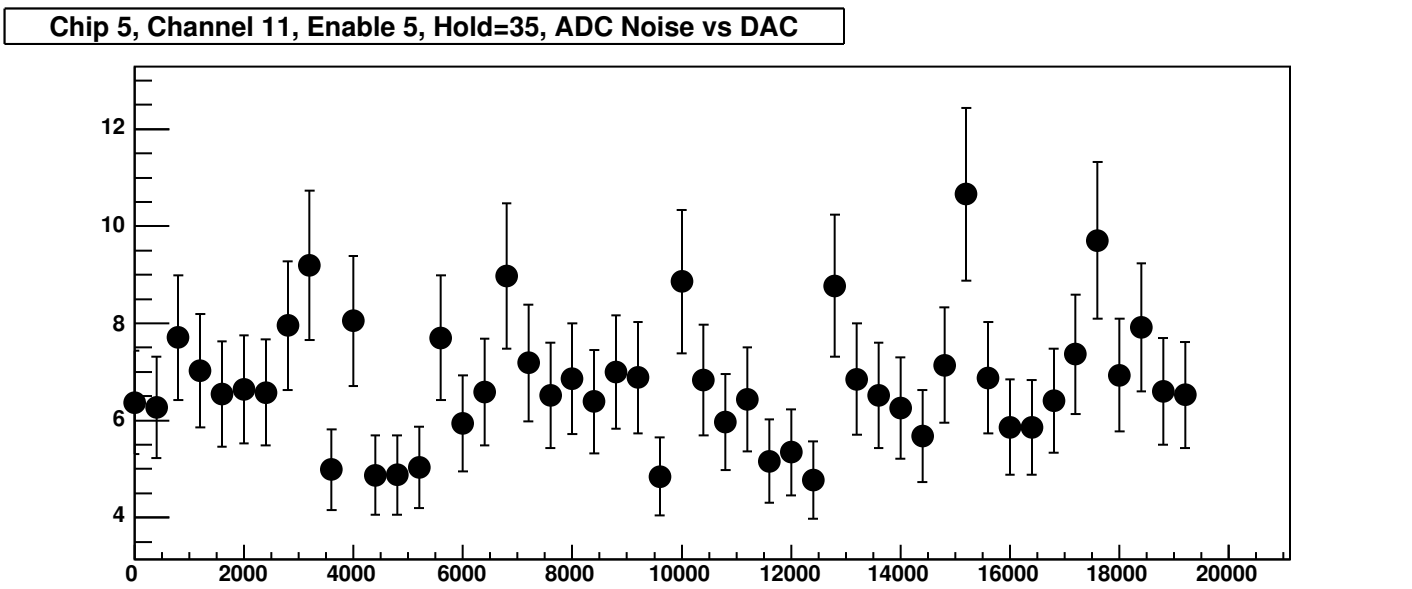

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

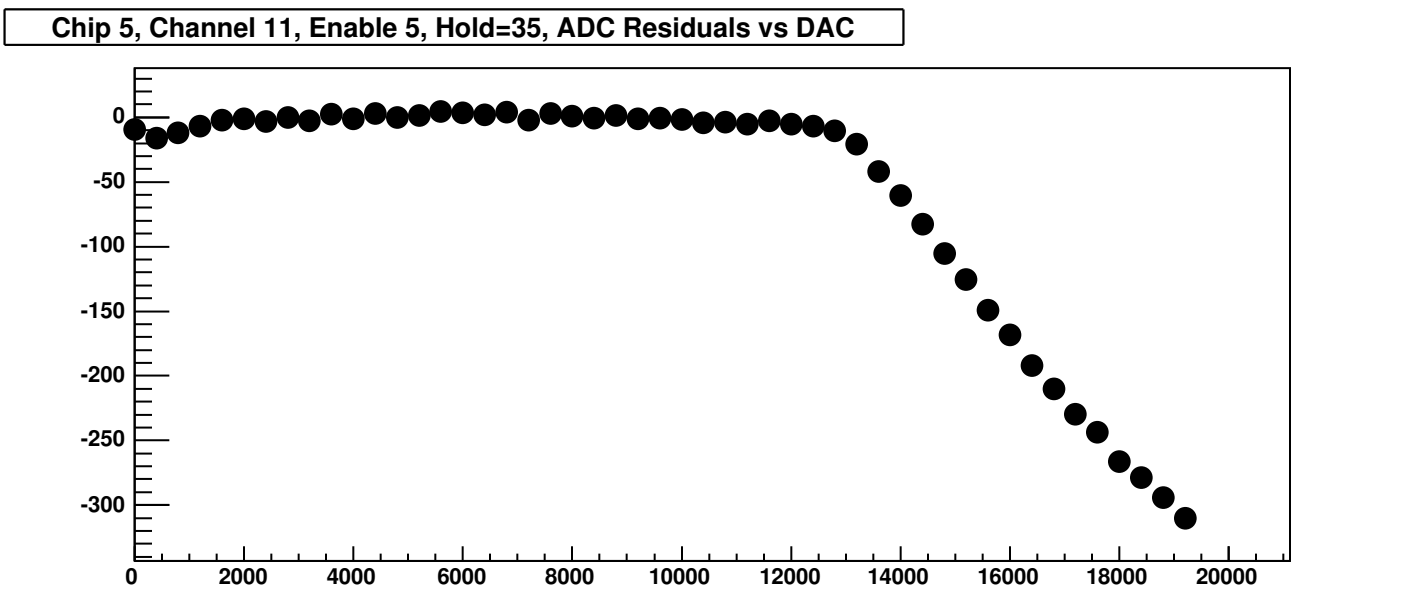

Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.32 / 23
p0 125.4 ± 0.6746
p1 0.02527 ± 0.0001049
500
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

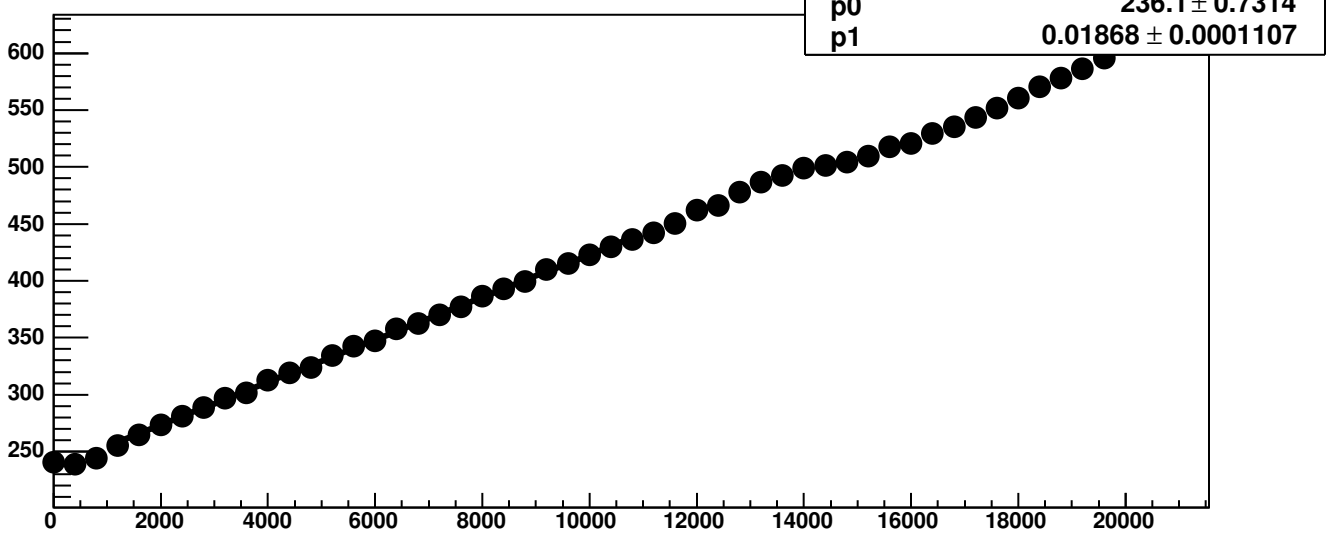

Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 82.25 / 23
p0 135.9 ± 0.6905
p1 0.00869 ± 0.0001042
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

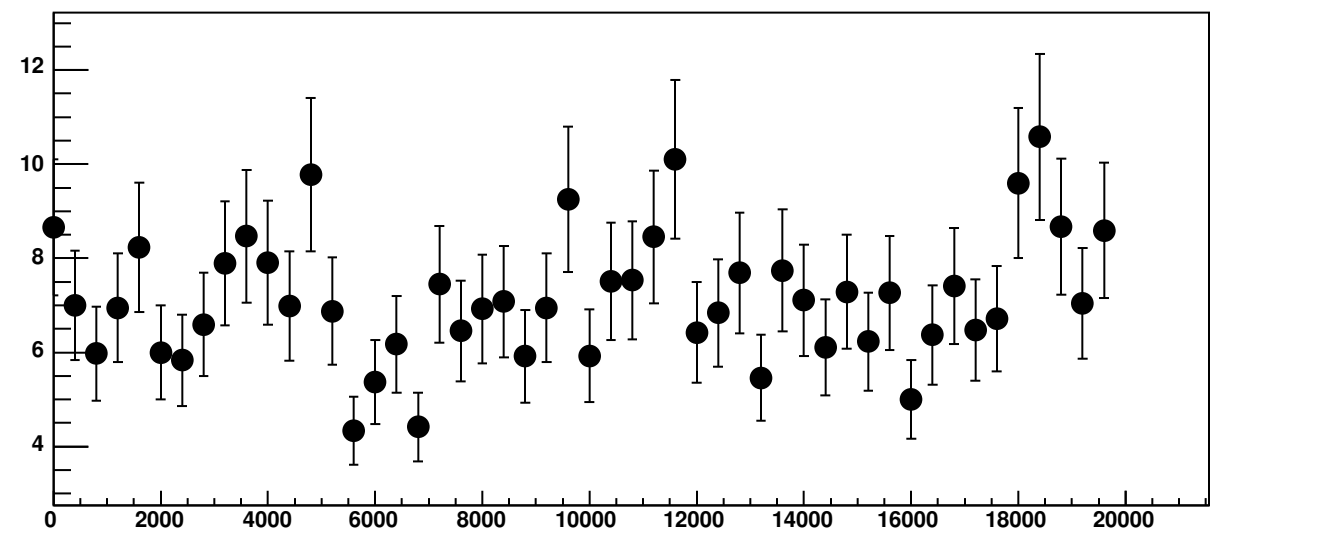

Chip 5, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

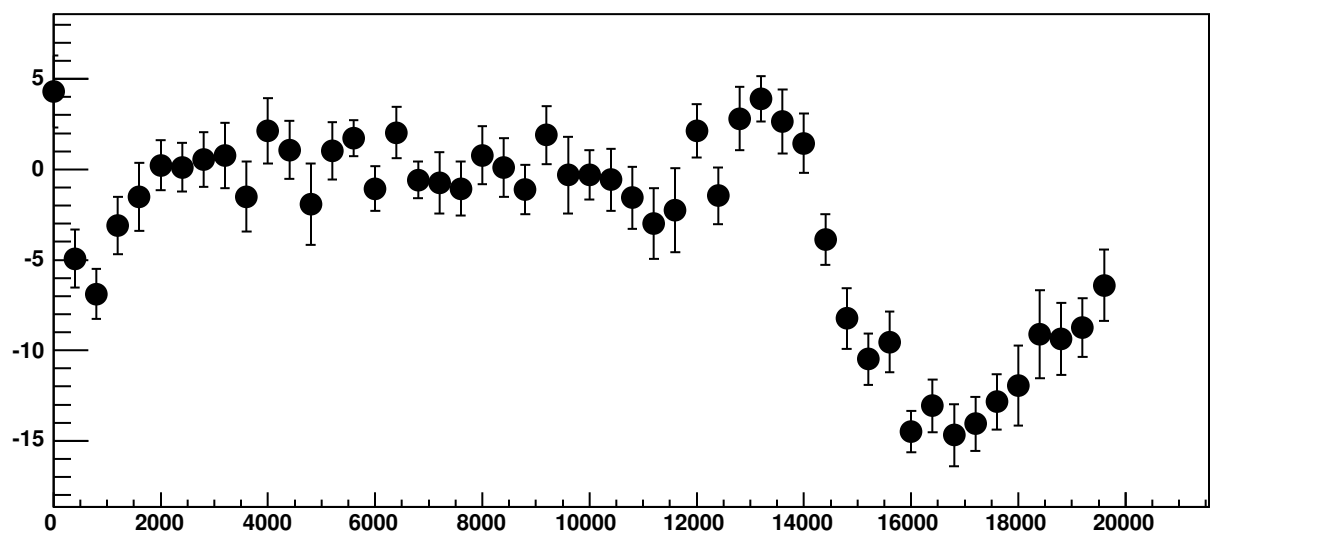
Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

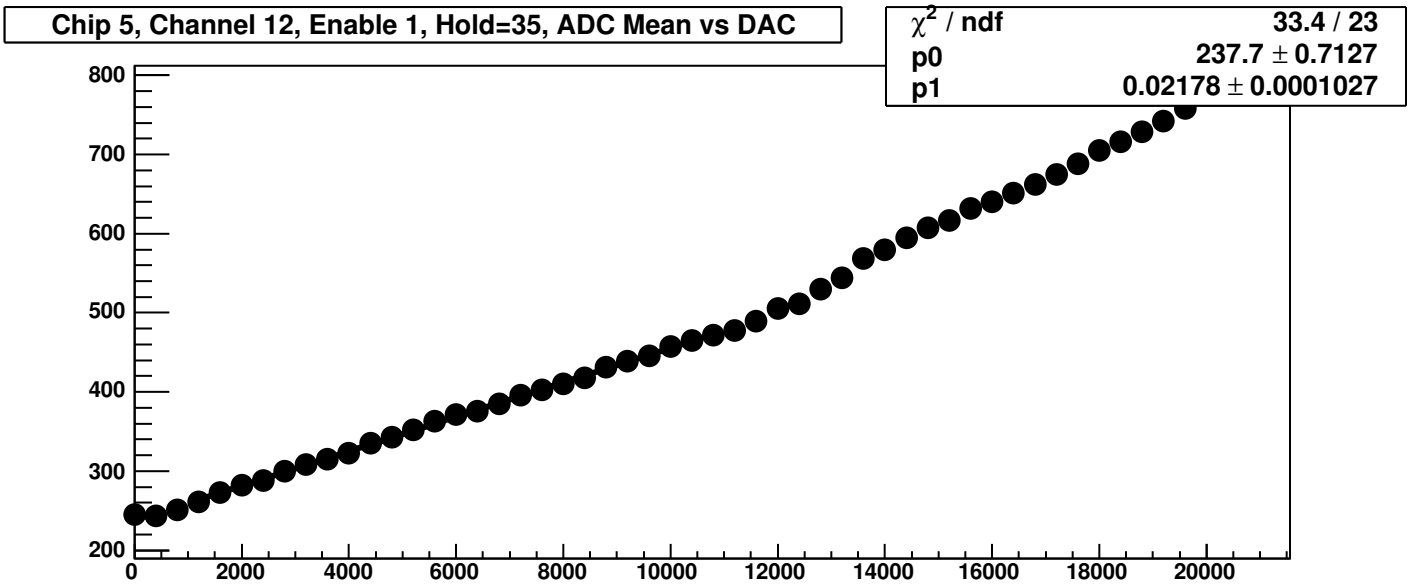


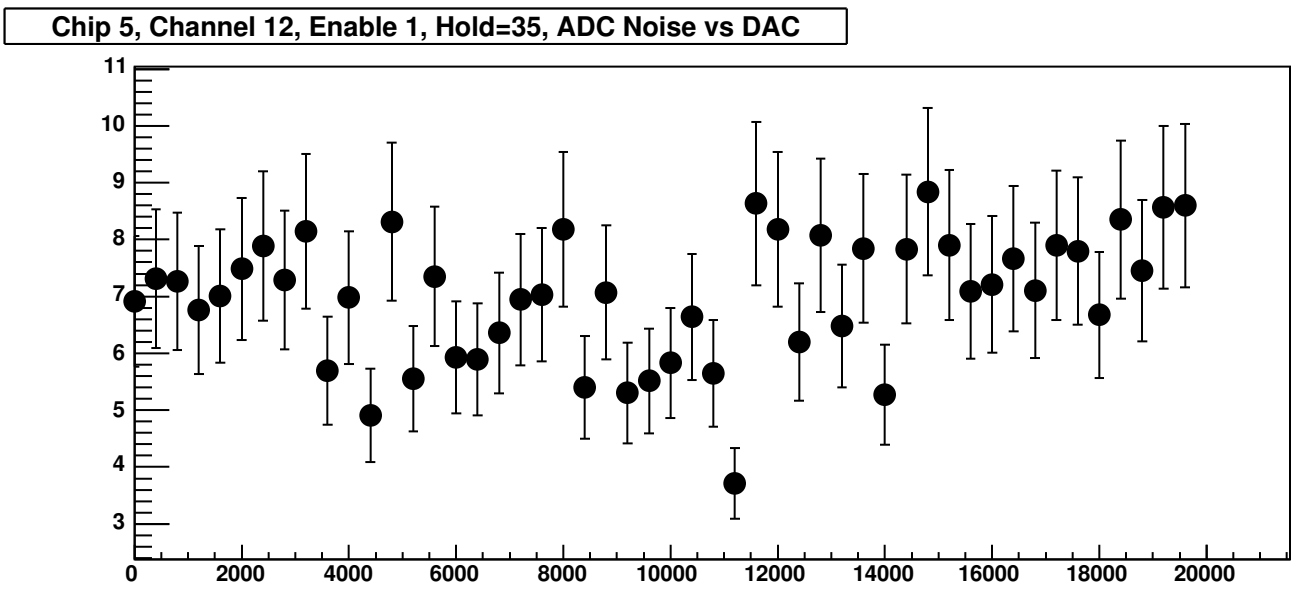
Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

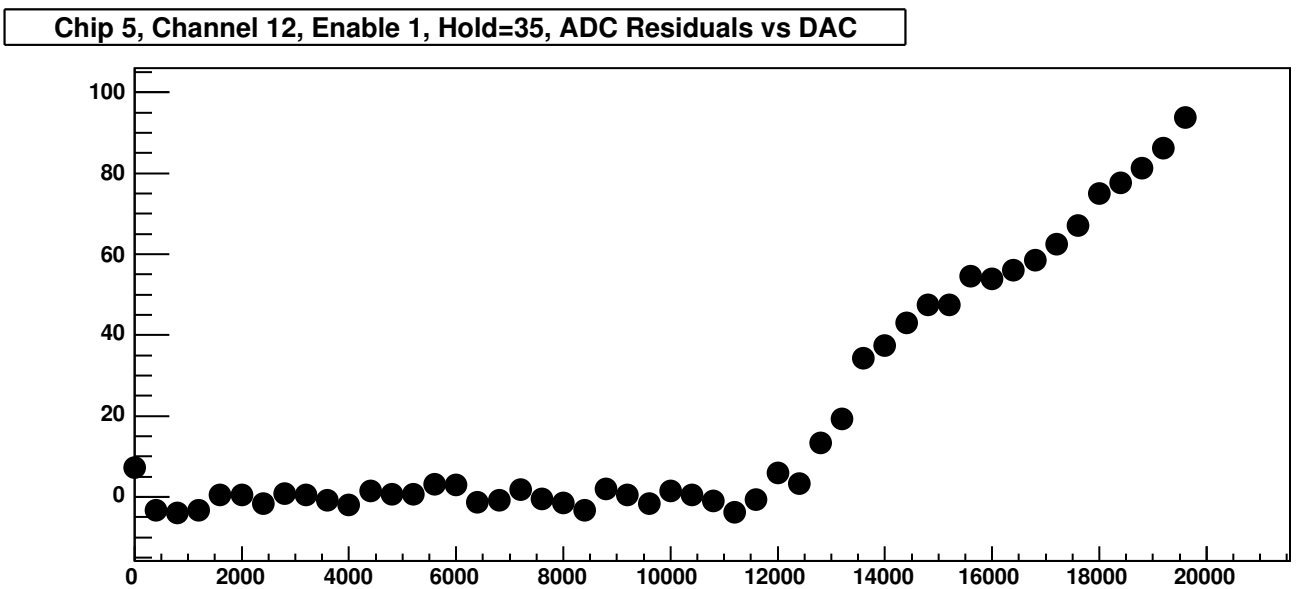


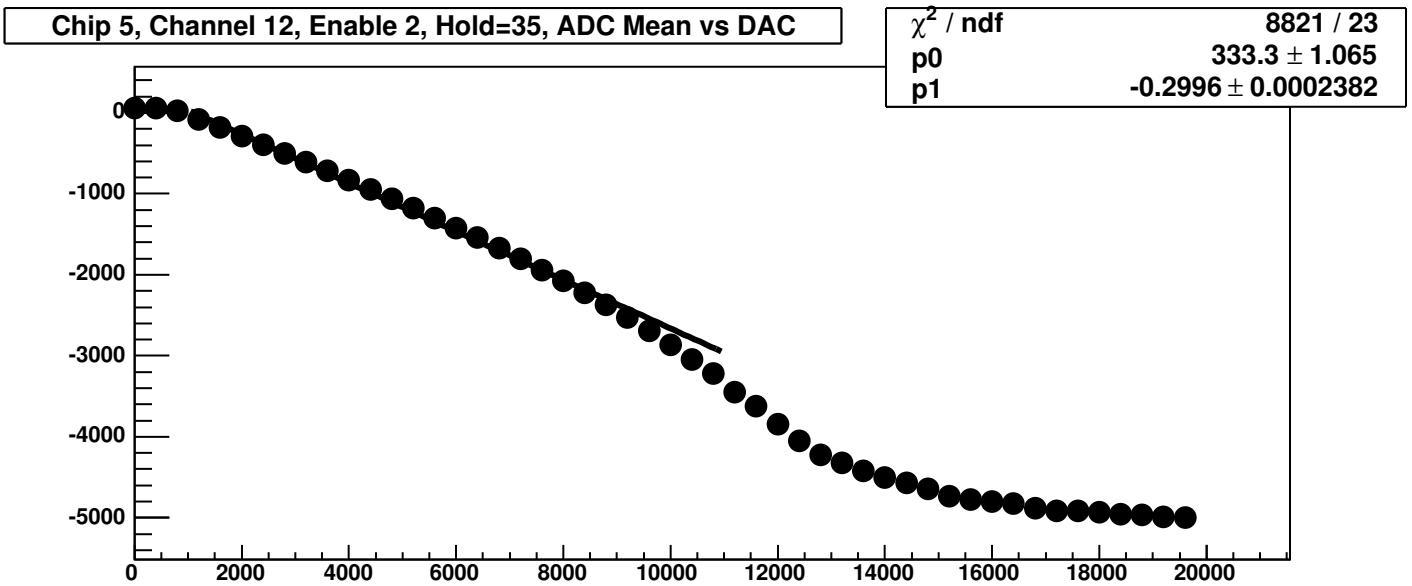
Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

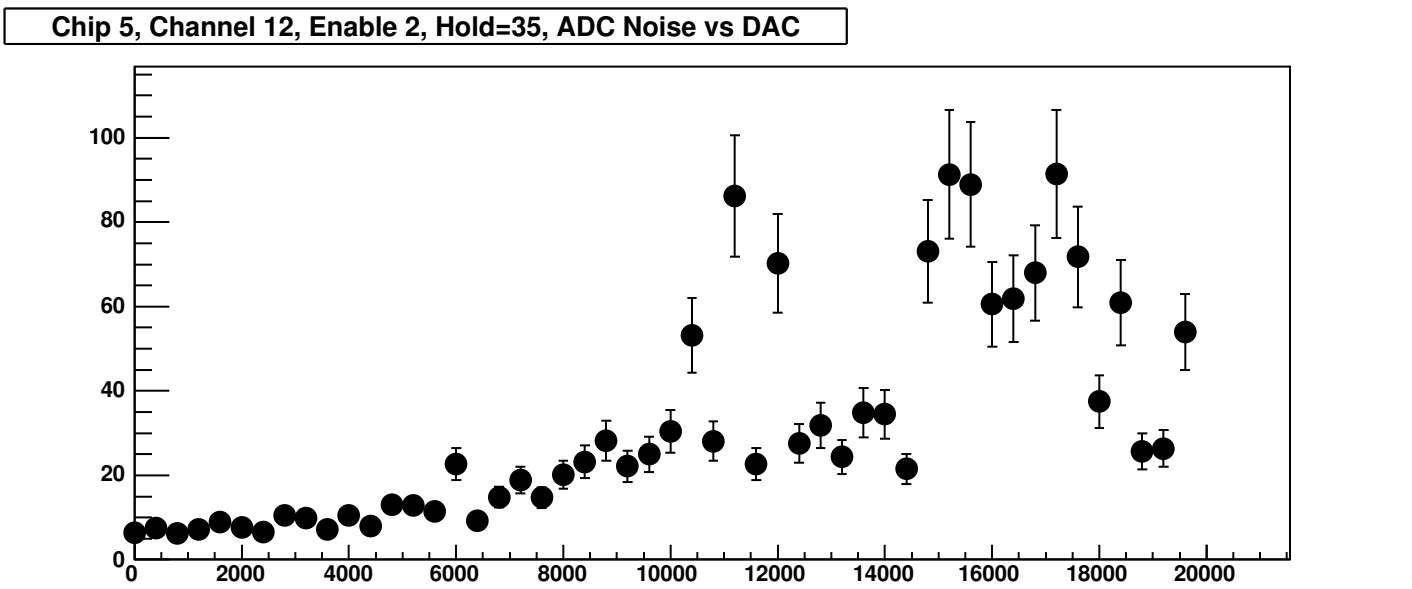


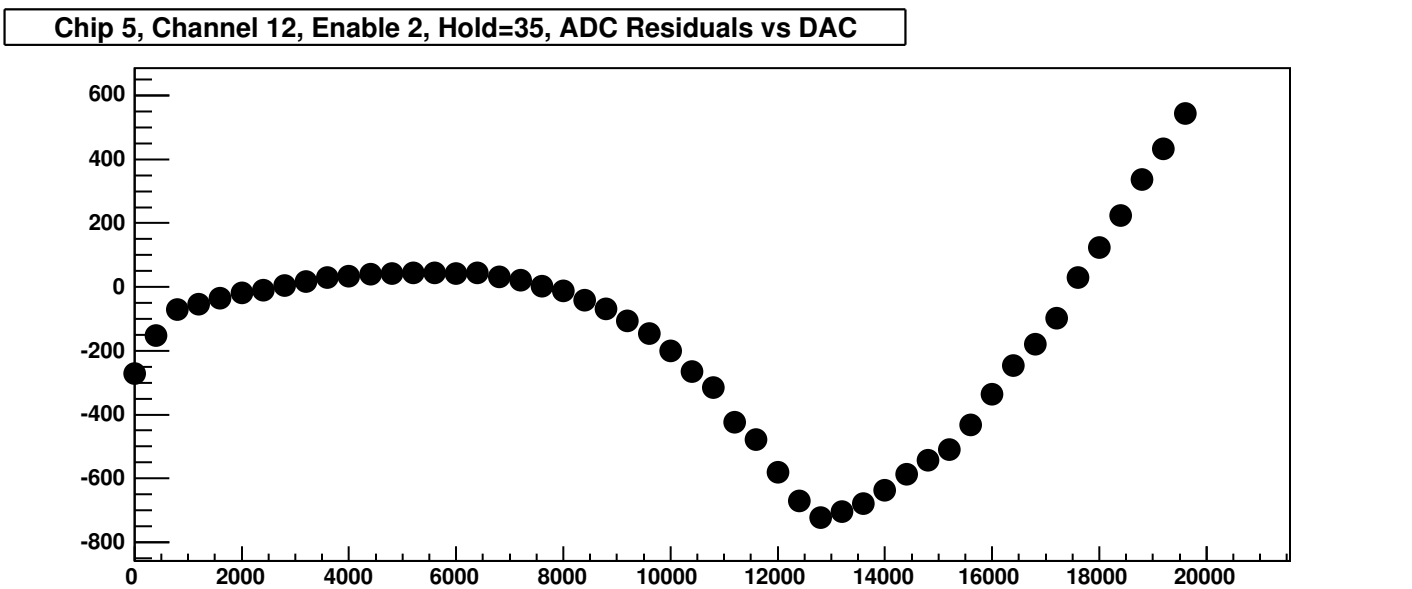

Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.4 / 23
p0 237.7 ± 0.7127
p1 0.02178 ± 0.0001027
800
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

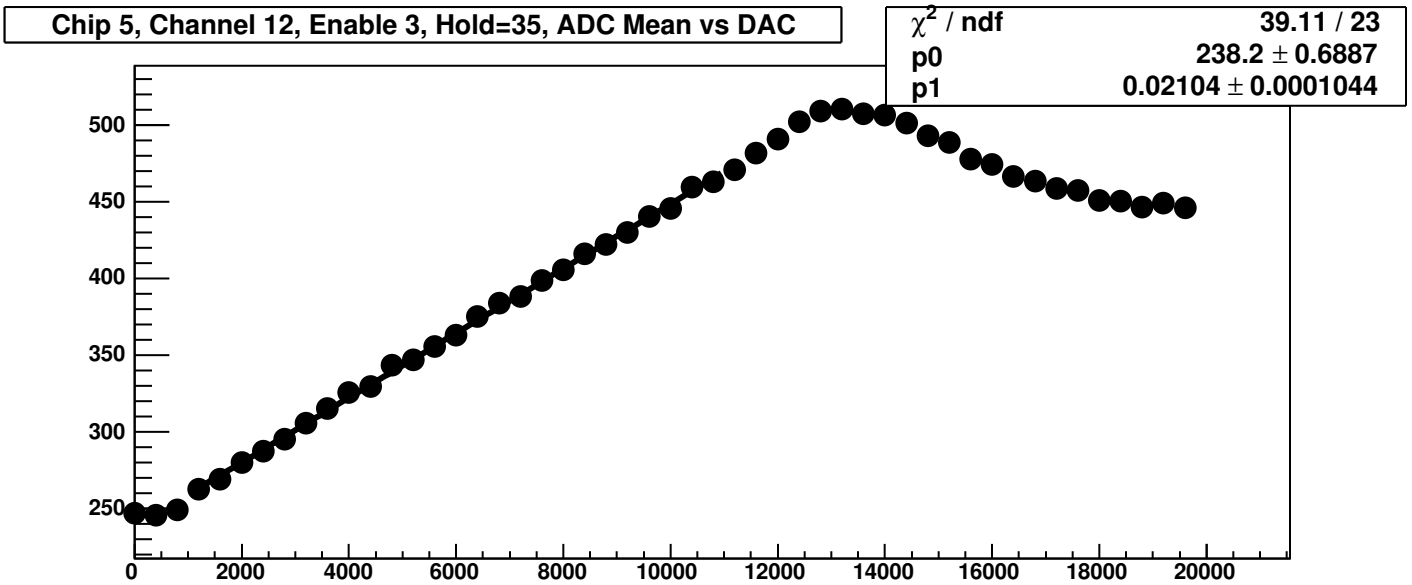

Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

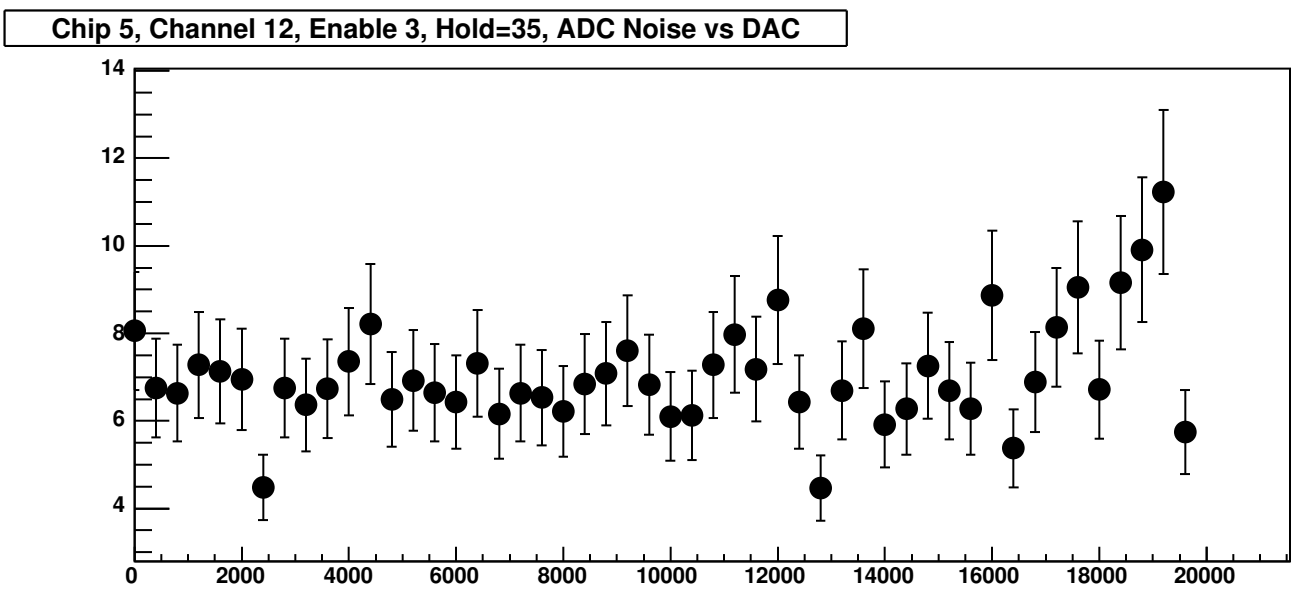

Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

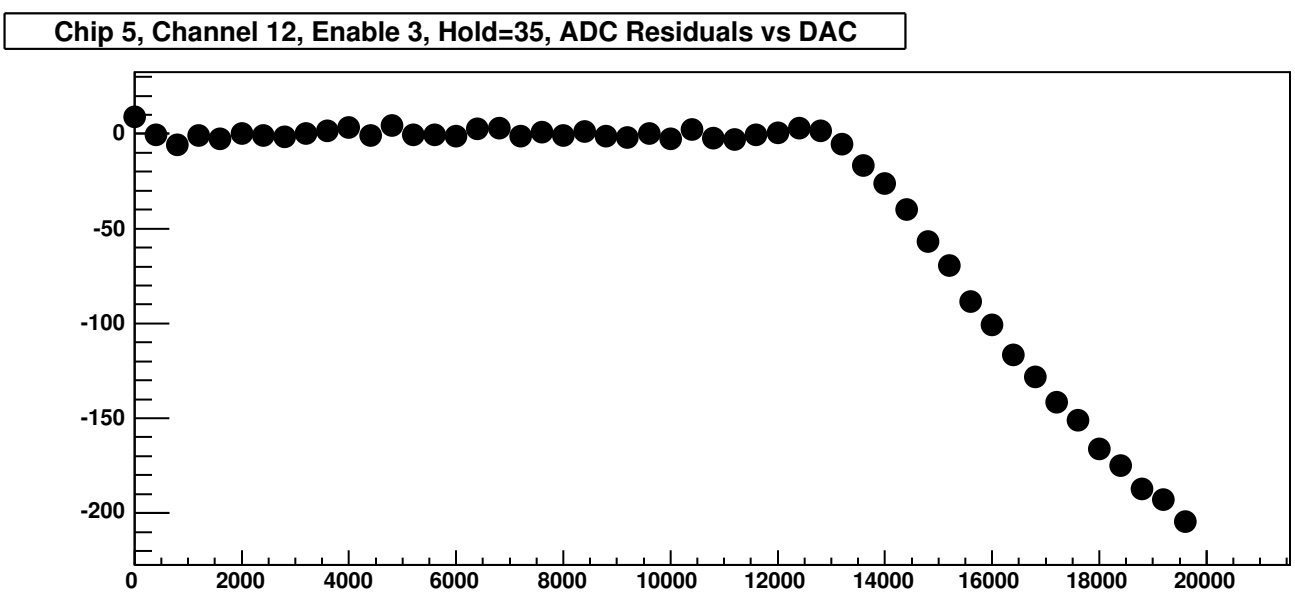

Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 8821 / 23
p0 333.3 ± 1.065
p1 -0.2996 ± 0.0002382
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

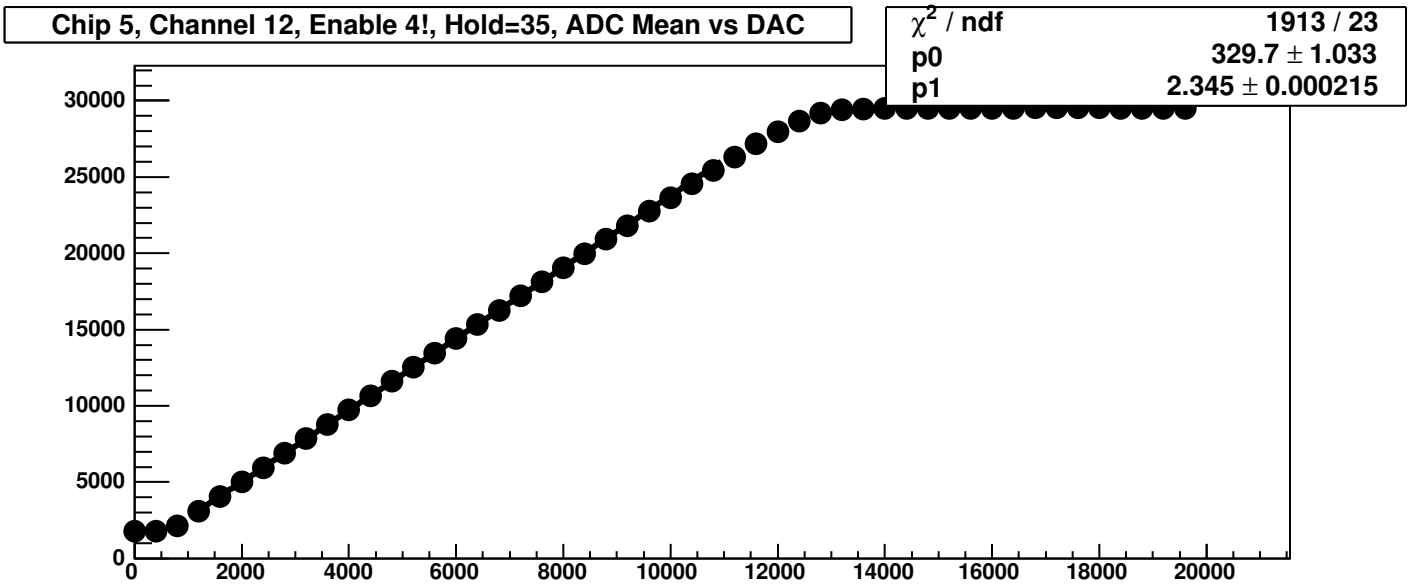

Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

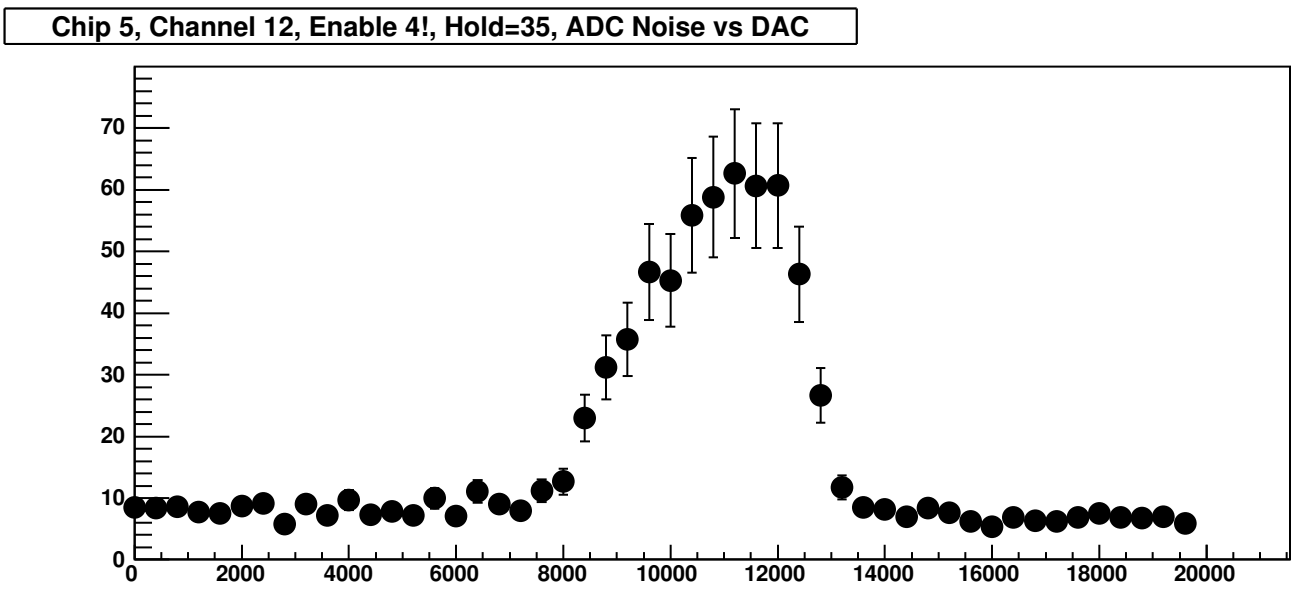

Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

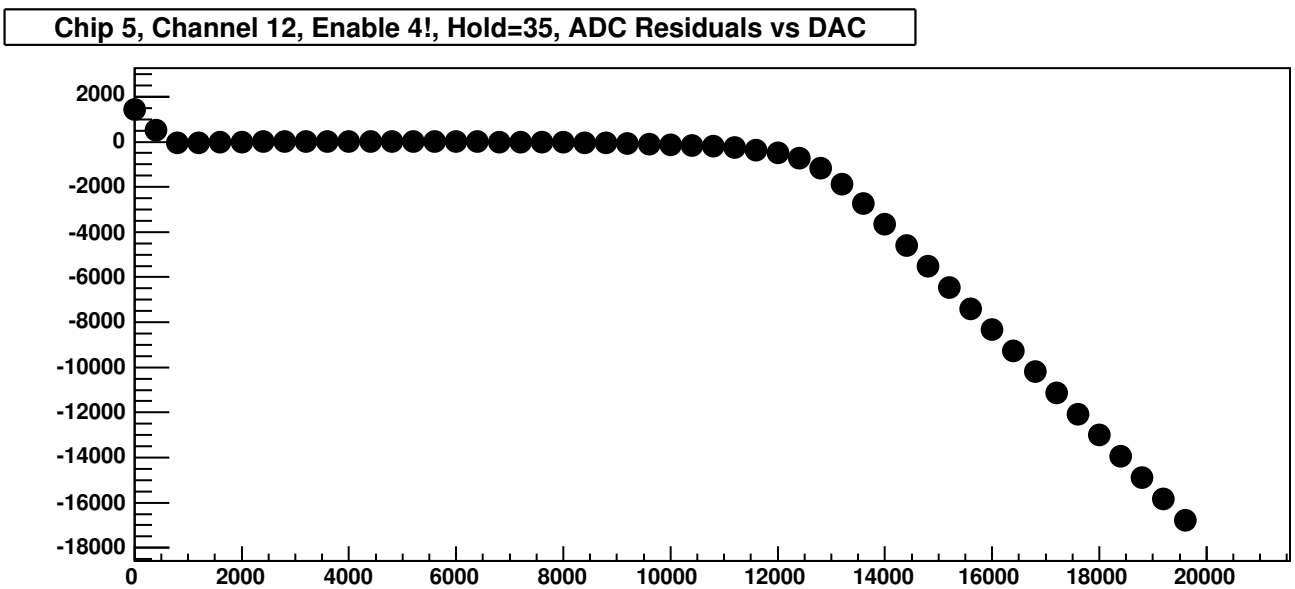

Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.11 / 23
p0 238.2 ± 0.6887
p1 0.02104 ± 0.0001044
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

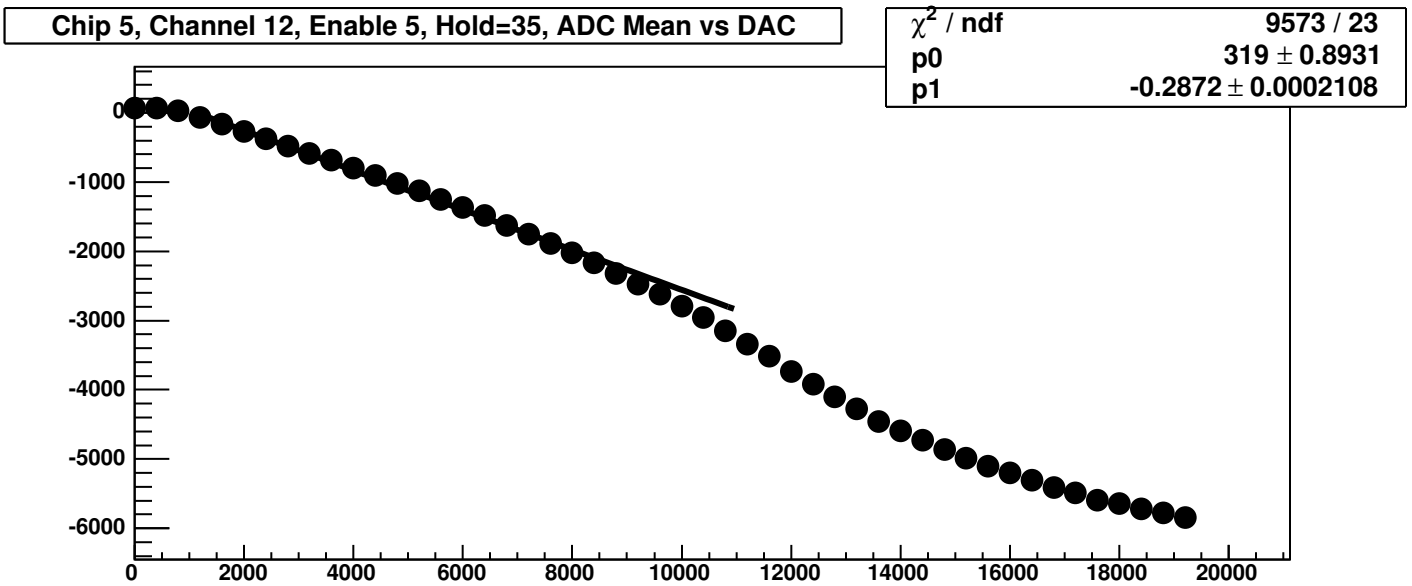

Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

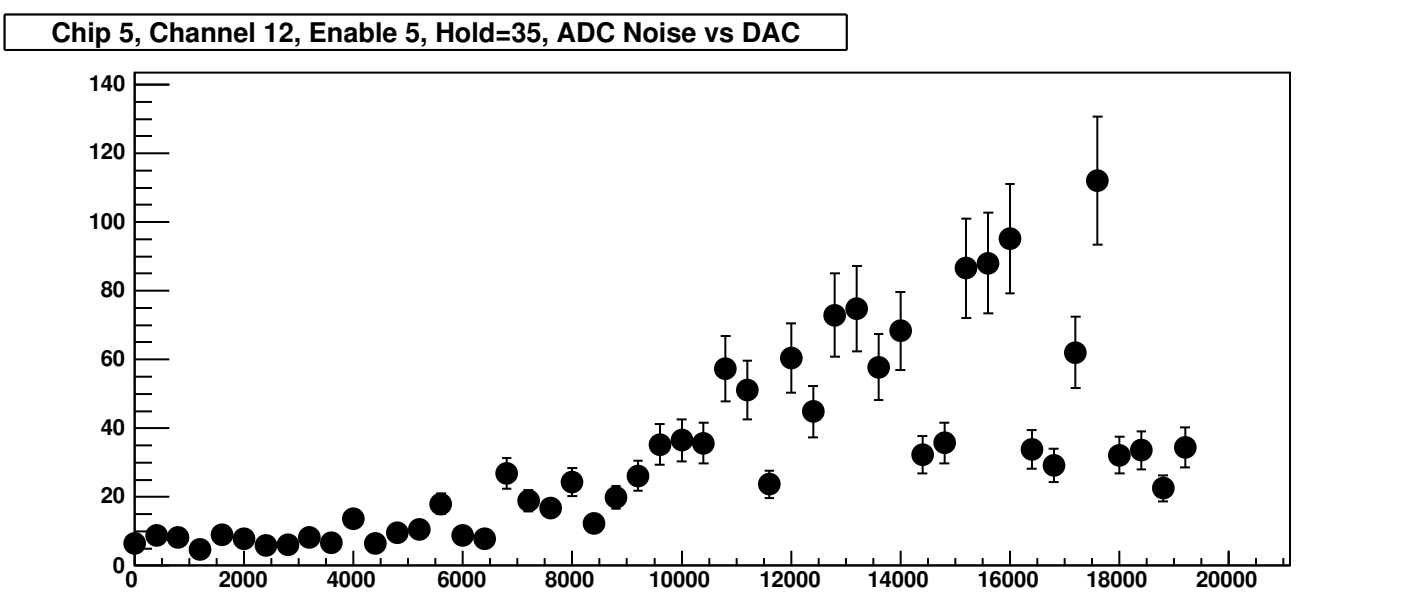

Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

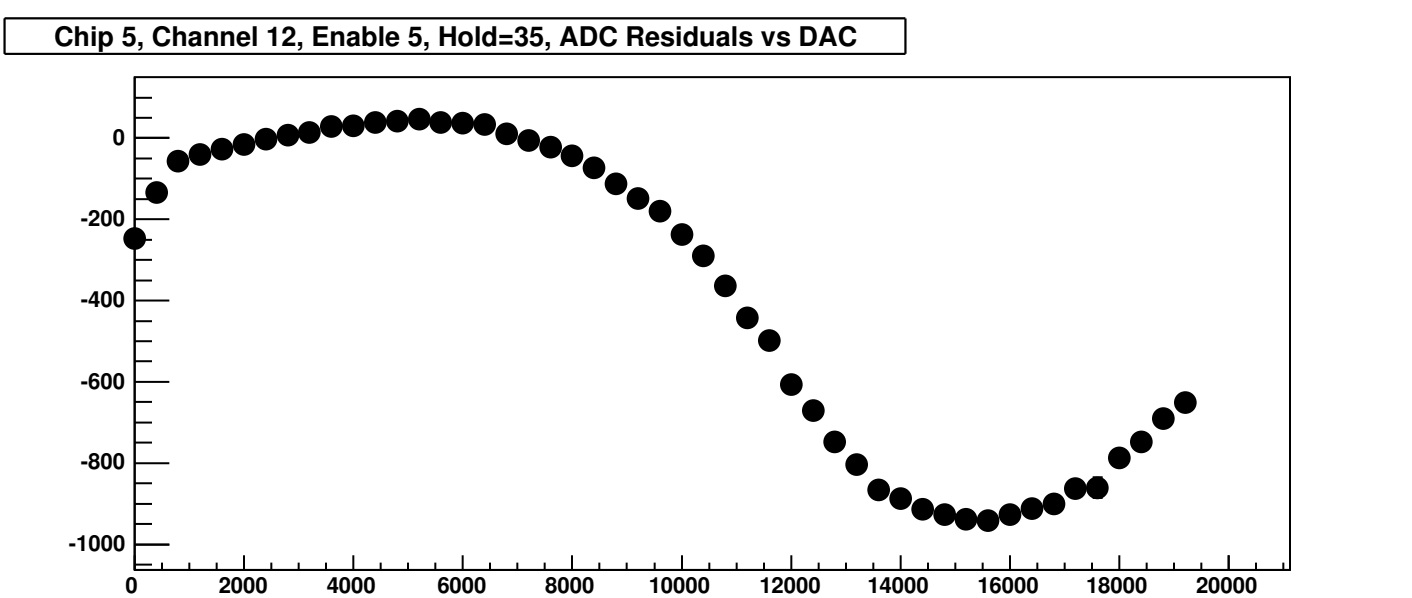

Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1913 / 23
p0 329.7 ± 1.033
p1 2.345 ± 0.000215
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

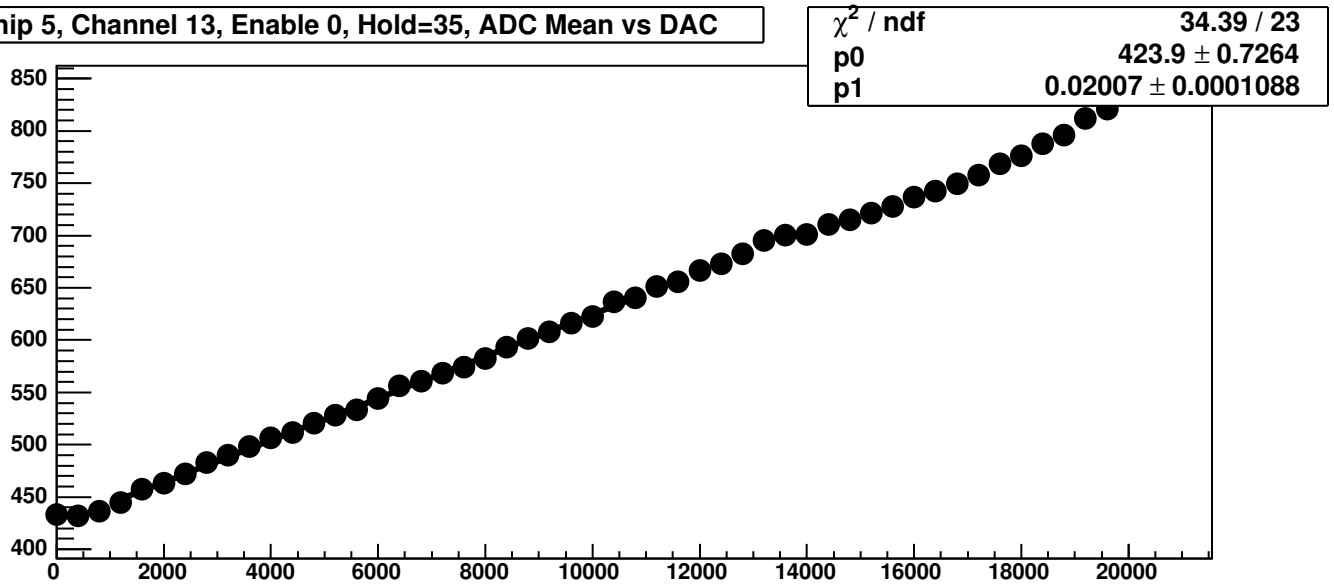

Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
-18000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 9573 / 23
p0 319 ± 0.8931
p1 -0.2872 ± 0.0002108
0
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

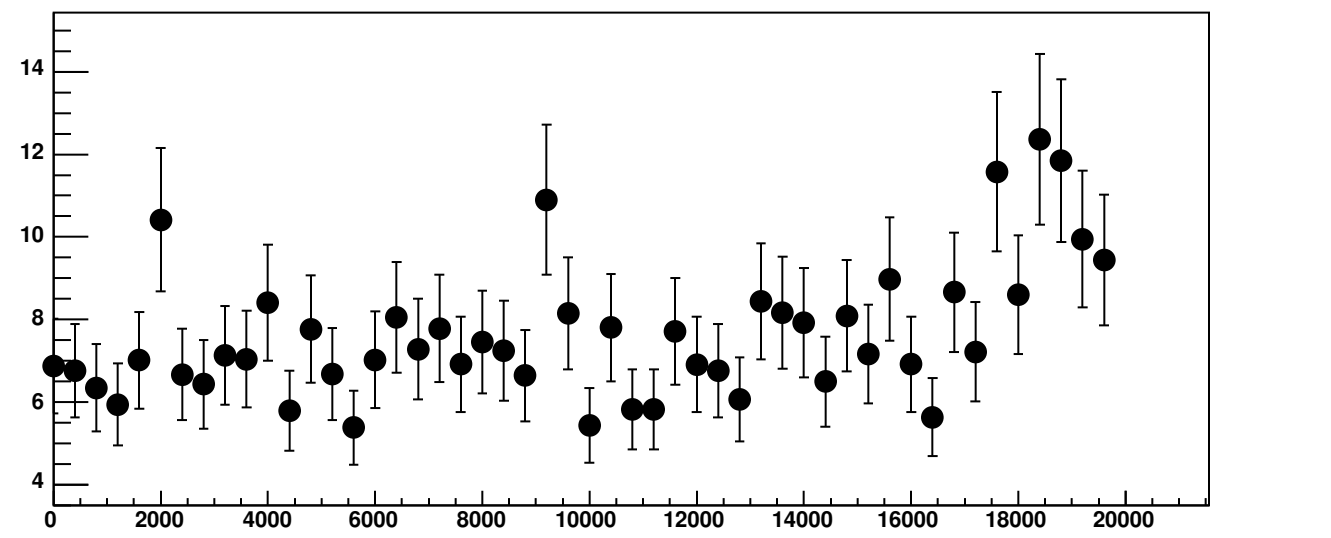

Chip 5, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

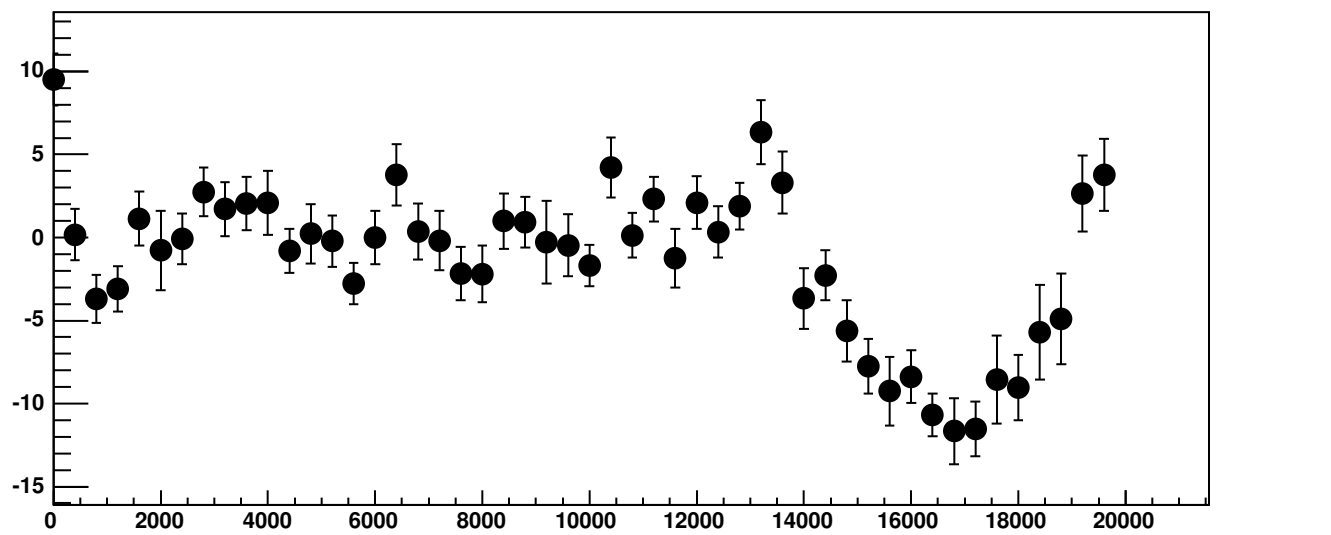
Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

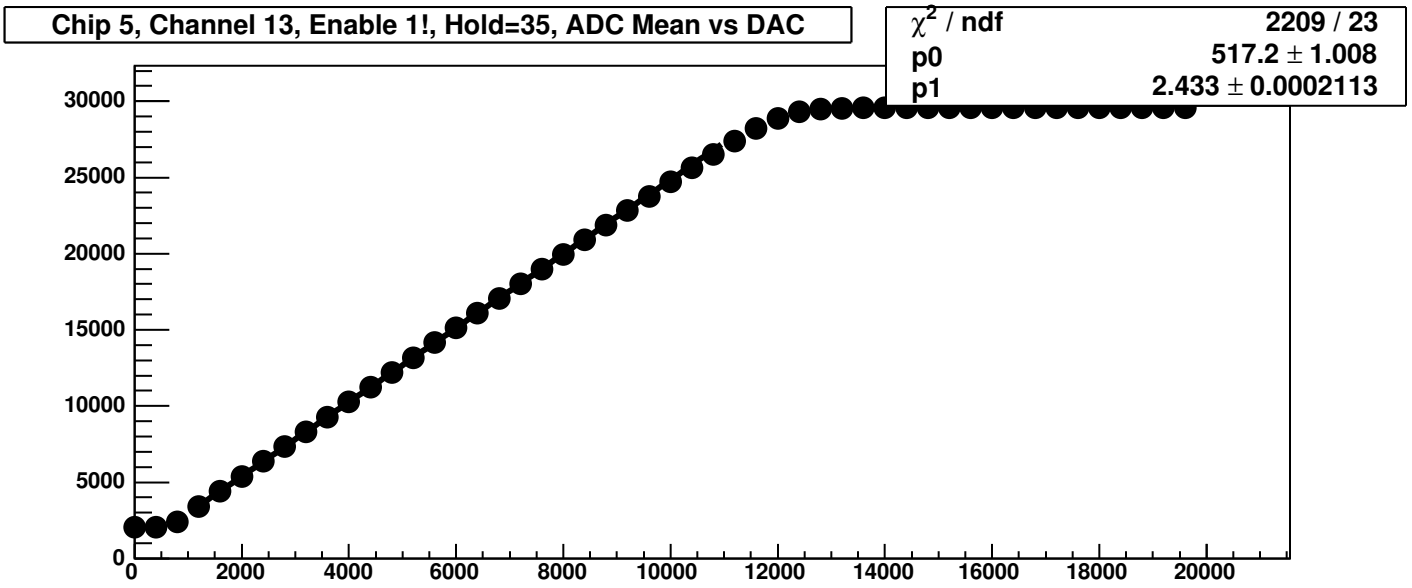


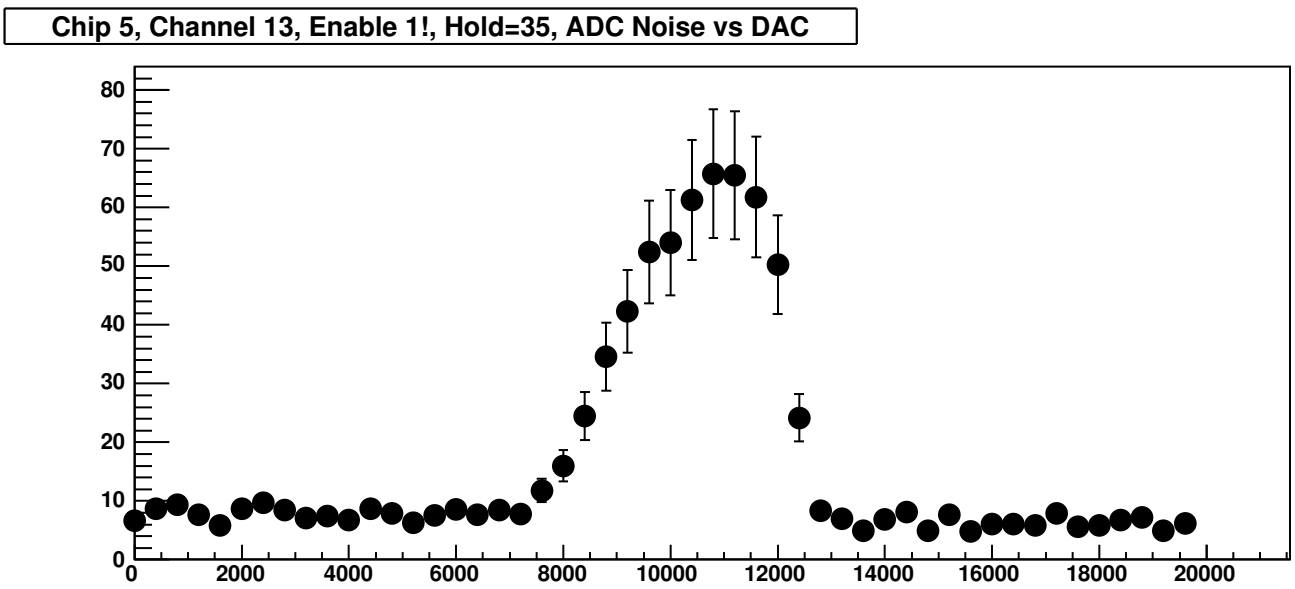
Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

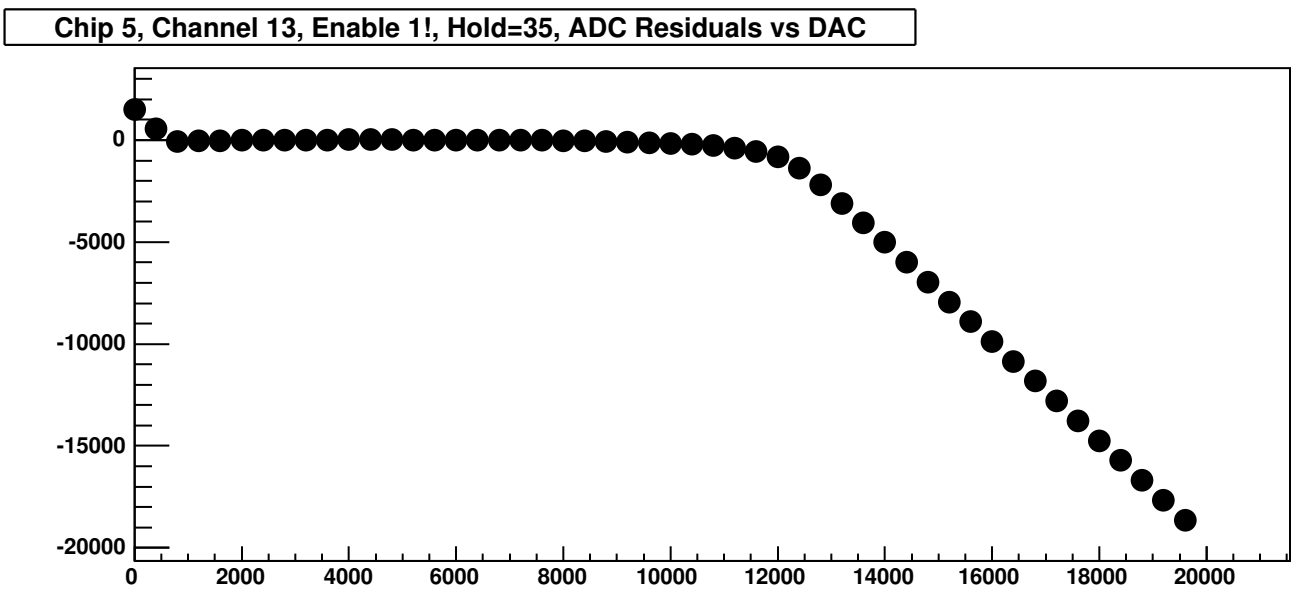


Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



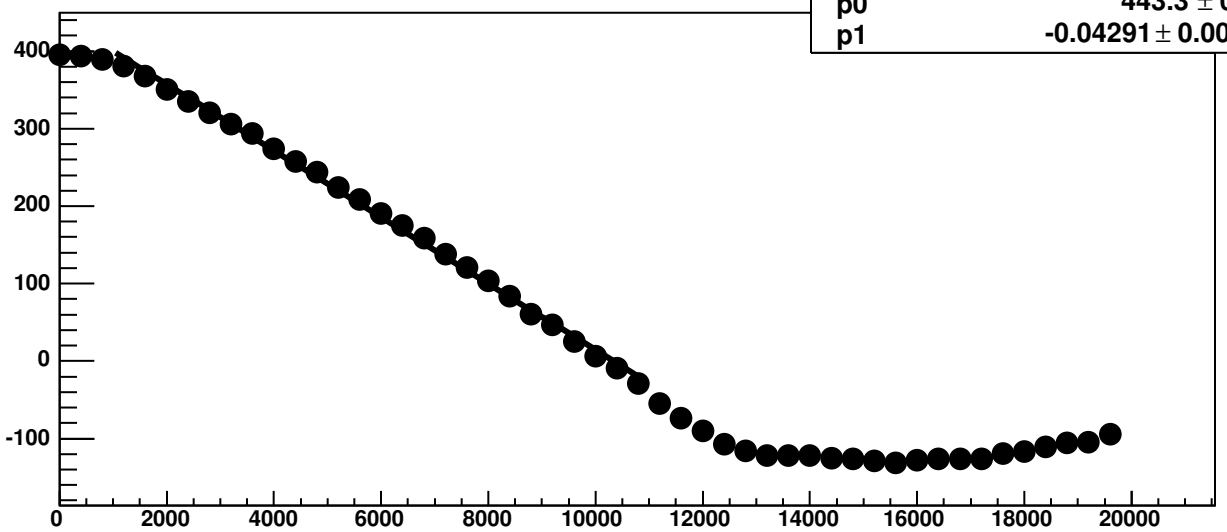

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 2209 / 23
p0 517.2 ± 1.008
p1 2.433 ± 0.0002113
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

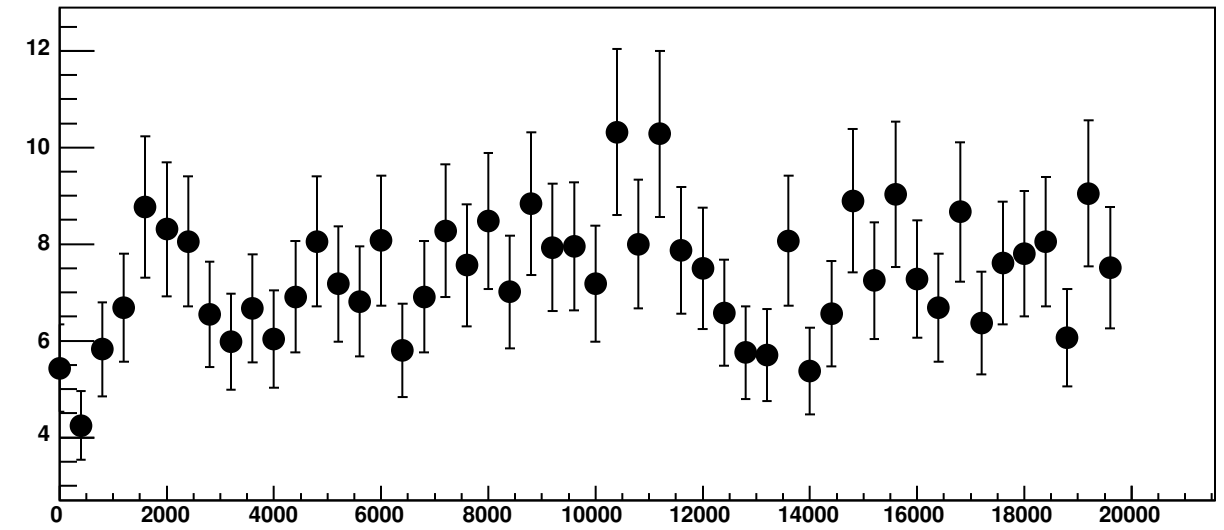

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

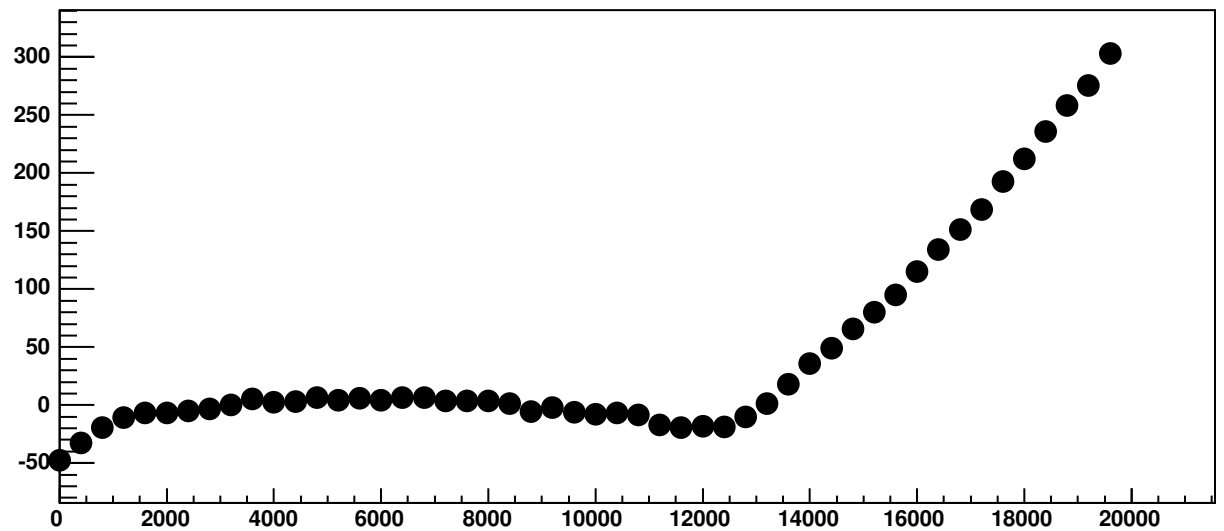
χ^2 / ndf	267.2 / 23
p0	443.3 ± 0.7759
p1	-0.04291 ± 0.0001221



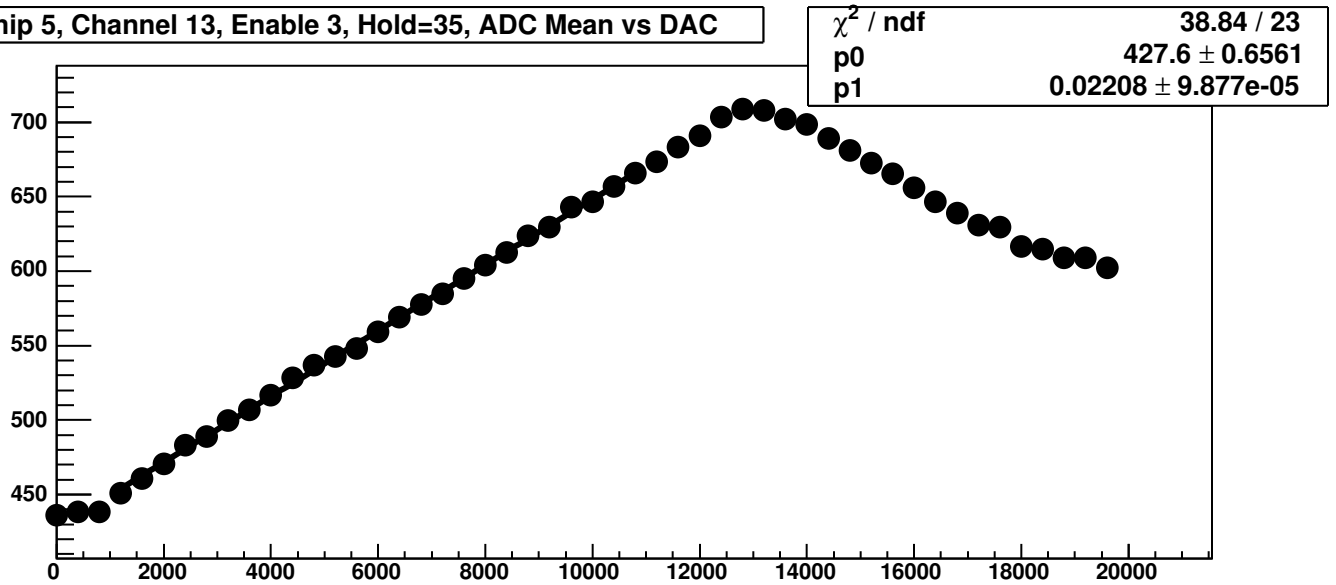
Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



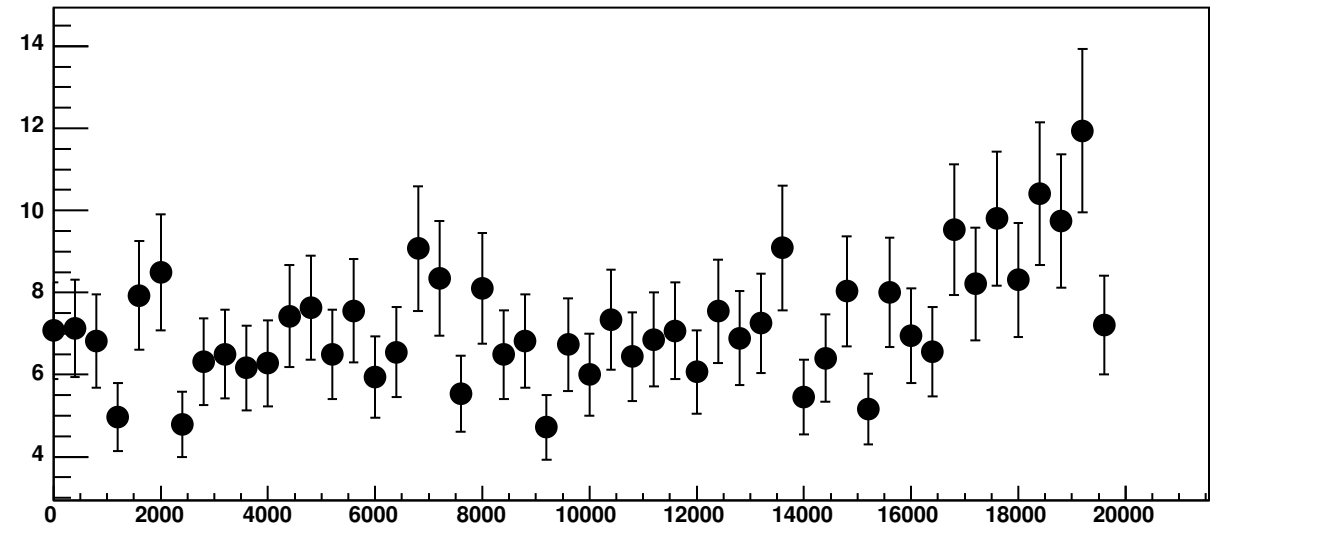
Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



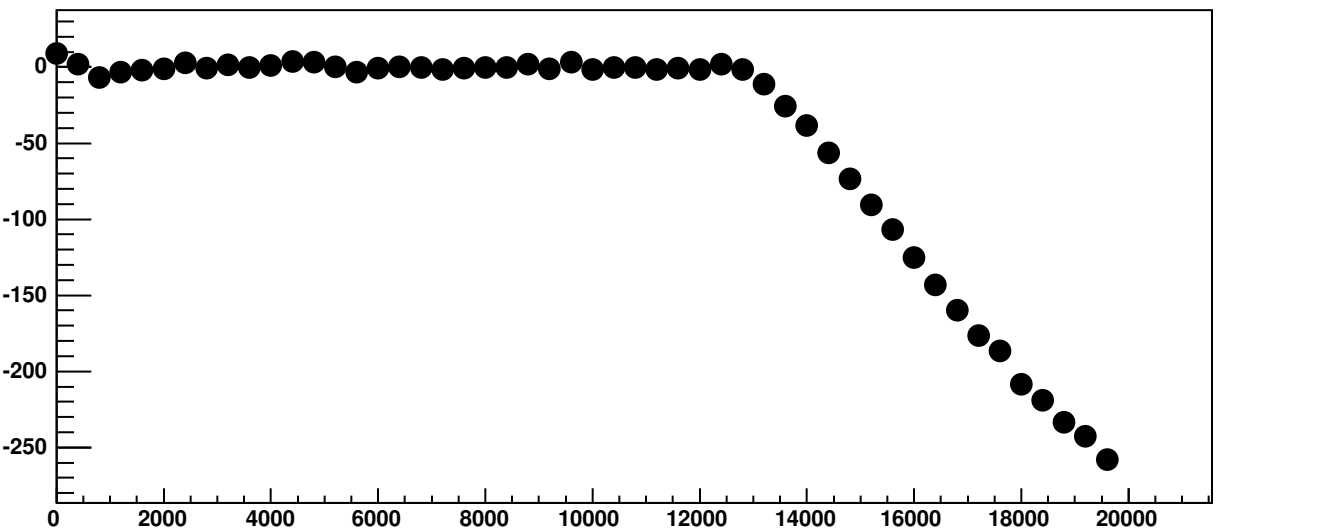
Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

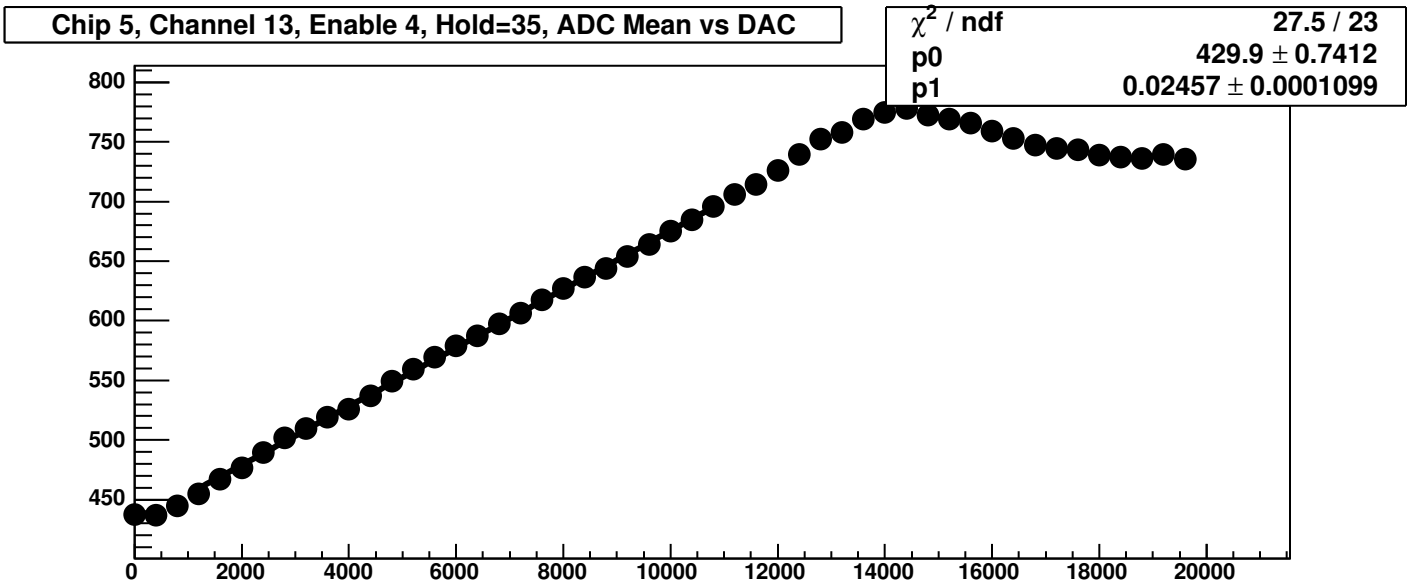


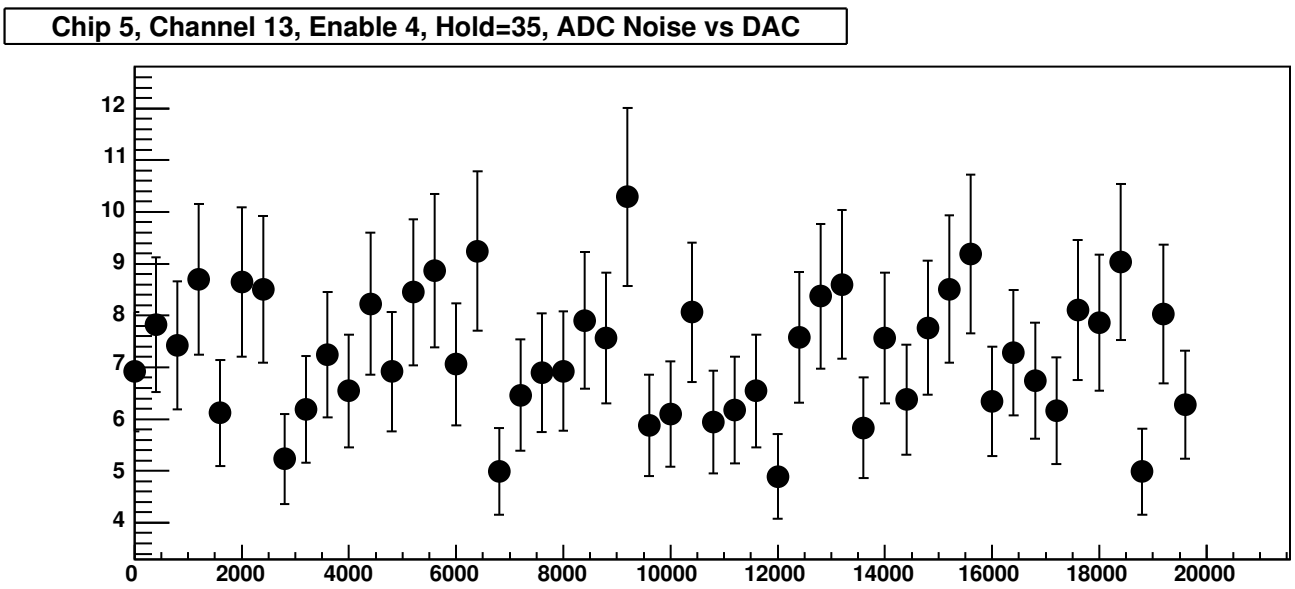
Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

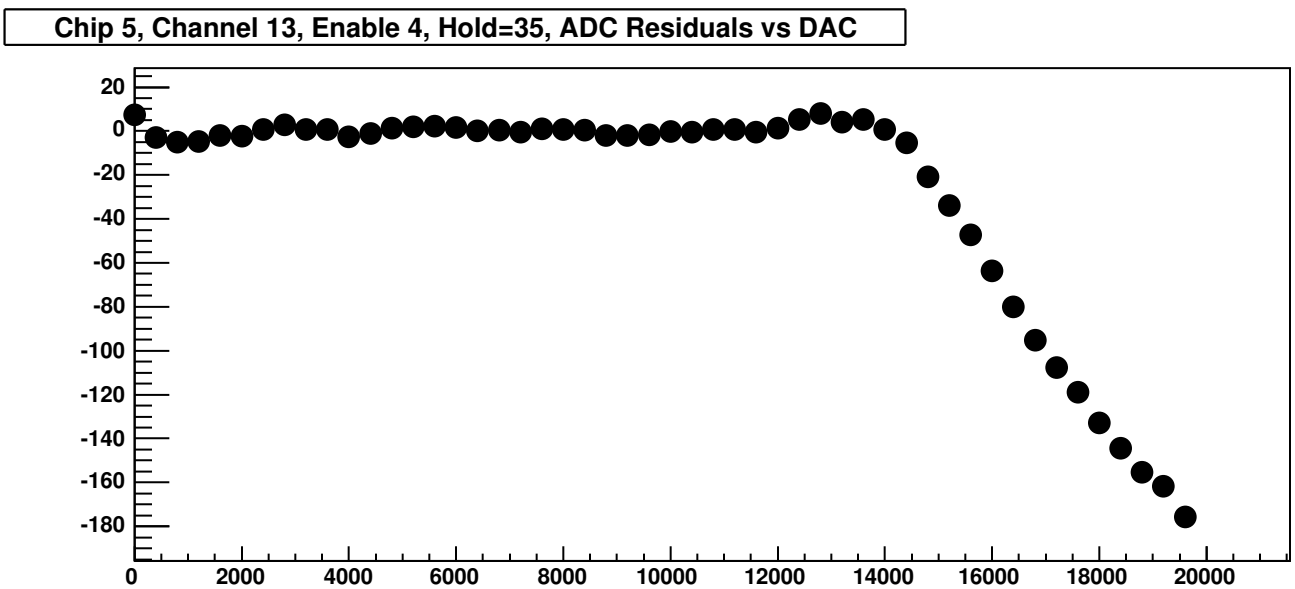


Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

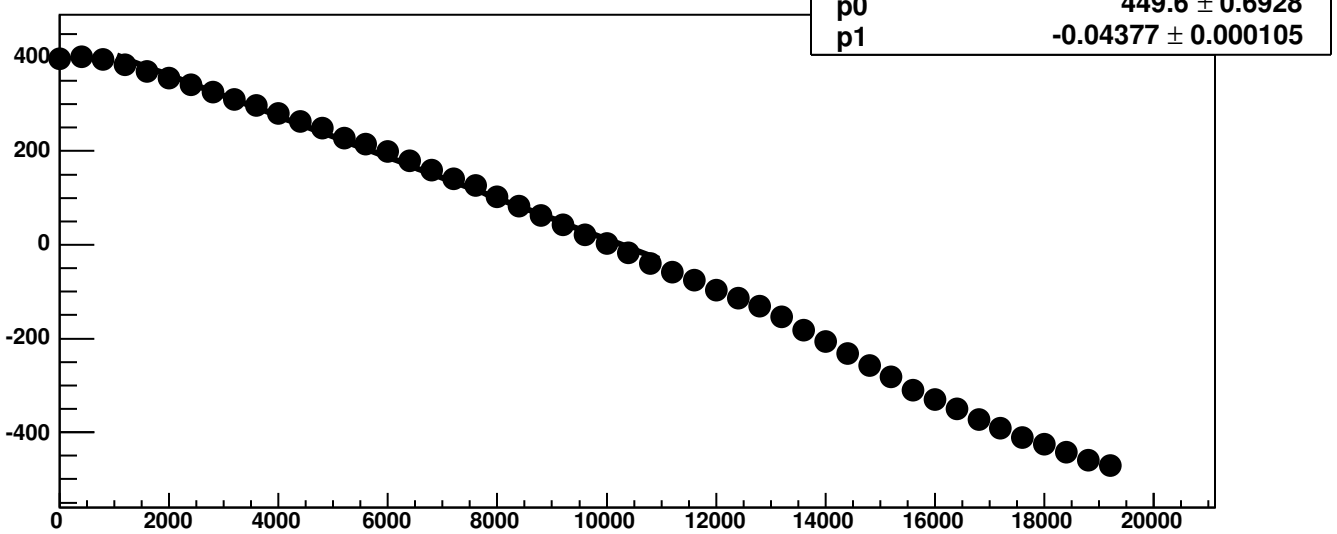



Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.5 / 23
p0 429.9 ± 0.7412
p1 0.02457 ± 0.0001099
800
750
700
650
600
550
500
450
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

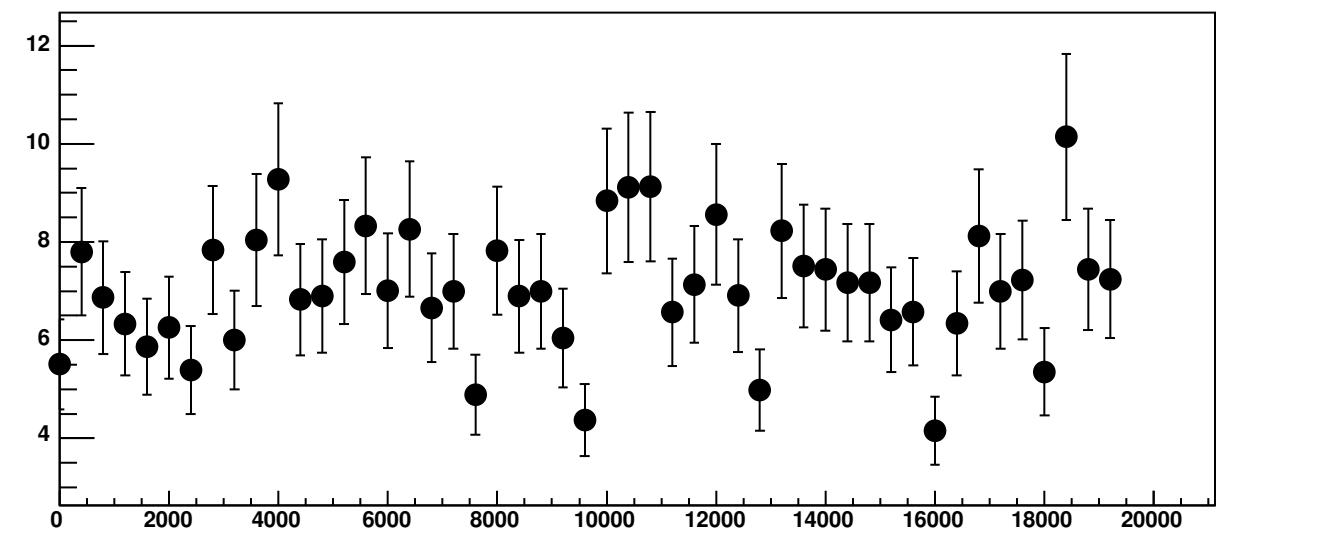

Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

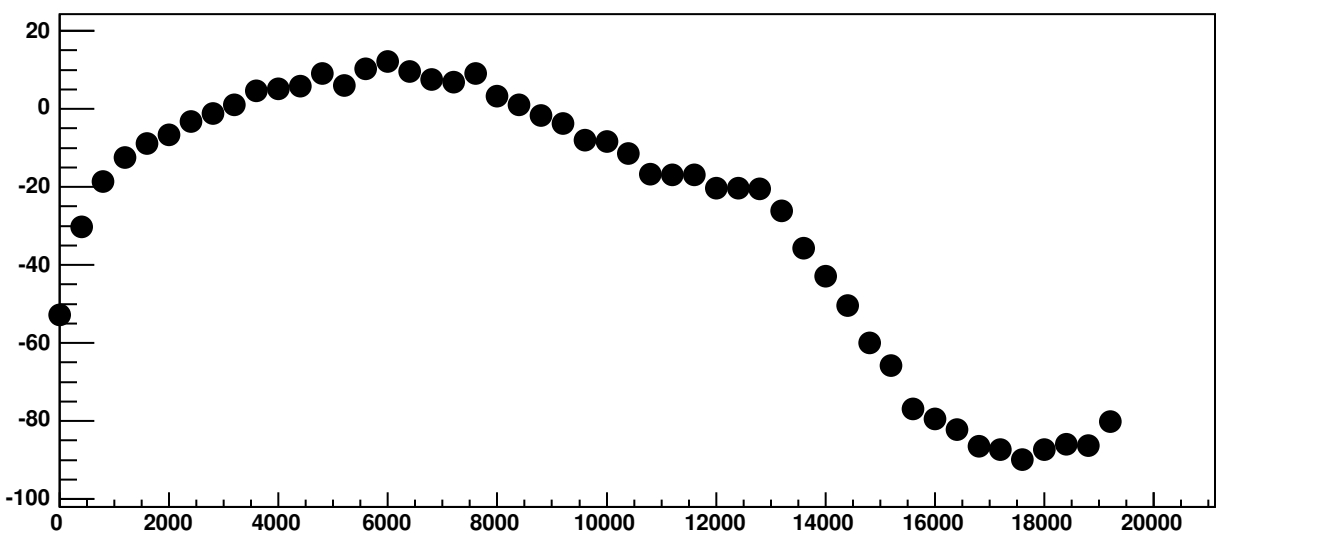
Chip 5, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



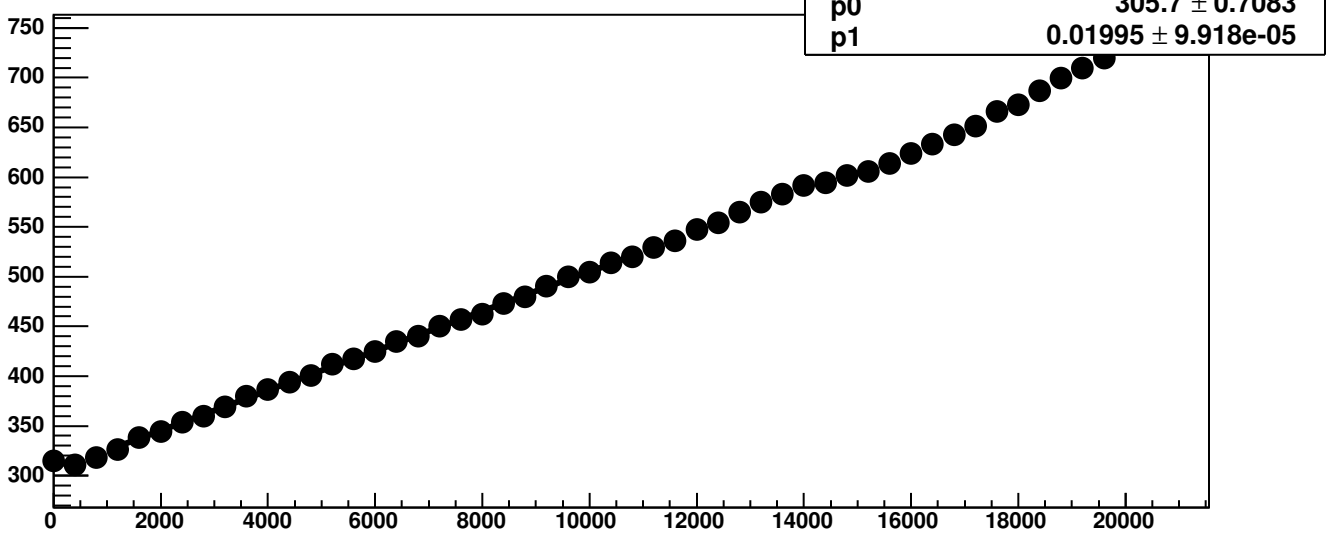
Chip 5, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



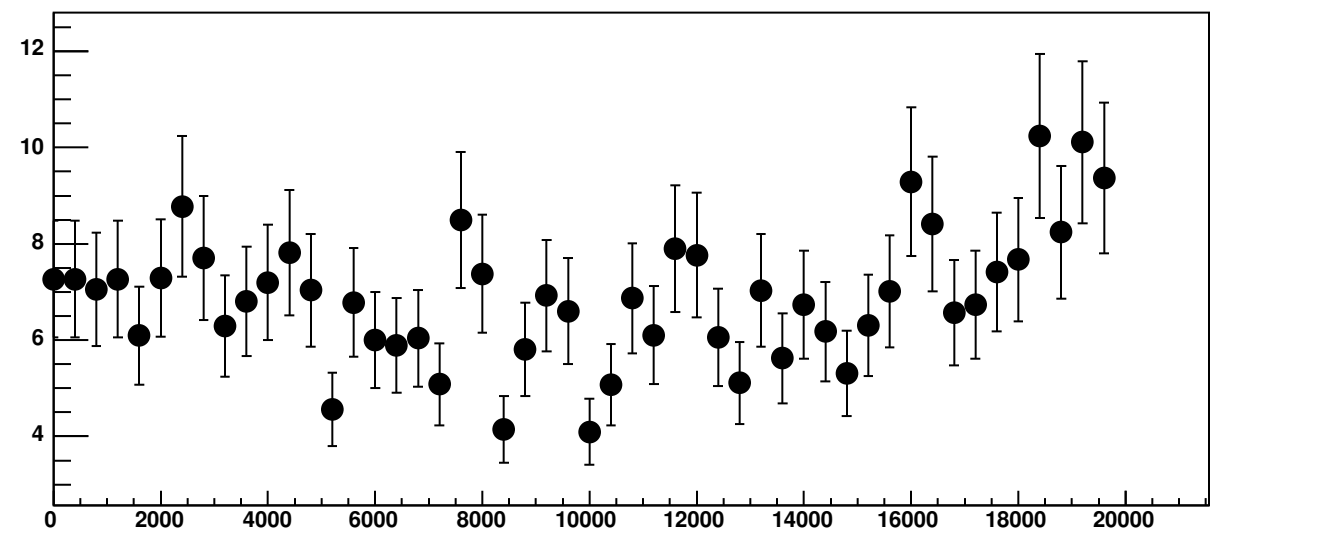
Chip 5, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



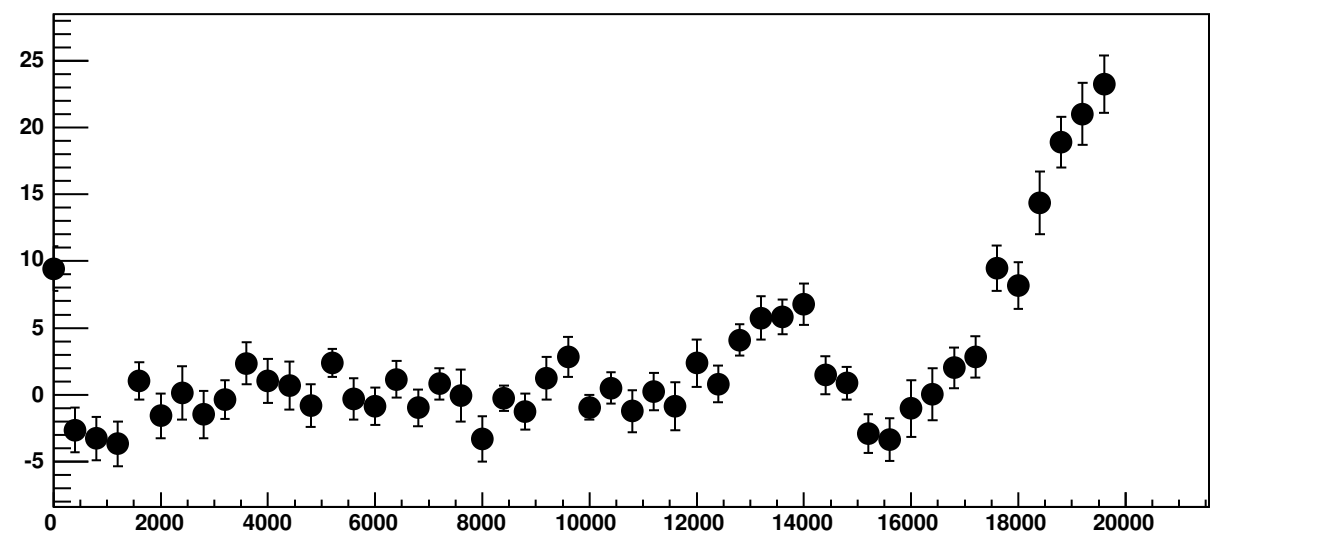
Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



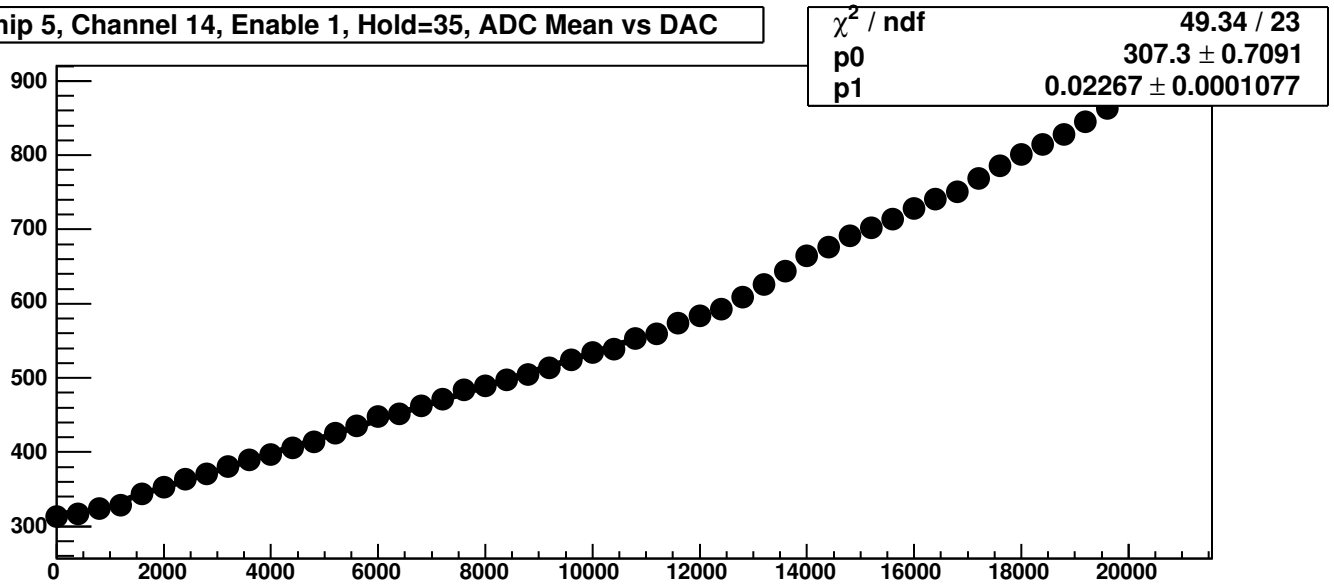
Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



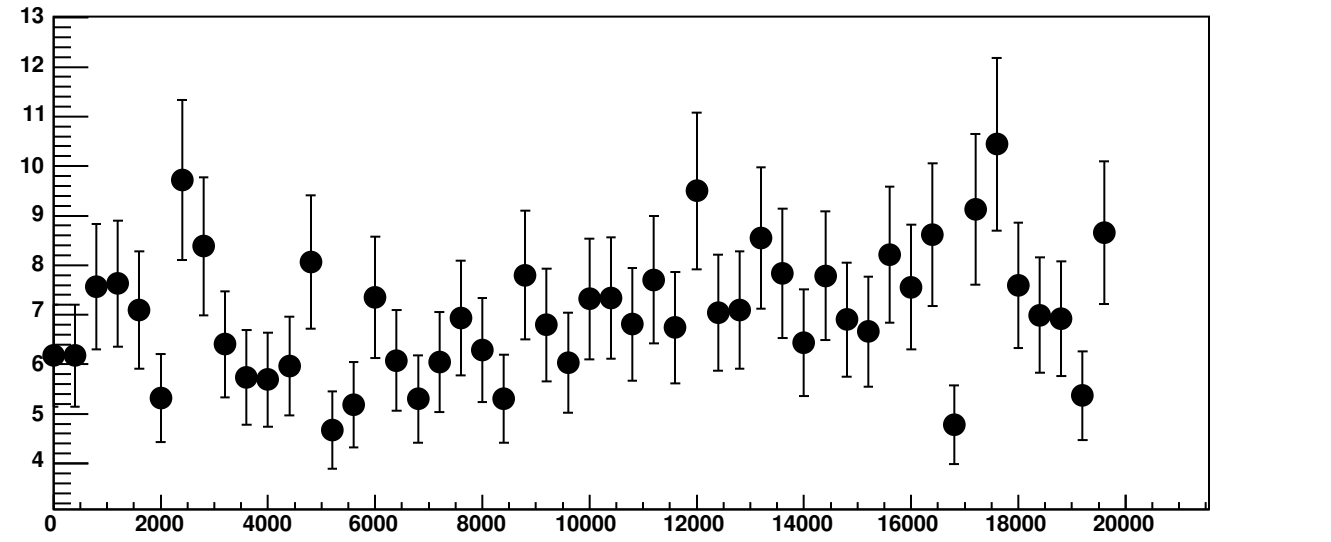
Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



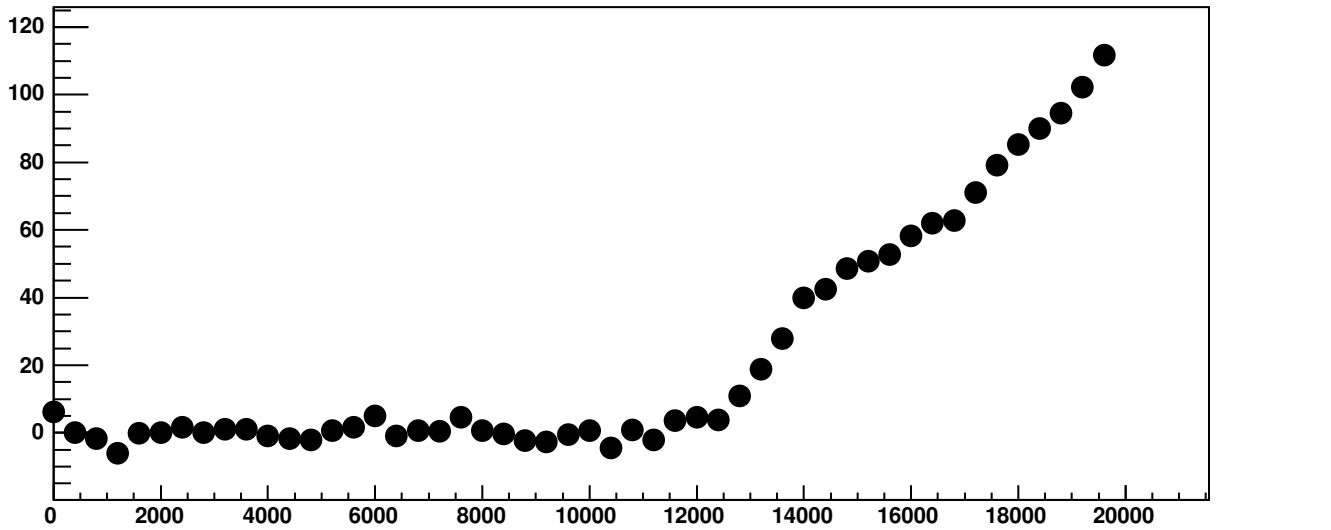
Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

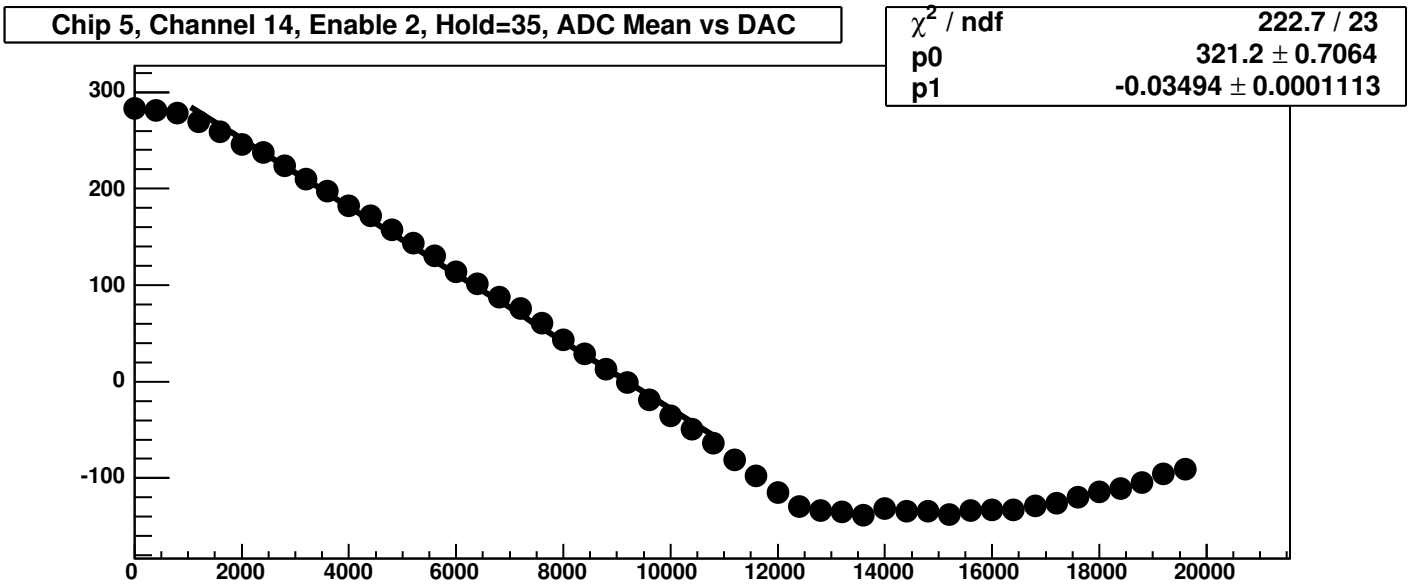


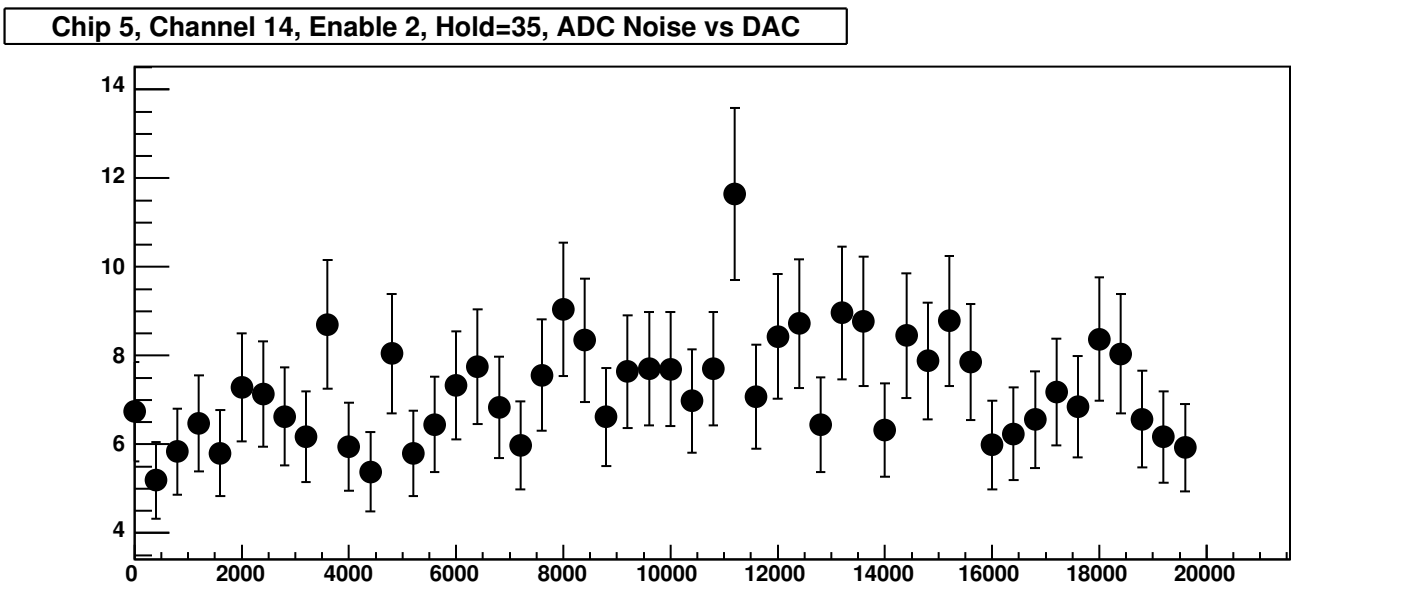
Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC

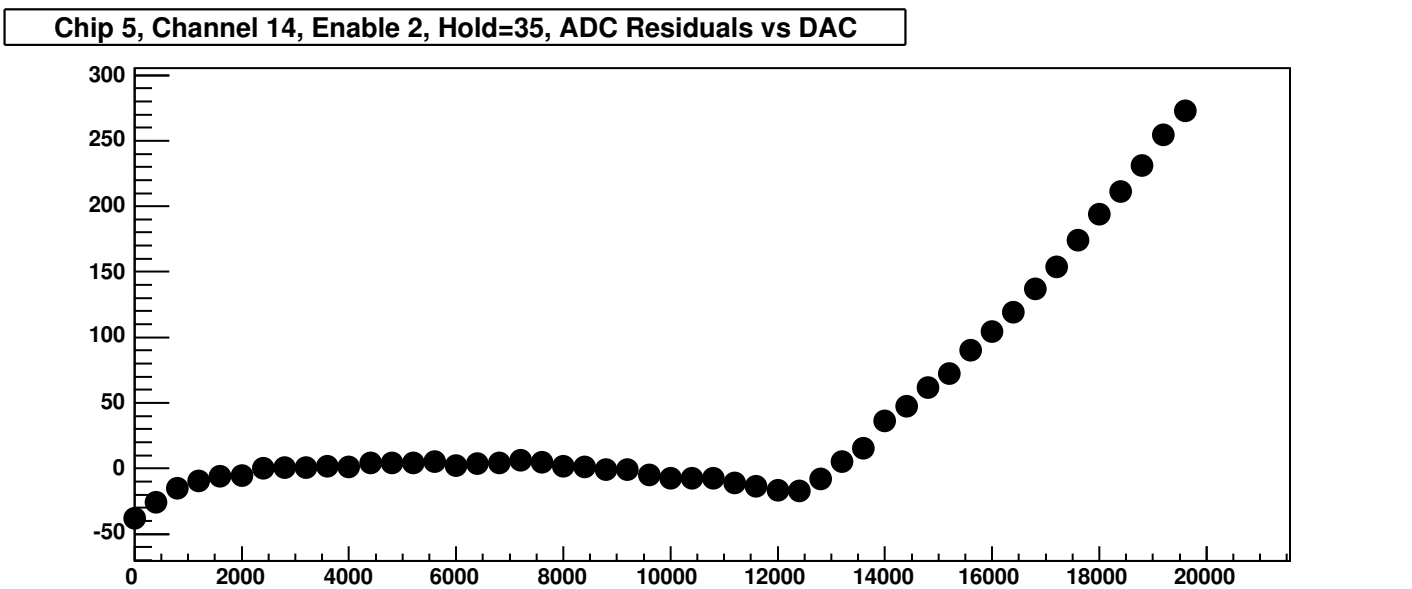


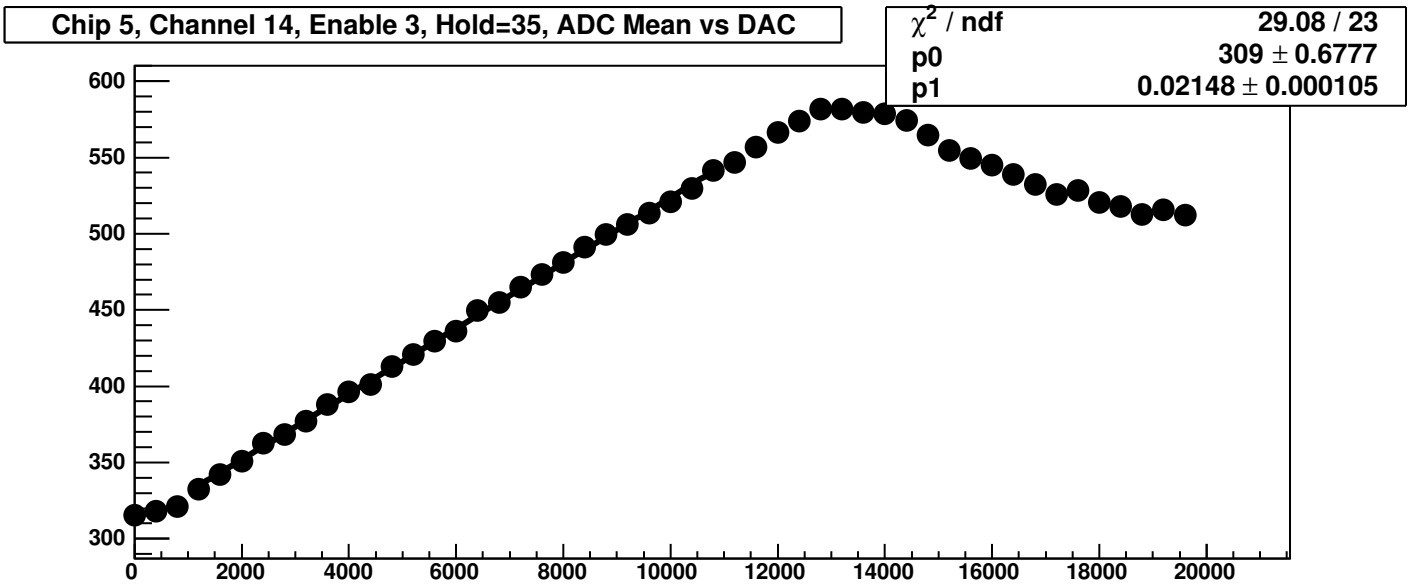
Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

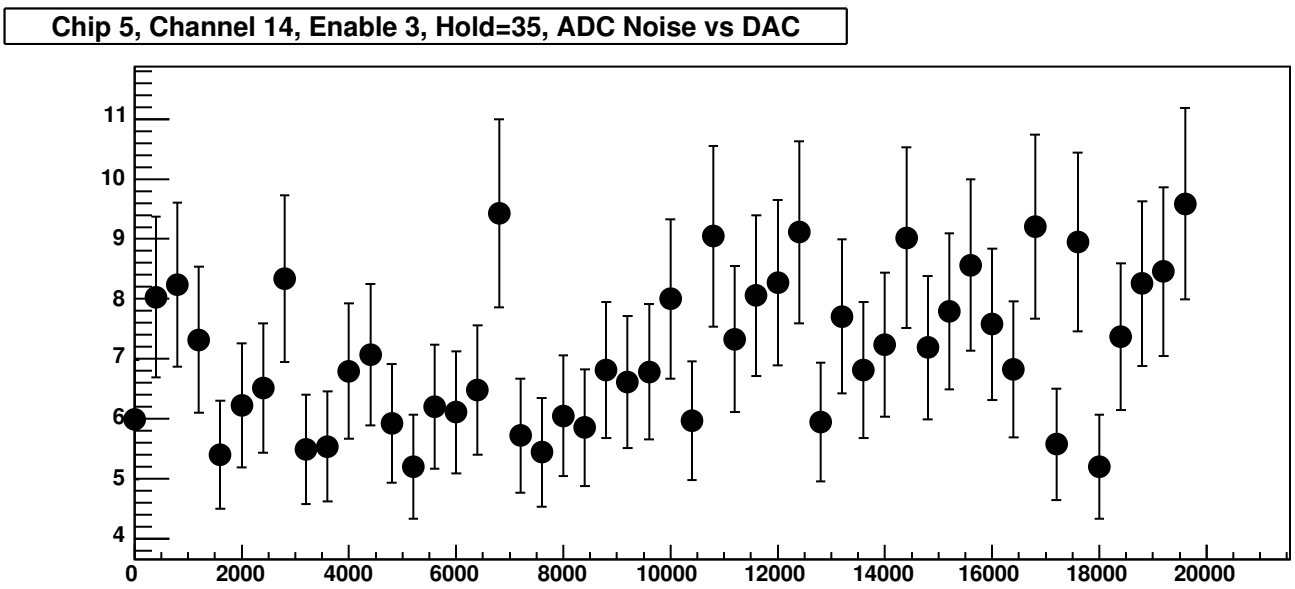


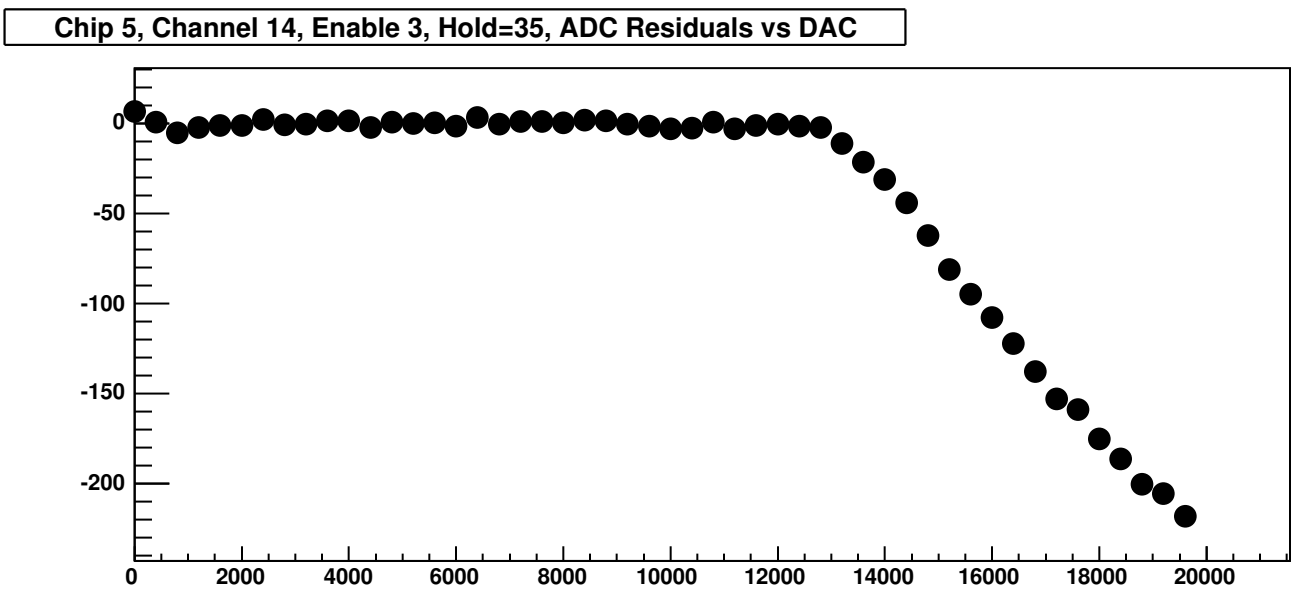

Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 222.7 / 23
p0 321.2 ± 0.7064
p1 -0.03494 ± 0.0001113
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

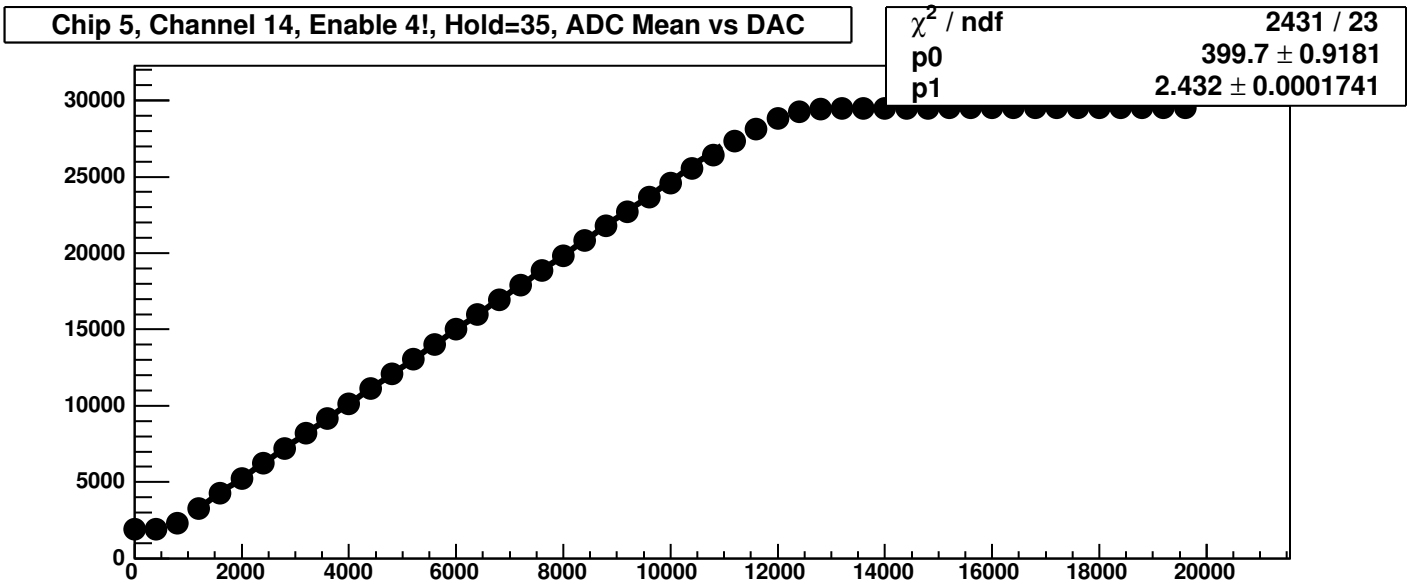

Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

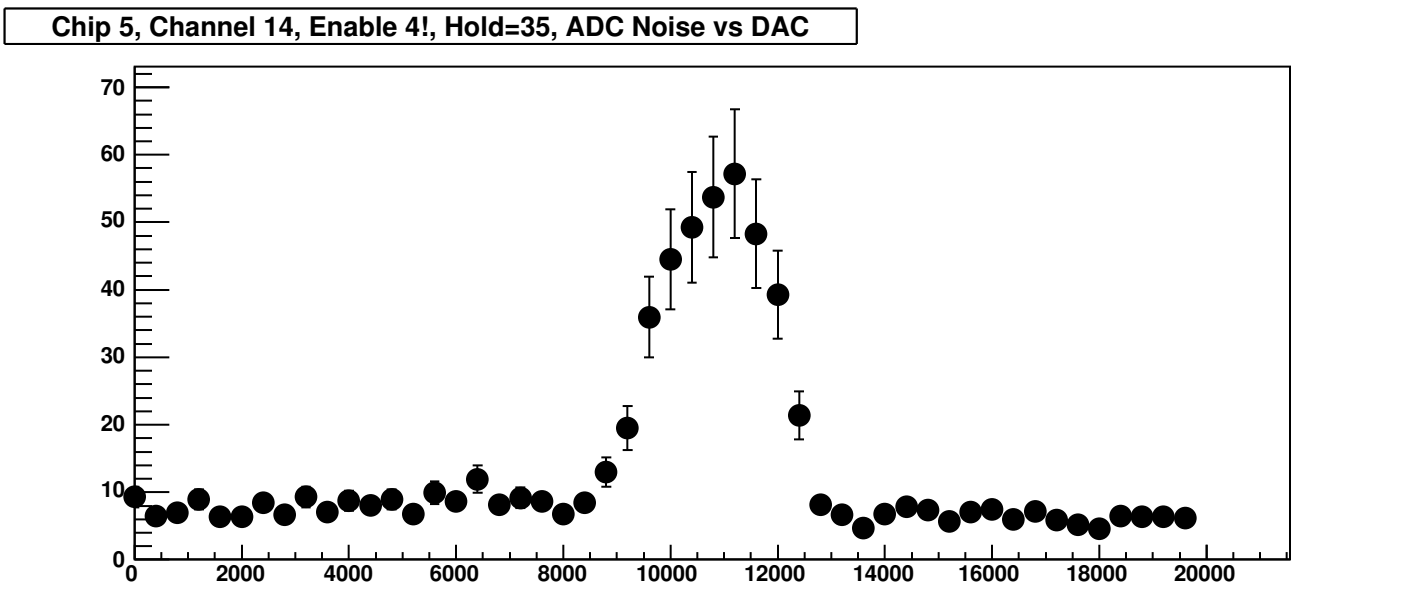

Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

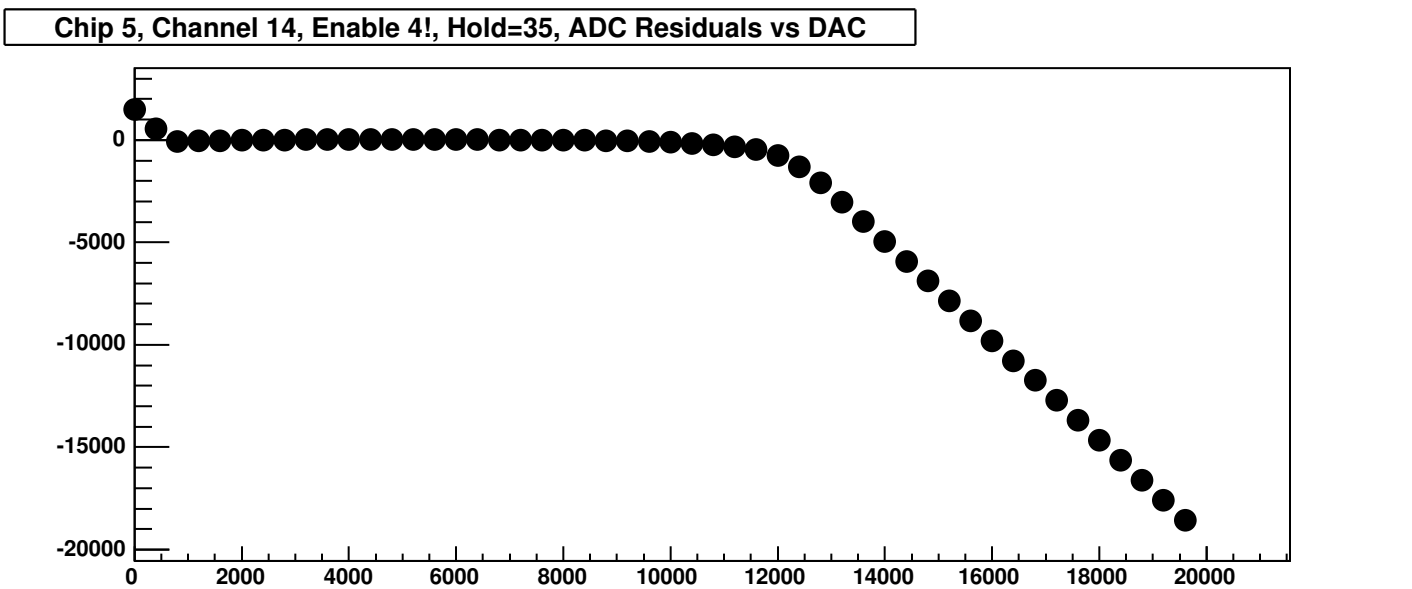

Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.08 / 23
p0 309 ± 0.6777
p1 0.02148 ± 0.000105
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

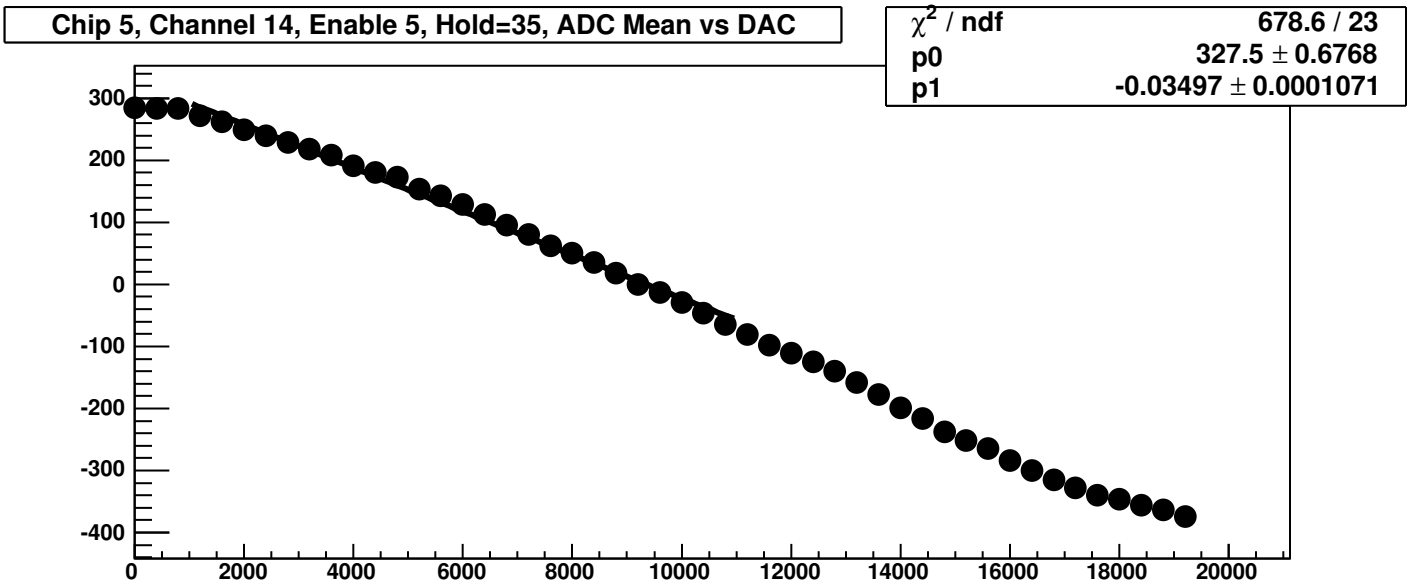

Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

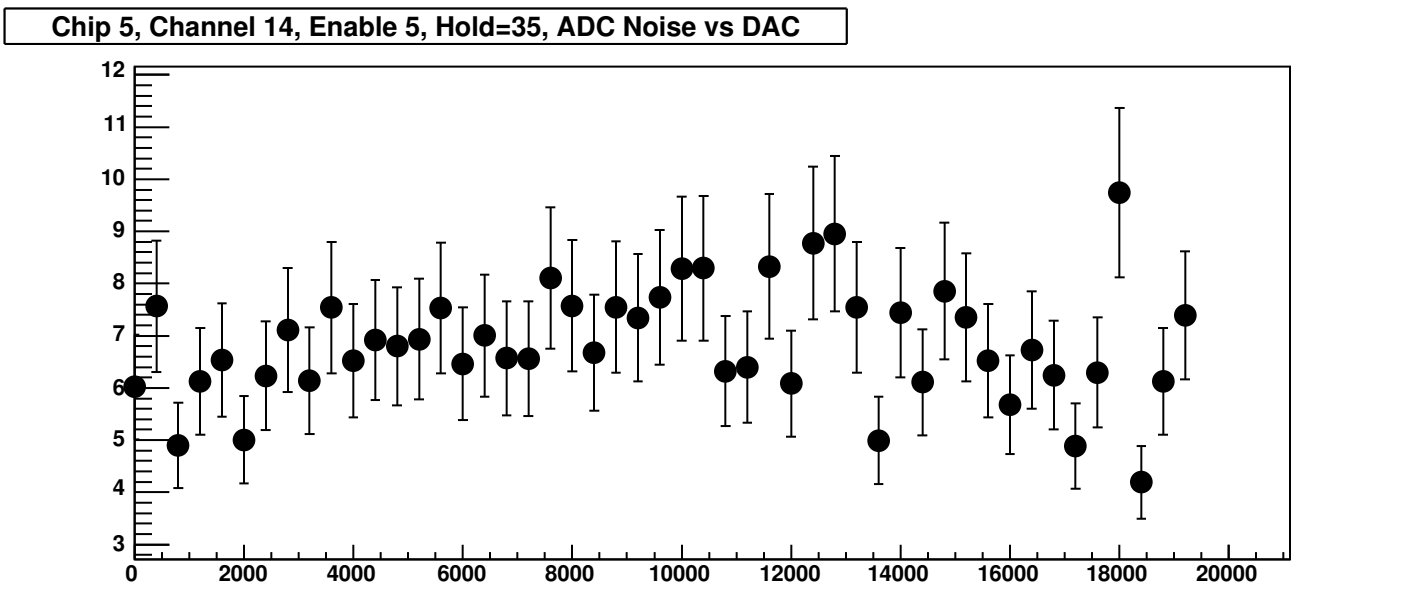

Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

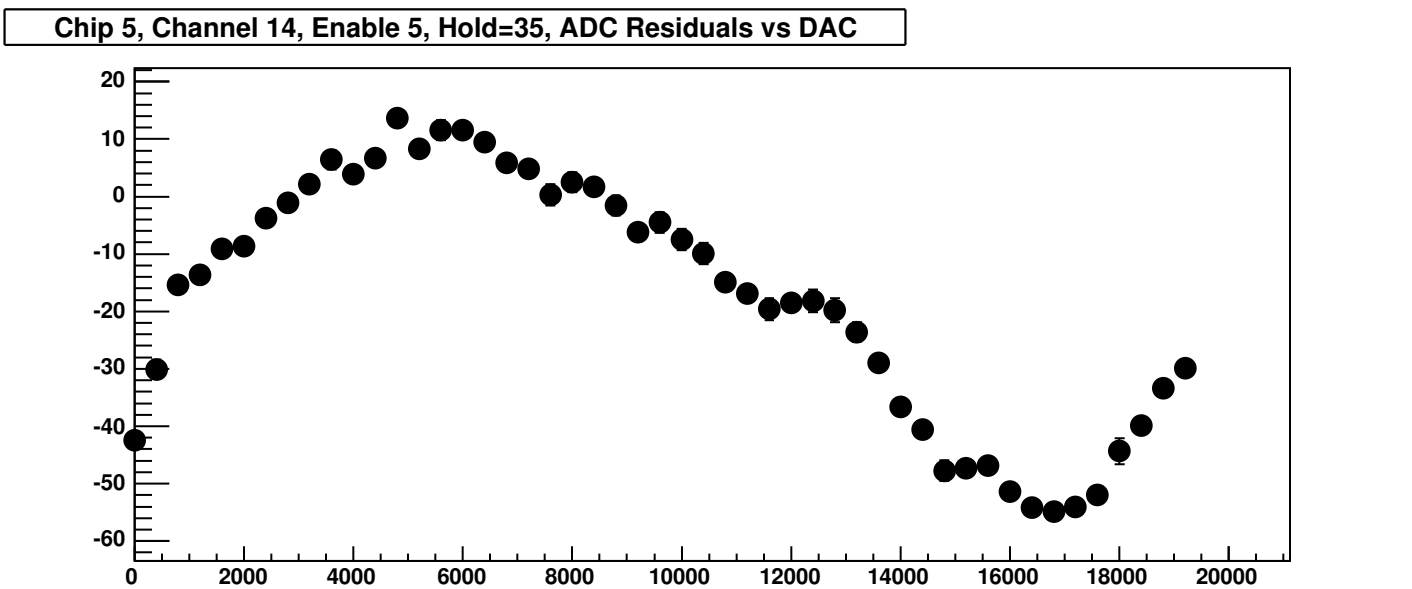

Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 2431 / 23
p0 399.7 ± 0.9181
p1 2.432 ± 0.0001741
30000
25000
20000
15000
10000
5000
0
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

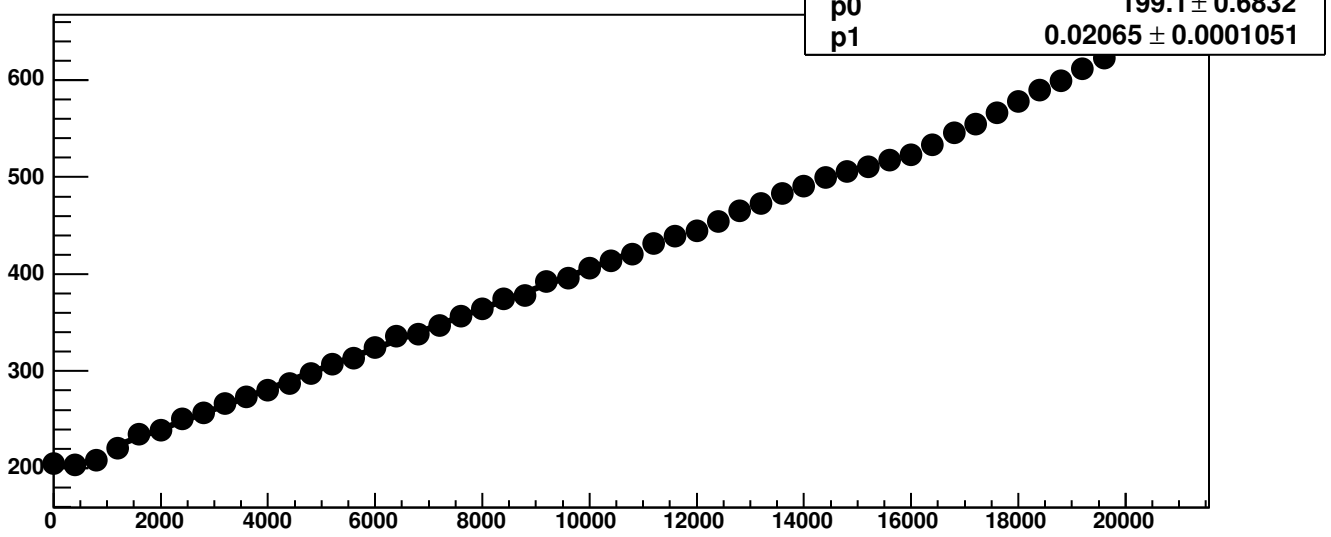

Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-5000
-10000
-15000
-20000
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000


Chip 5, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 678.6 / 23
p0 327.5 ± 0.6768
p1 -0.03497 ± 0.0001071
300
200
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

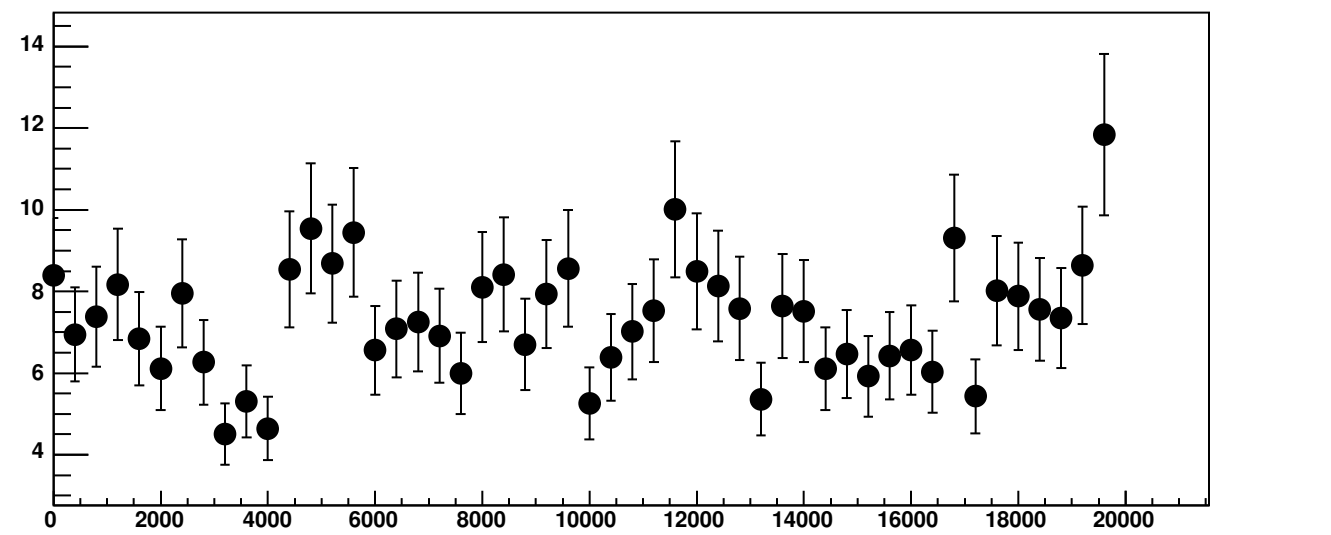

Chip 5, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
-60
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

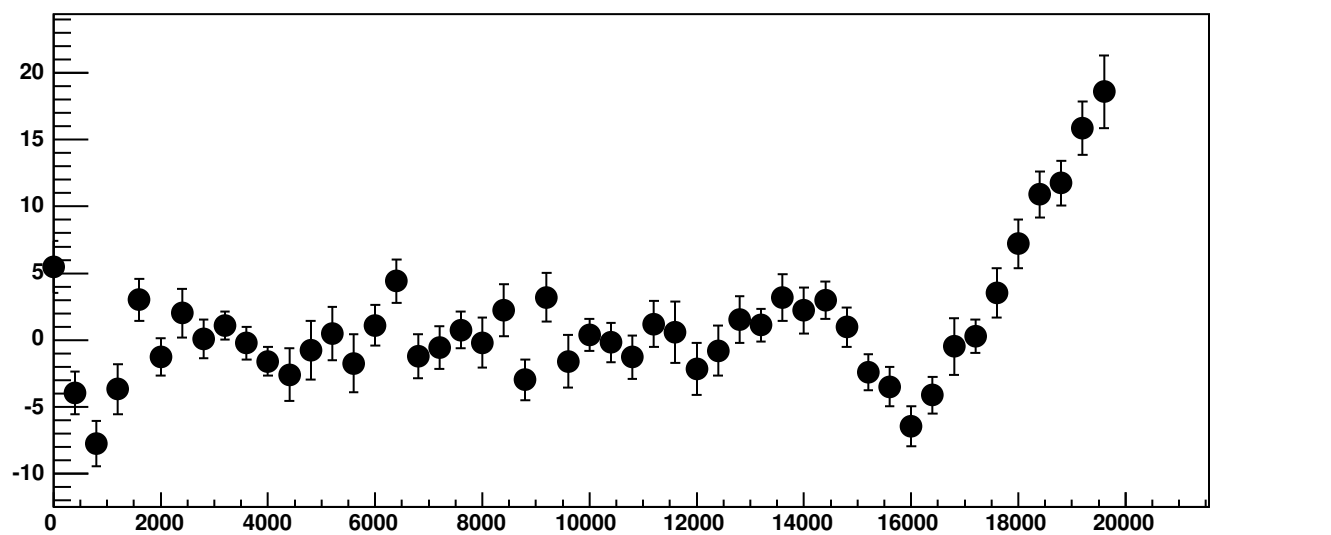
Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



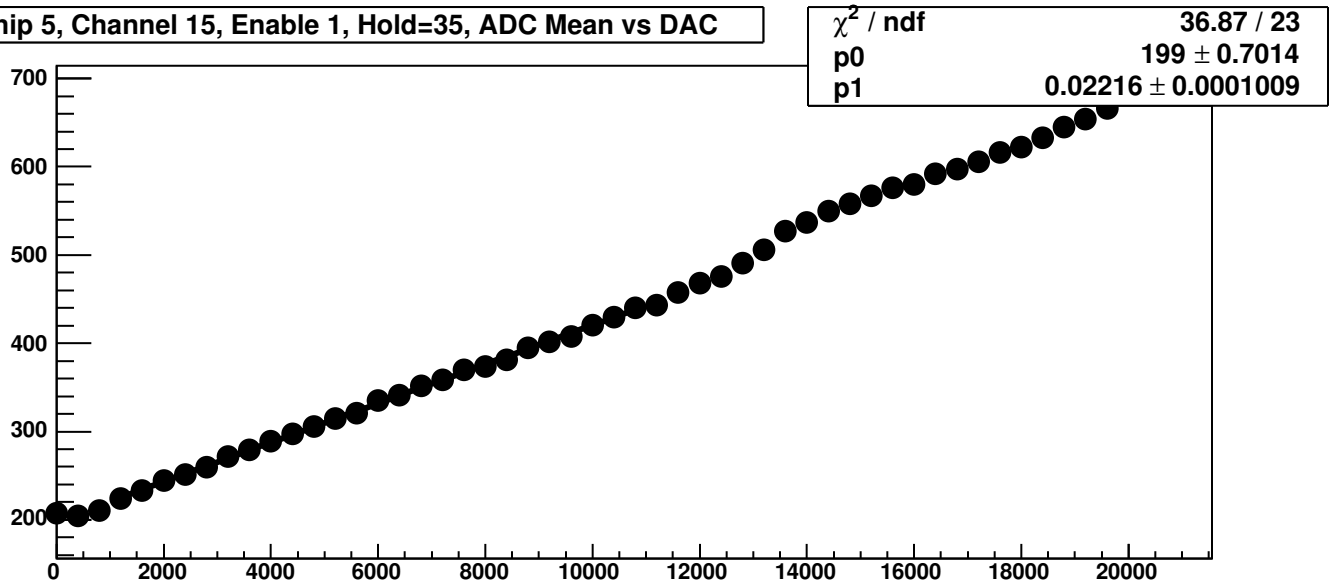
Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



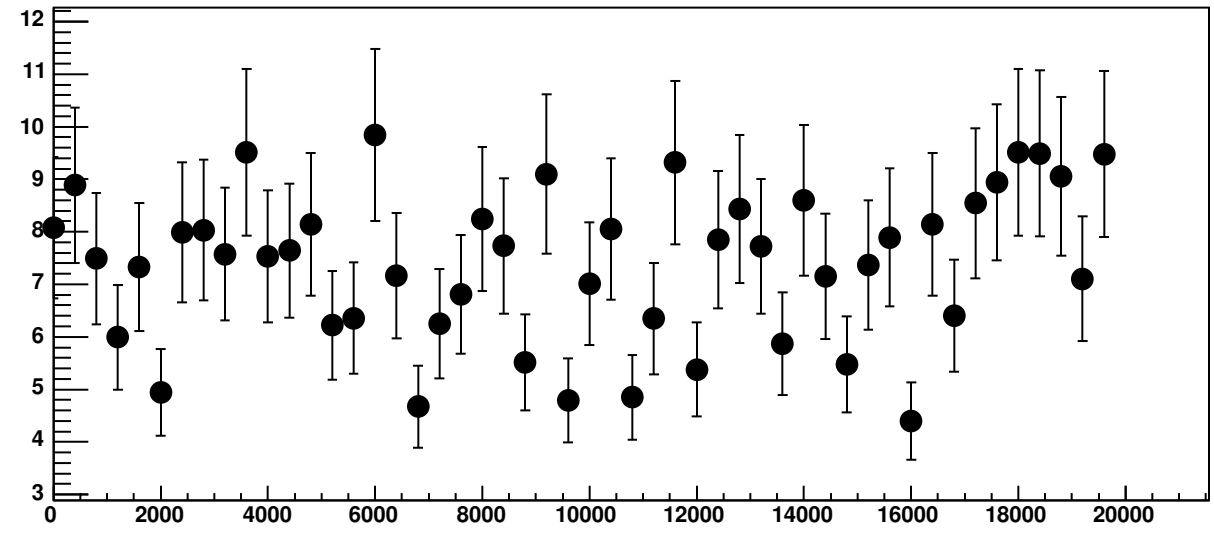
Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



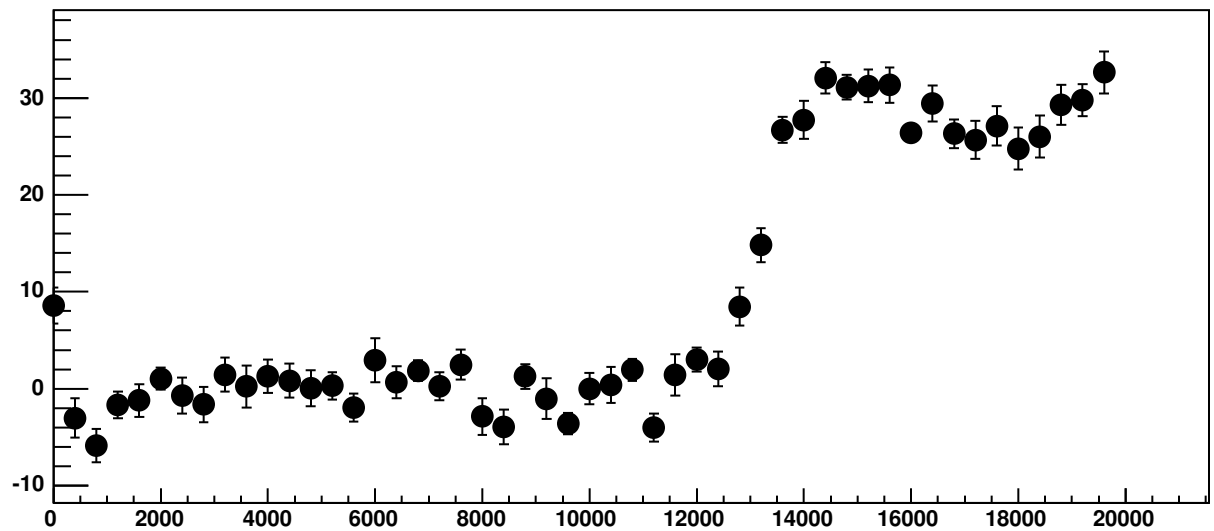
Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

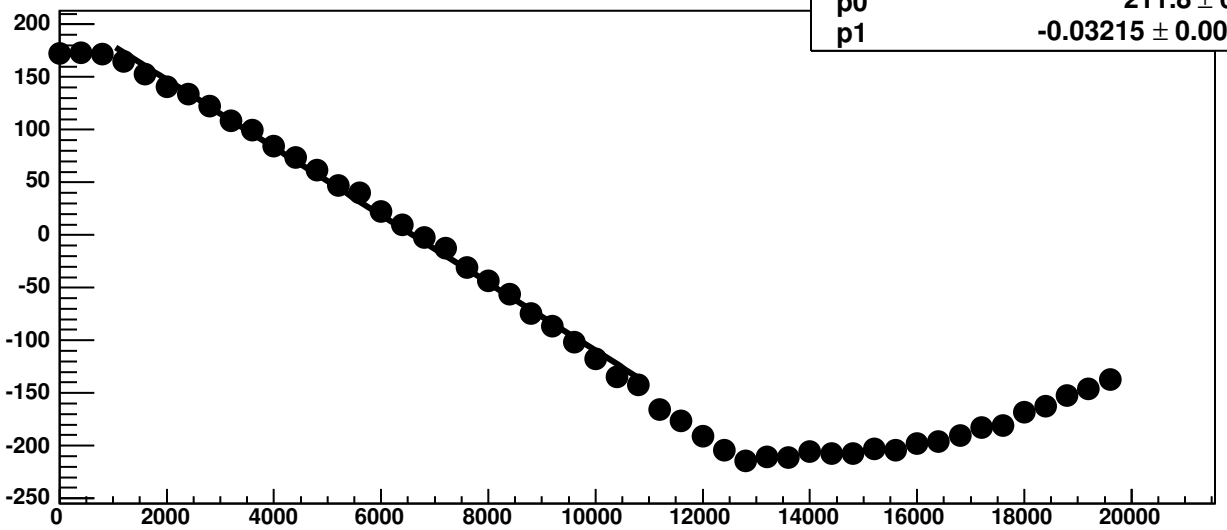


Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

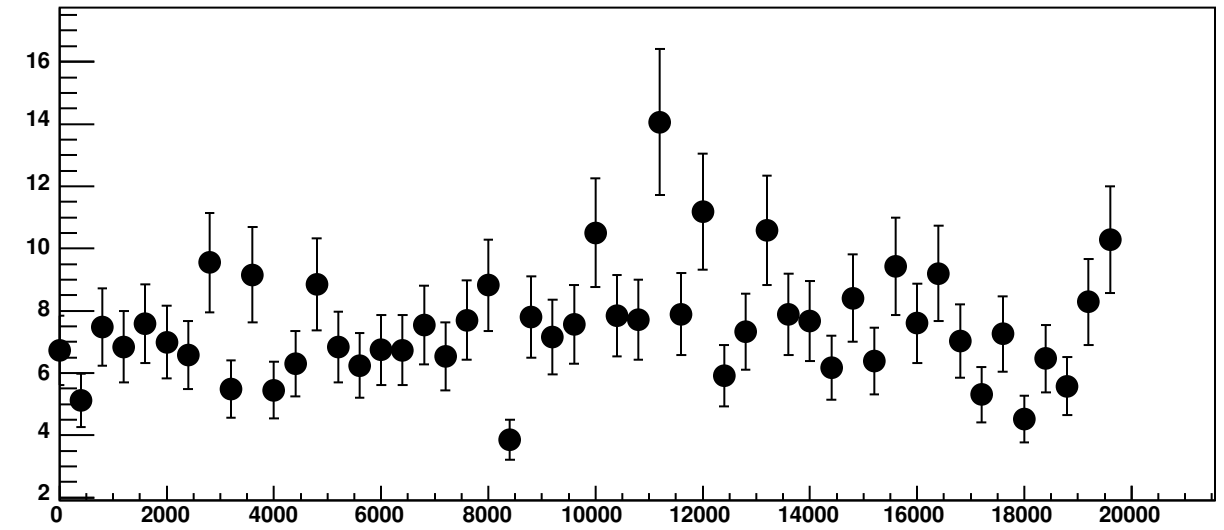


Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

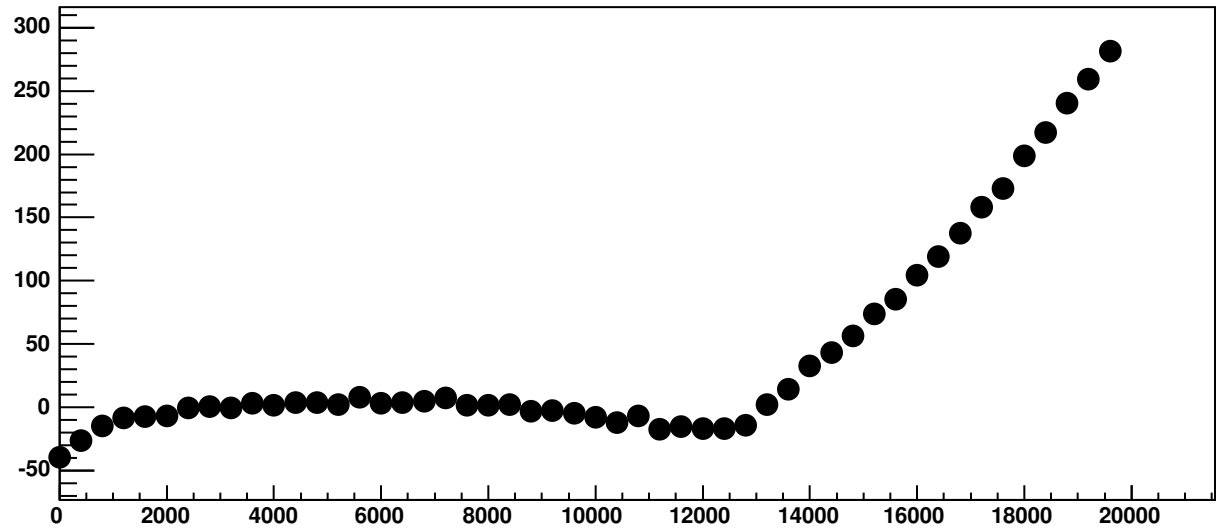
χ^2 / ndf	247.3 / 23
p0	211.8 ± 0.7464
p1	-0.03215 ± 0.0001138



Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC

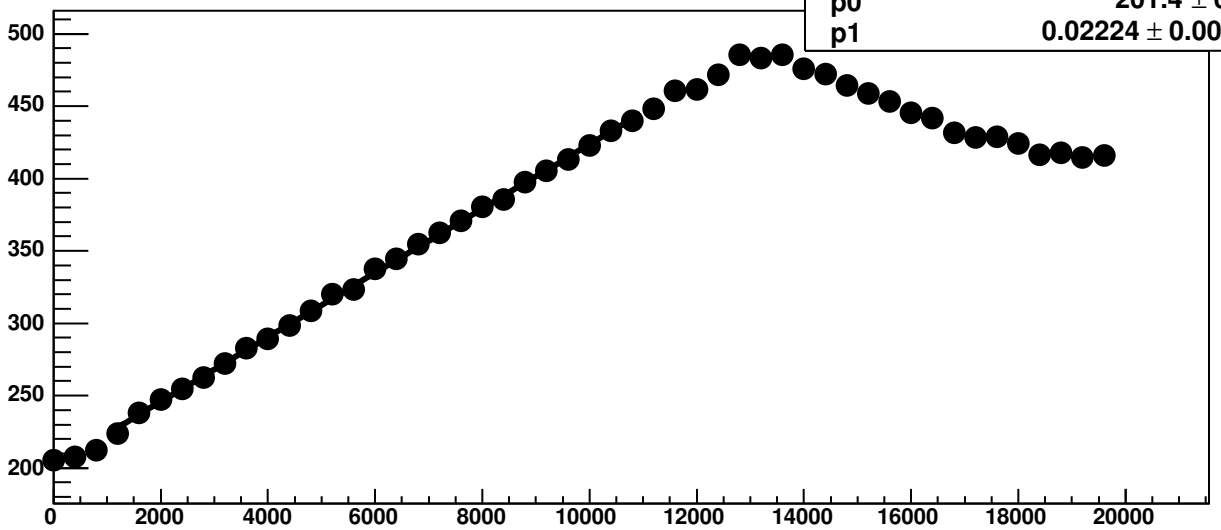


Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

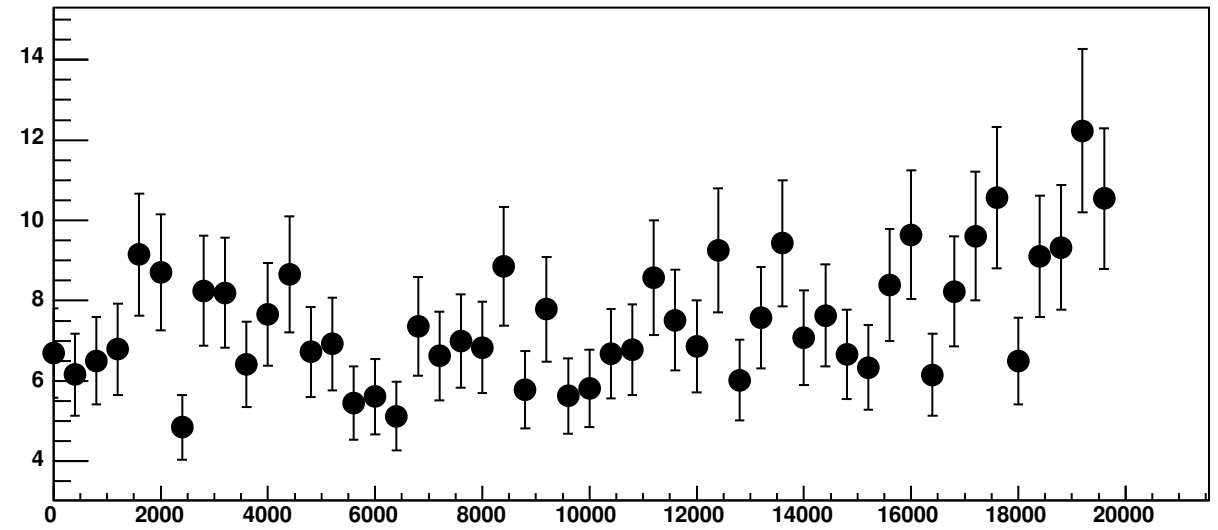


Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

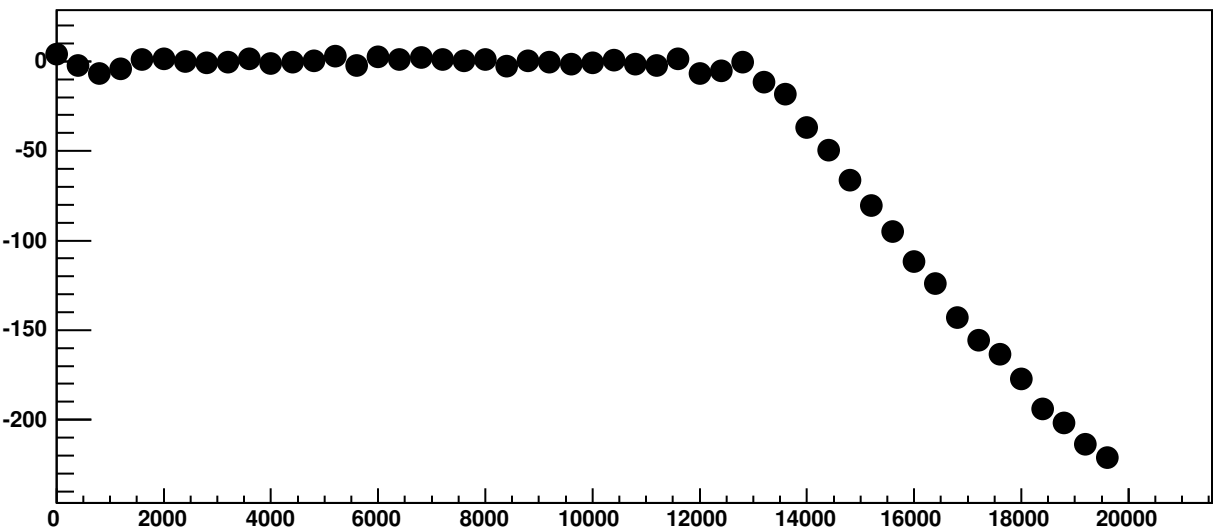
χ^2 / ndf	30.73 / 23
p0	201.4 $\pm$ 0.7352
p1	0.02224 $\pm$ 0.0001079



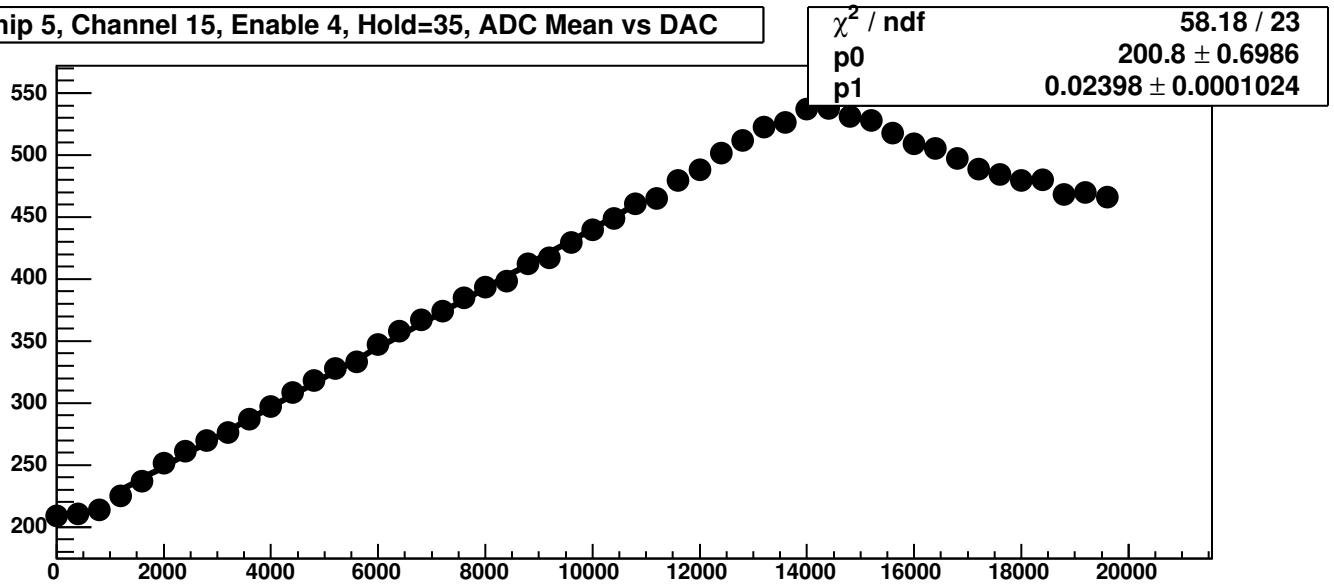
Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



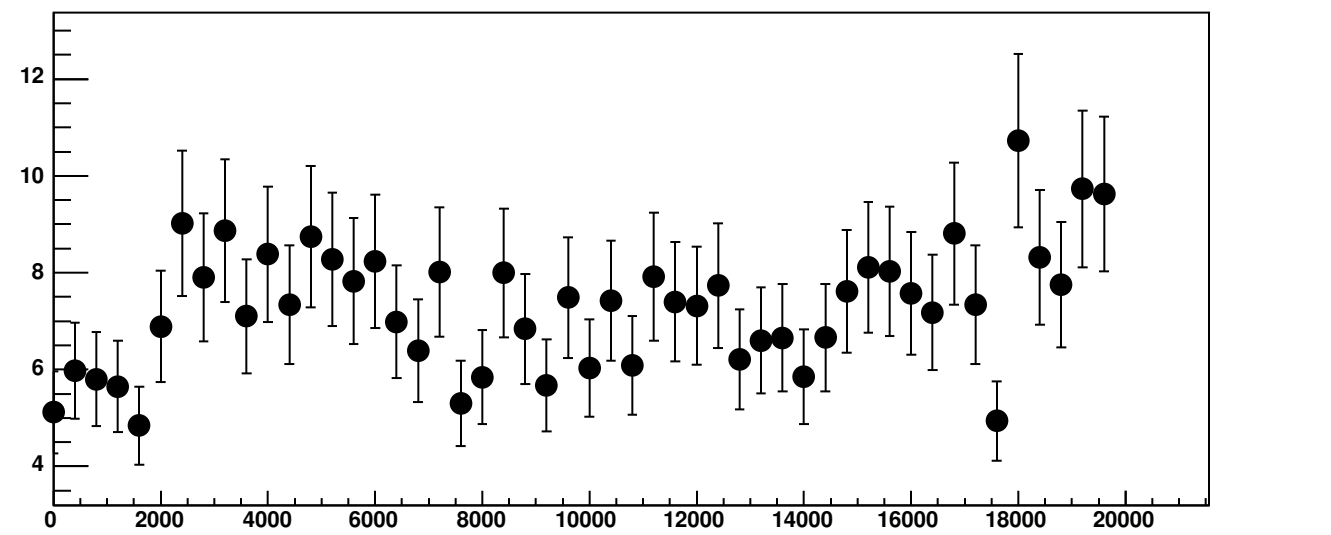
Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



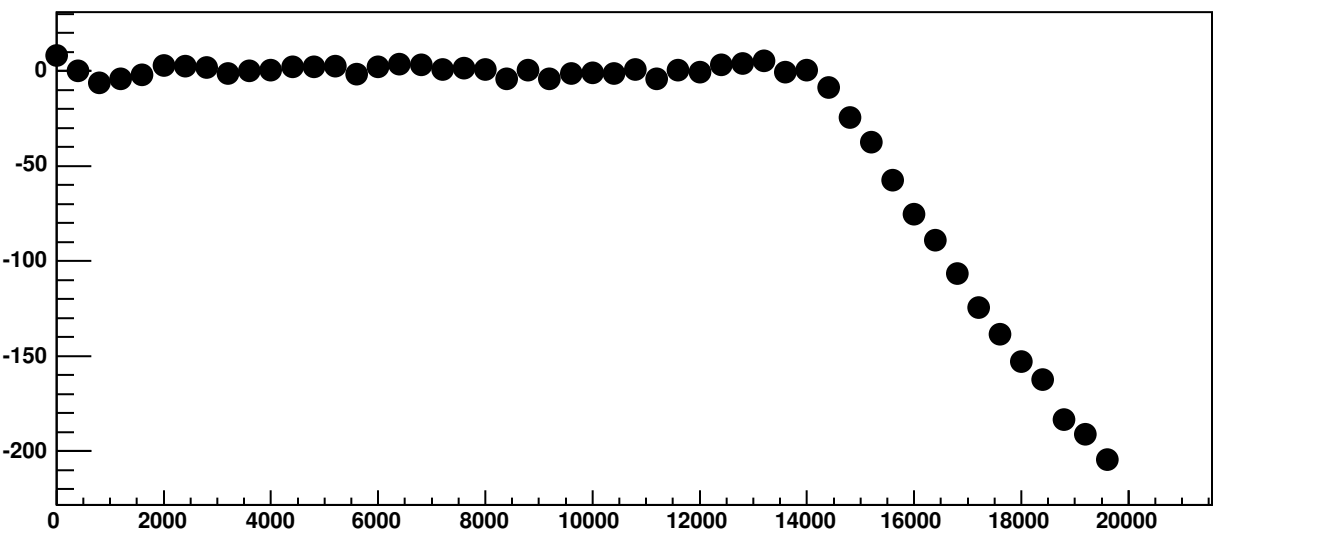
Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



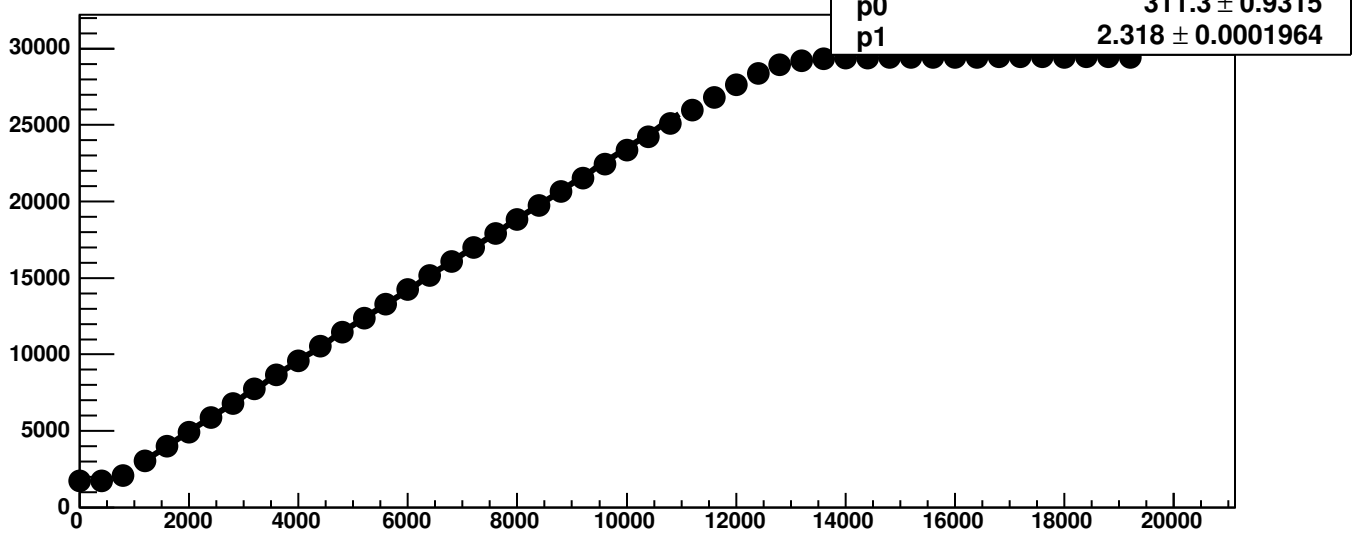
Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



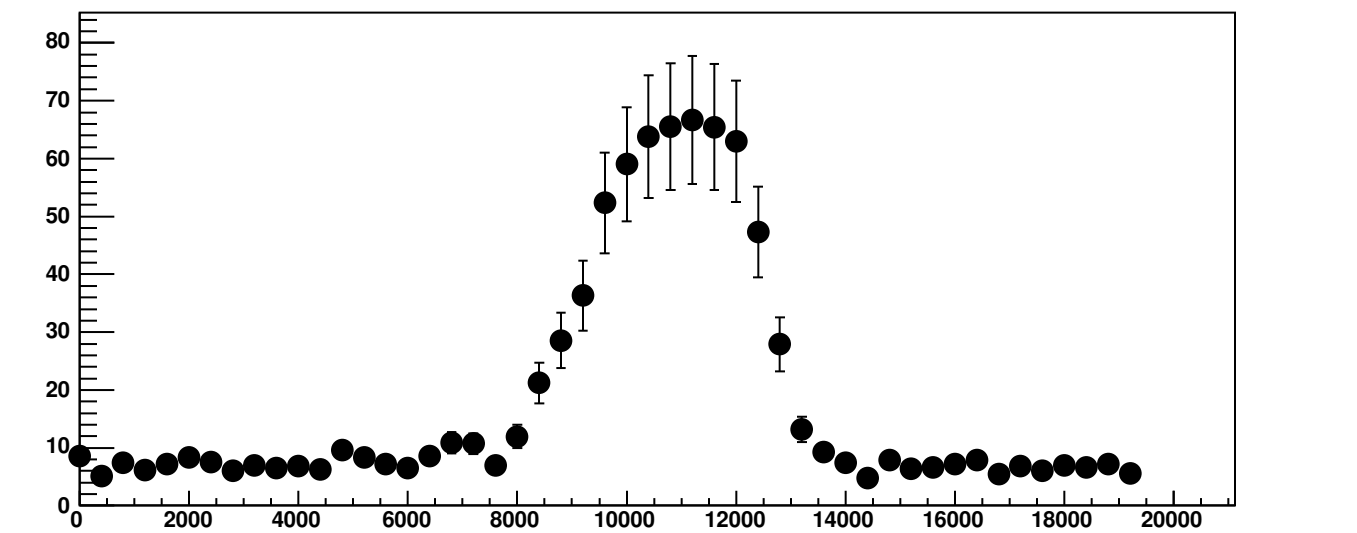
Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



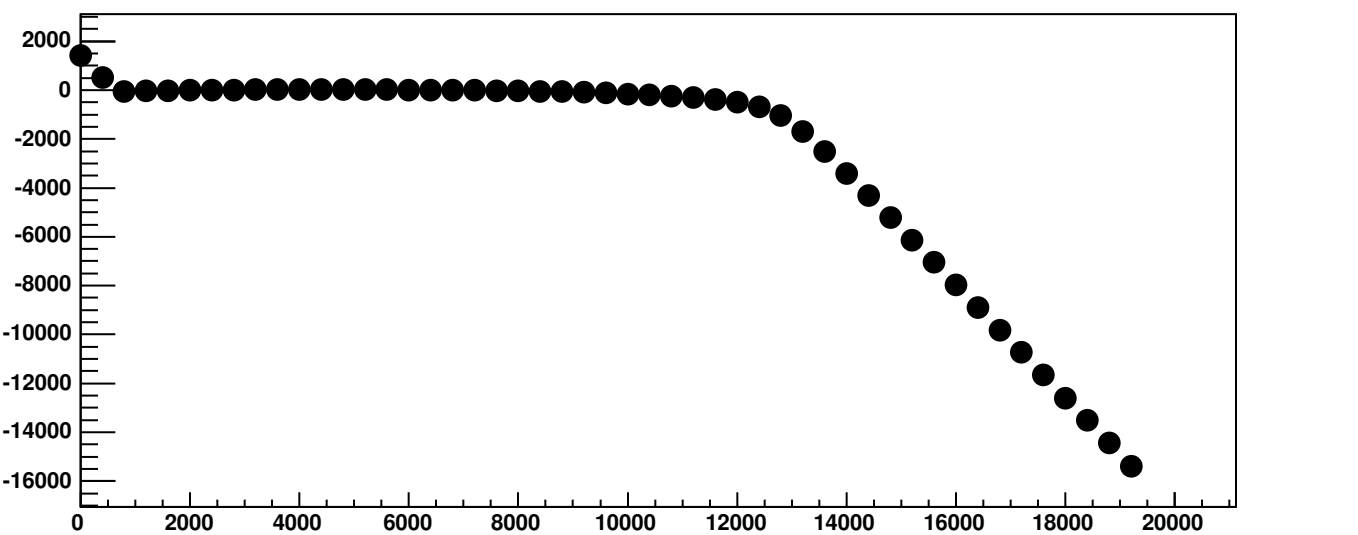
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

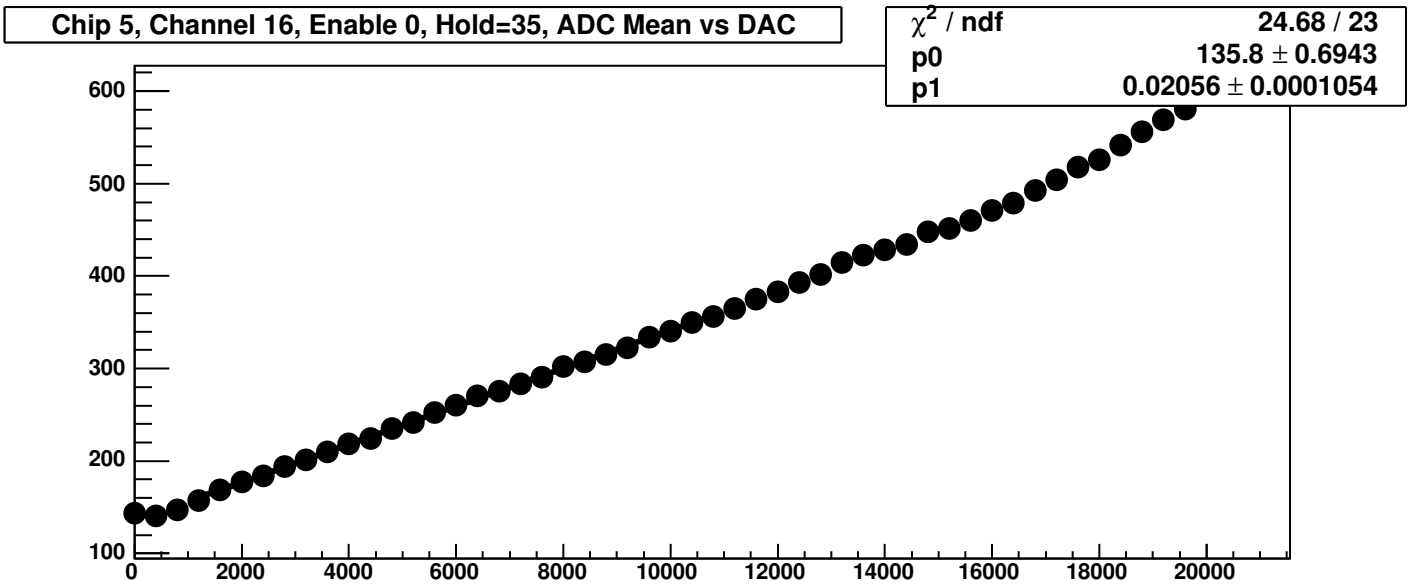


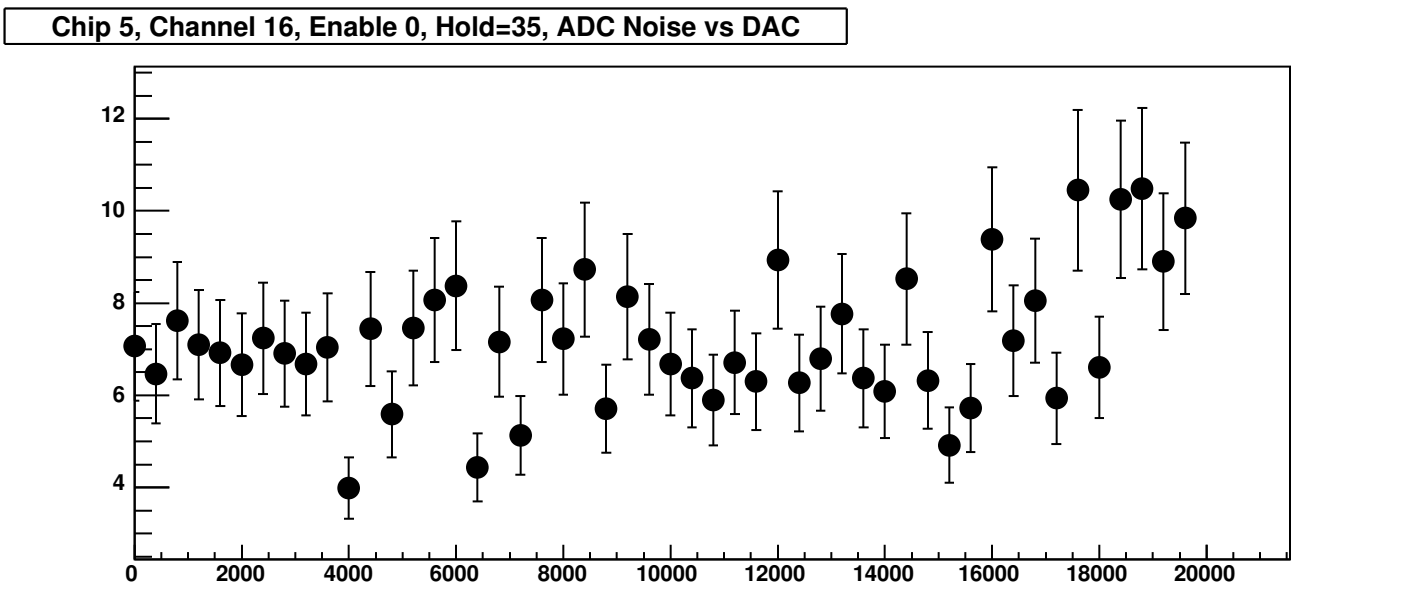
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC

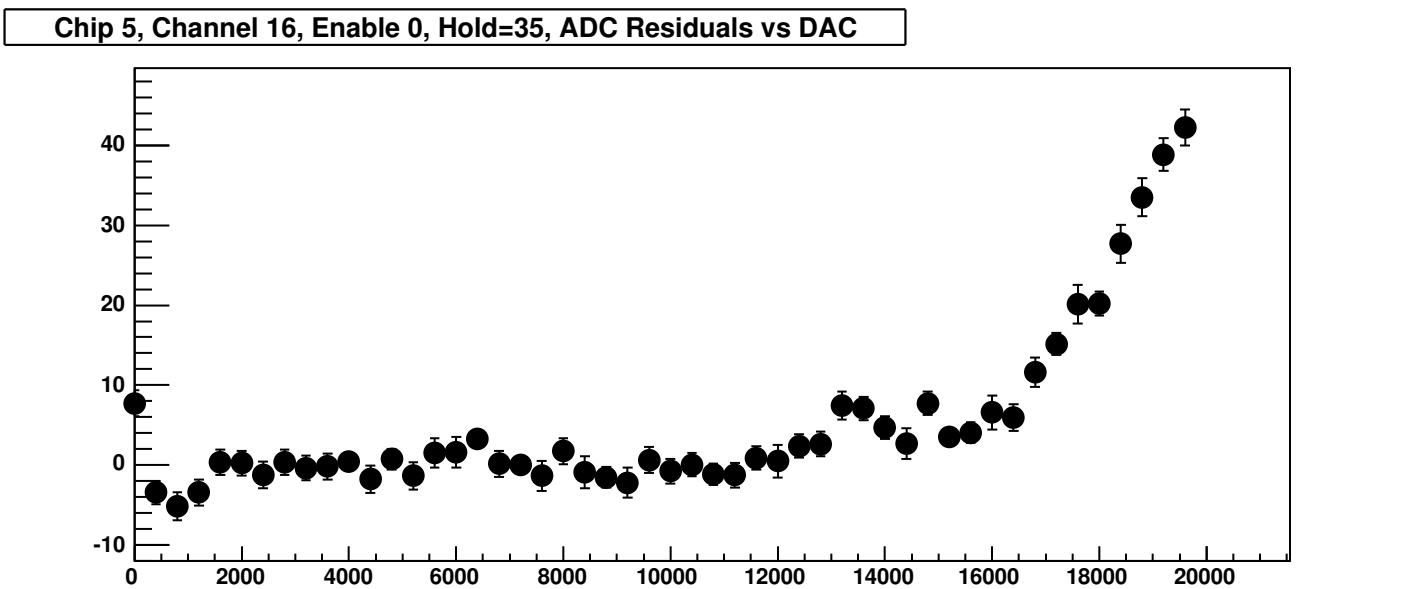


Chip 5, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



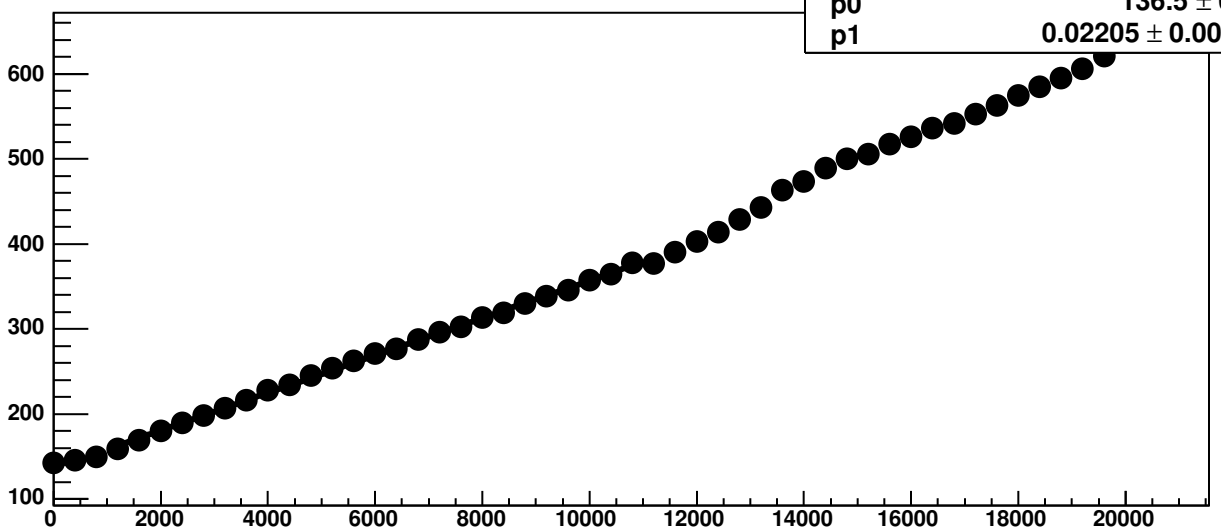

Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 24.68 / 23
p0 135.8 ± 0.6943
p1 0.02056 ± 0.0001054
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

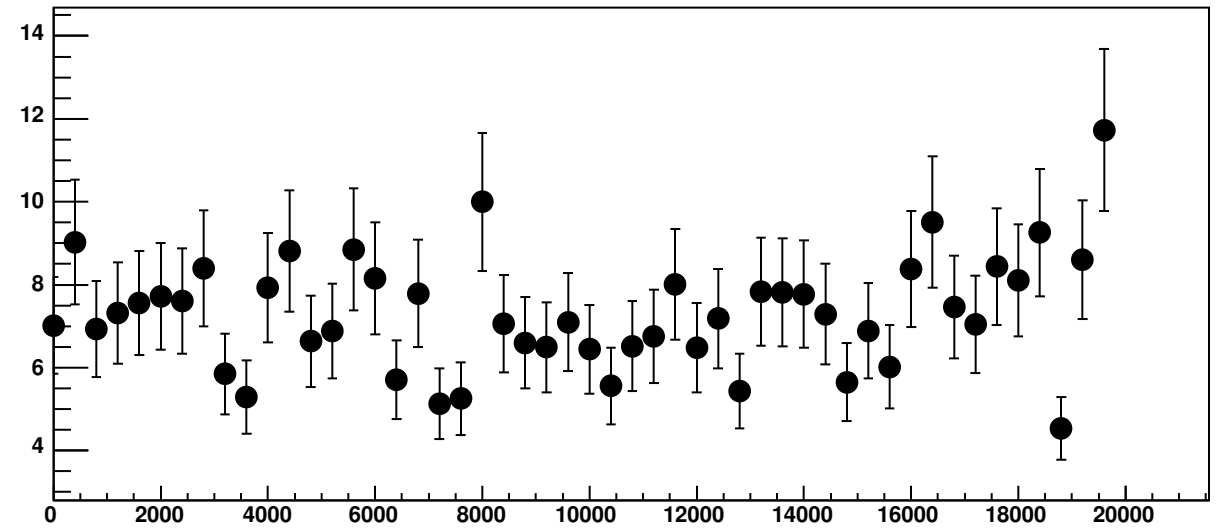

Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

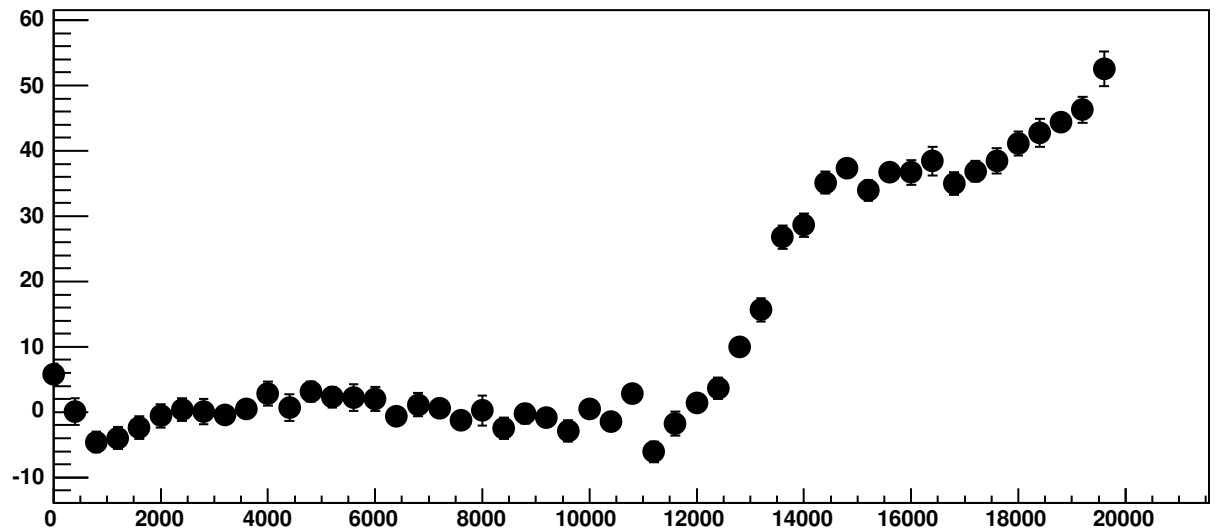
χ^2 / ndf	32.27 / 23
p0	136.5 ± 0.7461
p1	0.02205 ± 0.0001082



Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC

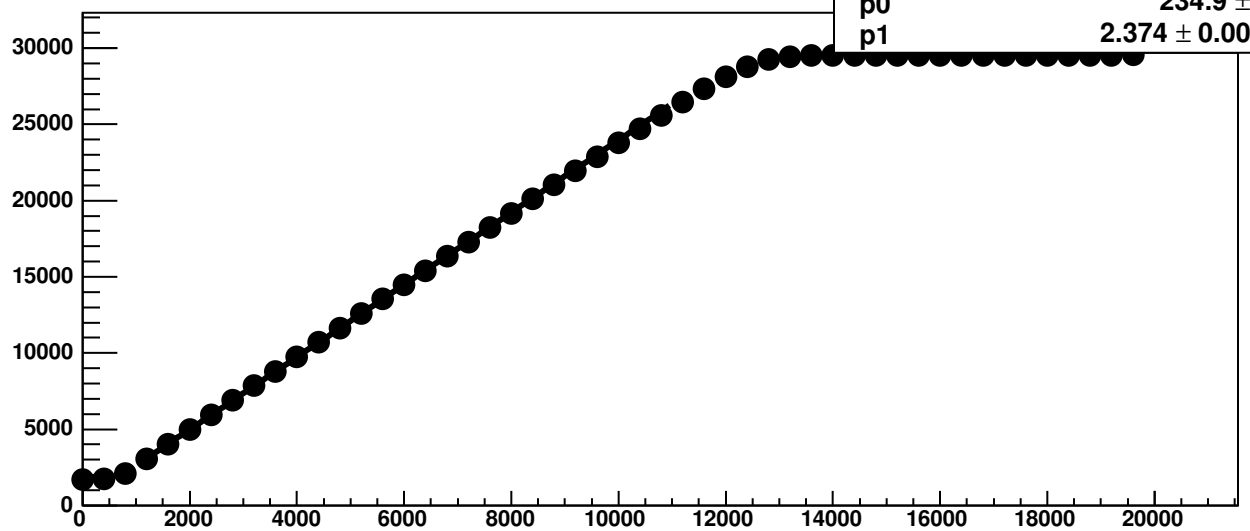


Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

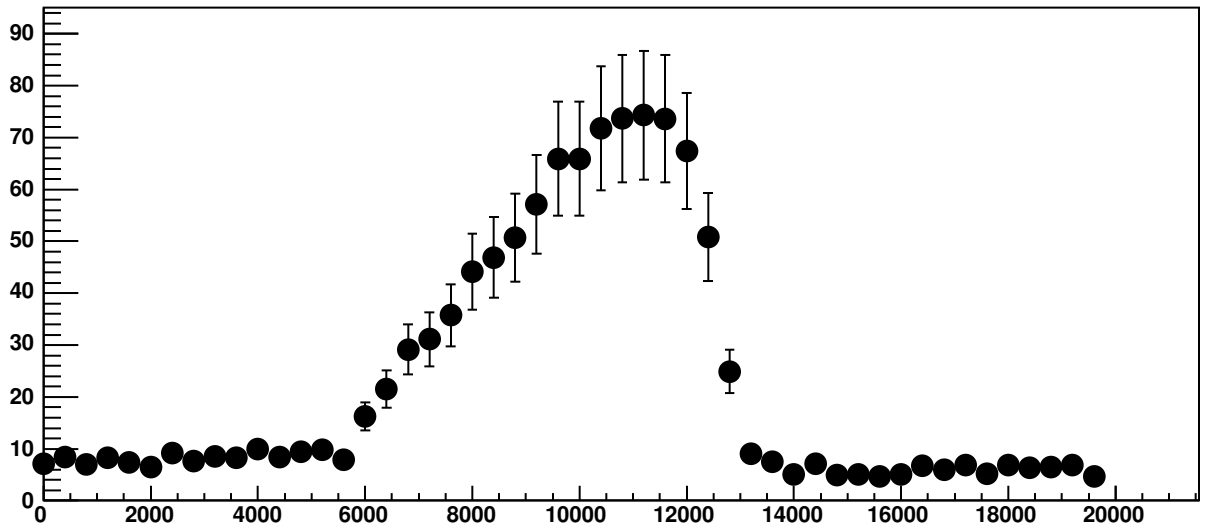


Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC

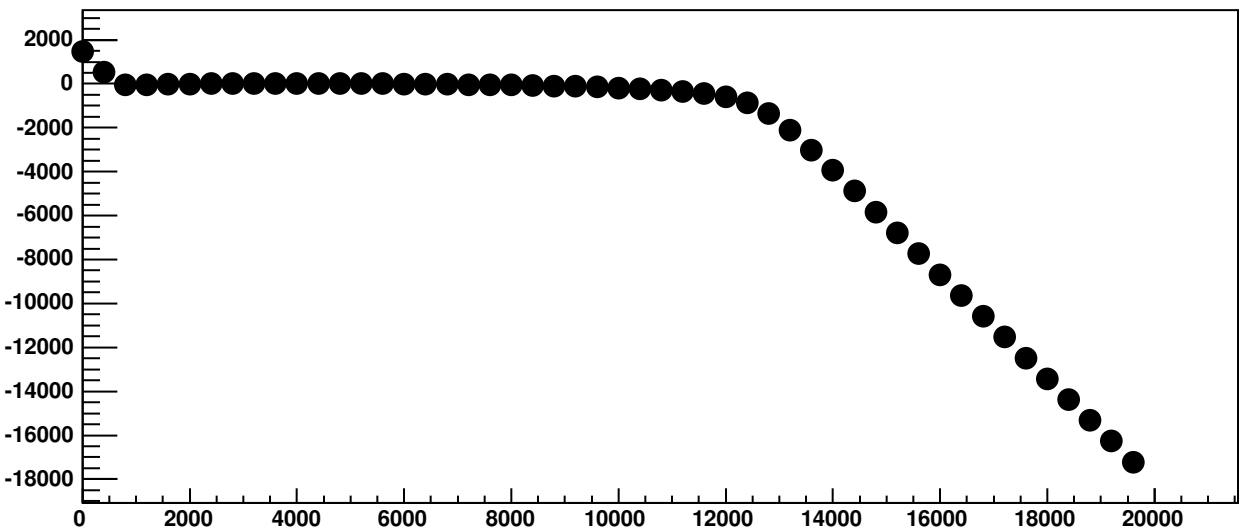
χ^2 / ndf	1680 / 23
p0	234.9 ± 1.226
p1	2.374 ± 0.0003196

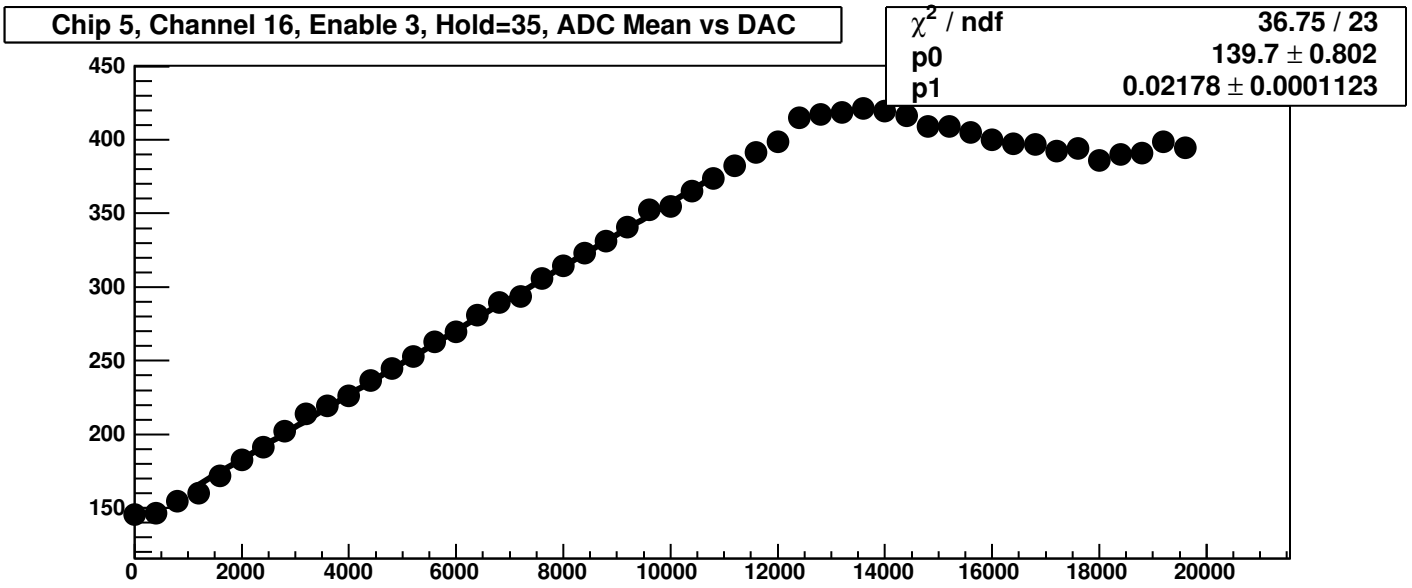


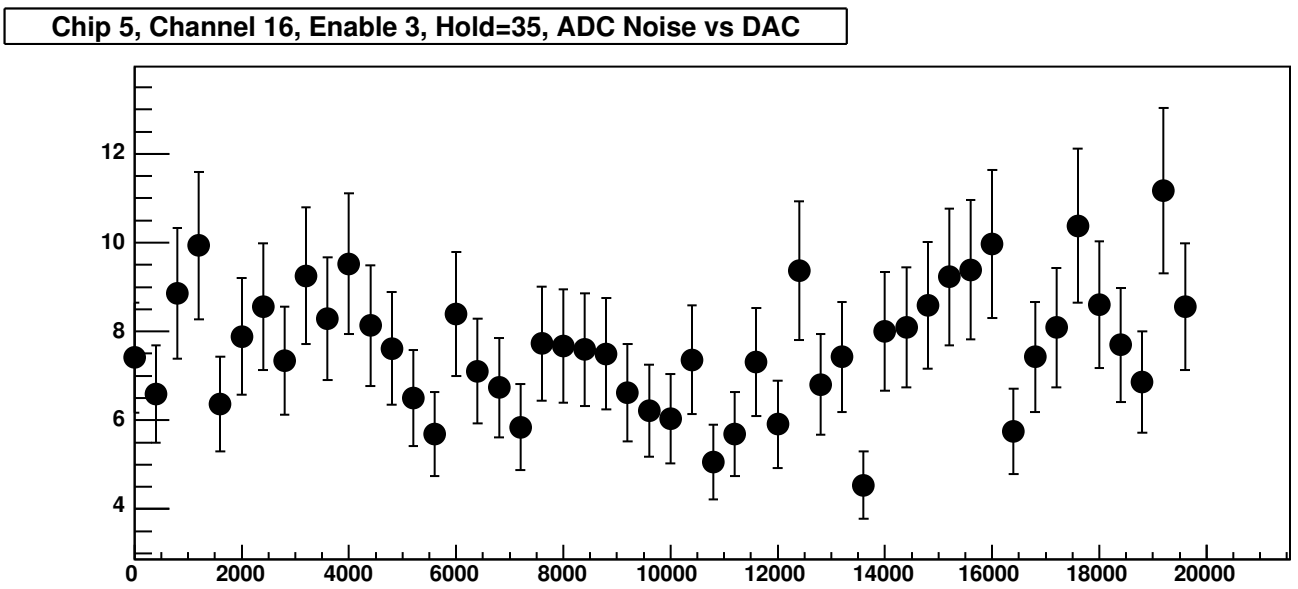
Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC

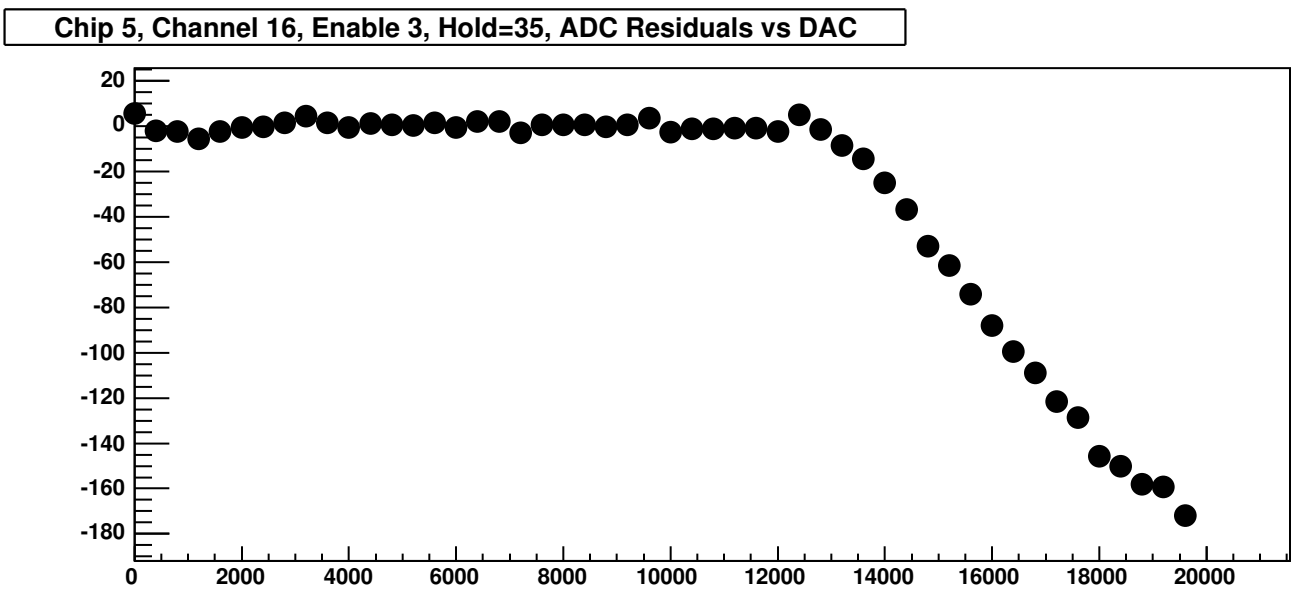


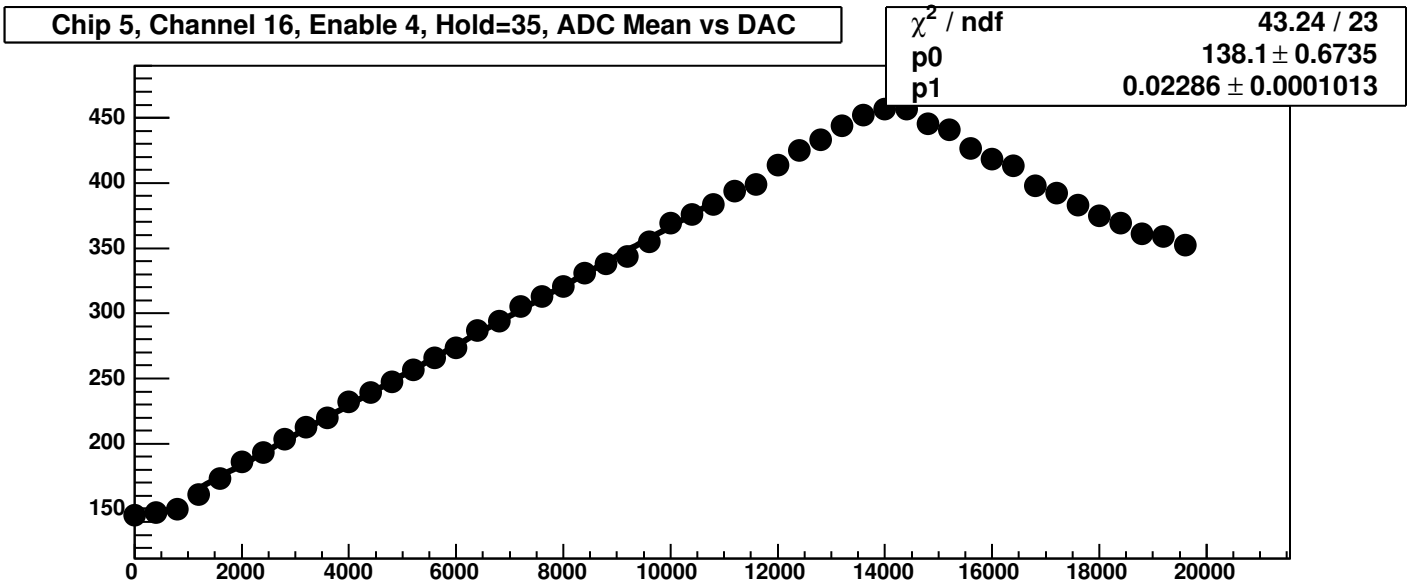
Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC

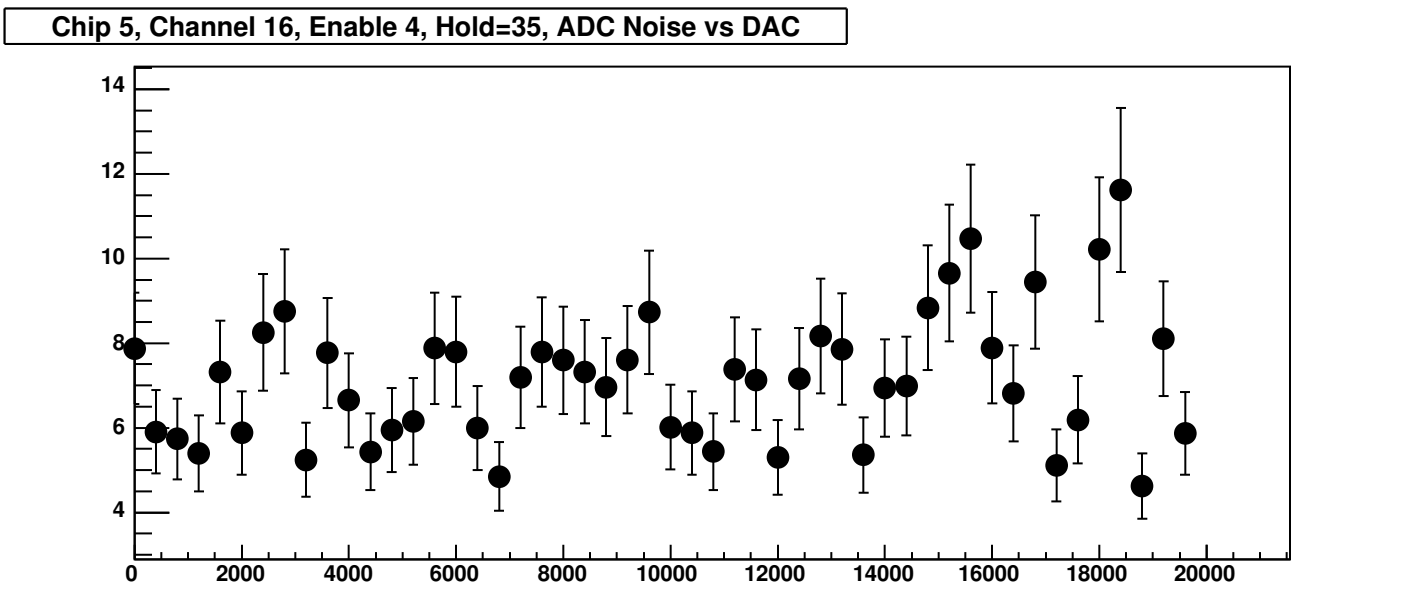


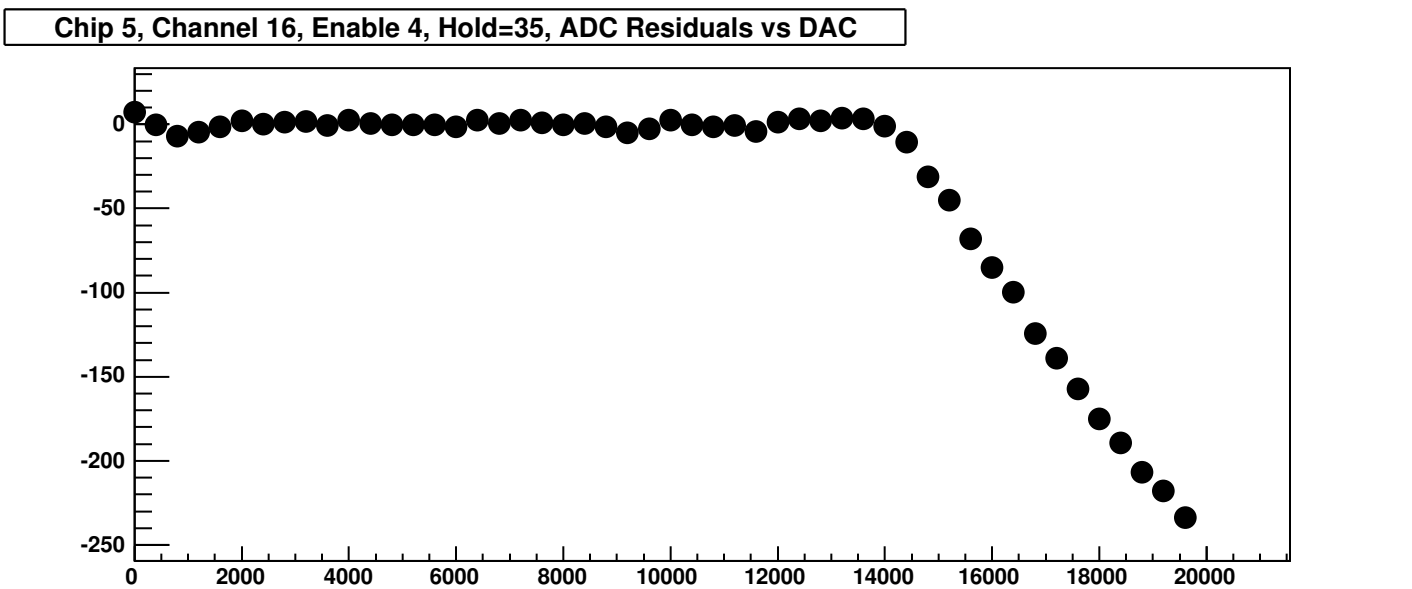

Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.75 / 23
p0 139.7 ± 0.802
p1 0.02178 ± 0.0001123
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

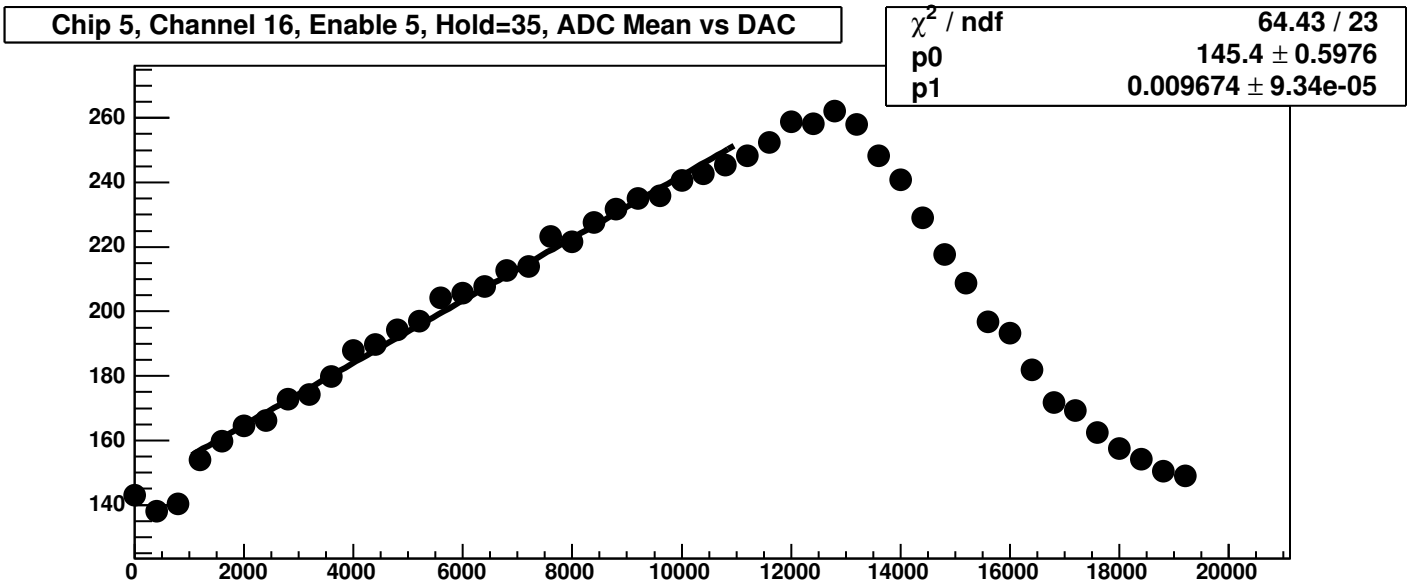

Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

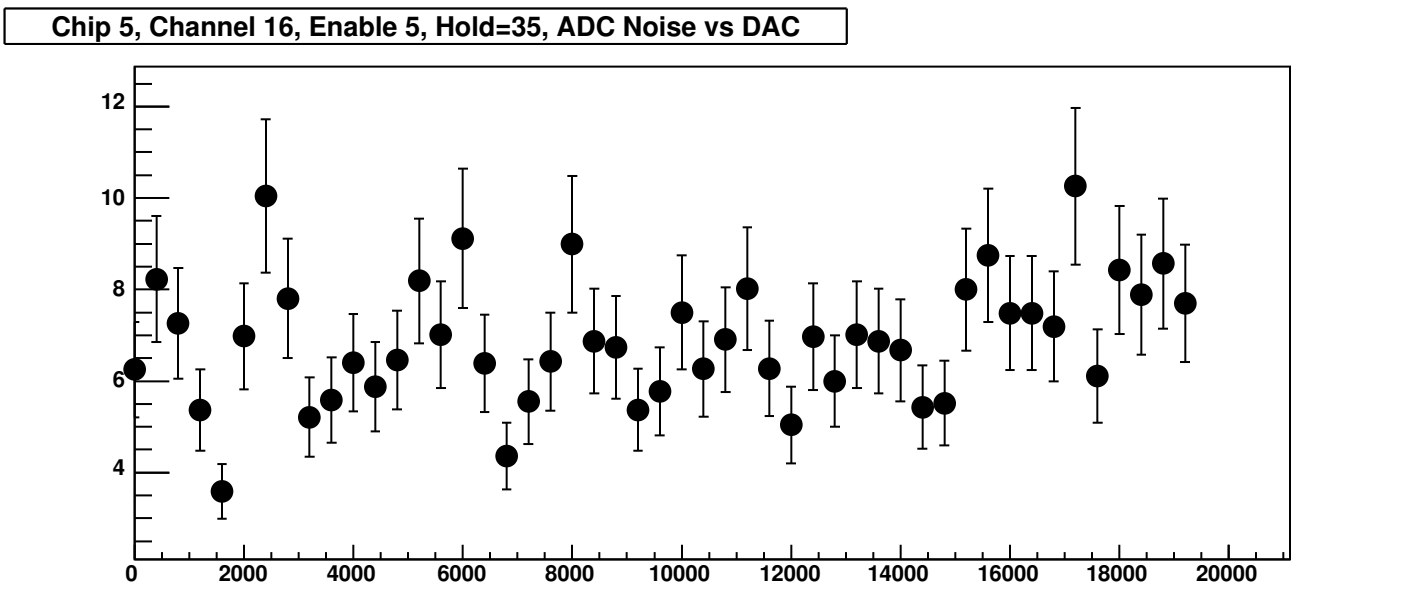

Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

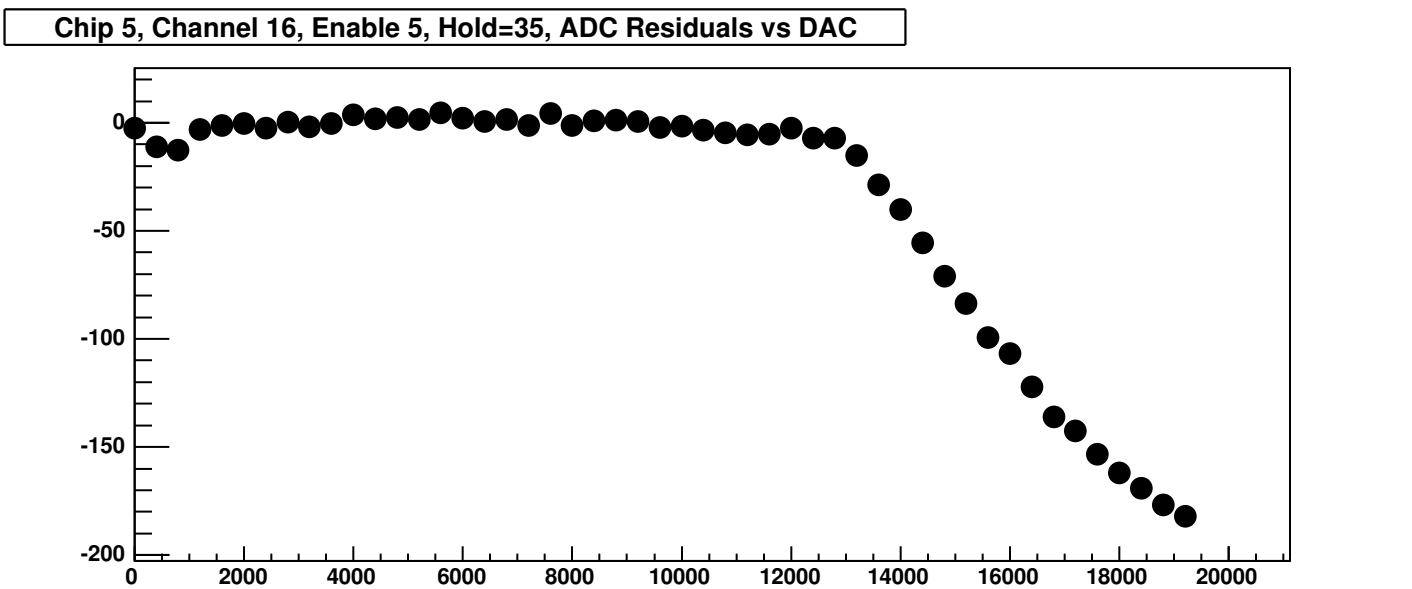

Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ^2 / ndf 43.24 / 23
p0 138.1 ± 0.6735
p1 0.02286 ± 0.0001013
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

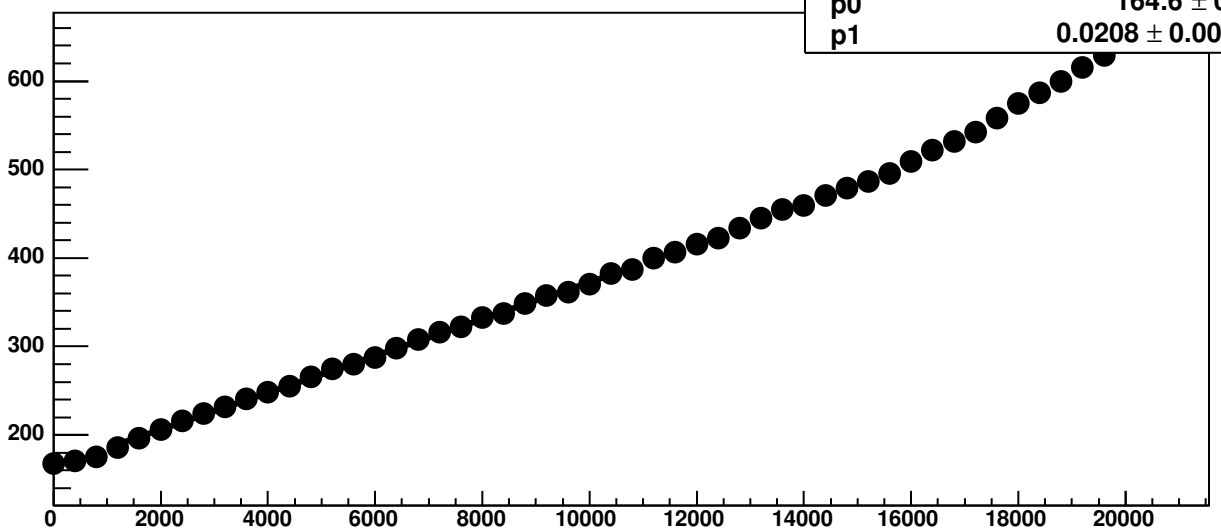

Chip 5, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 64.43 / 23
p0 145.4 ± 0.5976
p1 0.009674 ± 9.34e-05
260
240
220
200
180
160
140
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

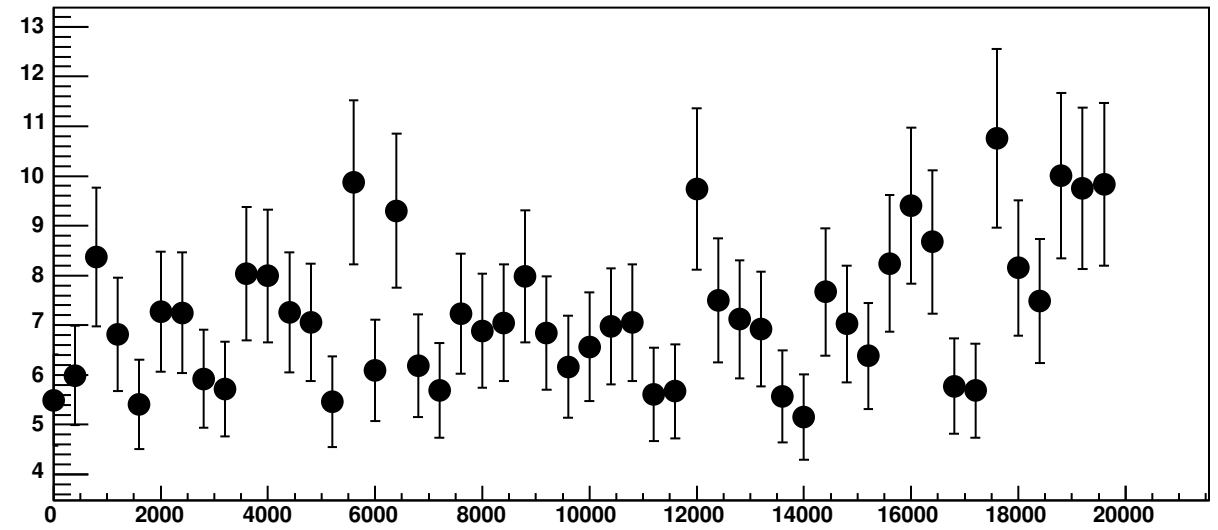

Chip 5, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

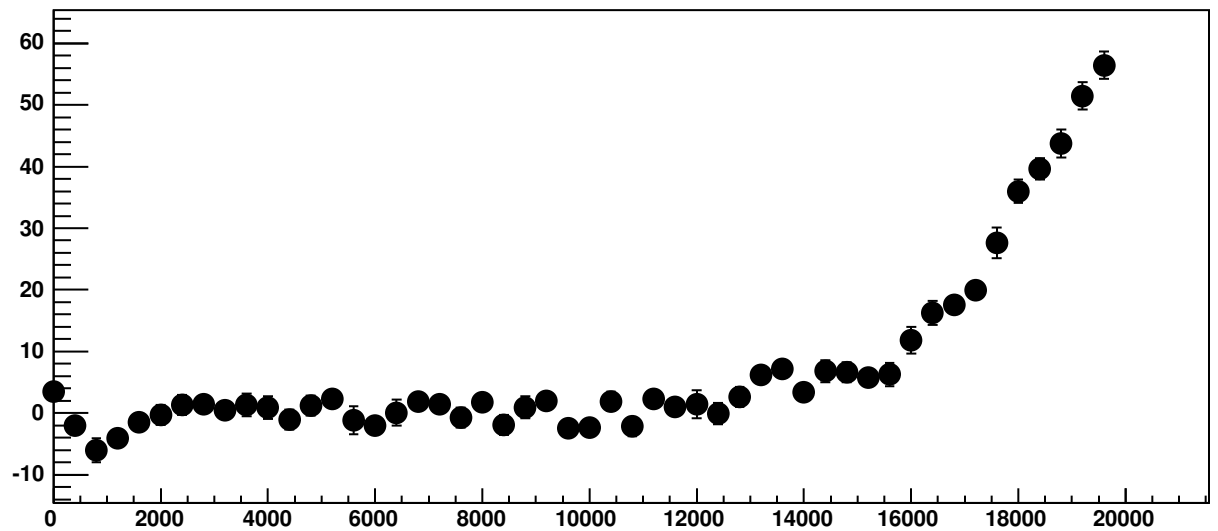
χ^2 / ndf	33.9 / 23
p0	164.6 ± 0.6956
p1	0.0208 ± 0.0001057

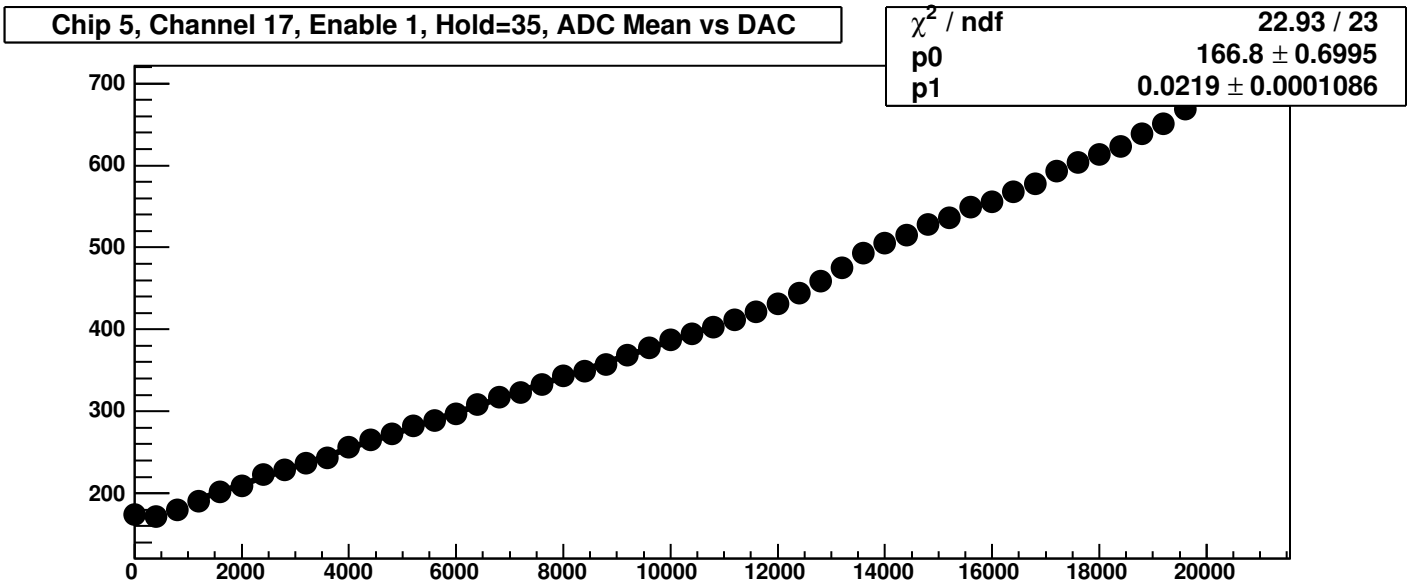


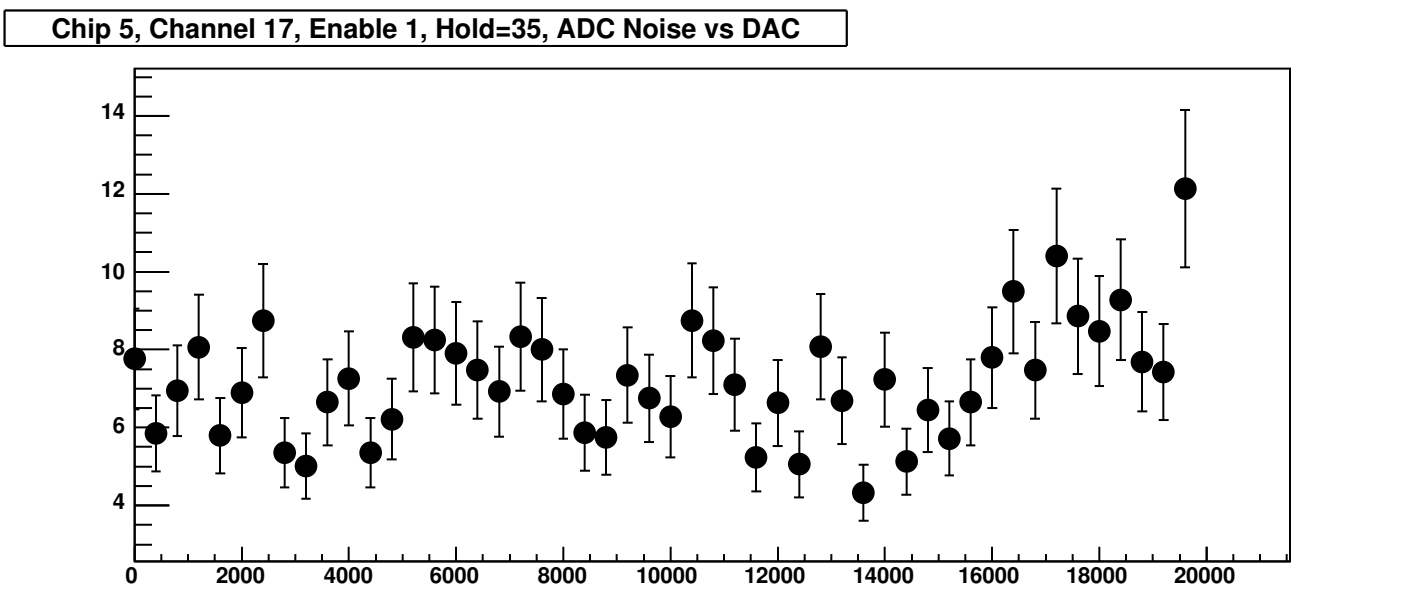
Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

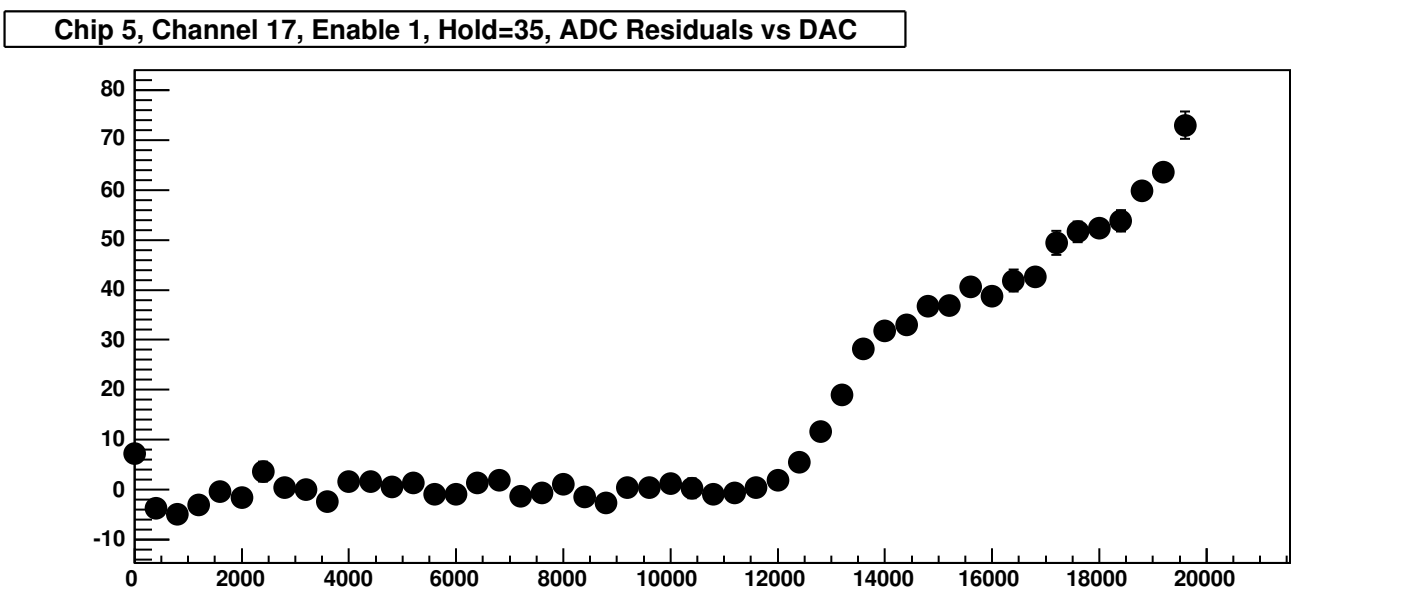


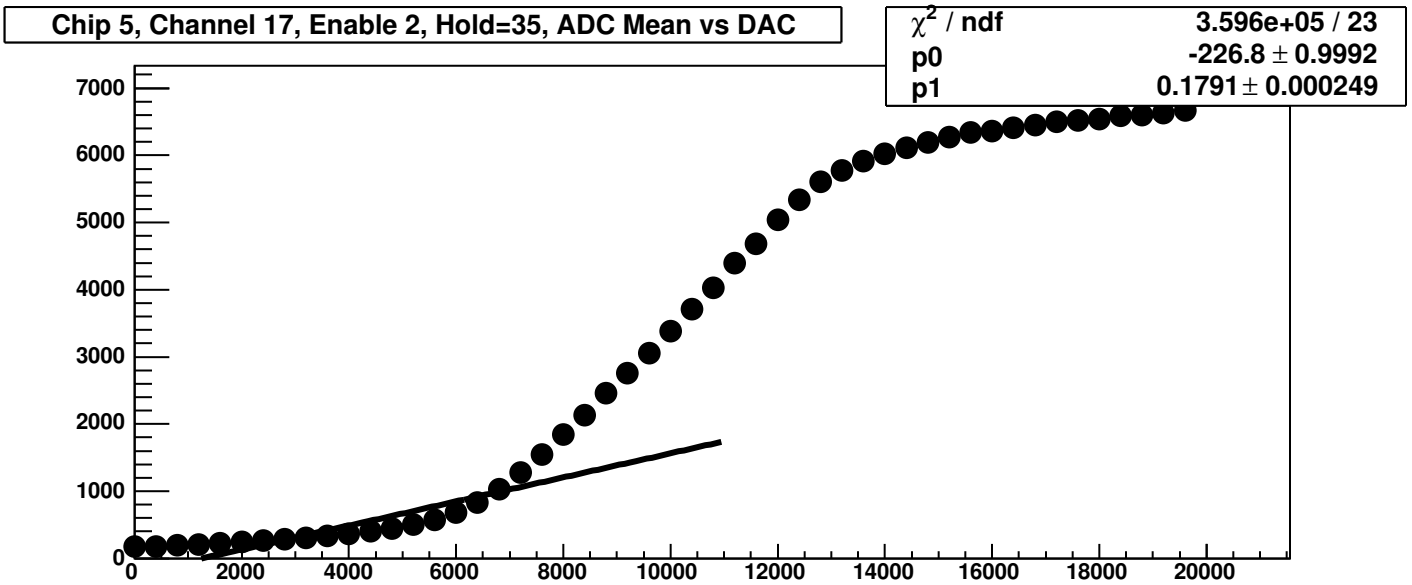
Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

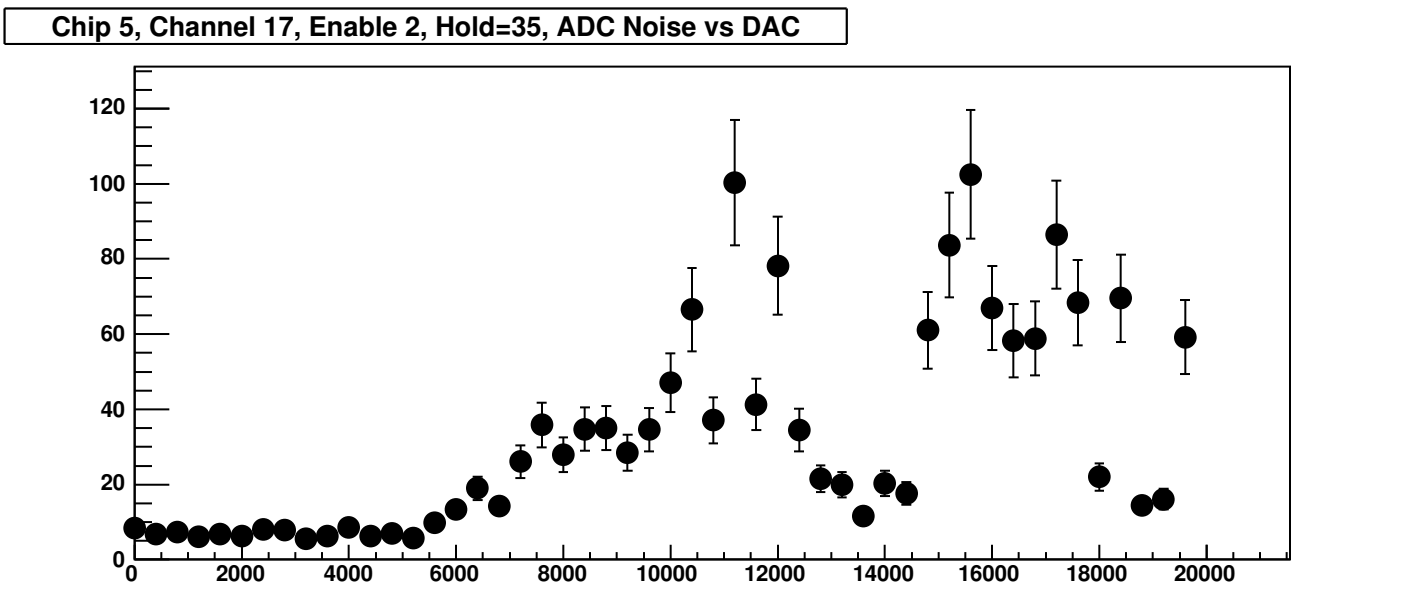


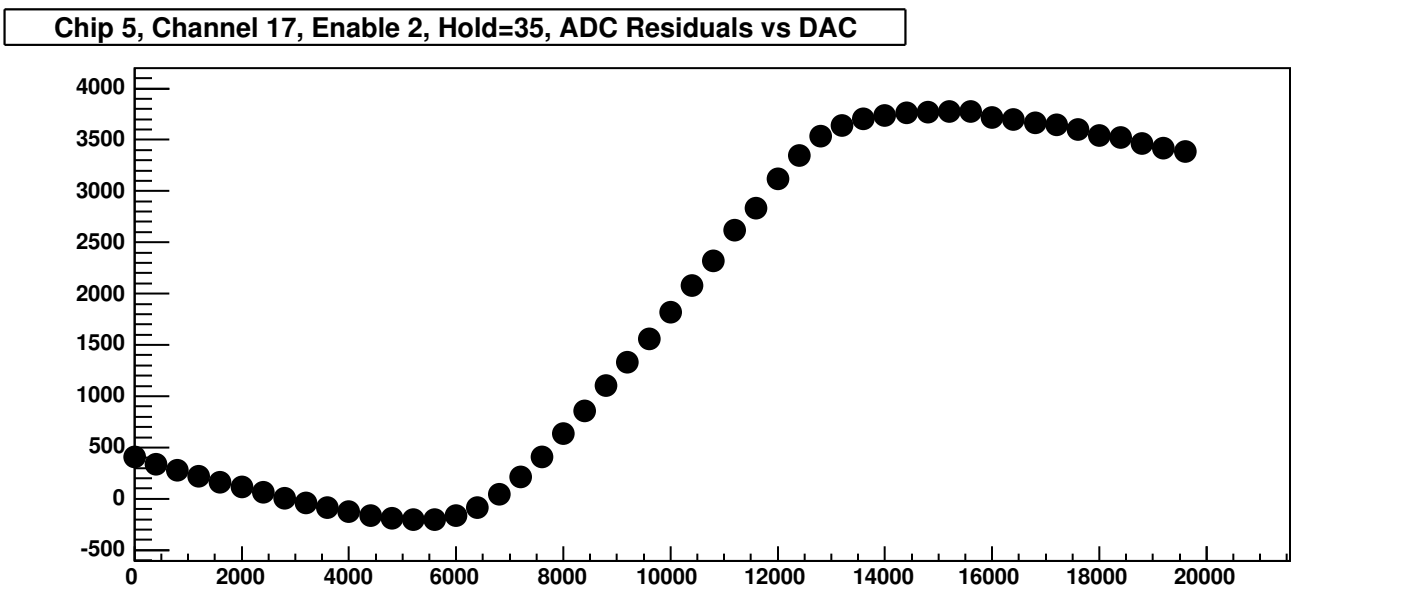

Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.93 / 23
p0 166.8 ± 0.6995
p1 0.0219 ± 0.0001086
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

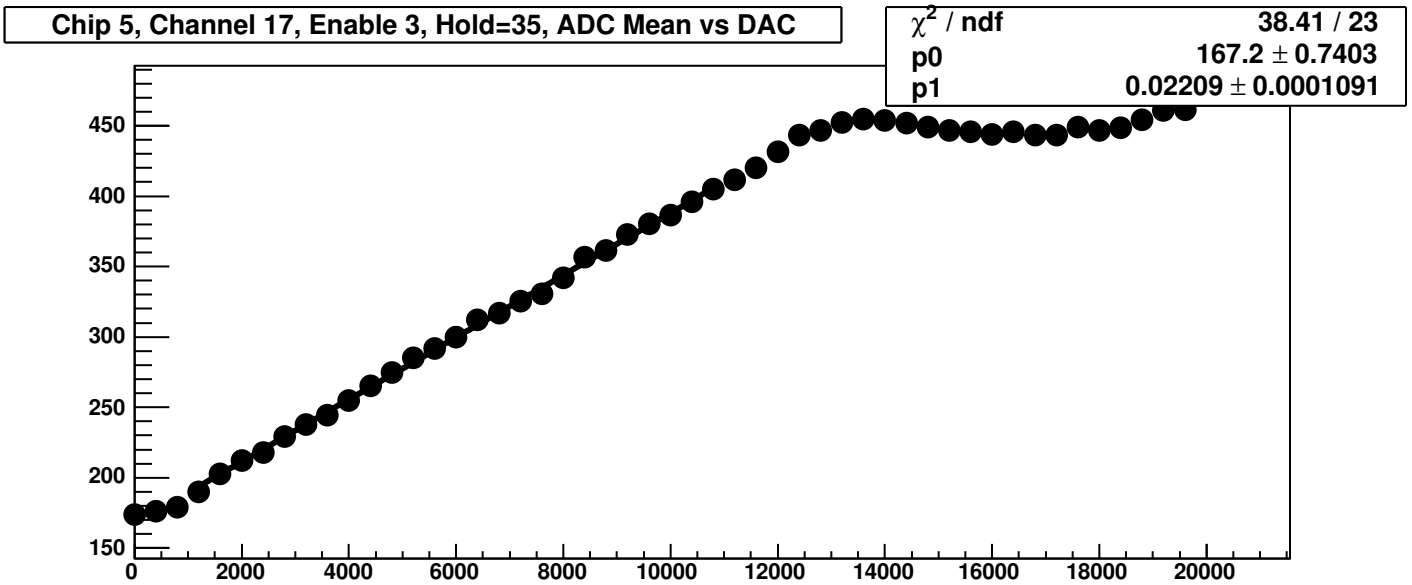

Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

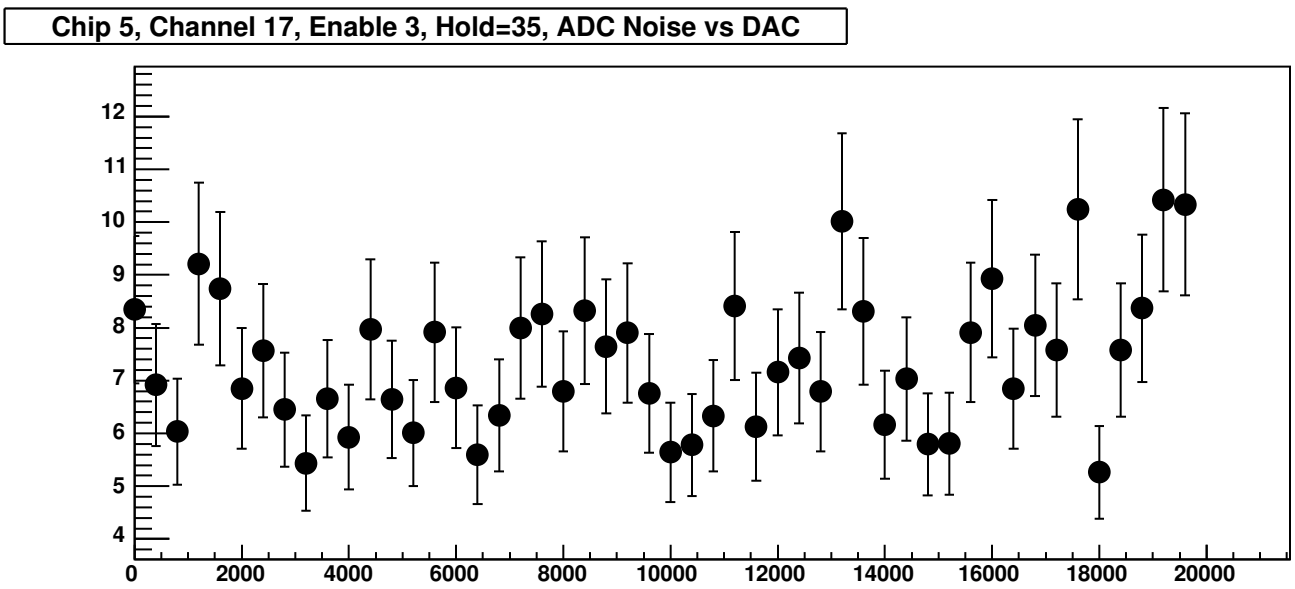

Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
80
70
60
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

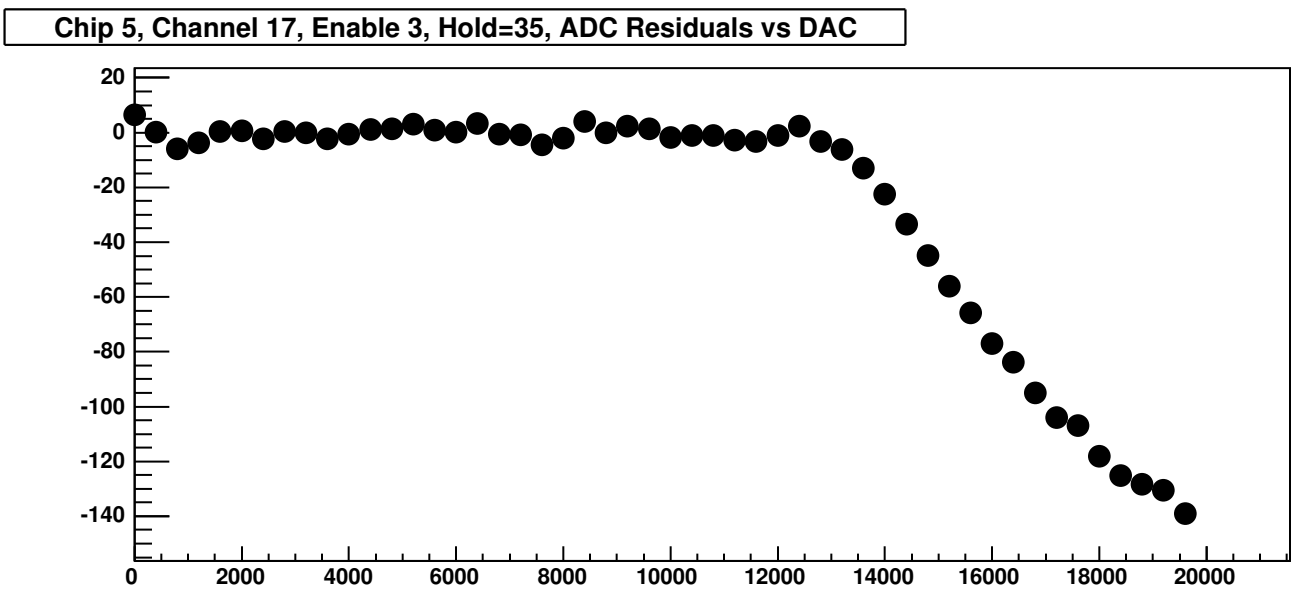

Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3.596e+05 / 23
p0 -226.8 ± 0.9992
p1 0.1791 ± 0.000249
7000
6000
5000
4000
3000
2000
1000
0
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000


Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000

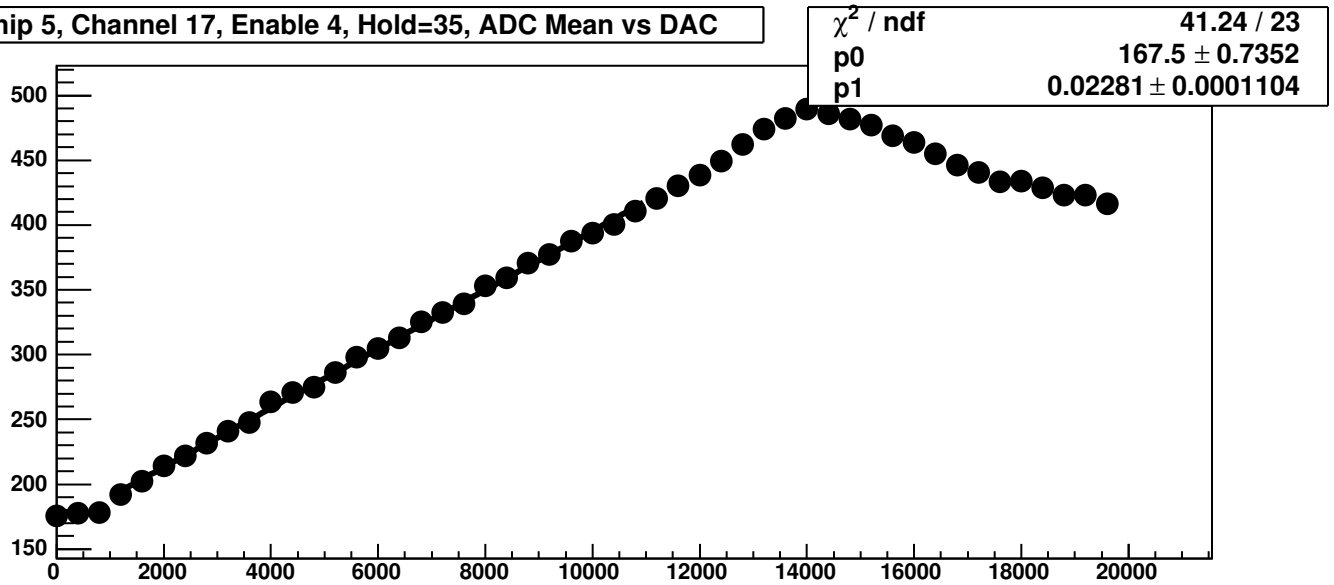

Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0 2000 4000 6000 8000 10000 12000 14000 16000 18000 20000


Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.41 / 23
p0 167.2 ± 0.7403
p1 0.02209 ± 0.0001091
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

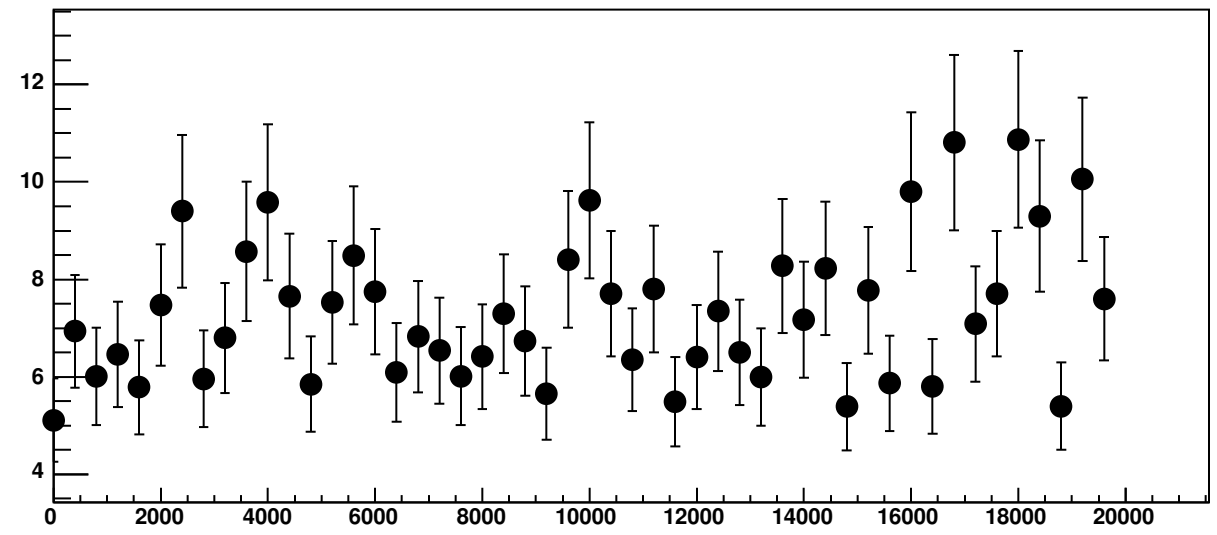
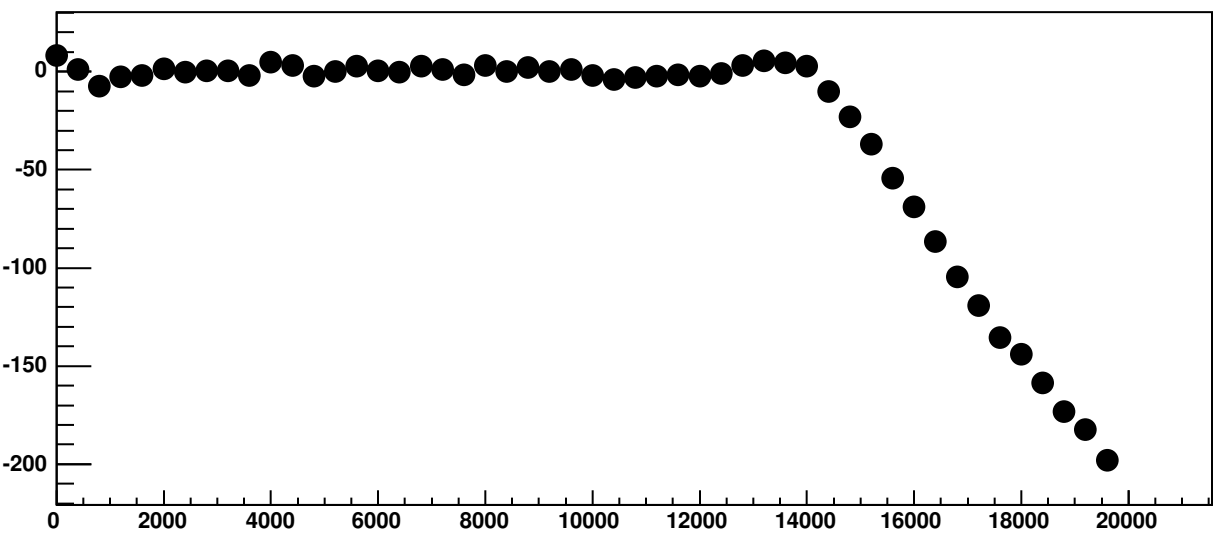

Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000


Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
2000
4000
6000
8000
10000
12000
14000
16000
18000
20000

Chip 5, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



Chip 5, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC