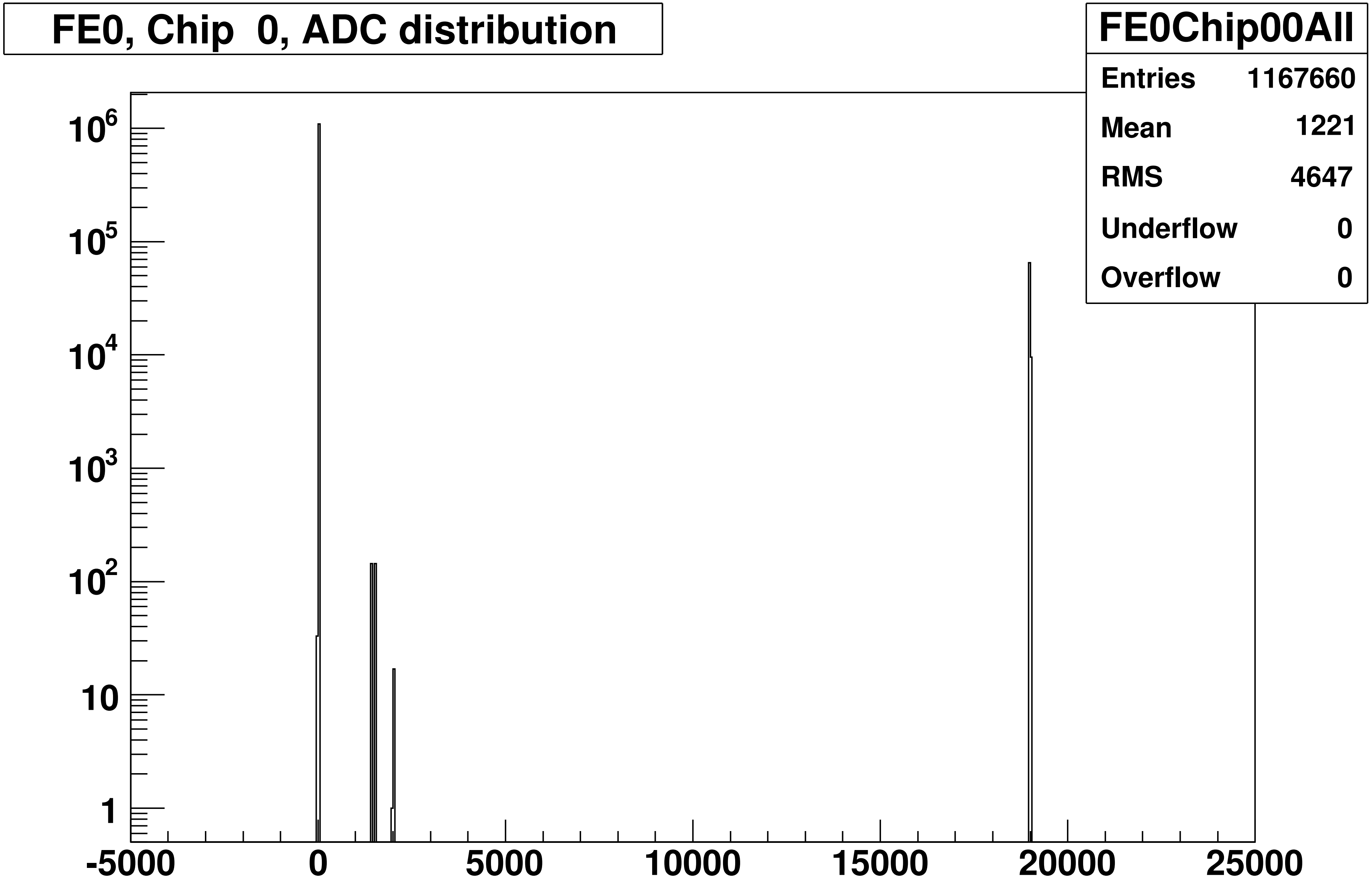
FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 1167660
Mean 1221
RMS 4647
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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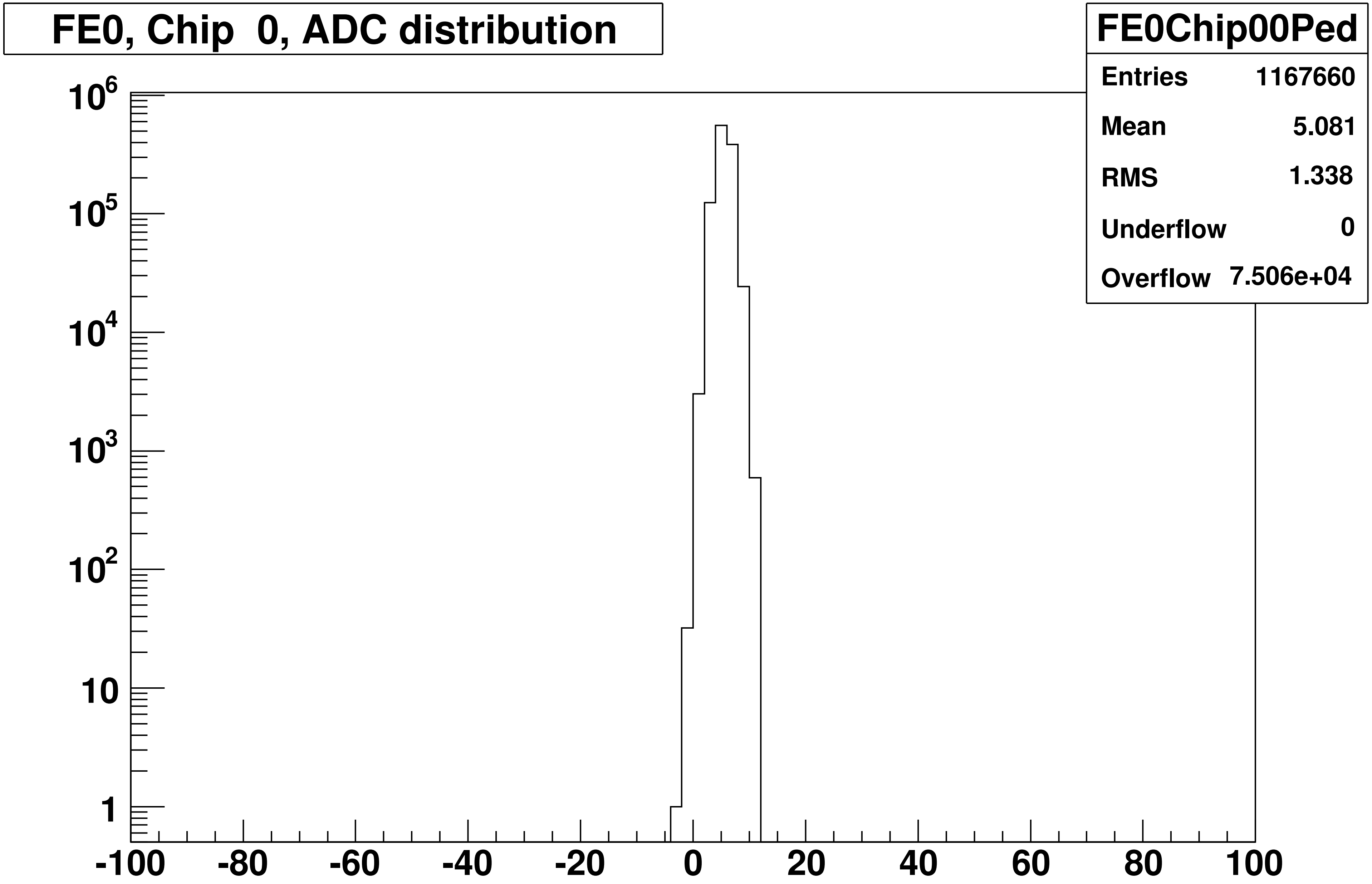
10000
15000
20000
25000



FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 1167660
Mean 5.081
RMS 1.338
Underflow 0
Overflow 7.506e+04
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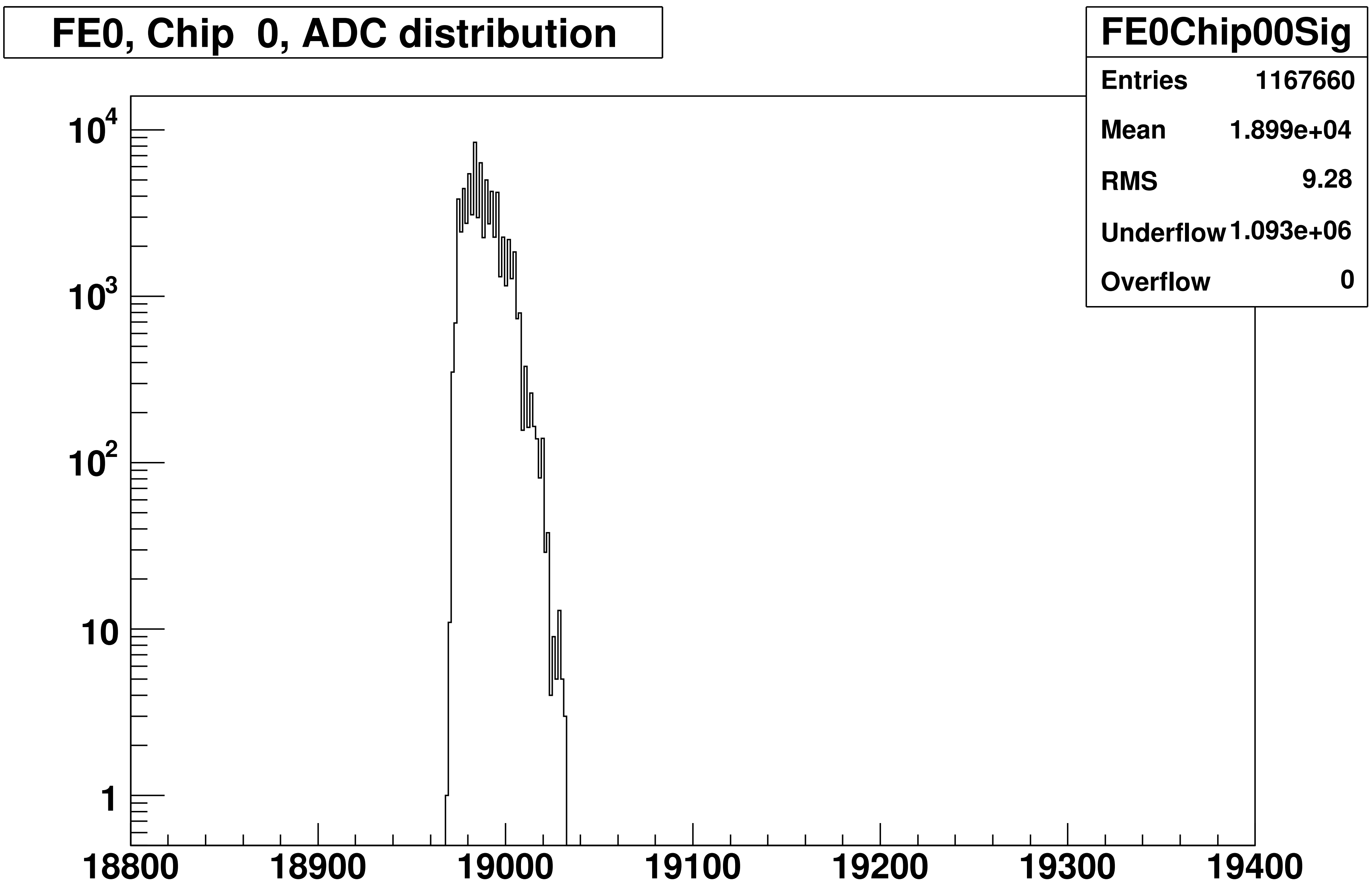
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 1167660
Mean 1.899e+04
RMS 9.28
Underflow 1.093e+06
Overflow 0
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10^3
10^2
10
1
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18900
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19300
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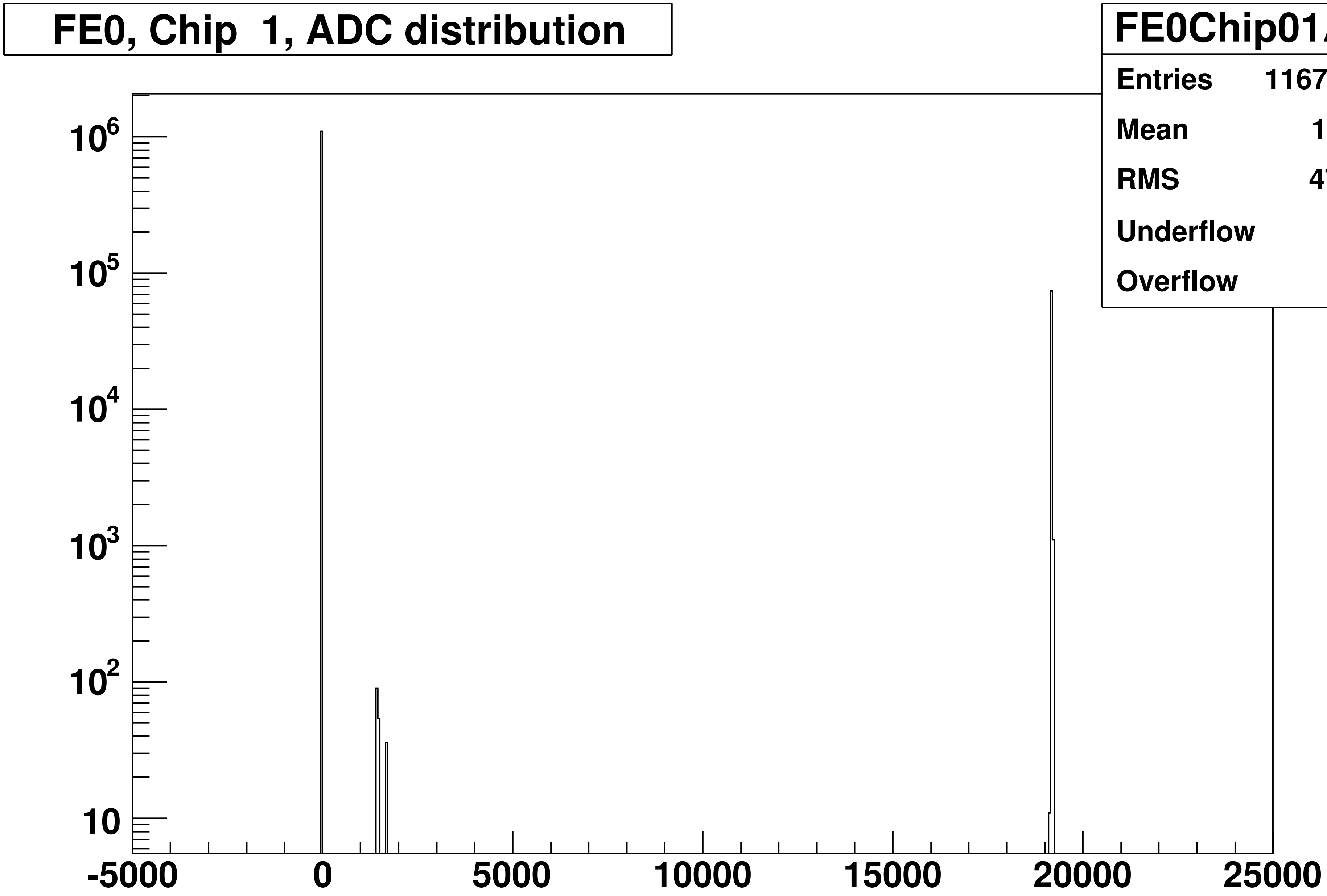

FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 1167660
Mean 1195
RMS 4709
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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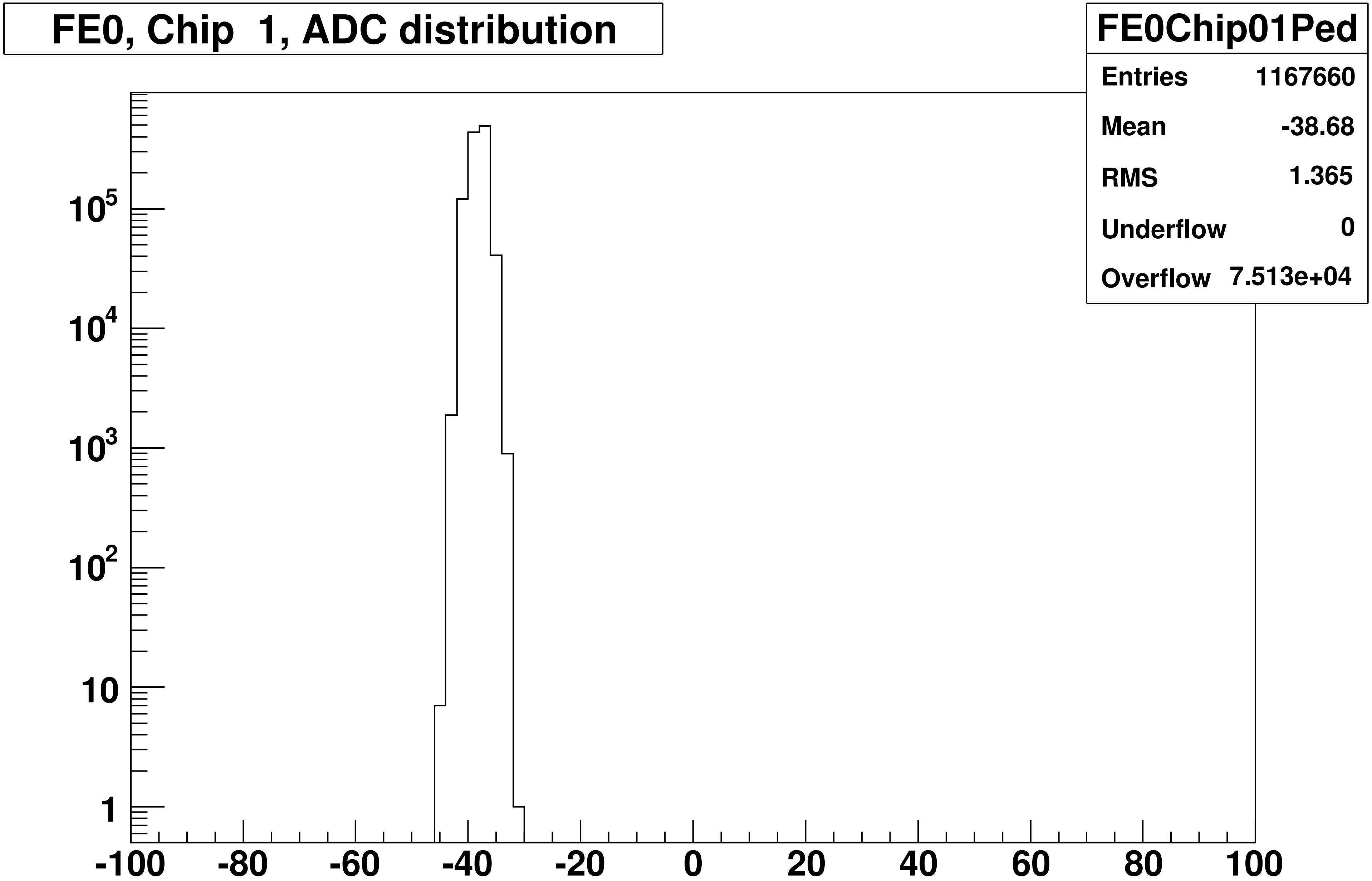
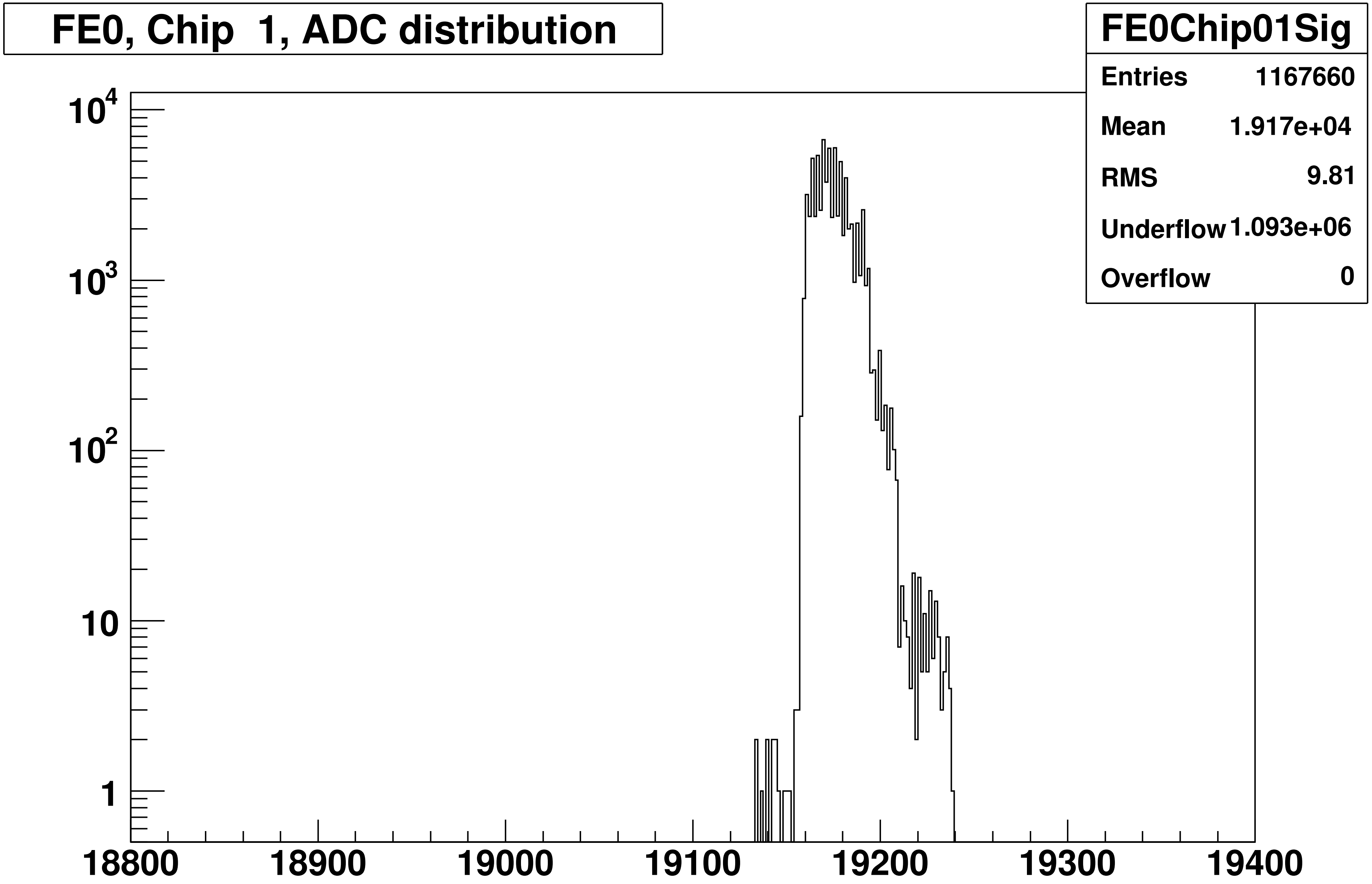

FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 1167660
Mean -38.68
RMS 1.365
Underflow 0
Overflow 7.513e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 1167660
Mean 1.917e+04
RMS 9.81
Underflow 1.093e+06
Overflow 0
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10^3
10^2
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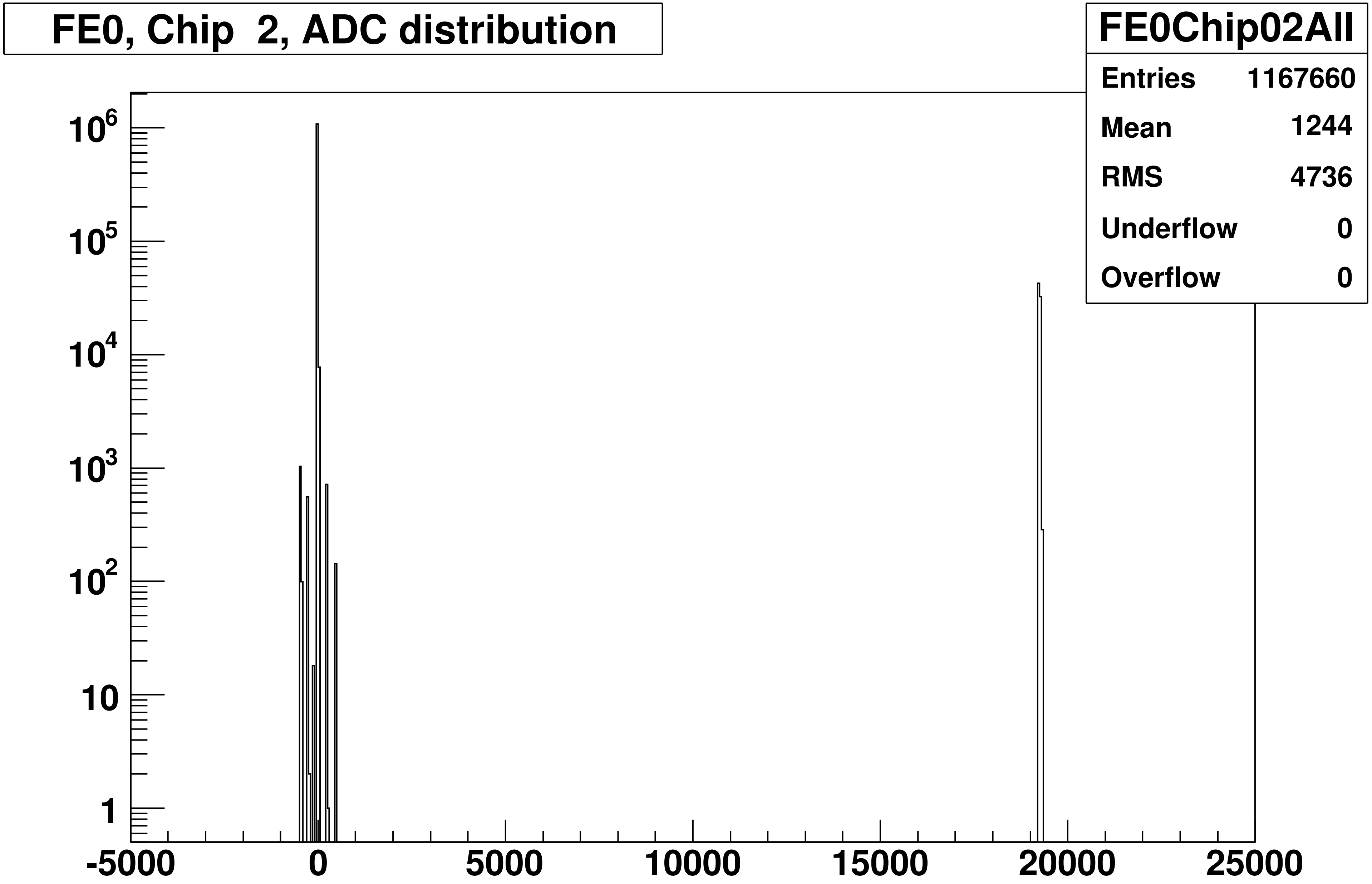

FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 1167660
Mean 1244
RMS 4736
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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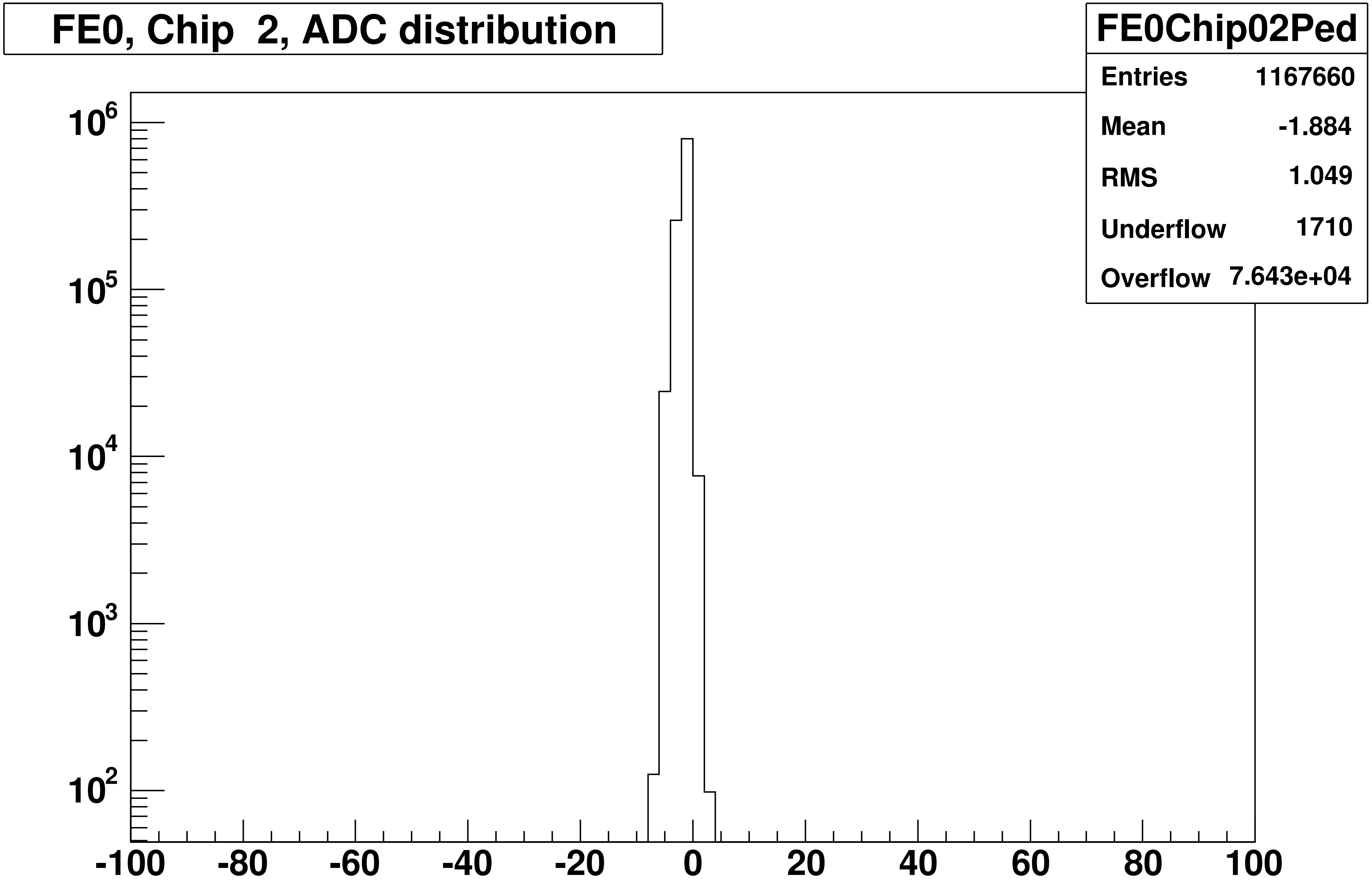
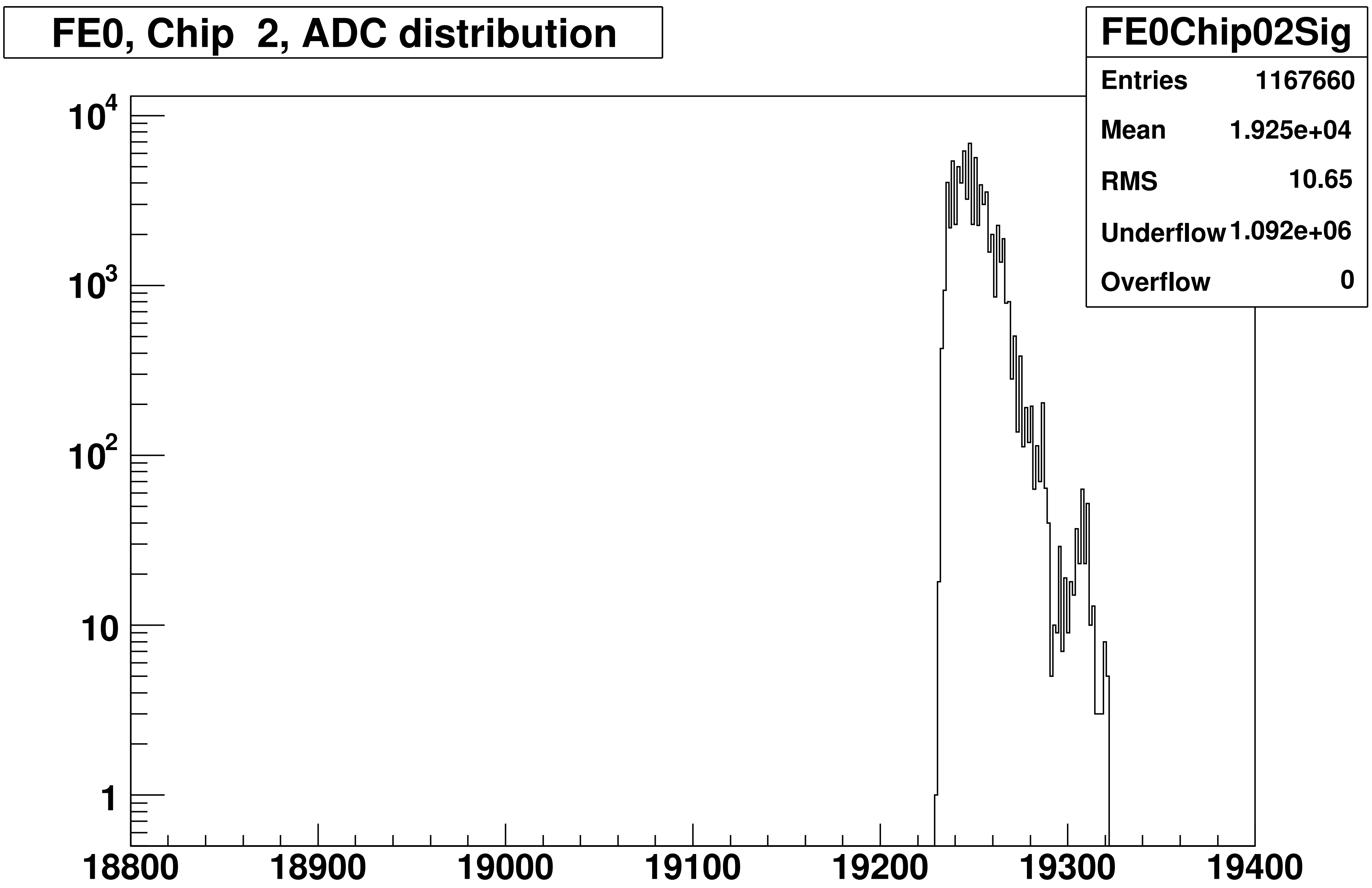
10000
15000
20000
25000


FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 1167660
Mean -1.884
RMS 1.049
Underflow 1710
Overflow 7.643e+04
10^6
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 1167660
Mean 1.925e+04
RMS 10.65
Underflow 1.092e+06
Overflow 0
10^4
10^3
10^2
10
1
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18900
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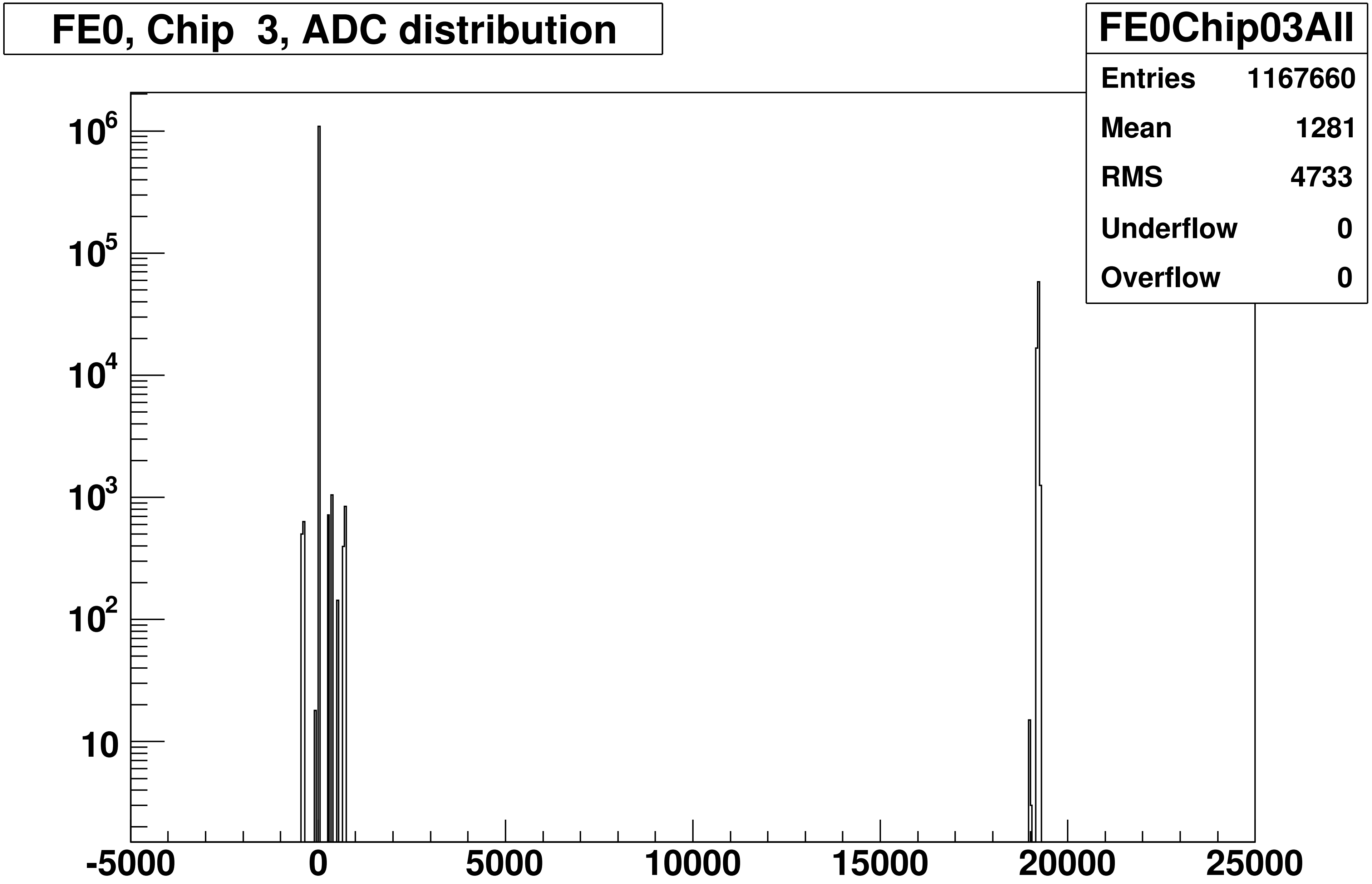
19200
19300
19400


FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 1167660
Mean 1281
RMS 4733
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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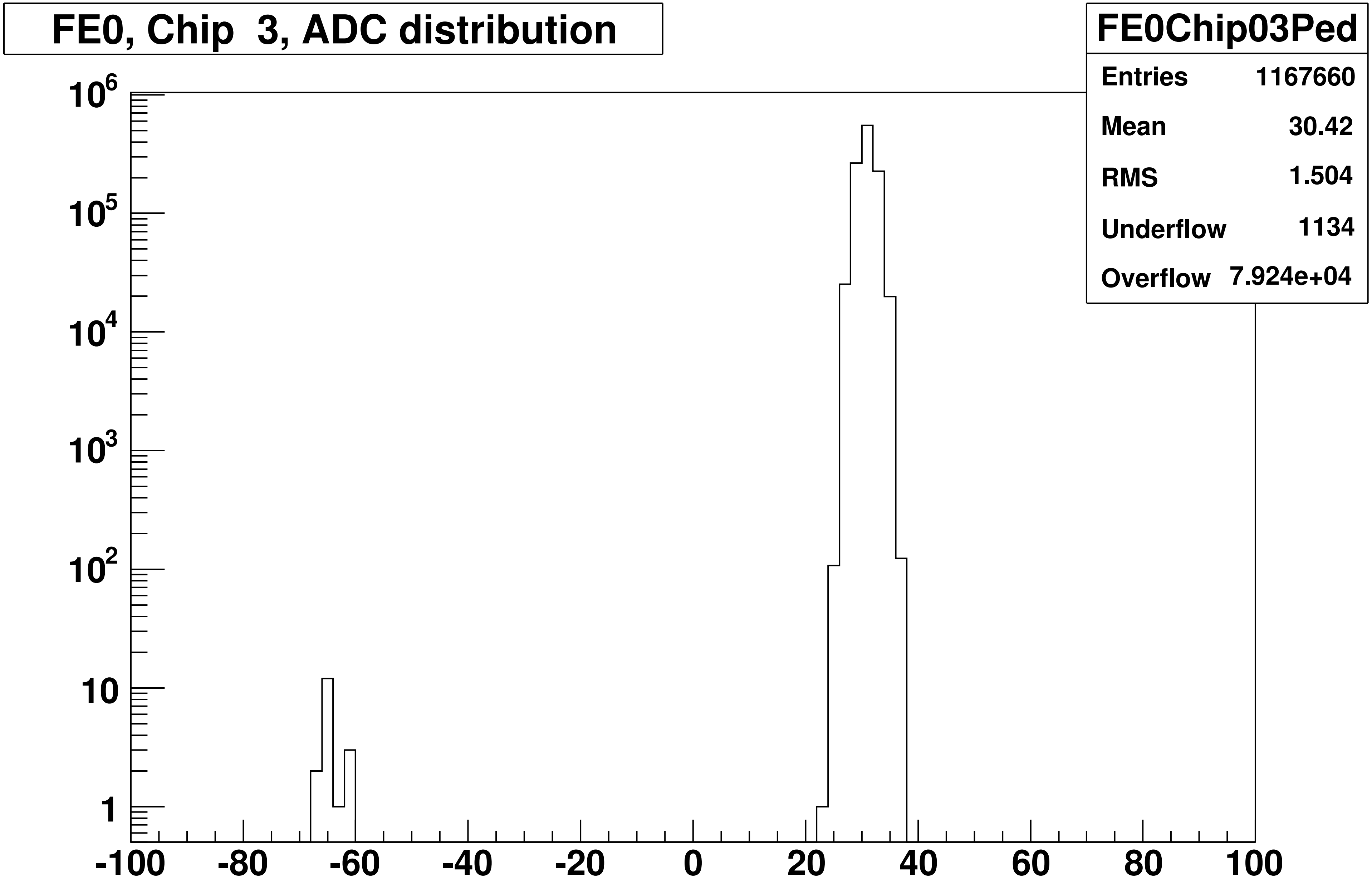
10000
15000
20000
25000


FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 1167660
Mean 30.42
RMS 1.504
Underflow 1134
Overflow 7.924e+04
10^6
10^5
10^4
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10^2
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-40
-20
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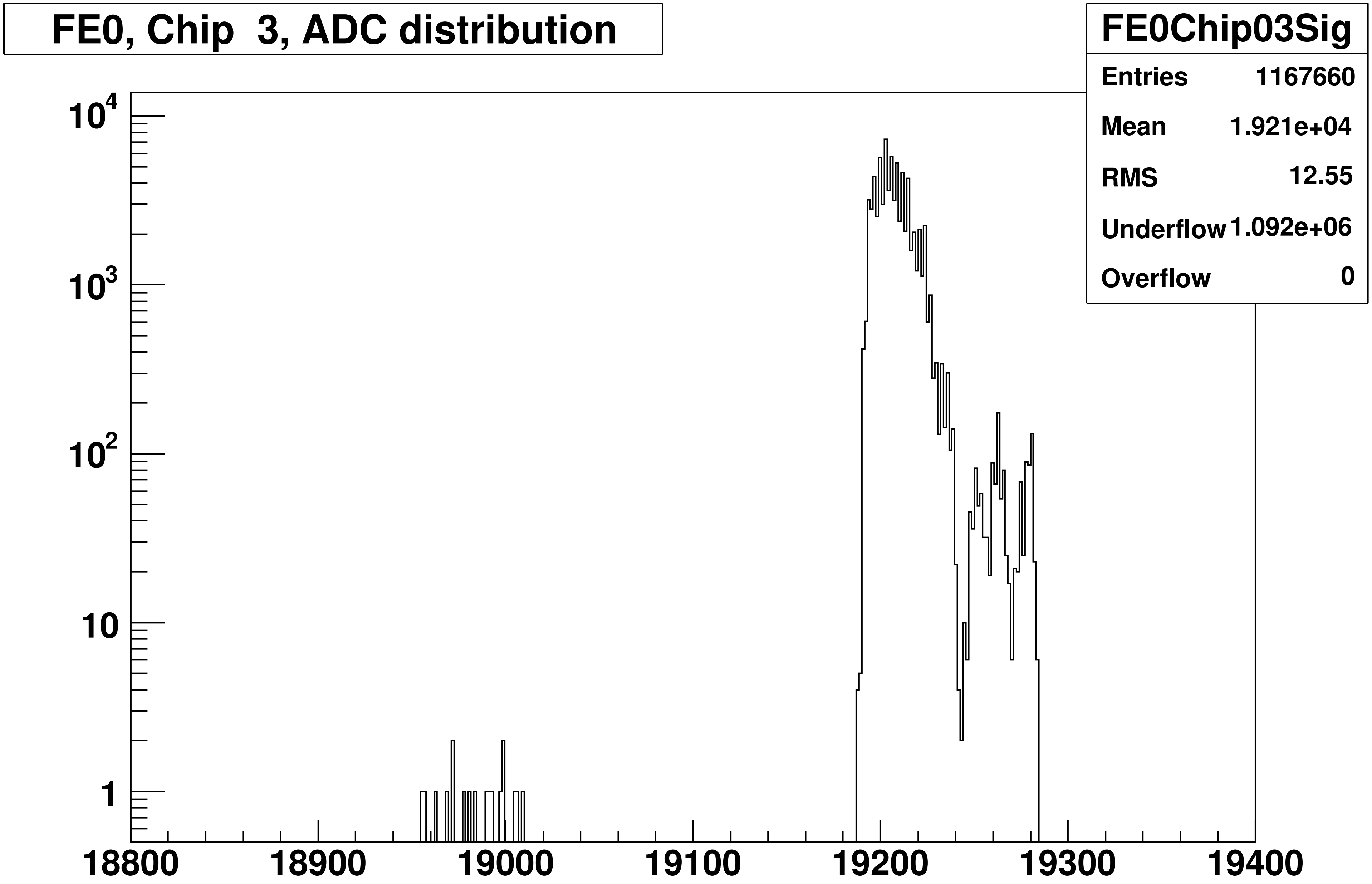
20
40
60
80
100


FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 1167660
Mean 1.921e+04
RMS 12.55
Underflow 1.092e+06
Overflow 0
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10^3
10^2
10
1
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18900
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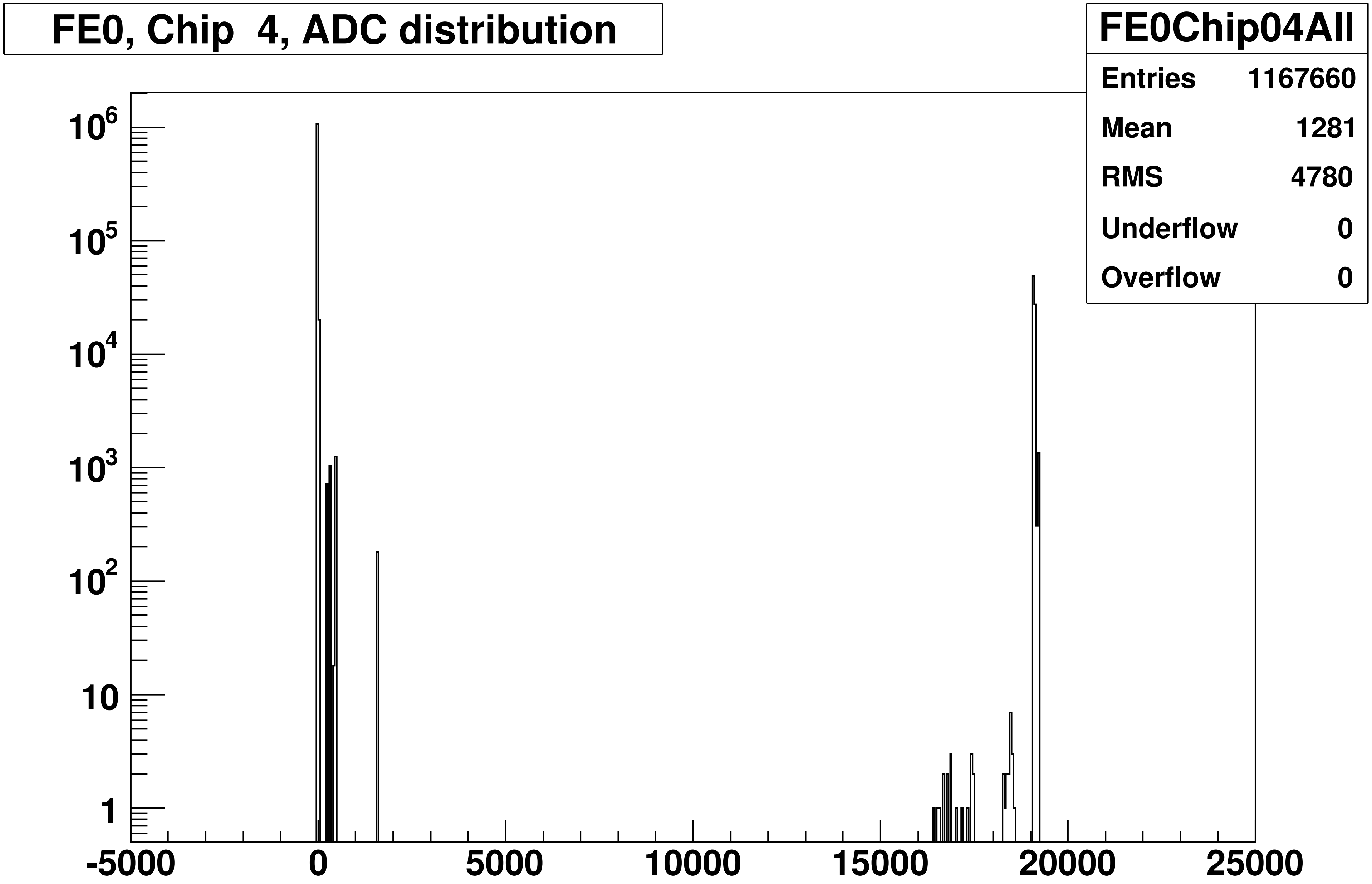

FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 1167660
Mean 1281
RMS 4780
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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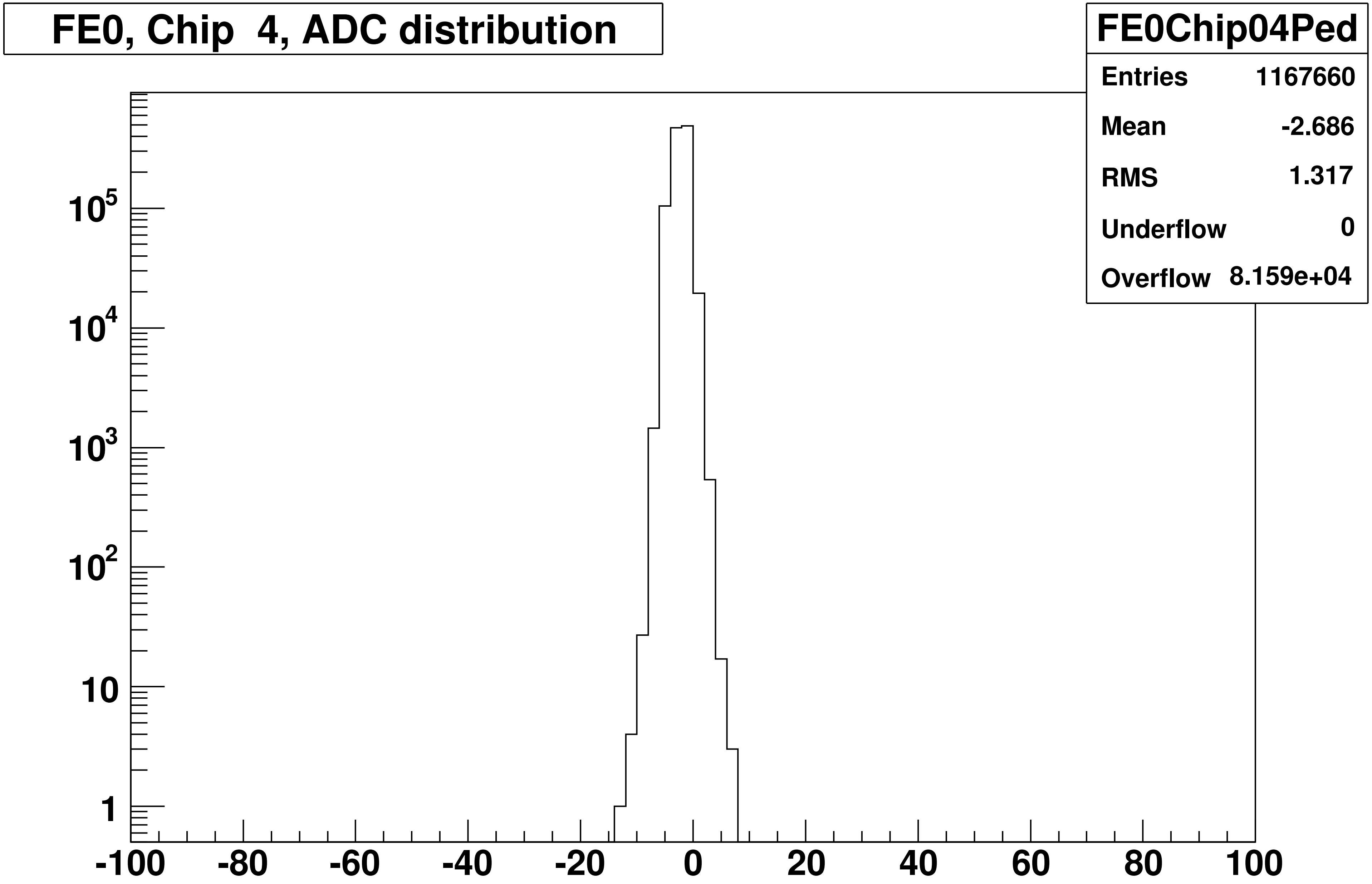
20000
25000


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 1167660
Mean -2.686
RMS 1.317
Underflow 0
Overflow 8.159e+04
10^5
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10^3
10^2
10
1
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-40
-20
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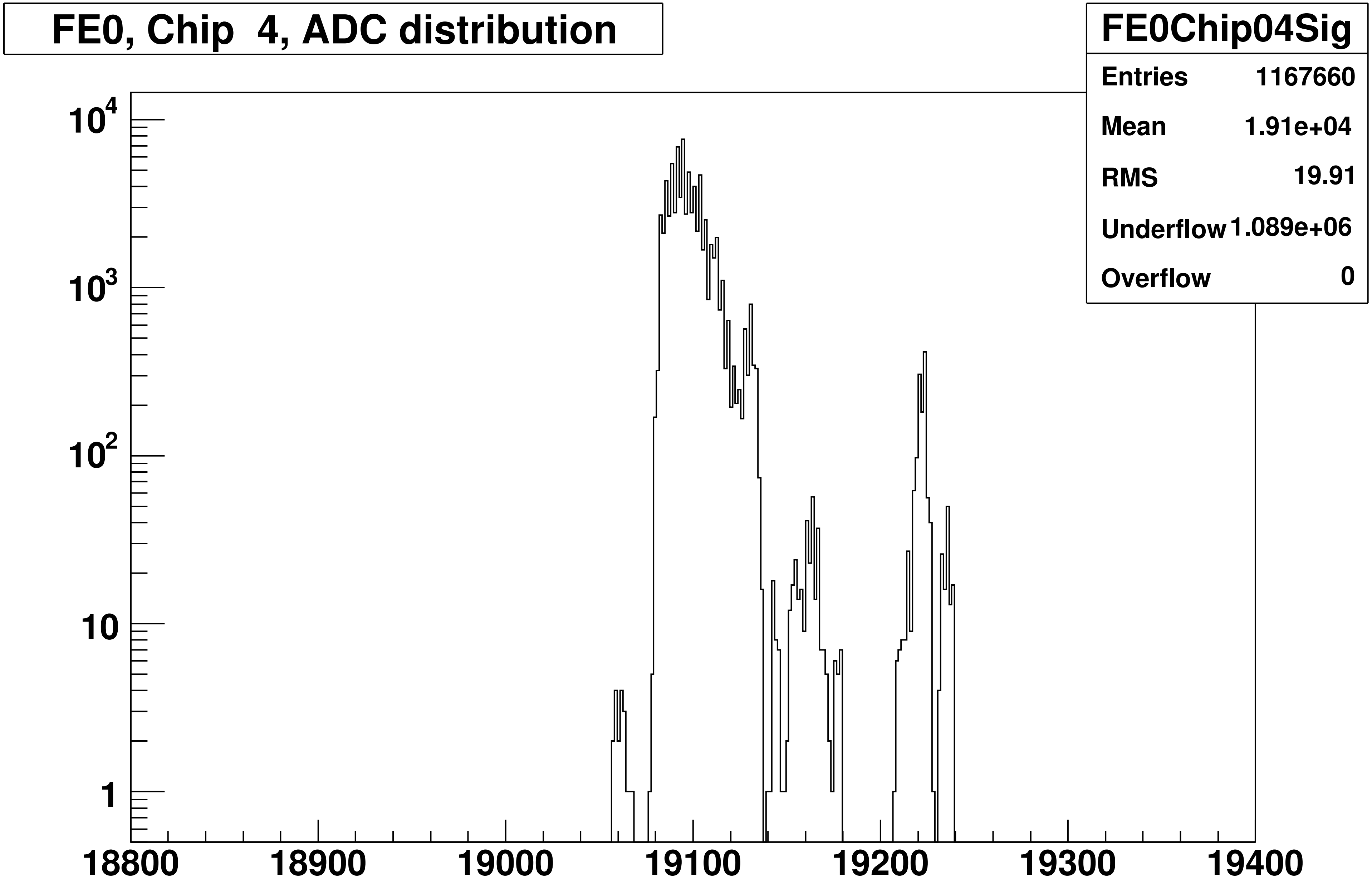
40
60
80
100



FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 1167660
Mean 1.91e+04
RMS 19.91
Underflow 1.089e+06
Overflow 0
10^4
10^3
10^2
10
1
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18900
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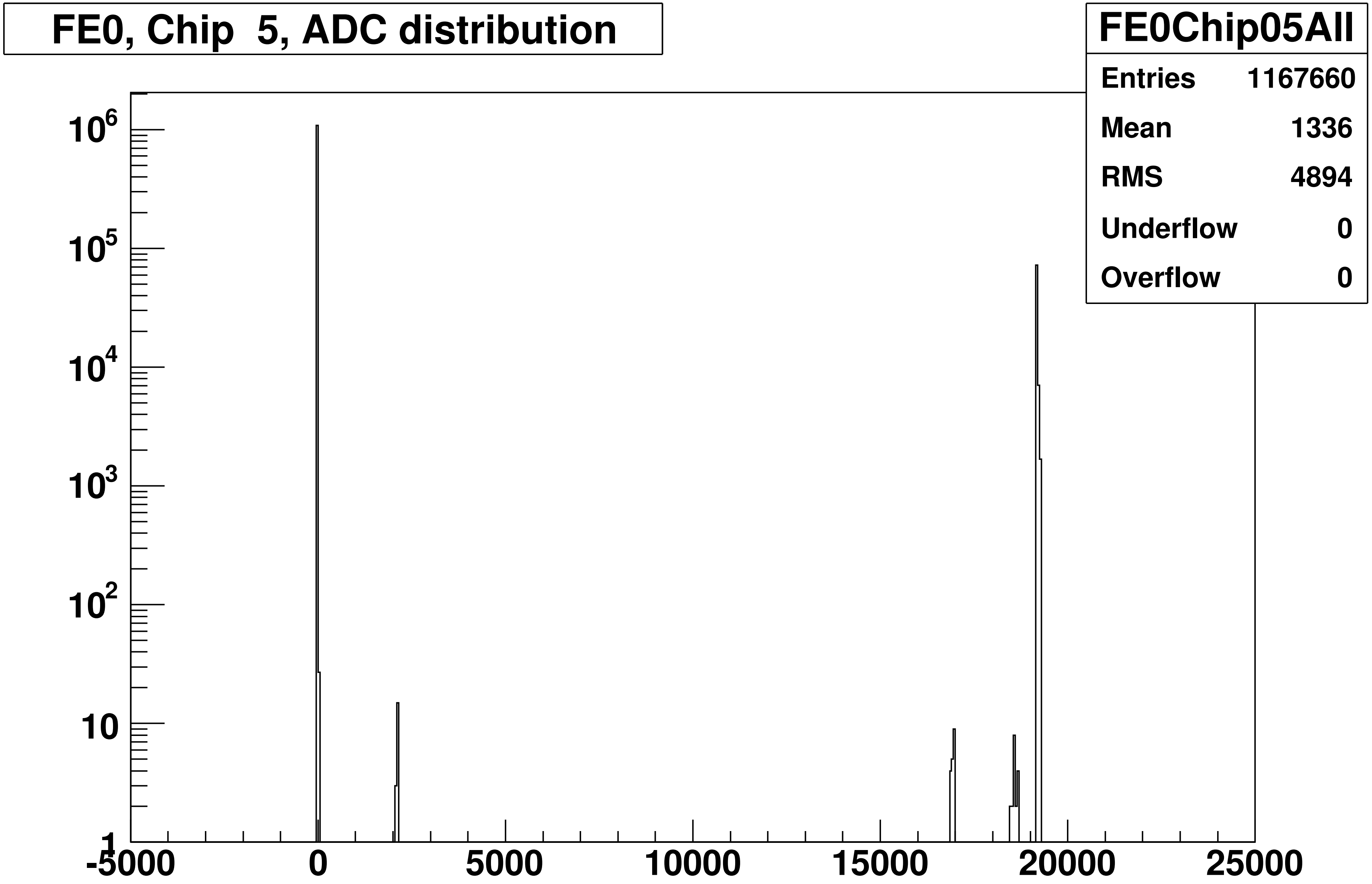

FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 1167660
Mean 1336
RMS 4894
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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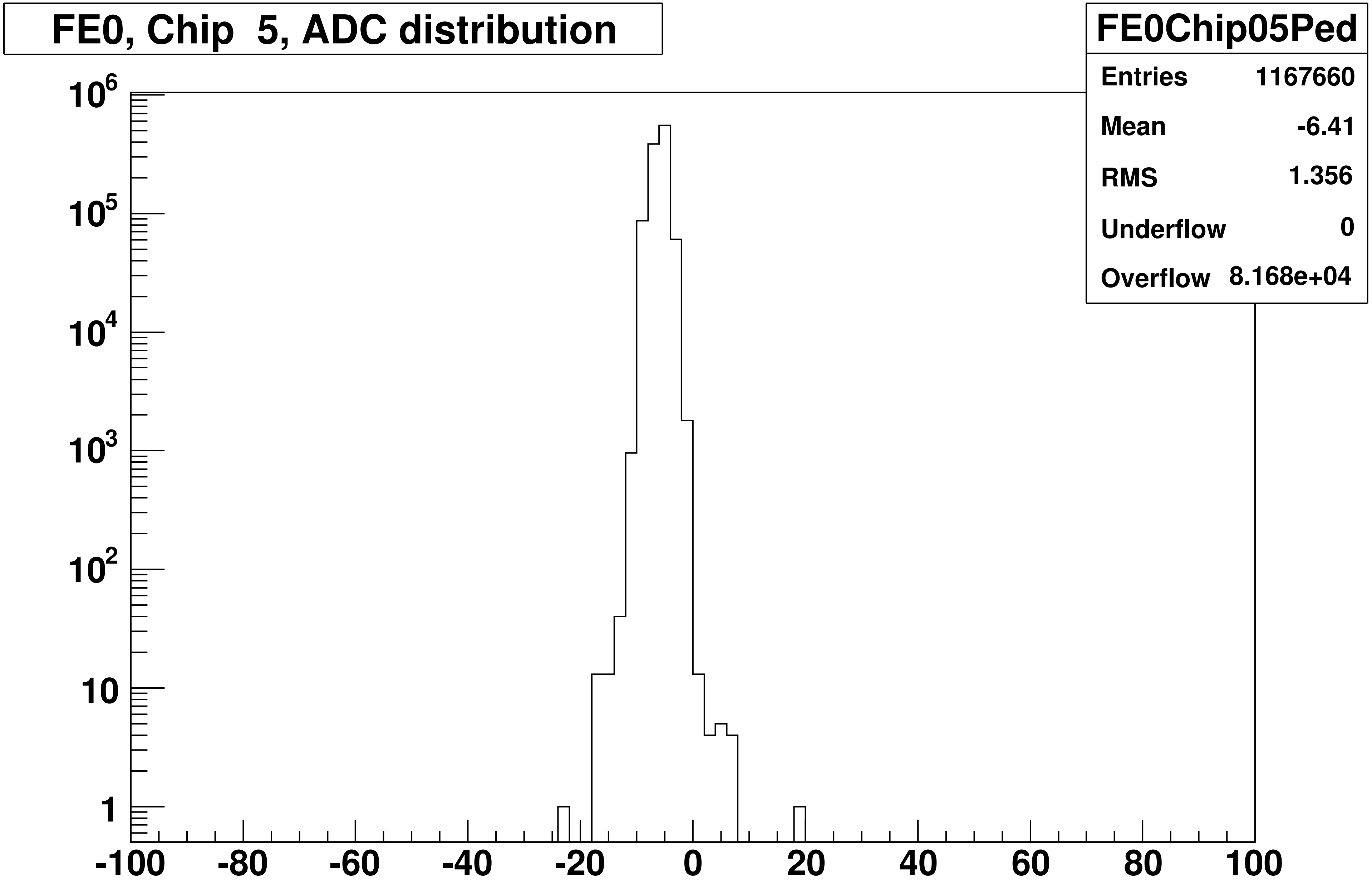
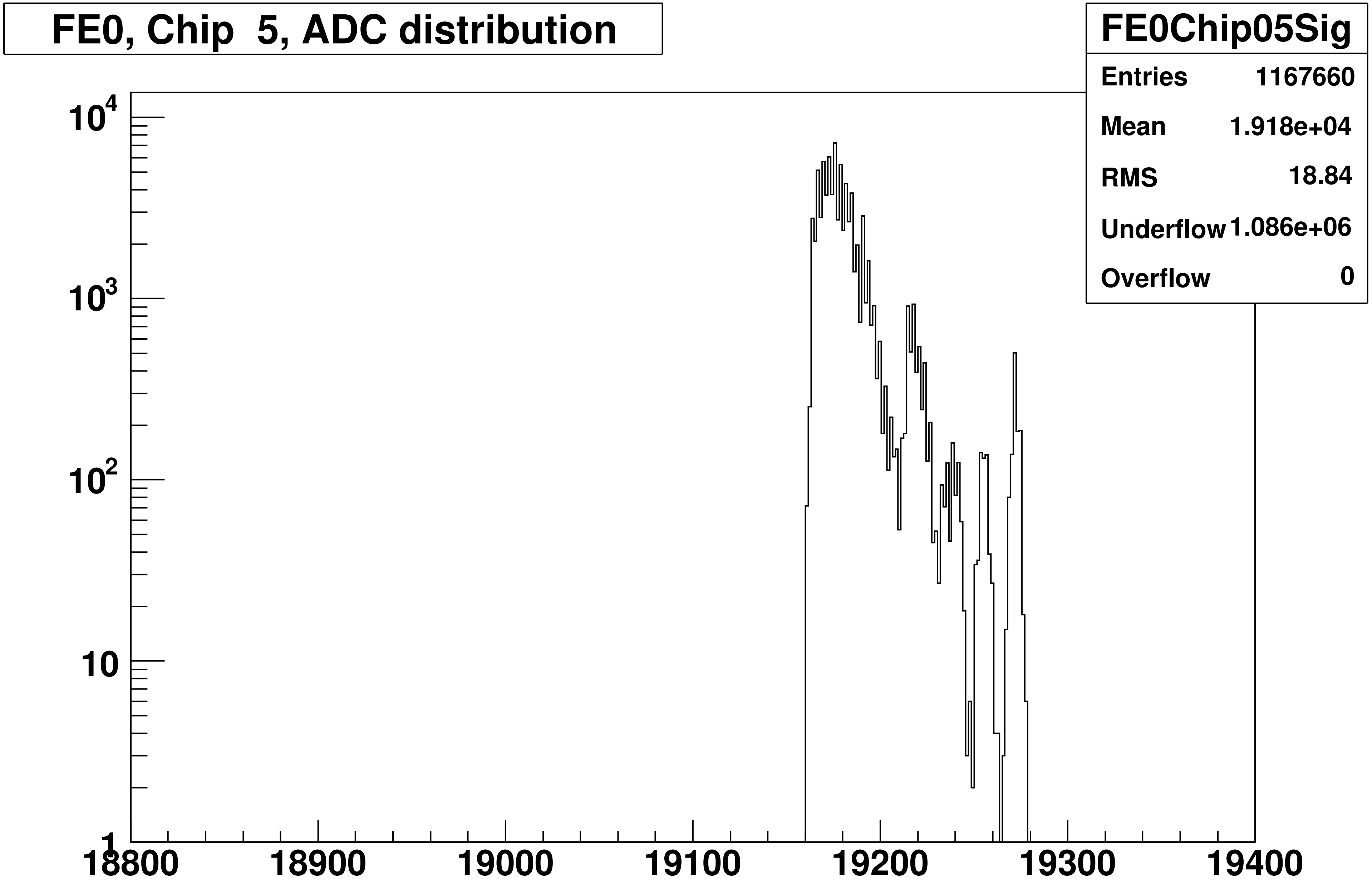
0
5000
10000
15000
20000
25000



FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 1167660
Mean -6.41
RMS 1.356
Underflow 0
Overflow 8.168e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 1167660
Mean 1.918e+04
RMS 18.84
Underflow 1.086e+06
Overflow 0
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10^3
10^2
10
1
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18900
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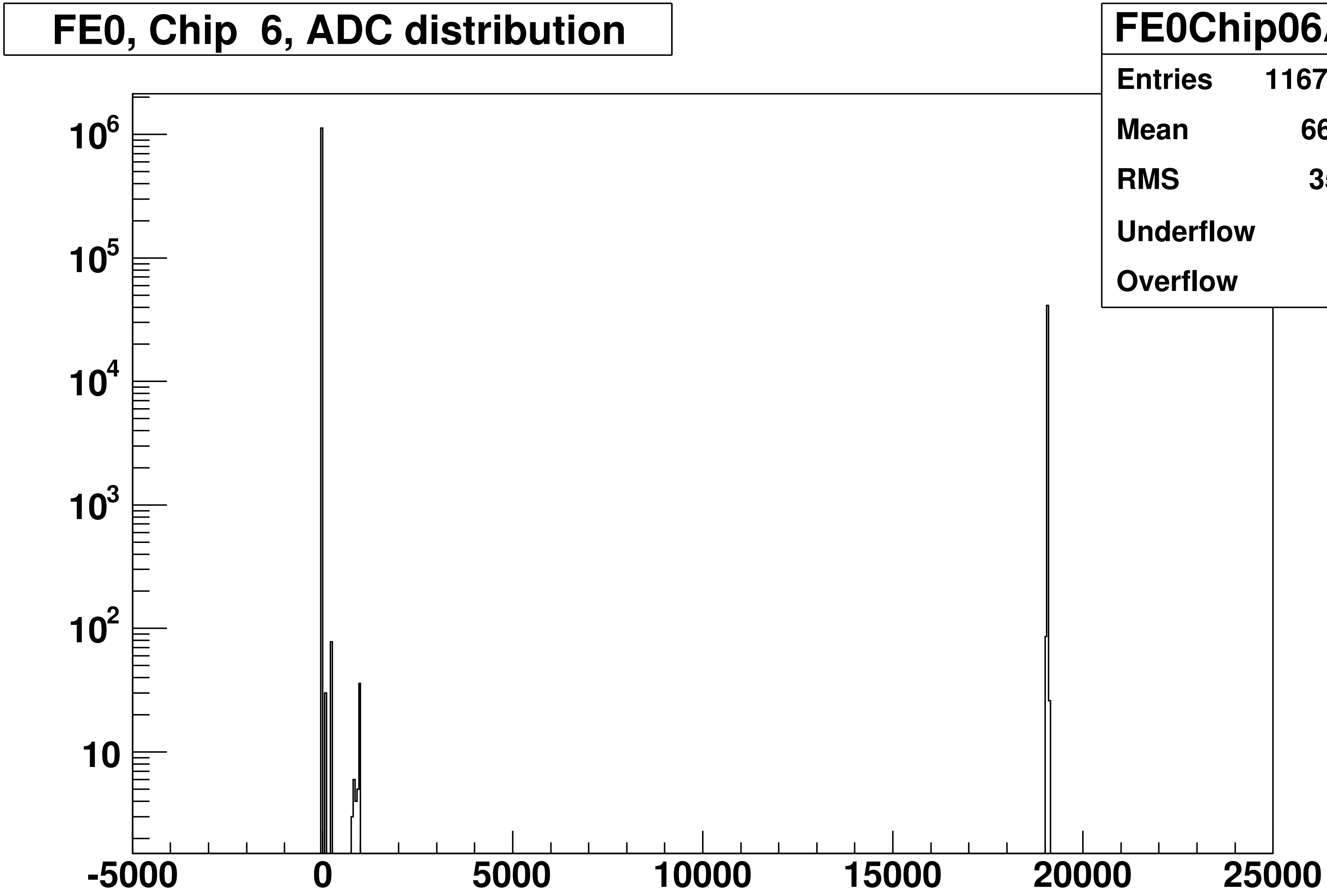
FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 1167660
Mean 663.8
RMS 3524
Underflow 0
Overflow 0
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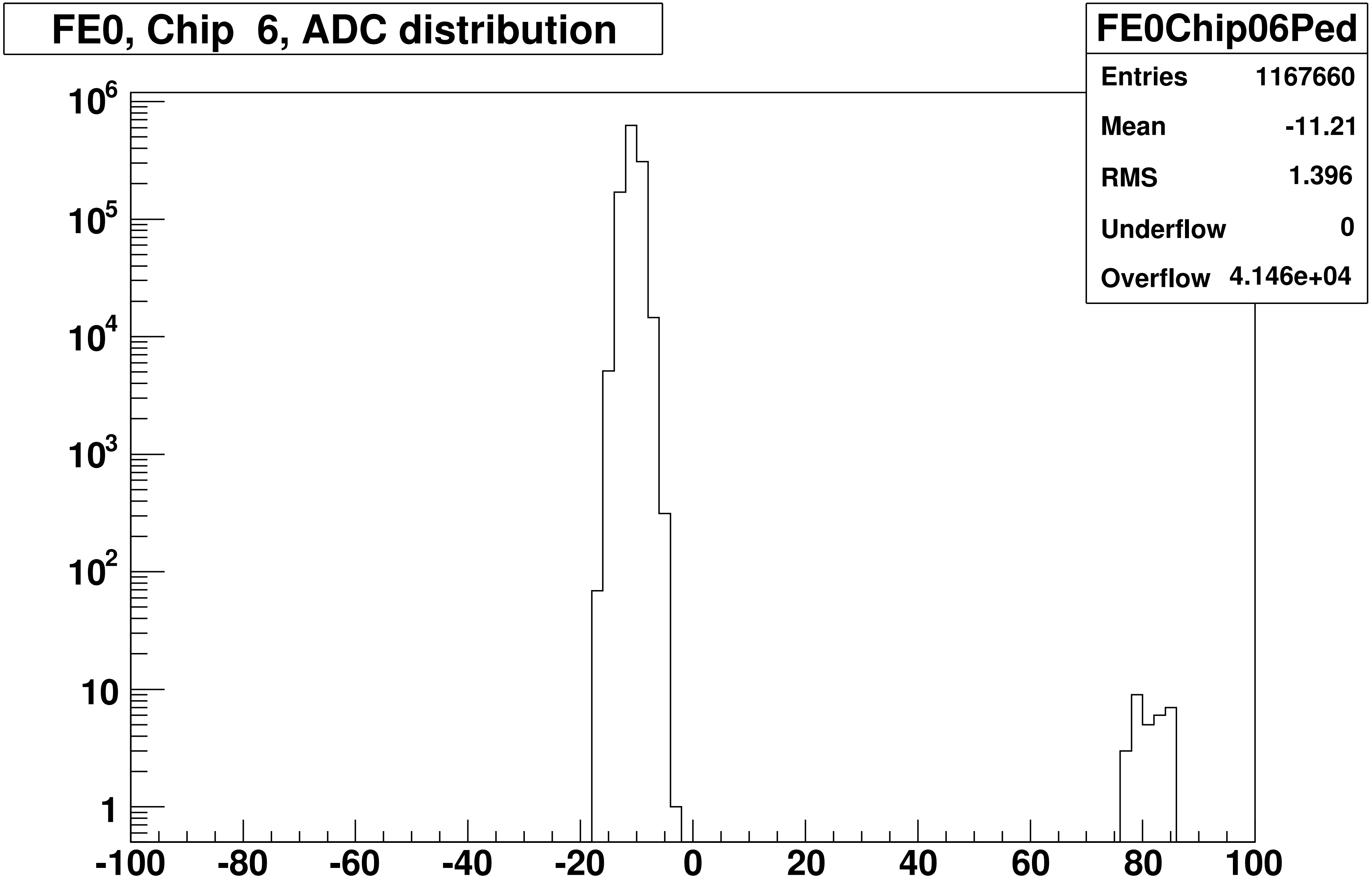
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 1167660
Mean -11.21
RMS 1.396
Underflow 0
Overflow 4.146e+04
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10^5
10^4
10^3
10^2
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1
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-20
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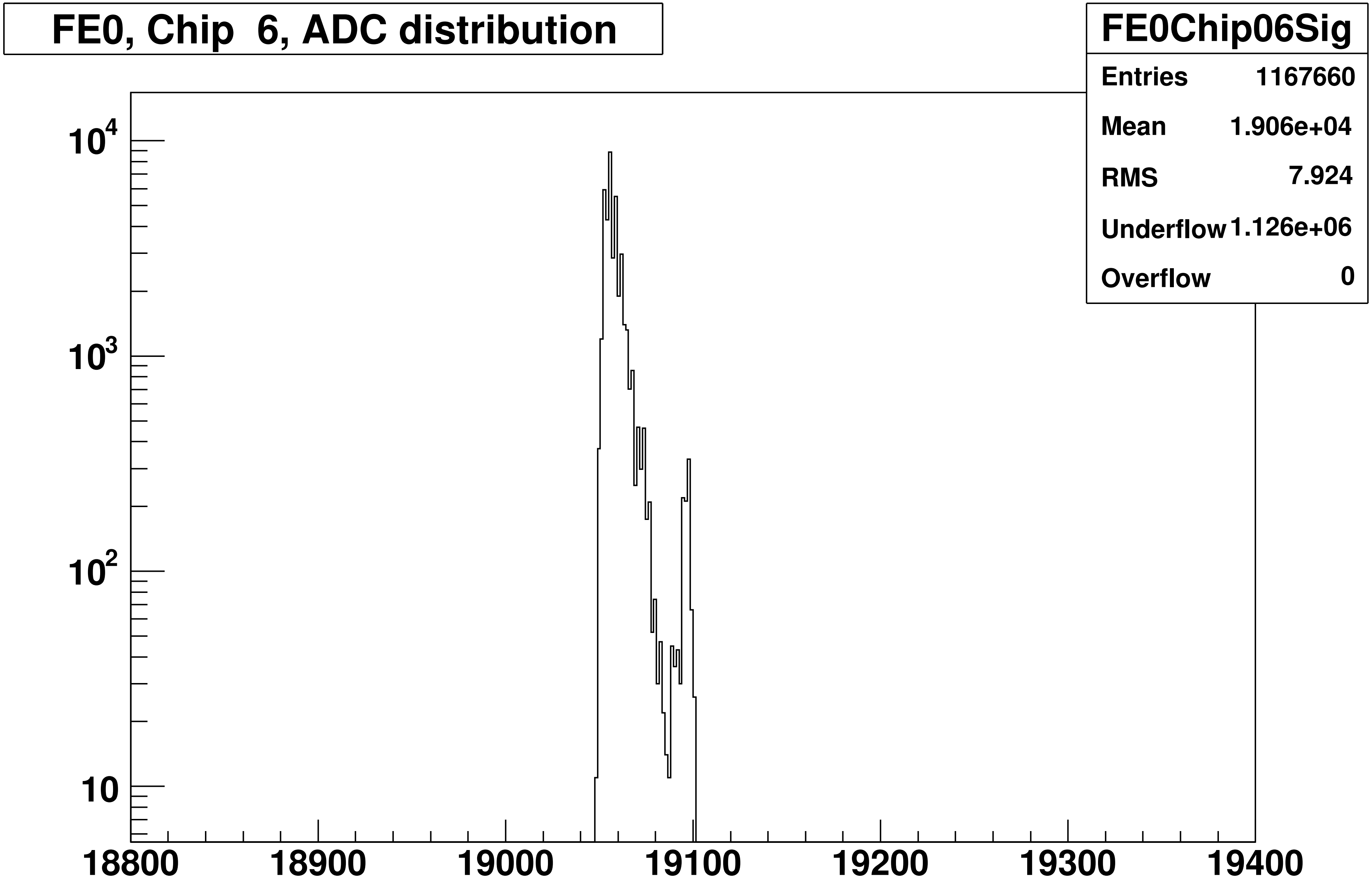
60
80
100



FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 1167660
Mean 1.906e+04
RMS 7.924
Underflow 1.126e+06
Overflow 0
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10^3
10^2
10
18800
18900
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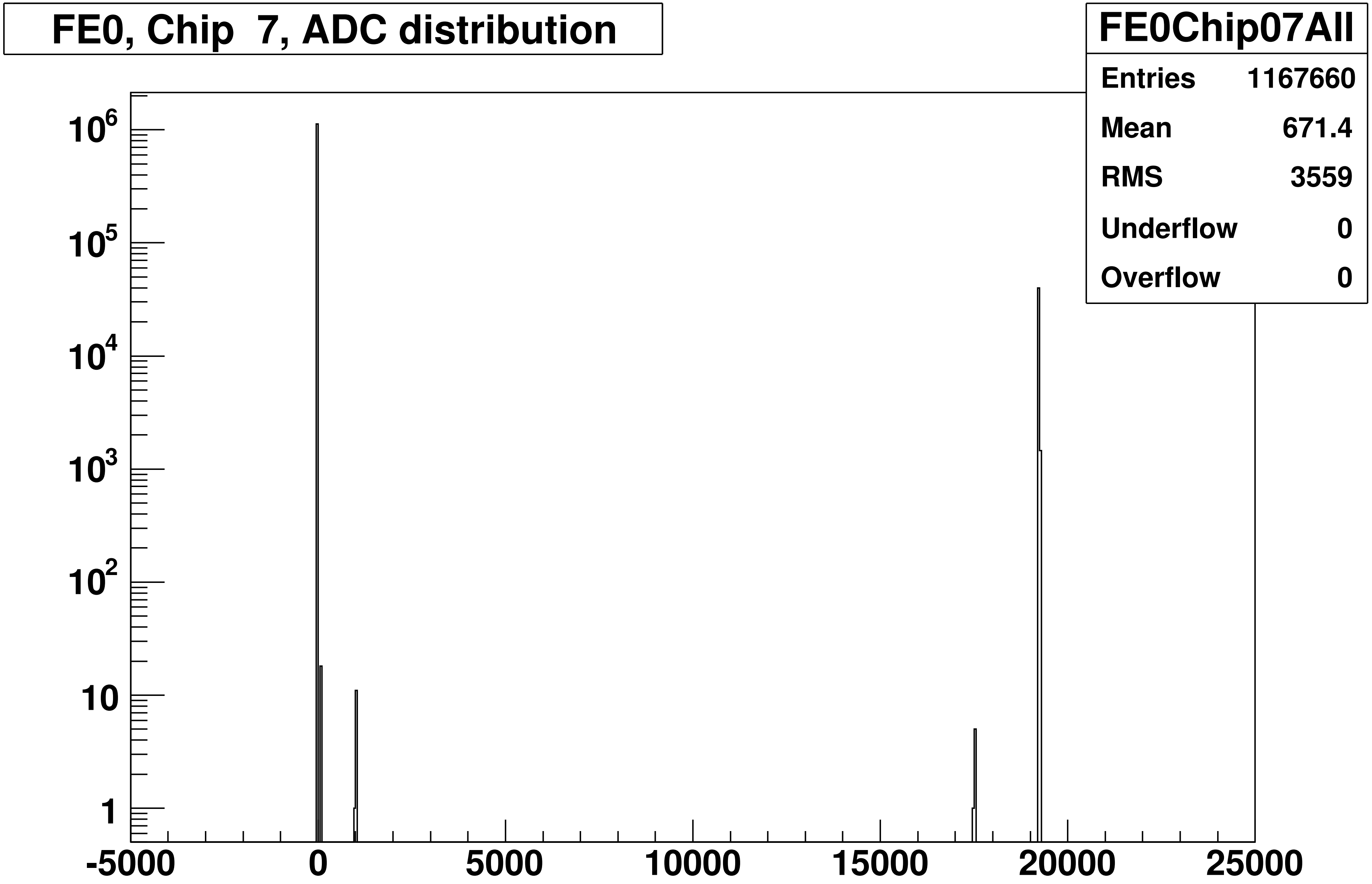
19100
19200
19300
19400


FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 1167660
Mean 671.4
RMS 3559
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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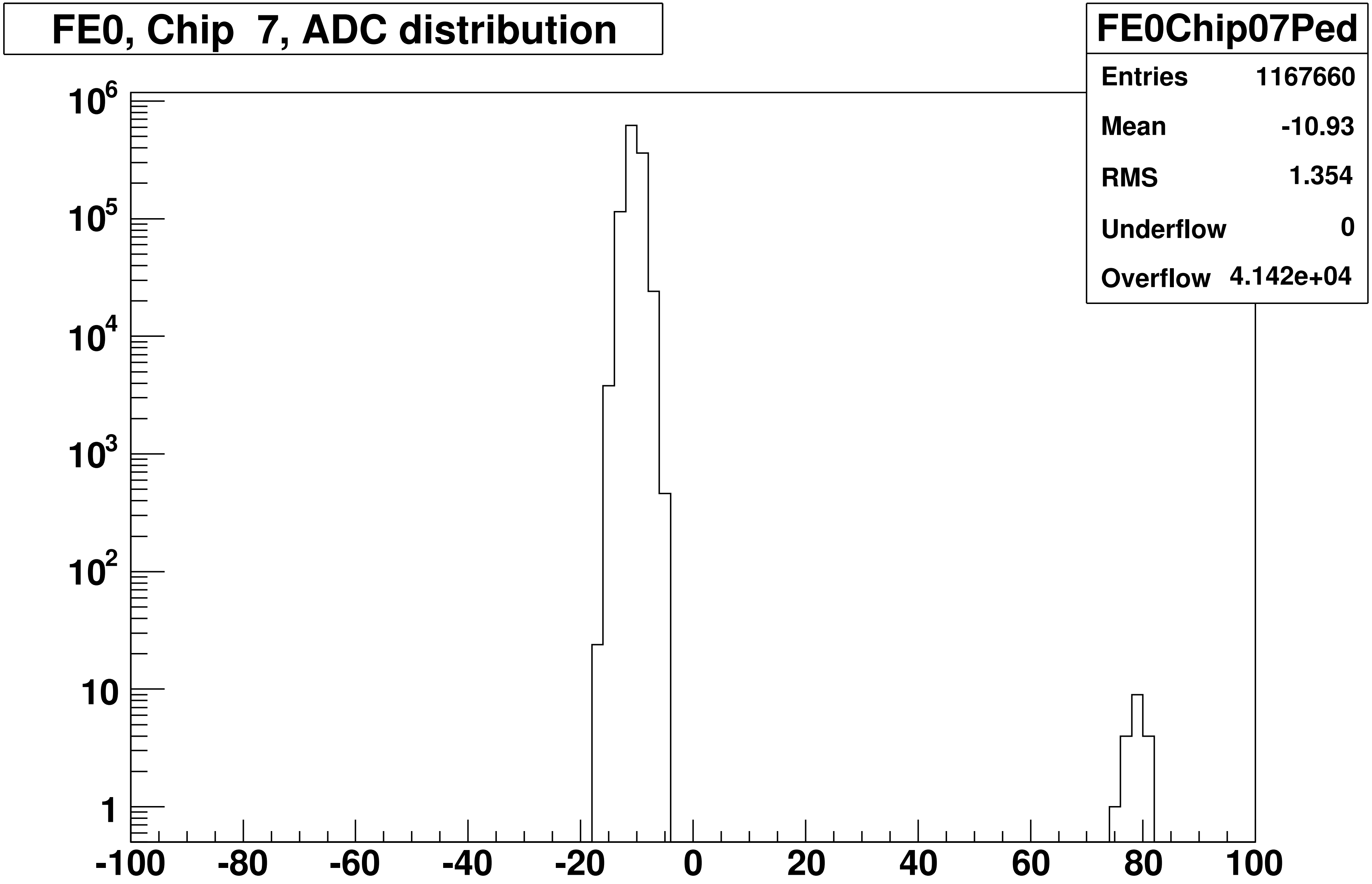
20000
25000



FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 1167660
Mean -10.93
RMS 1.354
Underflow 0
Overflow 4.142e+04
10^6
10^5
10^4
10^3
10^2
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1
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-20
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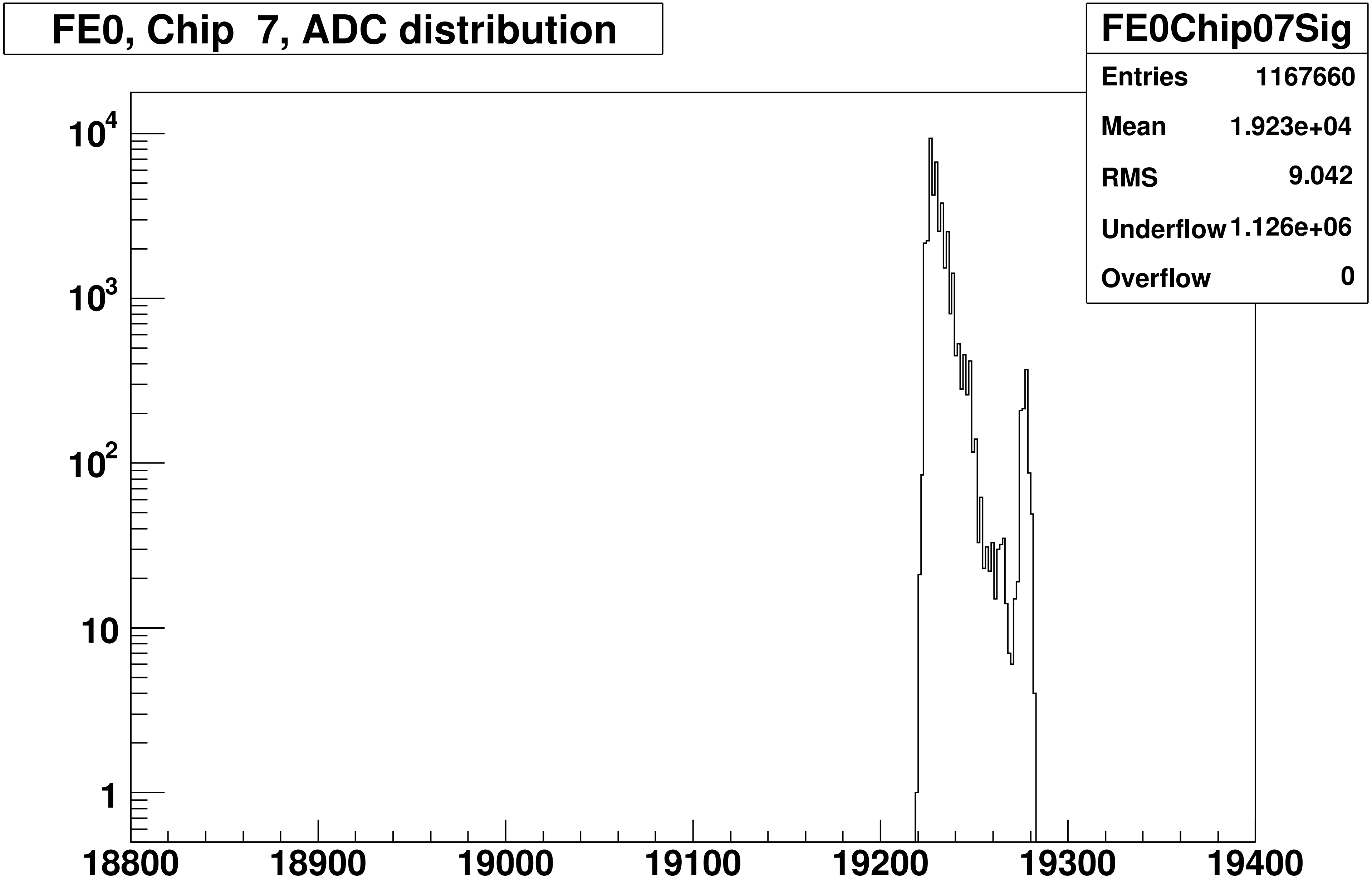

FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 1167660
Mean 1.923e+04
RMS 9.042
Underflow 1.126e+06
Overflow 0
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10^3
10^2
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18900
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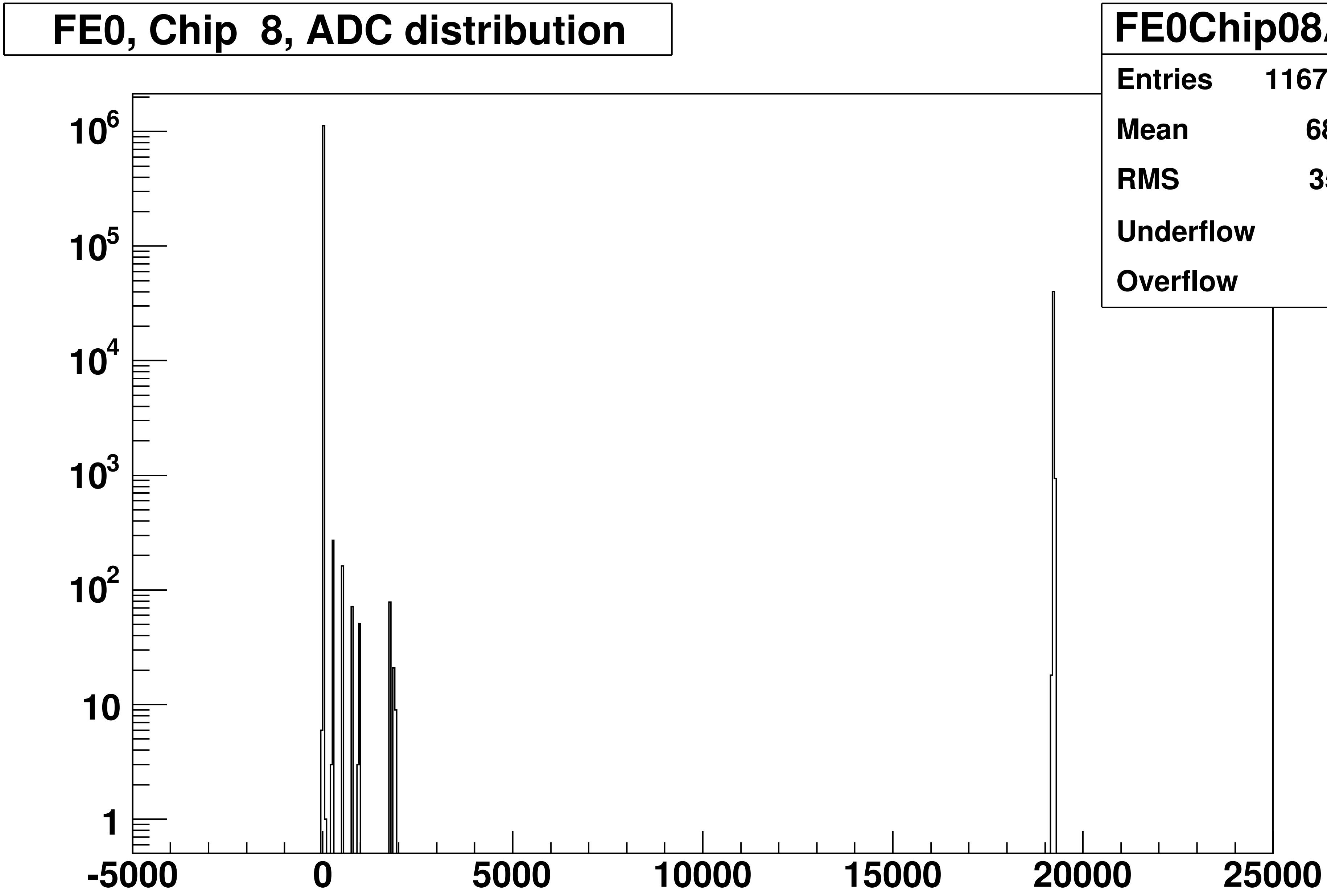

FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 1167660
Mean 686.1
RMS 3550
Underflow 0
Overflow 0
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10^4
10^3
10^2
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1
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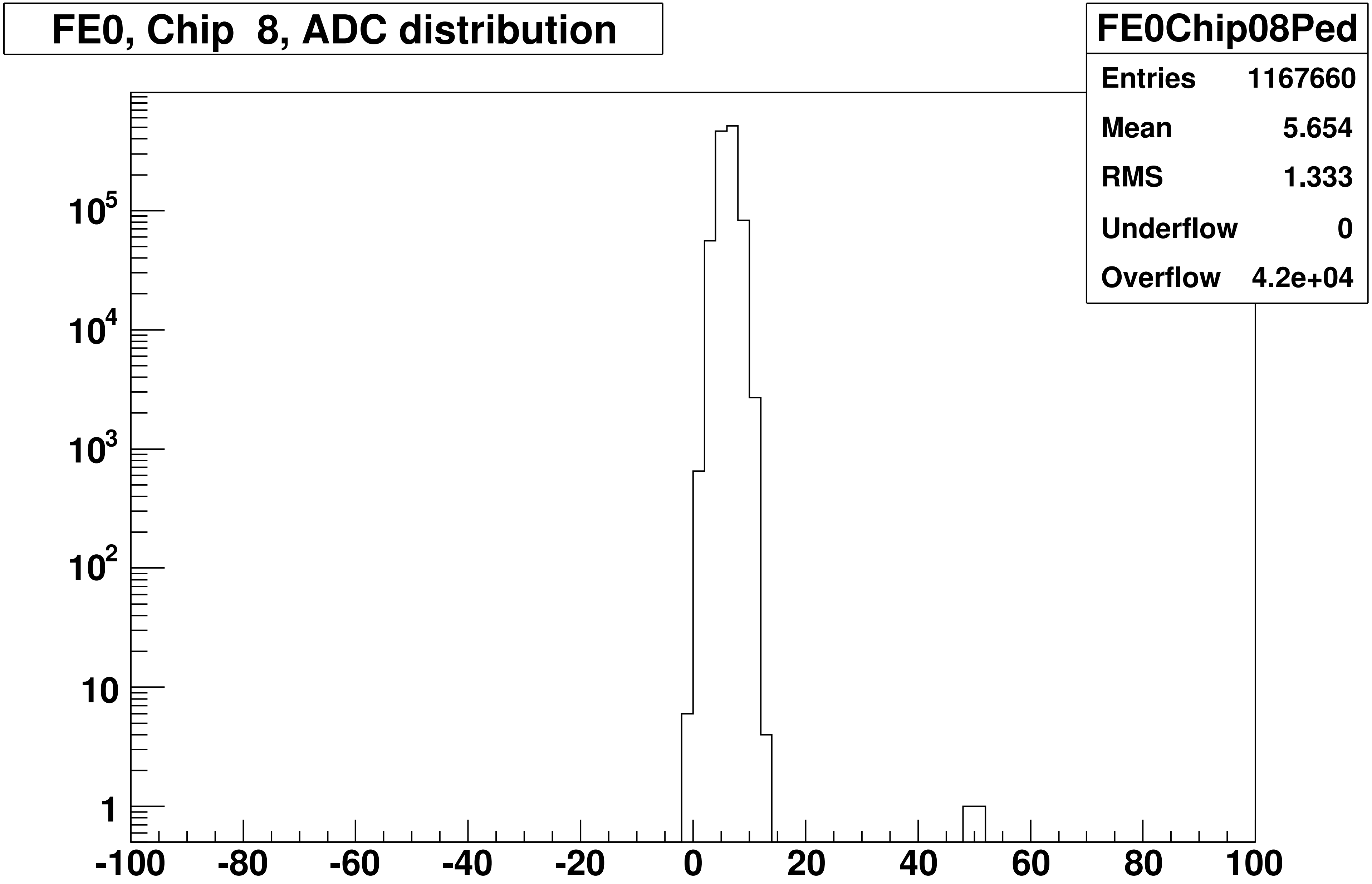

FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 1167660
Mean 5.654
RMS 1.333
Underflow 0
Overflow 4.2e+04
10^5
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10^3
10^2
10
1
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-80
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-40
-20
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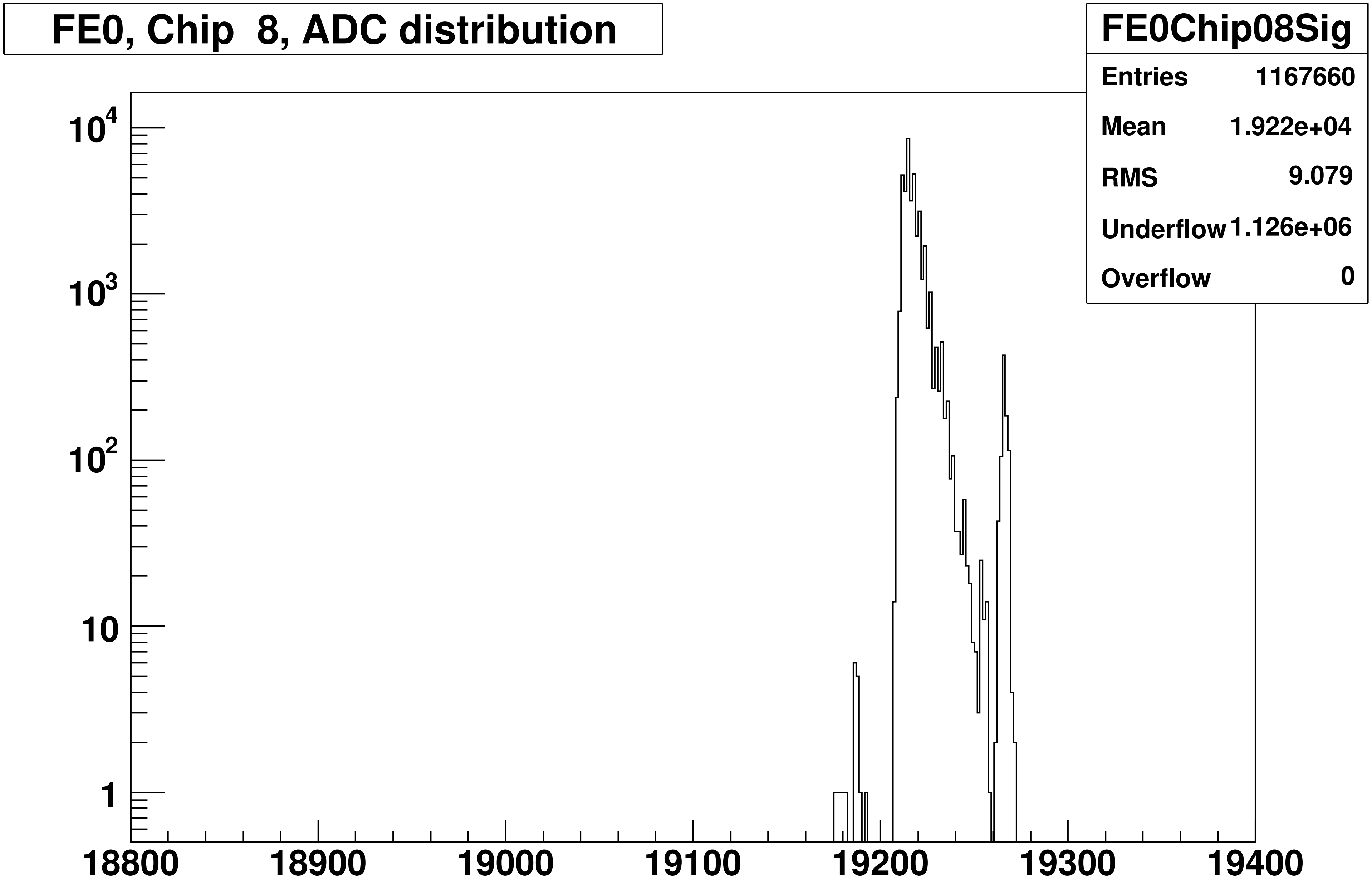
FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 1167660
Mean 1.922e+04
RMS 9.079
Underflow 1.126e+06
Overflow 0
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10^3
10^2
10
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18900
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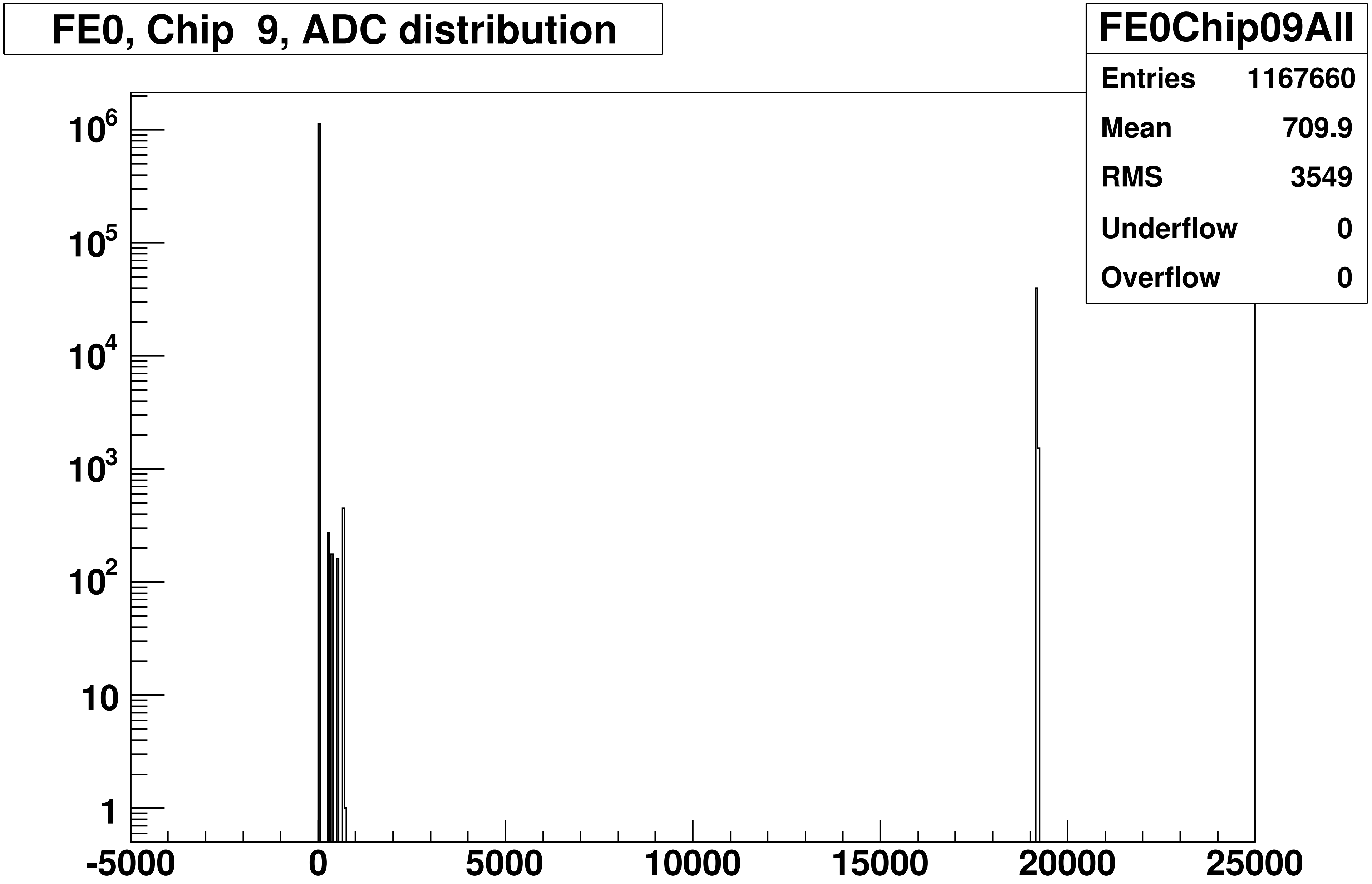
FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 1167660
Mean 709.9
RMS 3549
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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0
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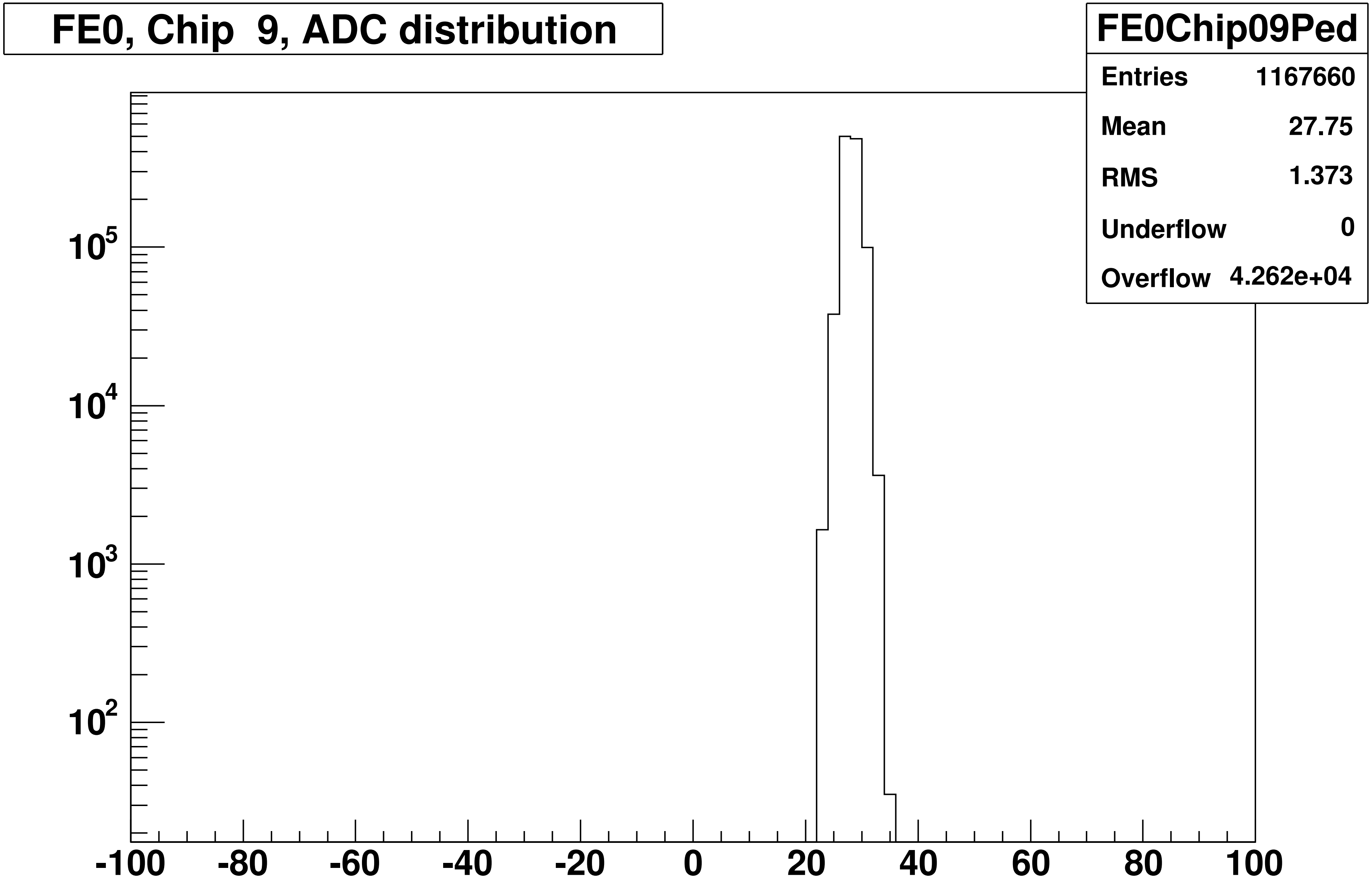
10000
15000
20000
25000


FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 1167660
Mean 27.75
RMS 1.373
Underflow 0
Overflow 4.262e+04
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10^3
10^2
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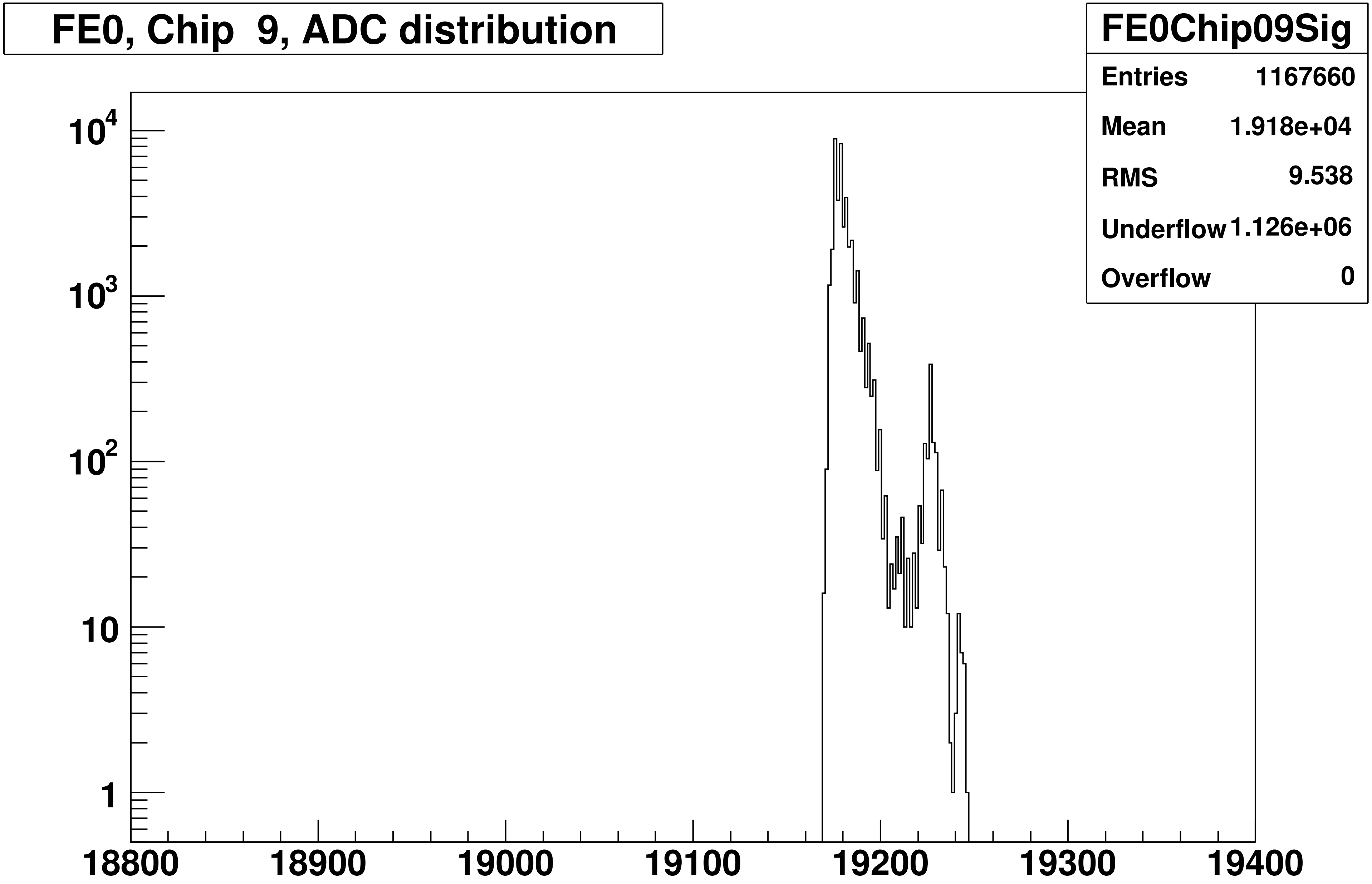
100


FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 1167660
Mean 1.918e+04
RMS 9.538
Underflow 1.126e+06
Overflow 0
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10^3
10^2
10
1
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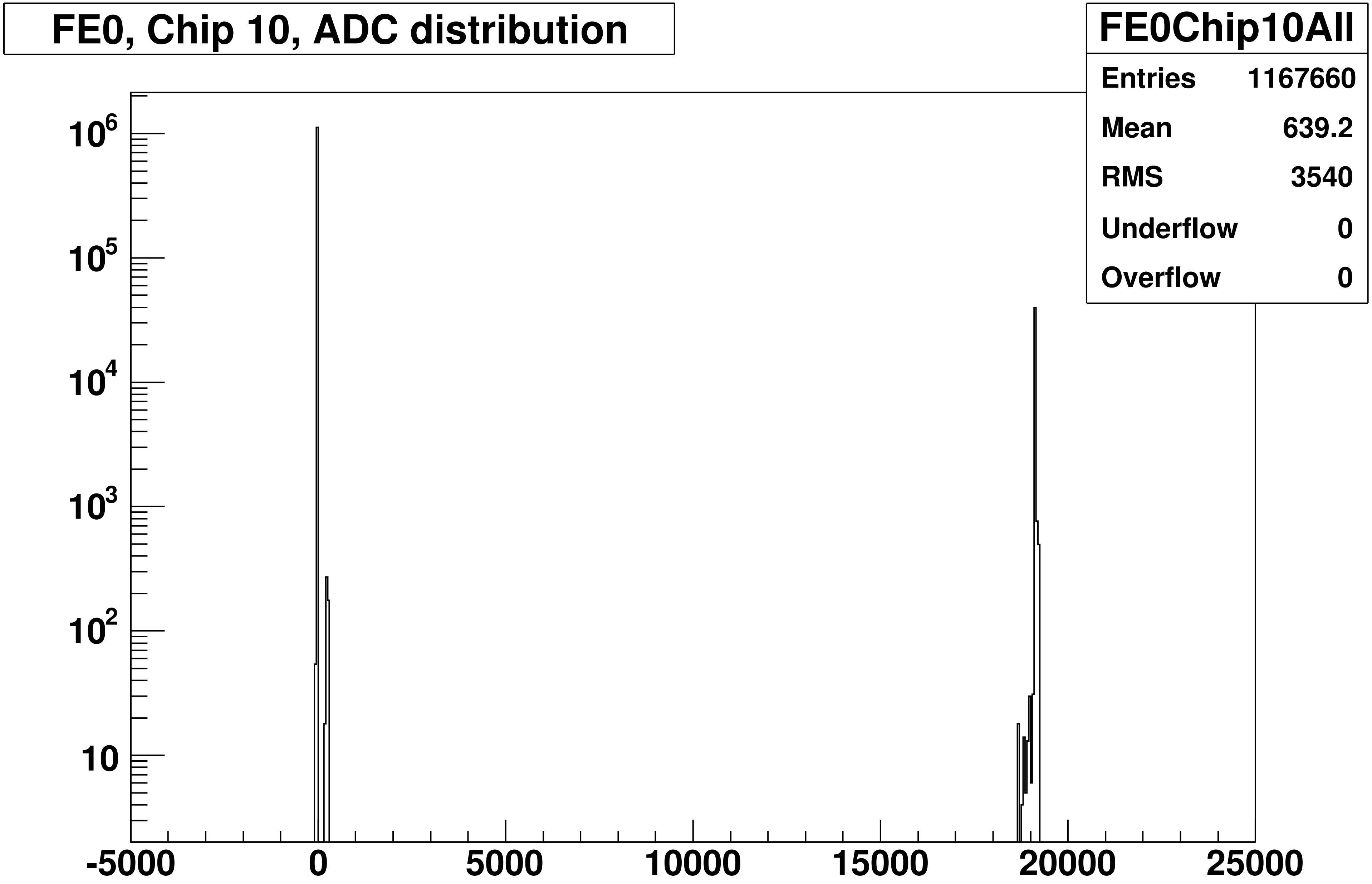
FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 1167660
Mean 639.2
RMS 3540
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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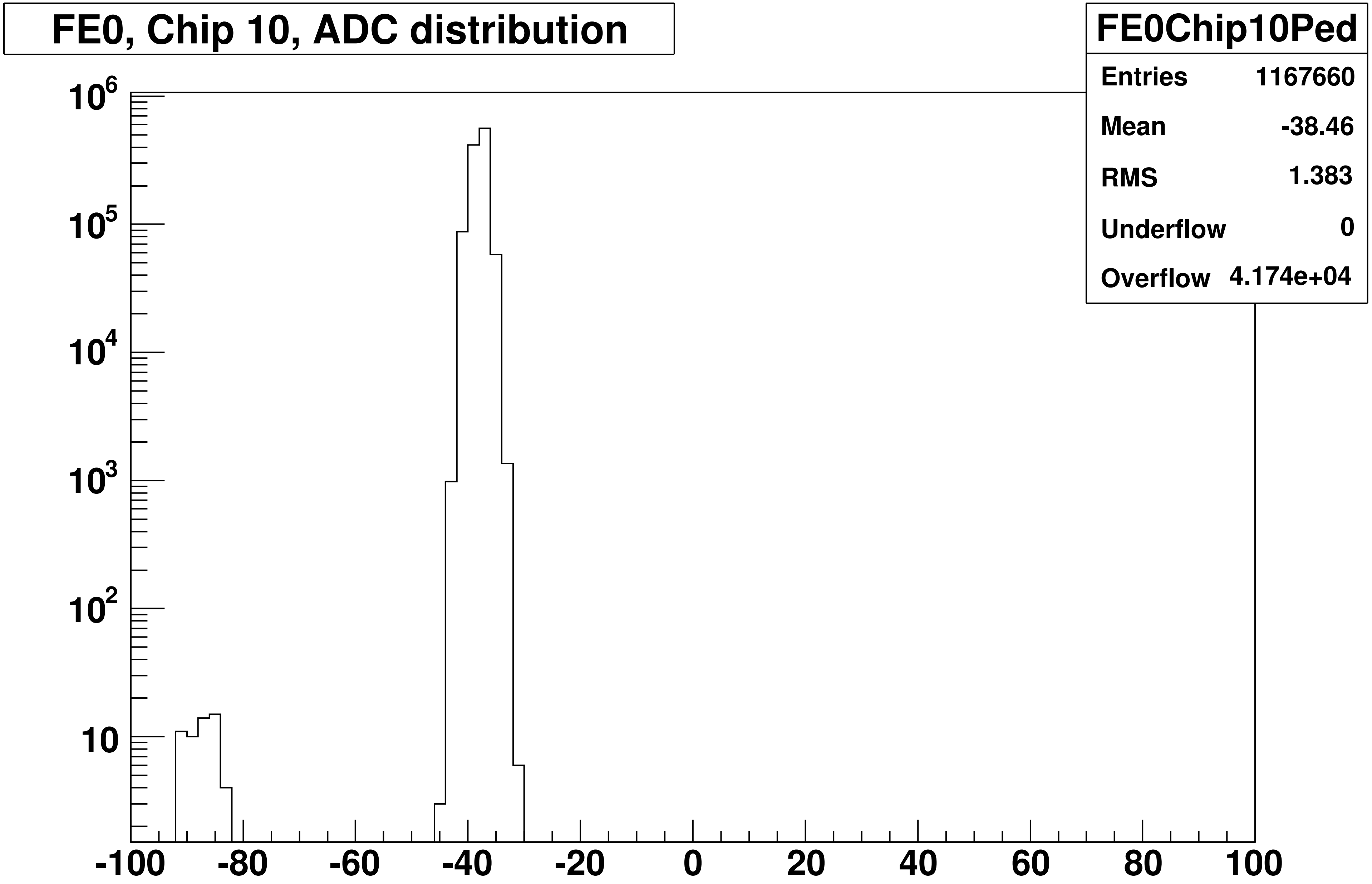

FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 1167660
Mean -38.46
RMS 1.383
Underflow 0
Overflow 4.174e+04
10^6
10^5
10^4
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10^2
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-20
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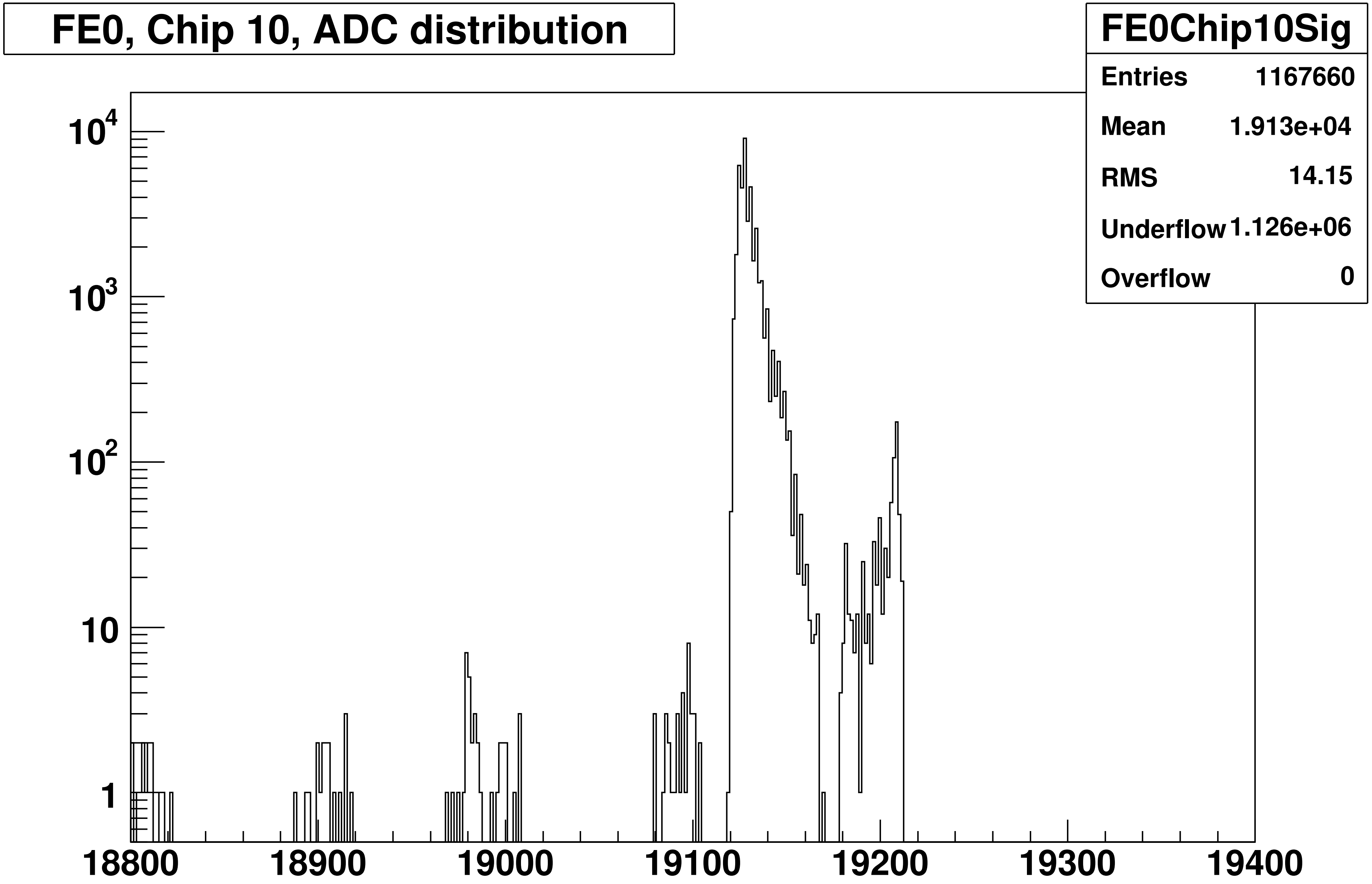
FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 1167660
Mean 1.913e+04
RMS 14.15
Underflow 1.126e+06
Overflow 0
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10^3
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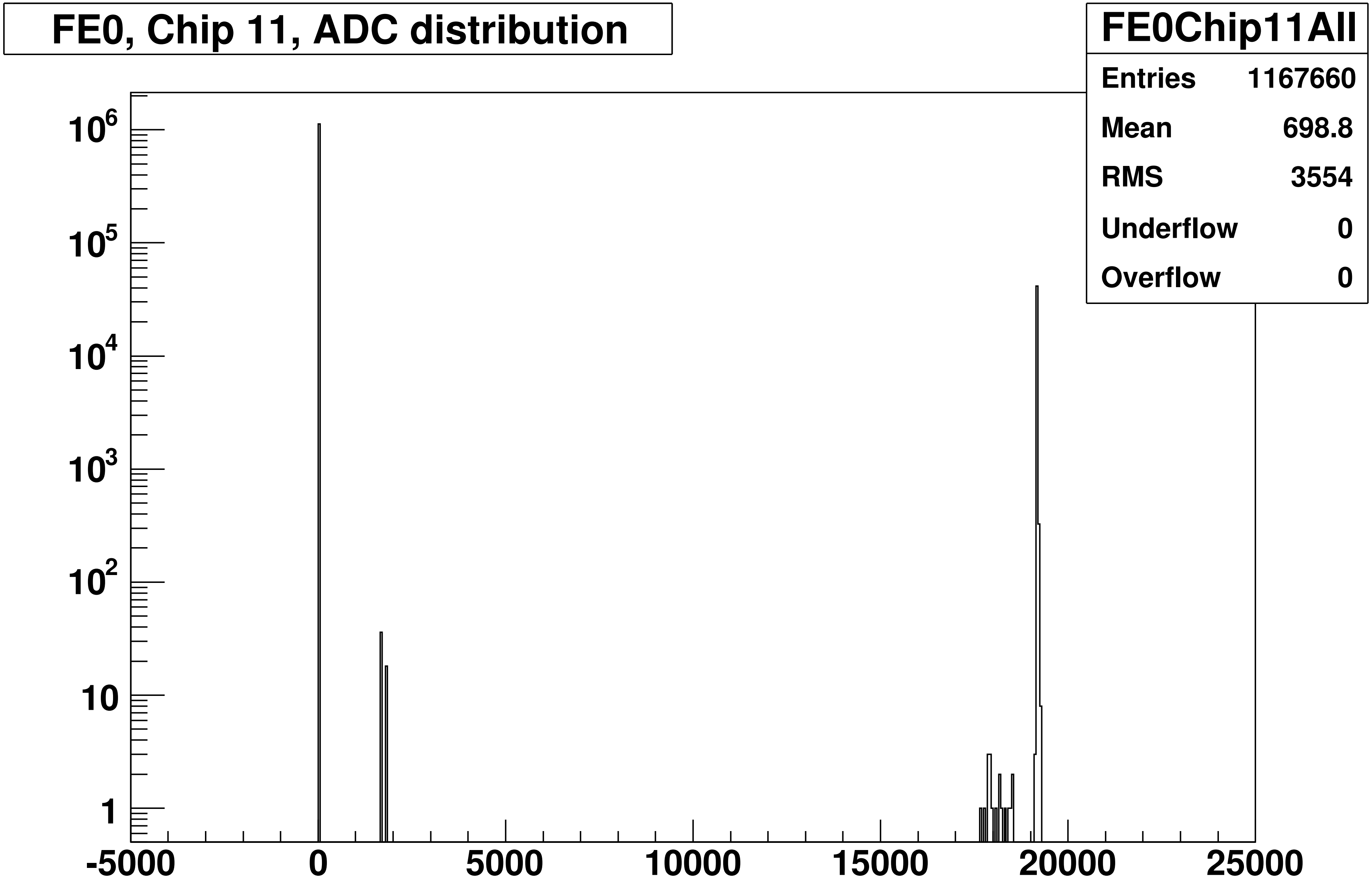
FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 1167660
Mean 698.8
RMS 3554
Underflow 0
Overflow 0
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10^4
10^3
10^2
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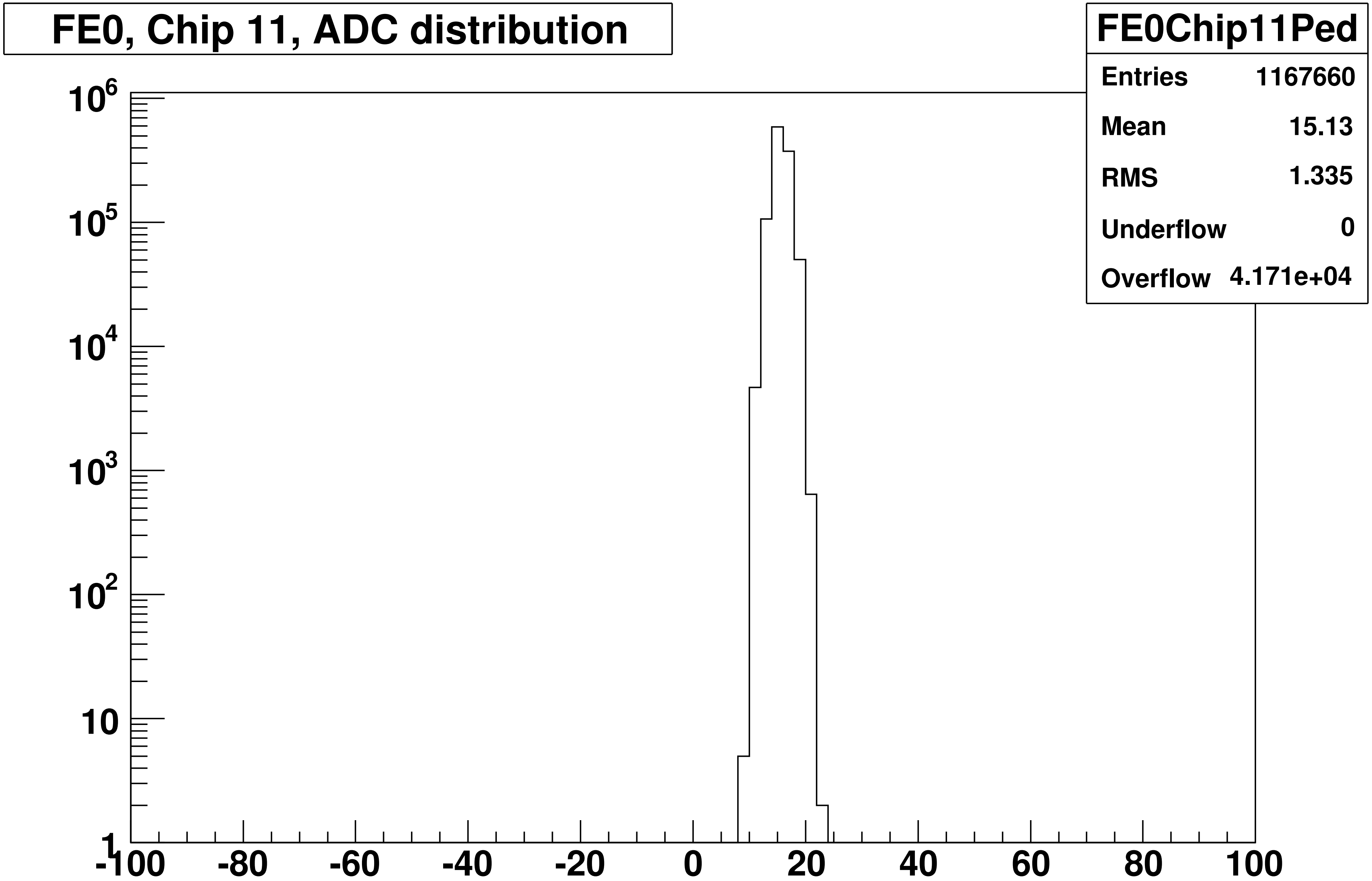

FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 1167660
Mean 15.13
RMS 1.335
Underflow 0
Overflow 4.171e+04
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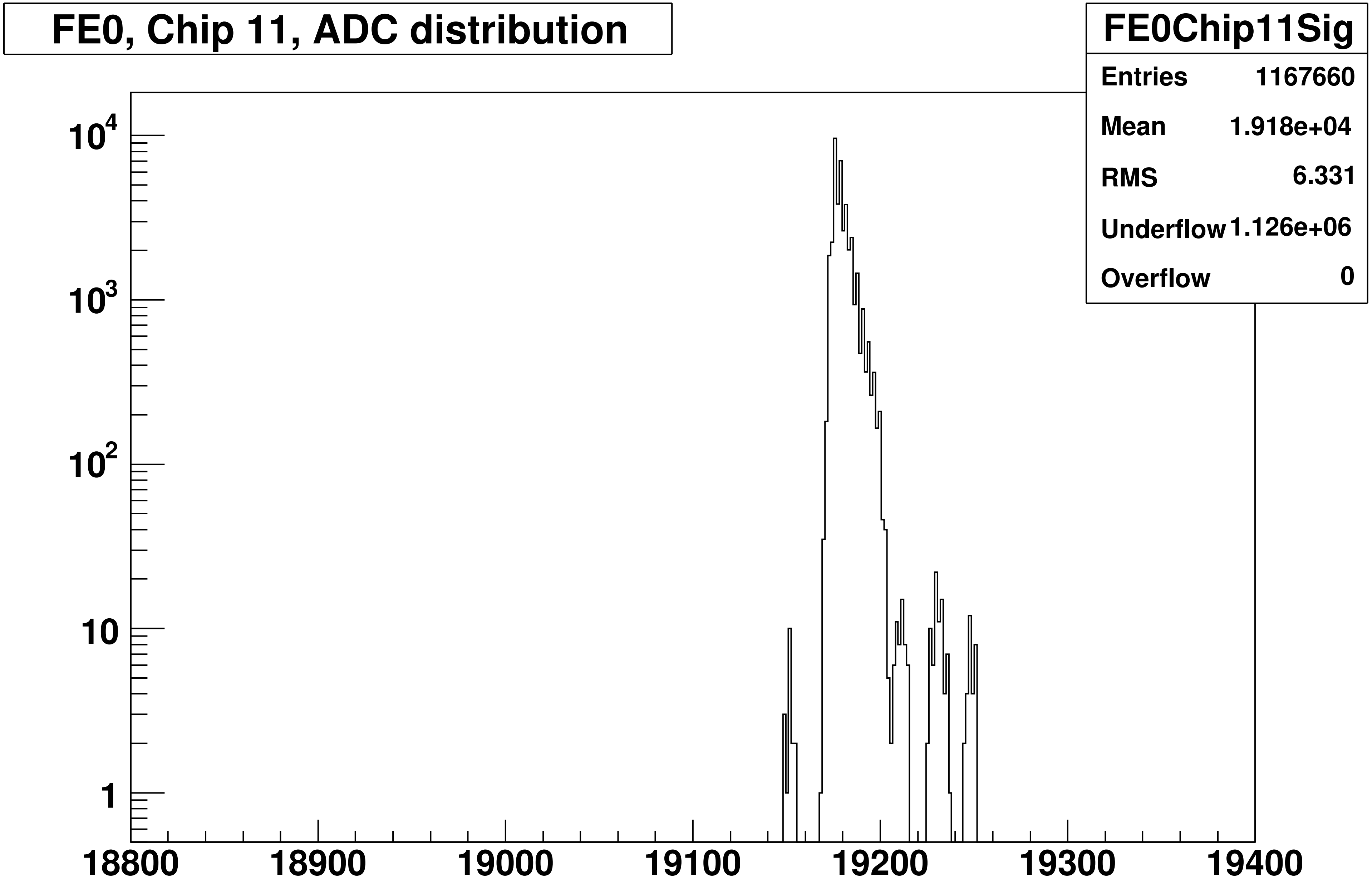
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 1167660
Mean 1.918e+04
RMS 6.331
Underflow 1.126e+06
Overflow 0
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10^3
10^2
10
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18900
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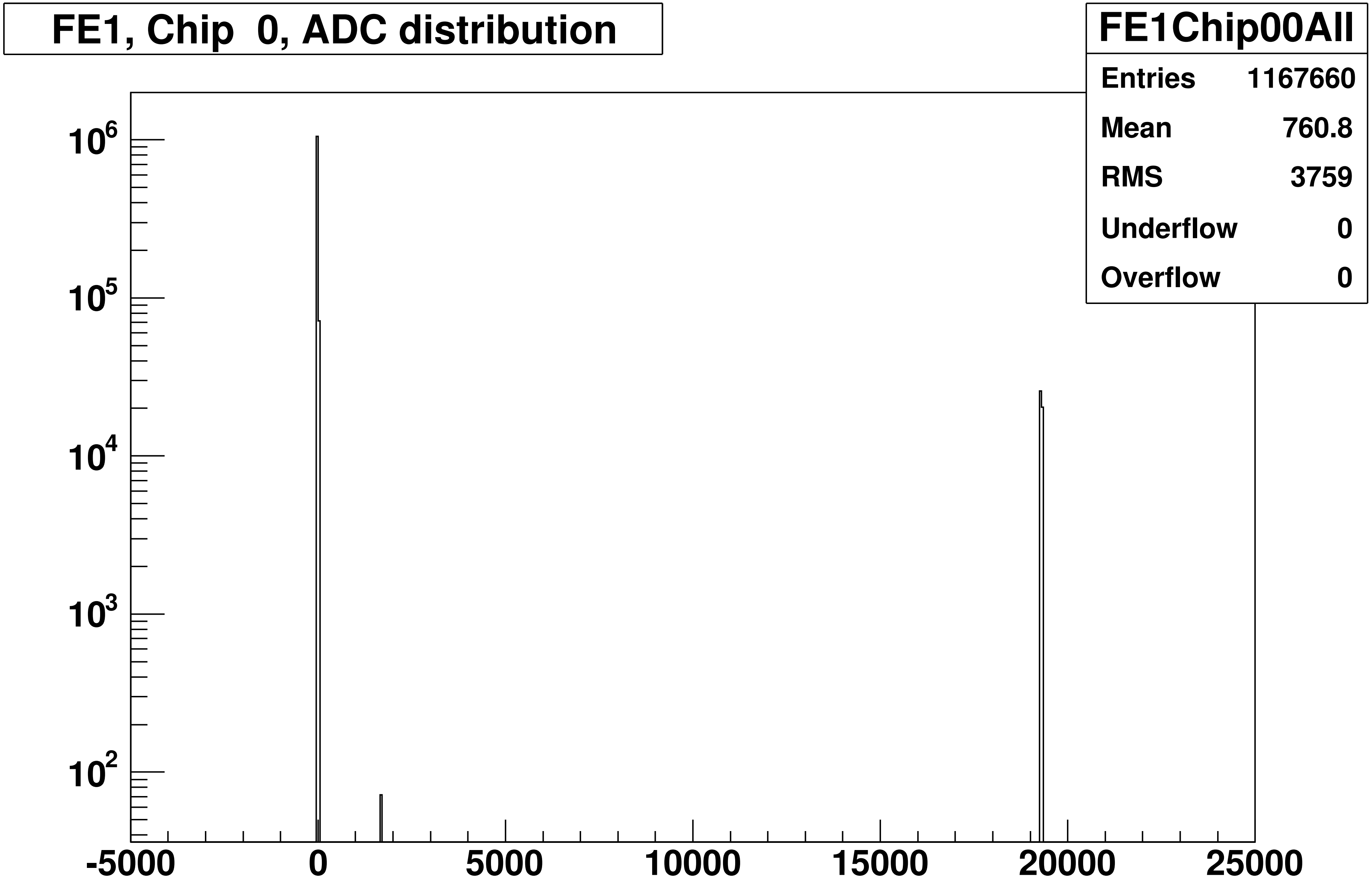
19400



FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 1167660
Mean 760.8
RMS 3759
Underflow 0
Overflow 0
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10^5
10^4
10^3
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-5000
0
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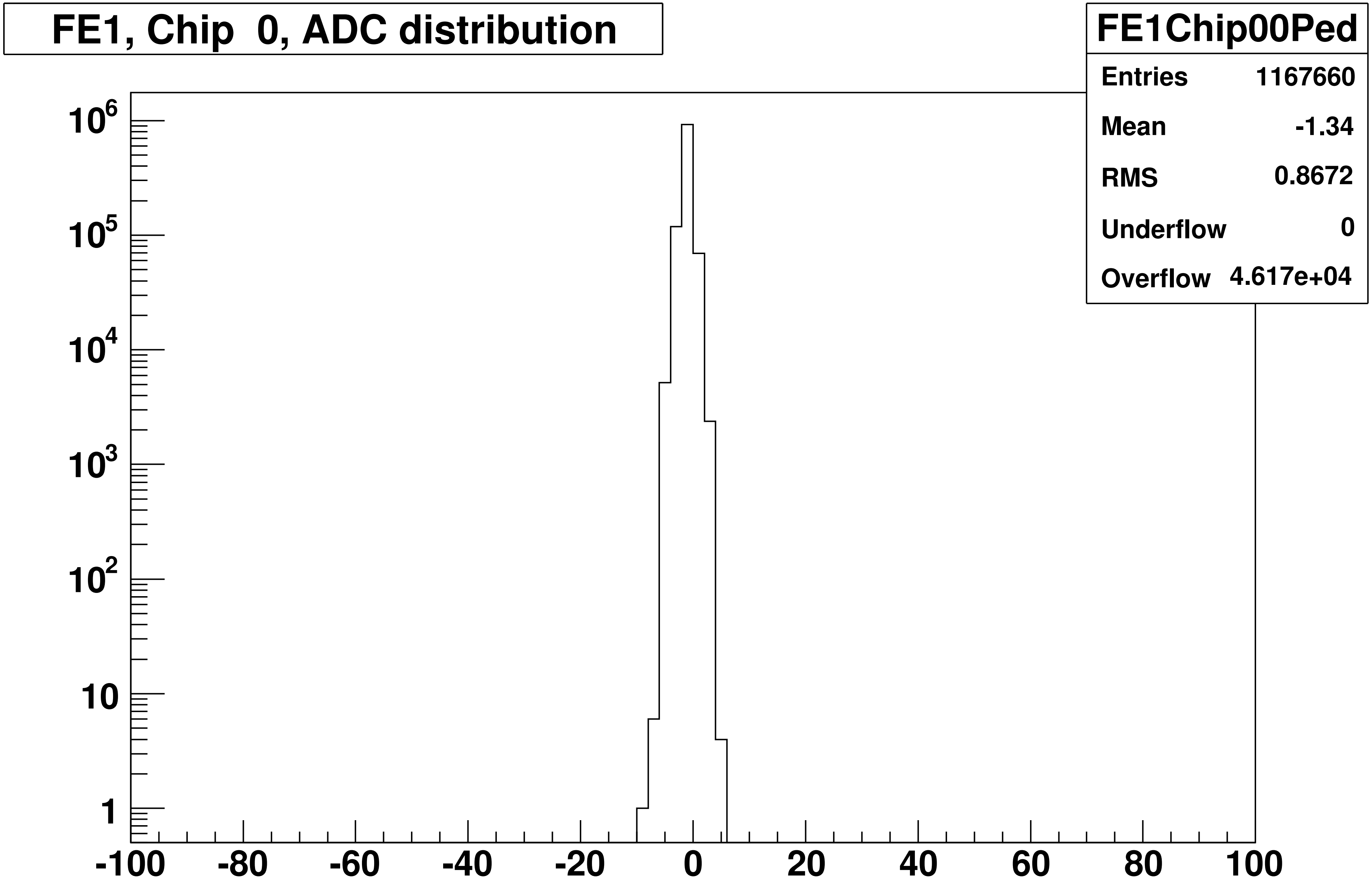
FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 1167660
Mean -1.34
RMS 0.8672
Underflow 0
Overflow 4.617e+04
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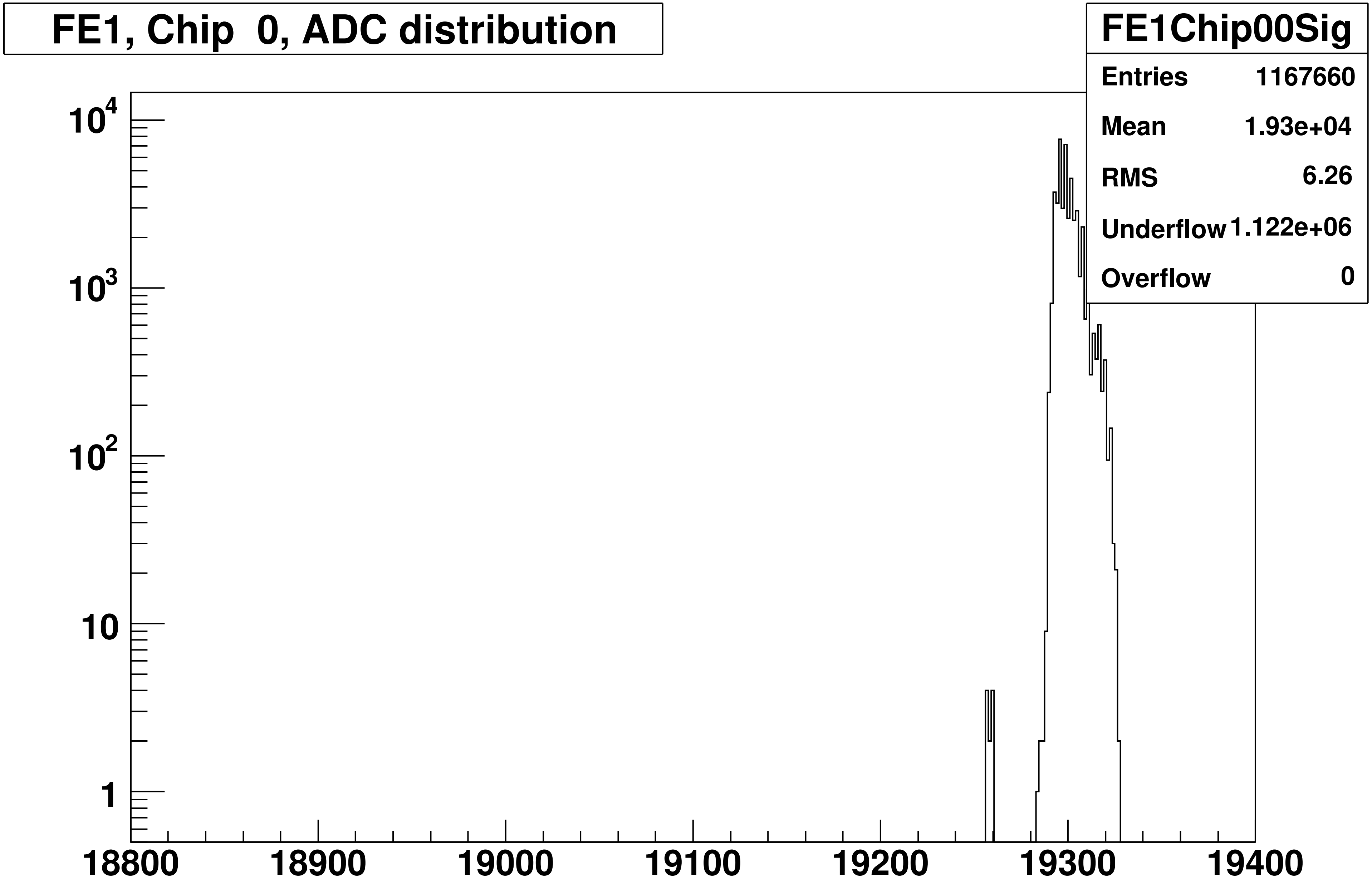
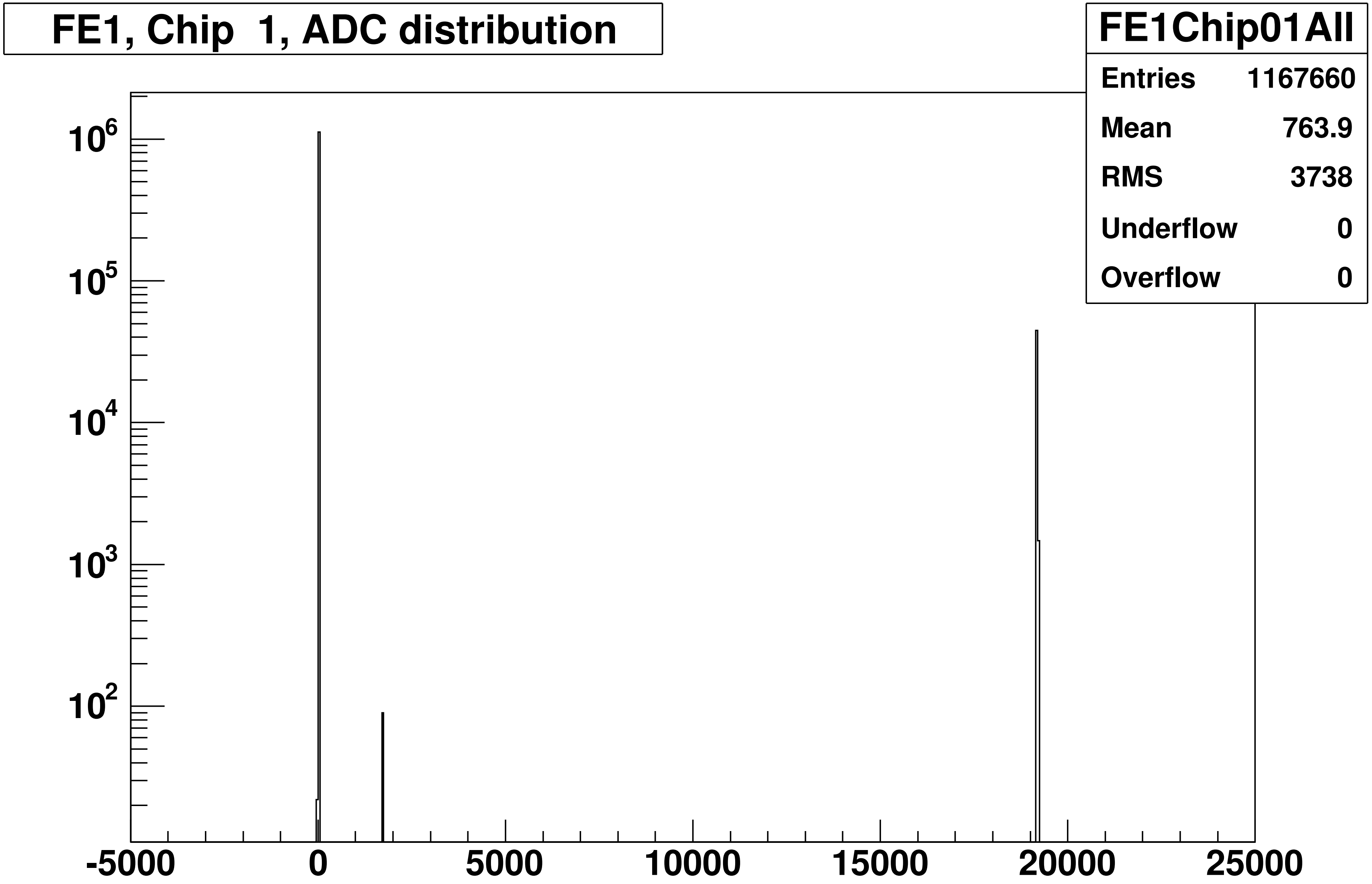


FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 1167660
Mean 1.93e+04
RMS 6.26
Underflow 1.122e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 1167660
Mean 763.9
RMS 3738
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
-5000
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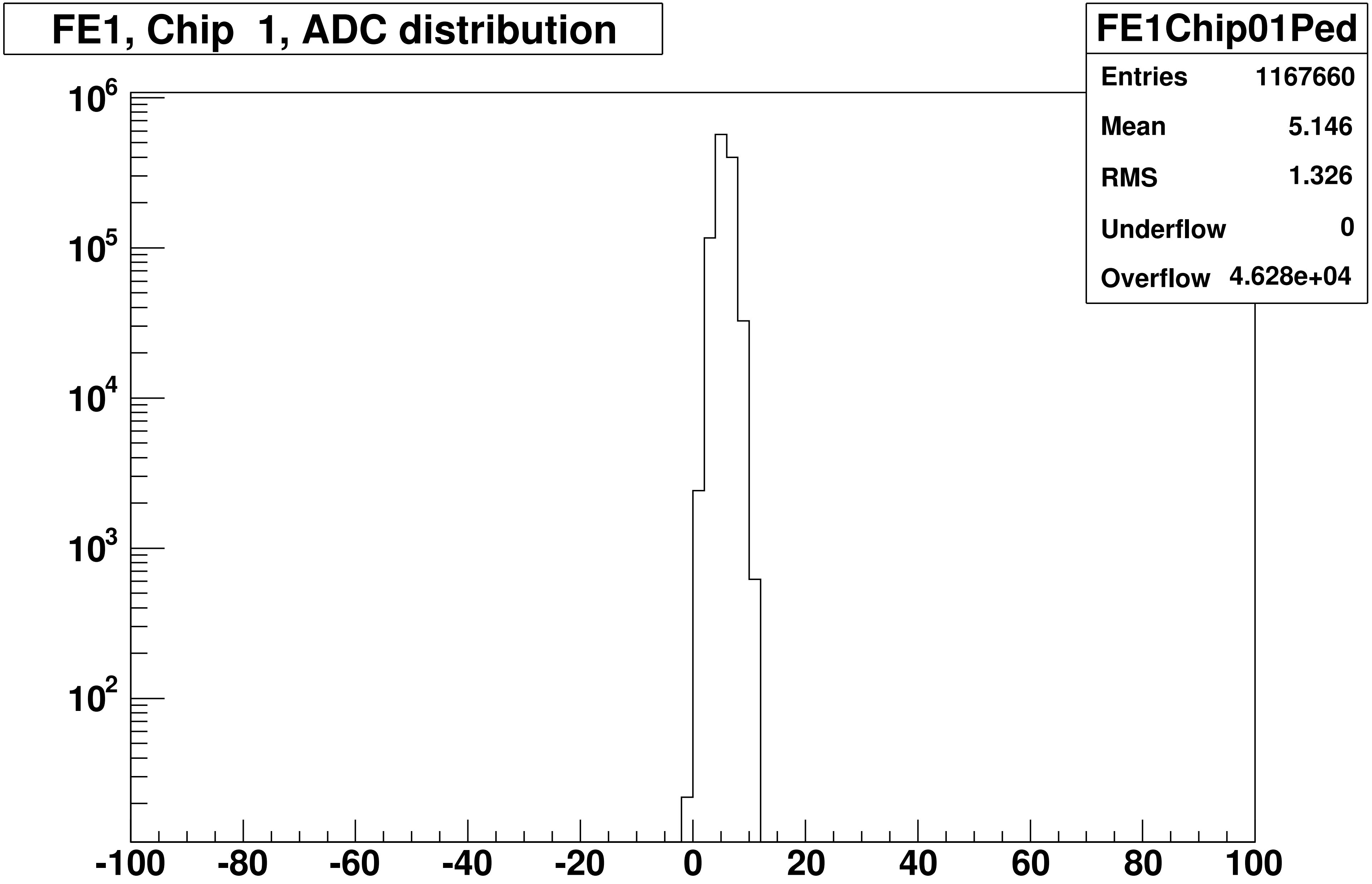
20000
25000


FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 1167660
Mean 5.146
RMS 1.326
Underflow 0
Overflow 4.628e+04
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10^5
10^4
10^3
10^2
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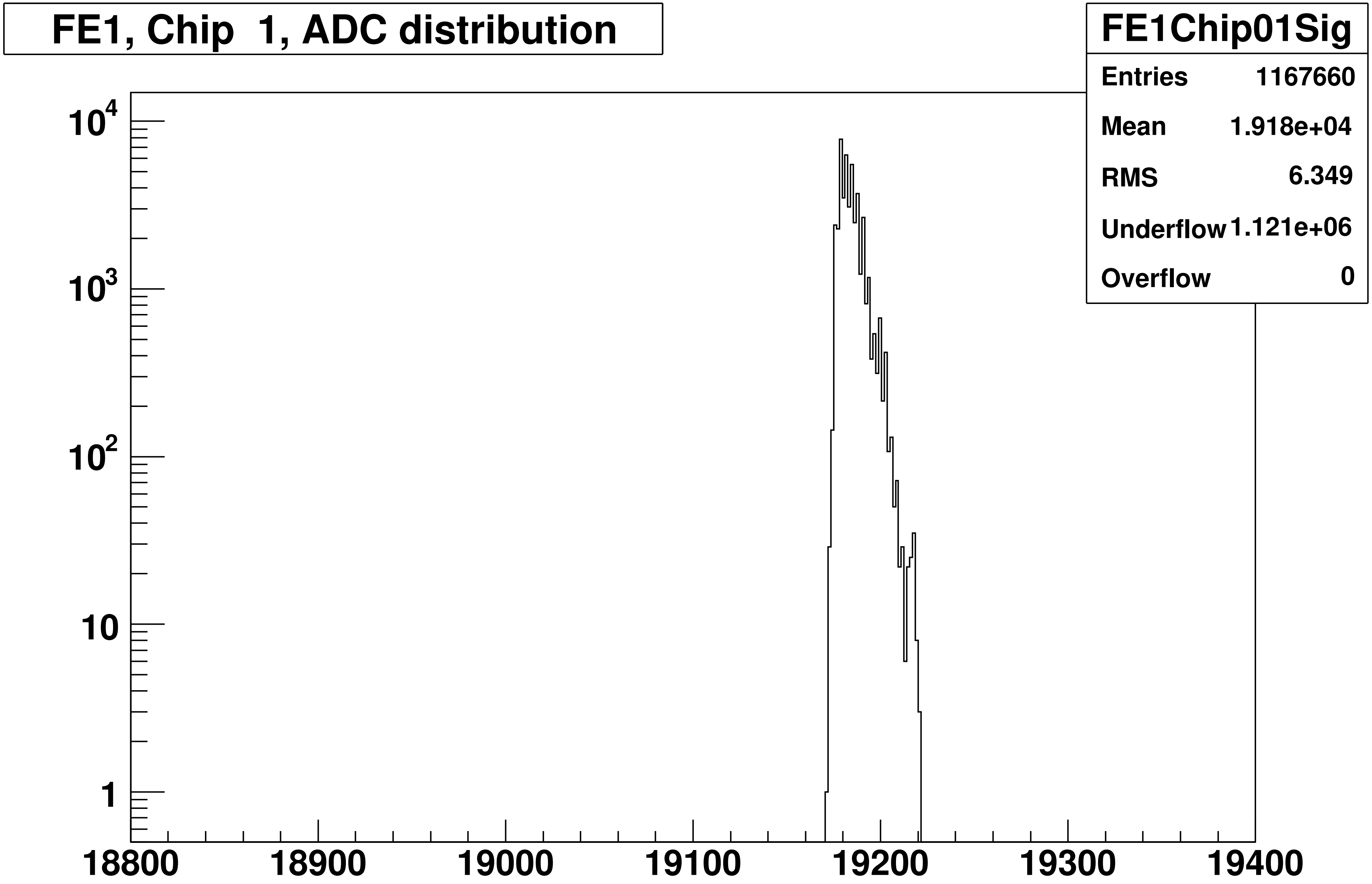

FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 1167660
Mean 1.918e+04
RMS 6.349
Underflow 1.121e+06
Overflow 0
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10^3
10^2
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18900
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19300
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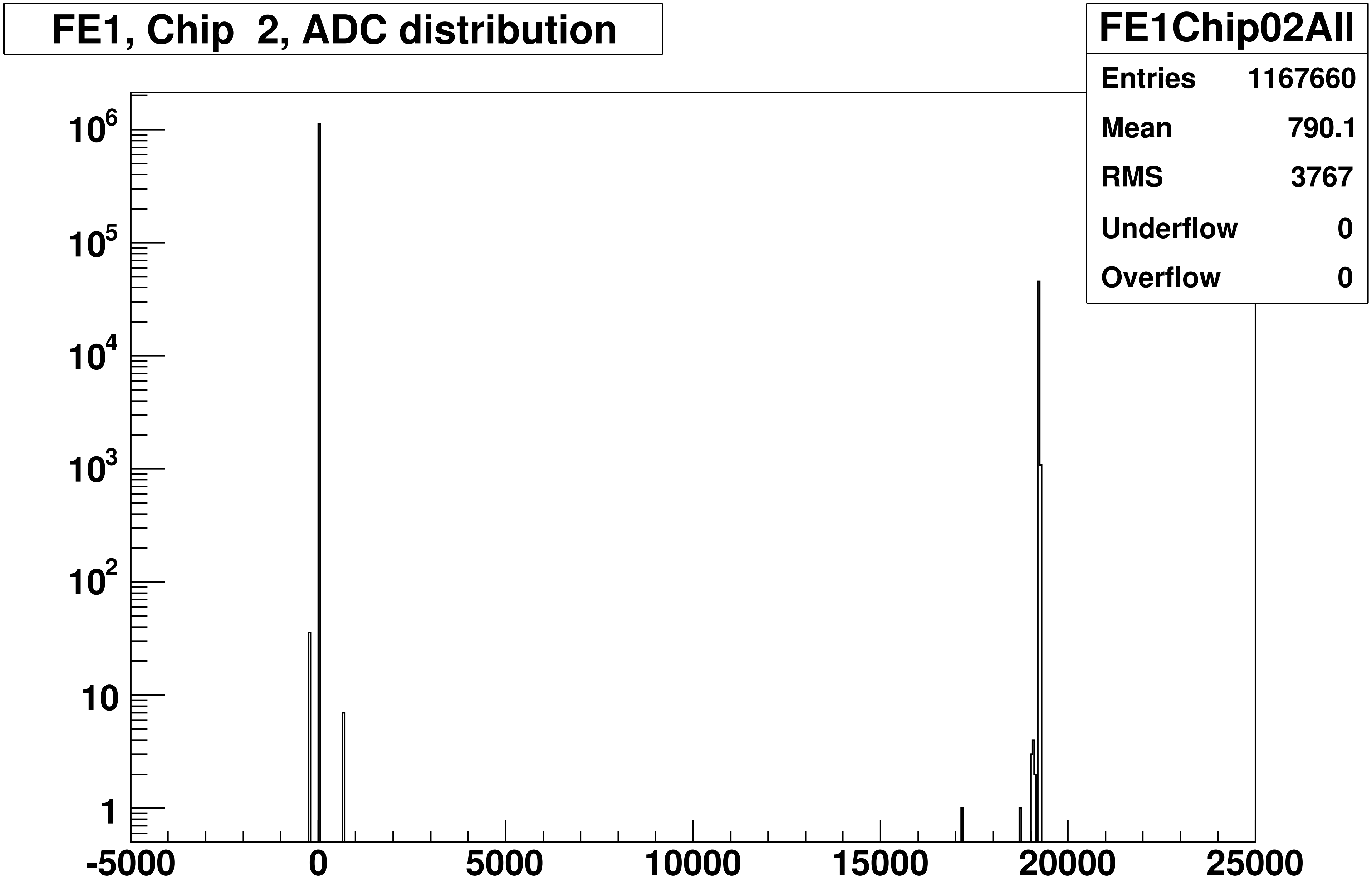
FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 1167660
Mean 790.1
RMS 3767
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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20000
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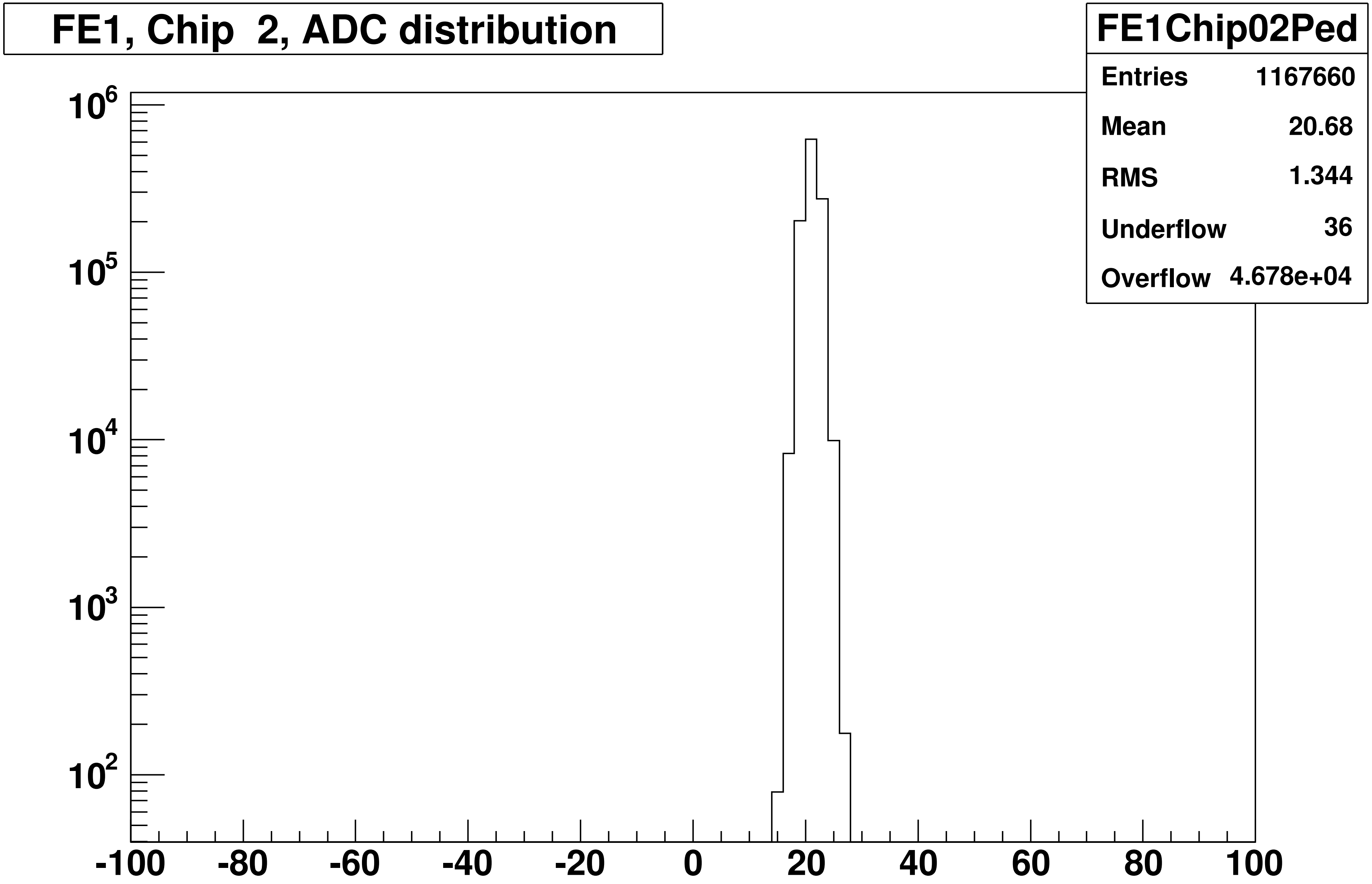
FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 1167660
Mean 20.68
RMS 1.344
Underflow 36
Overflow 4.678e+04
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10^5
10^4
10^3
10^2
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-60
-40
-20
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80
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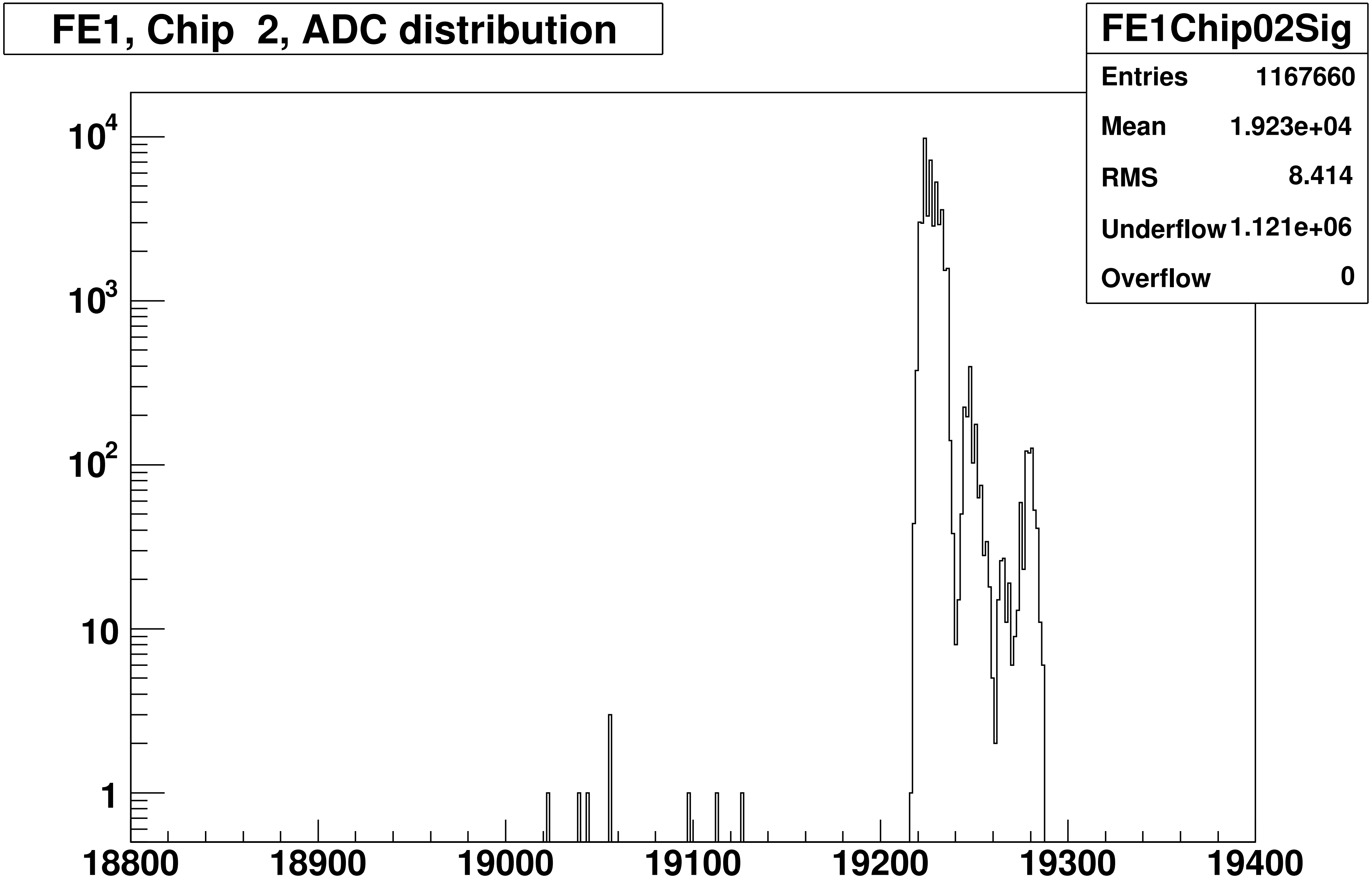
FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 1167660
Mean 1.923e+04
RMS 8.414
Underflow 1.121e+06
Overflow 0
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10^3
10^2
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18900
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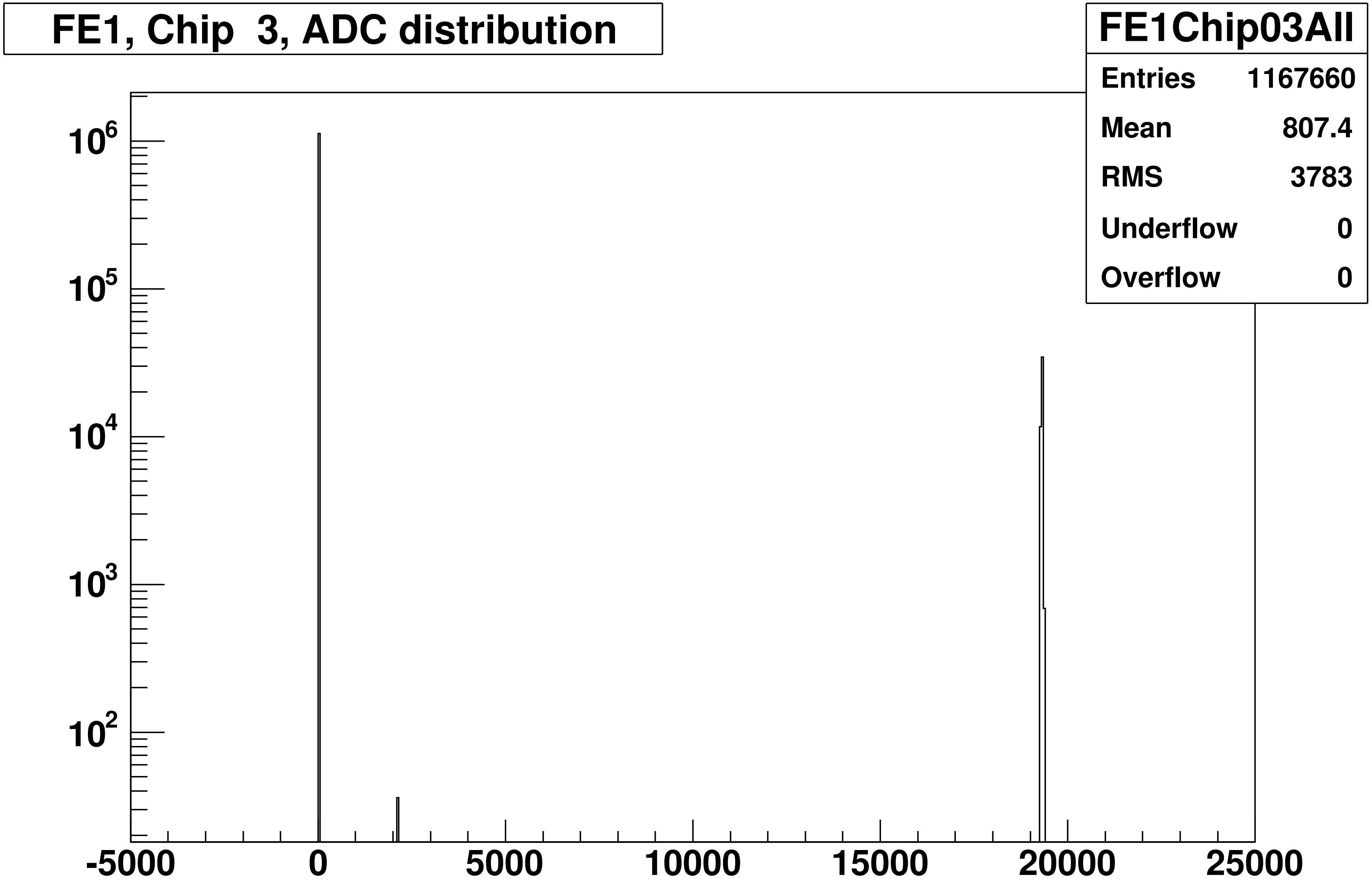
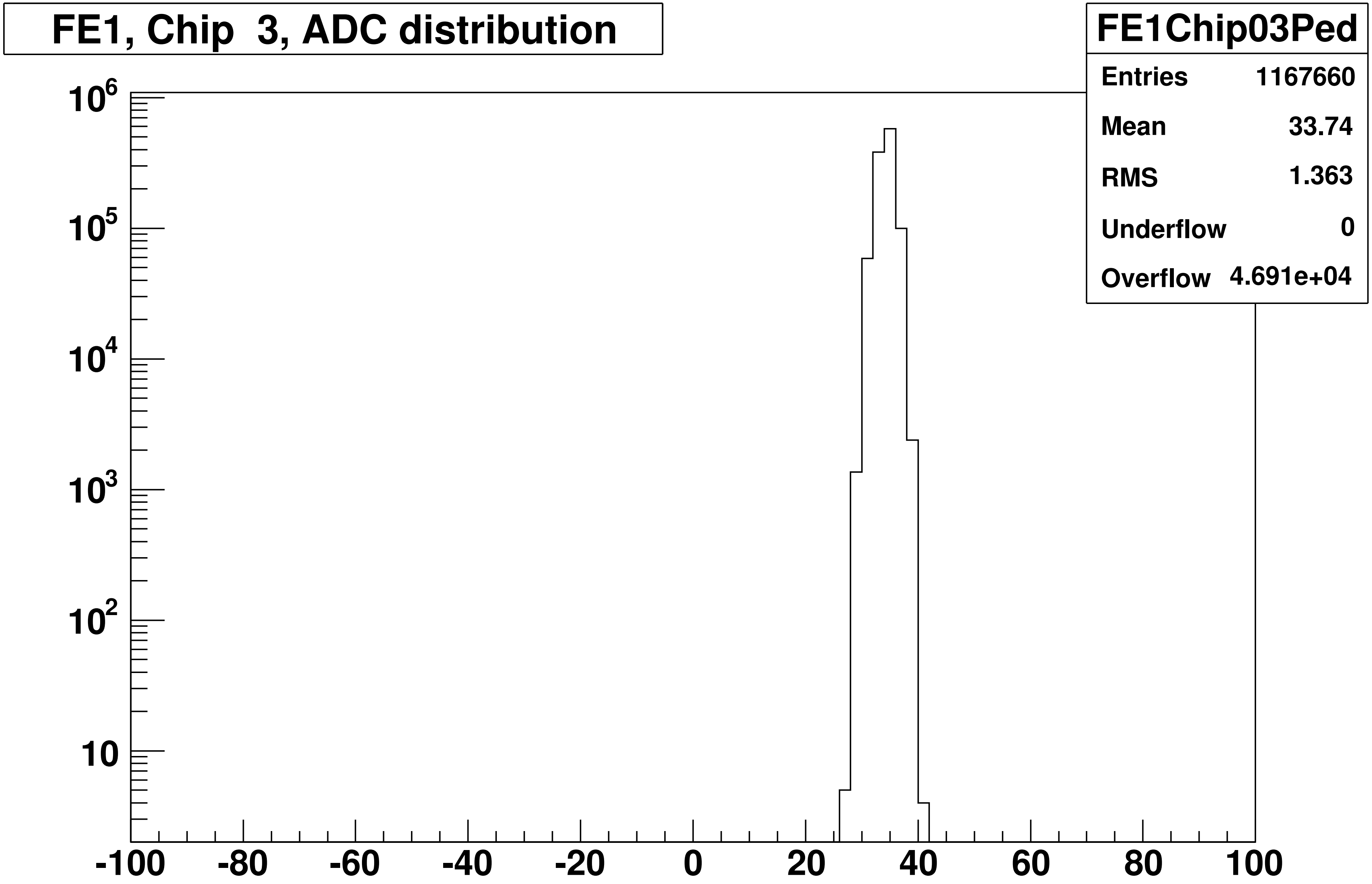

FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 1167660
Mean 807.4
RMS 3783
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 1167660
Mean 33.74
RMS 1.363
Underflow 0
Overflow 4.691e+04
10^6
10^5
10^4
10^3
10^2
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-100
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80
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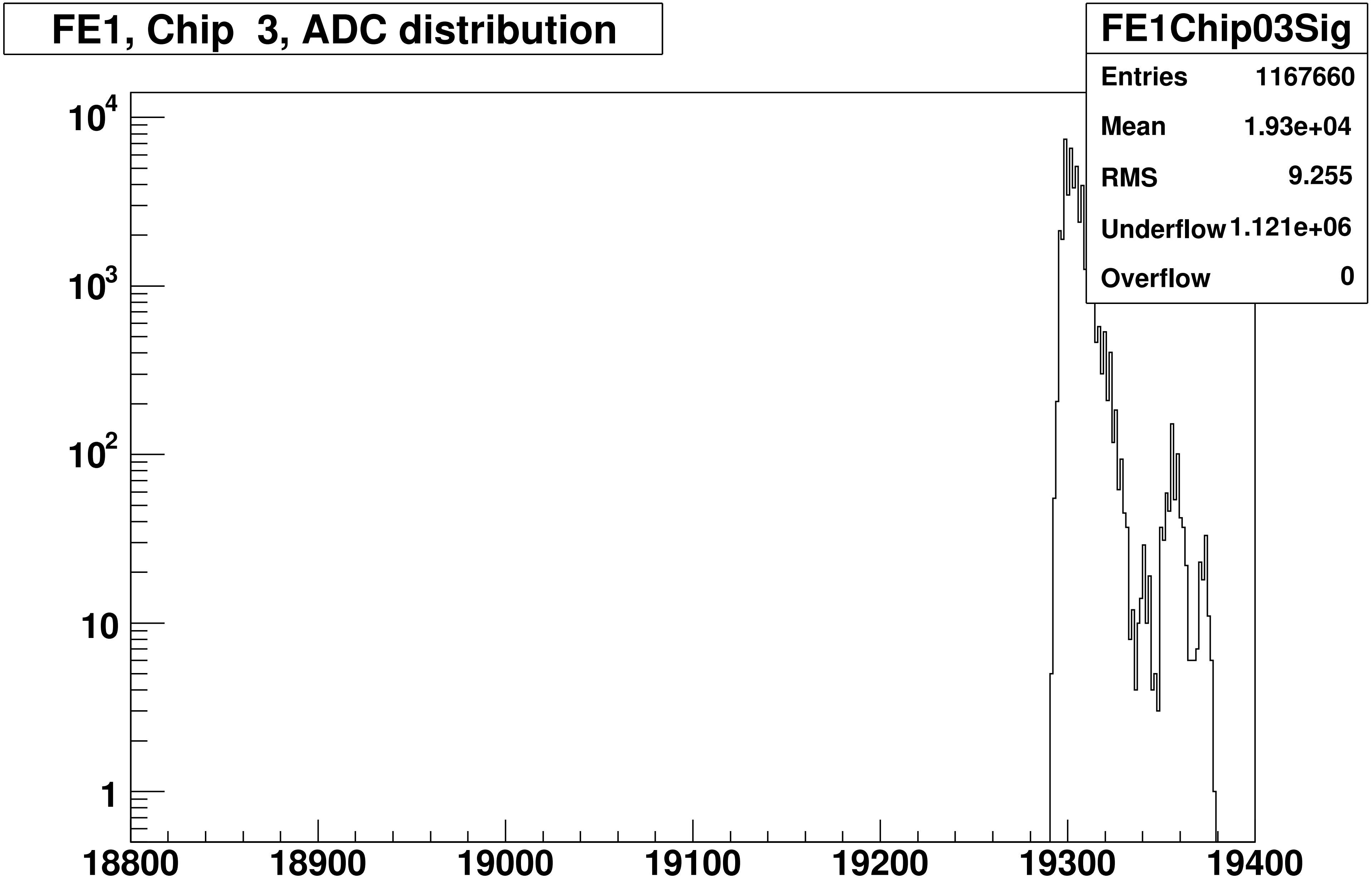
FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 1167660
Mean 1.93e+04
RMS 9.255
Underflow 1.121e+06
Overflow 0
10^4
10^3
10^2
10
1
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18900
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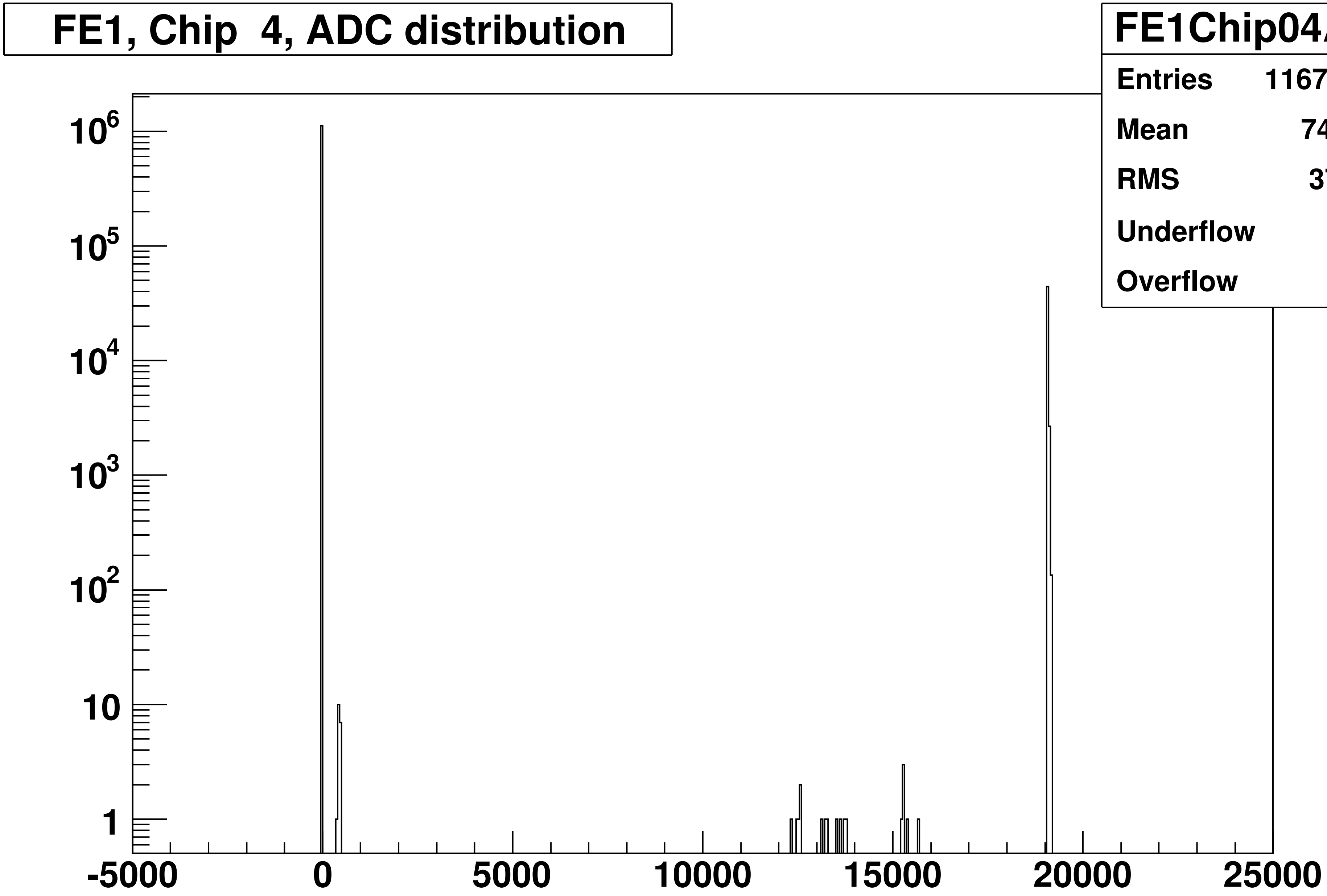

FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 1167660
Mean 744.9
RMS 3758
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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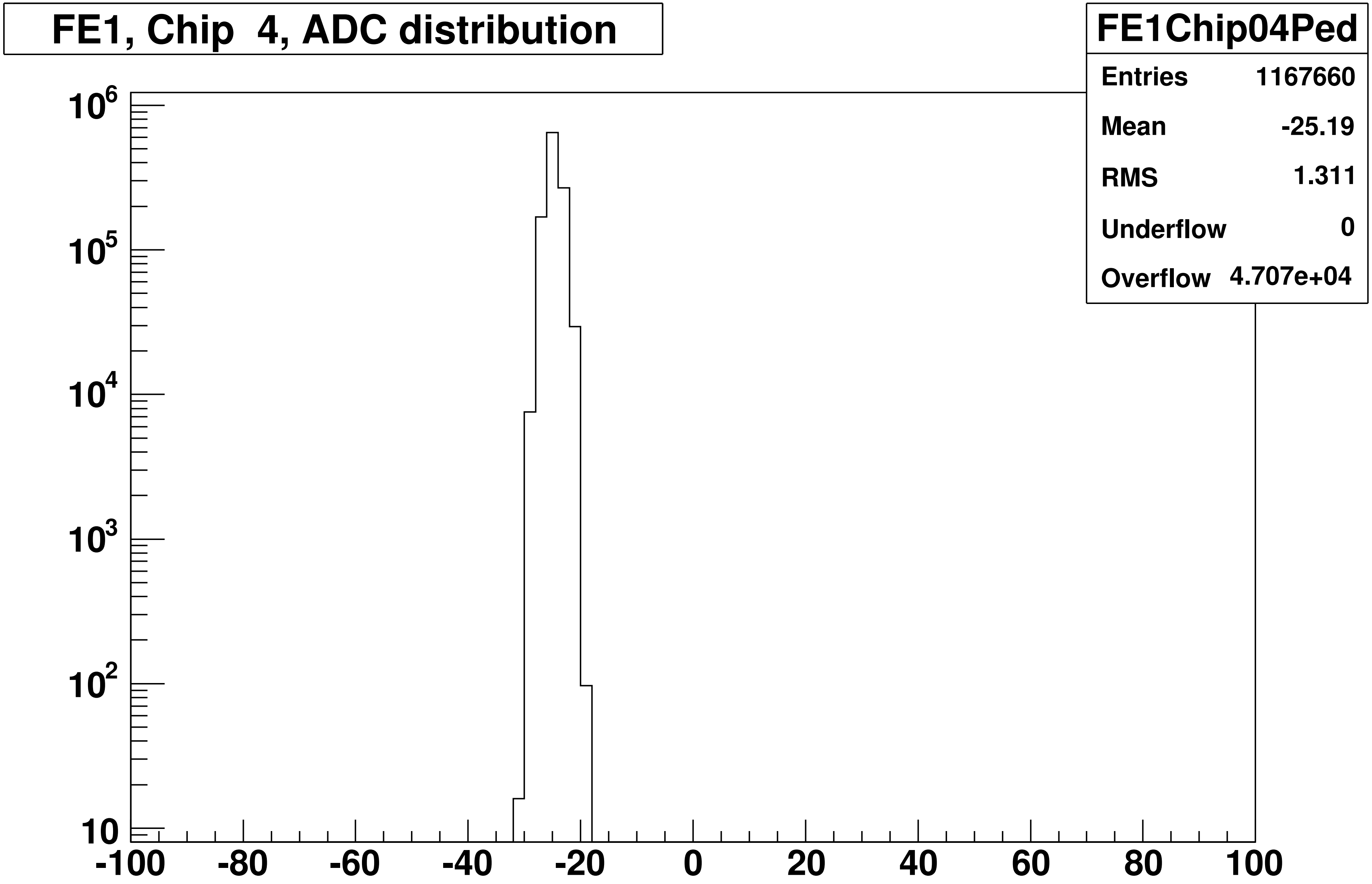
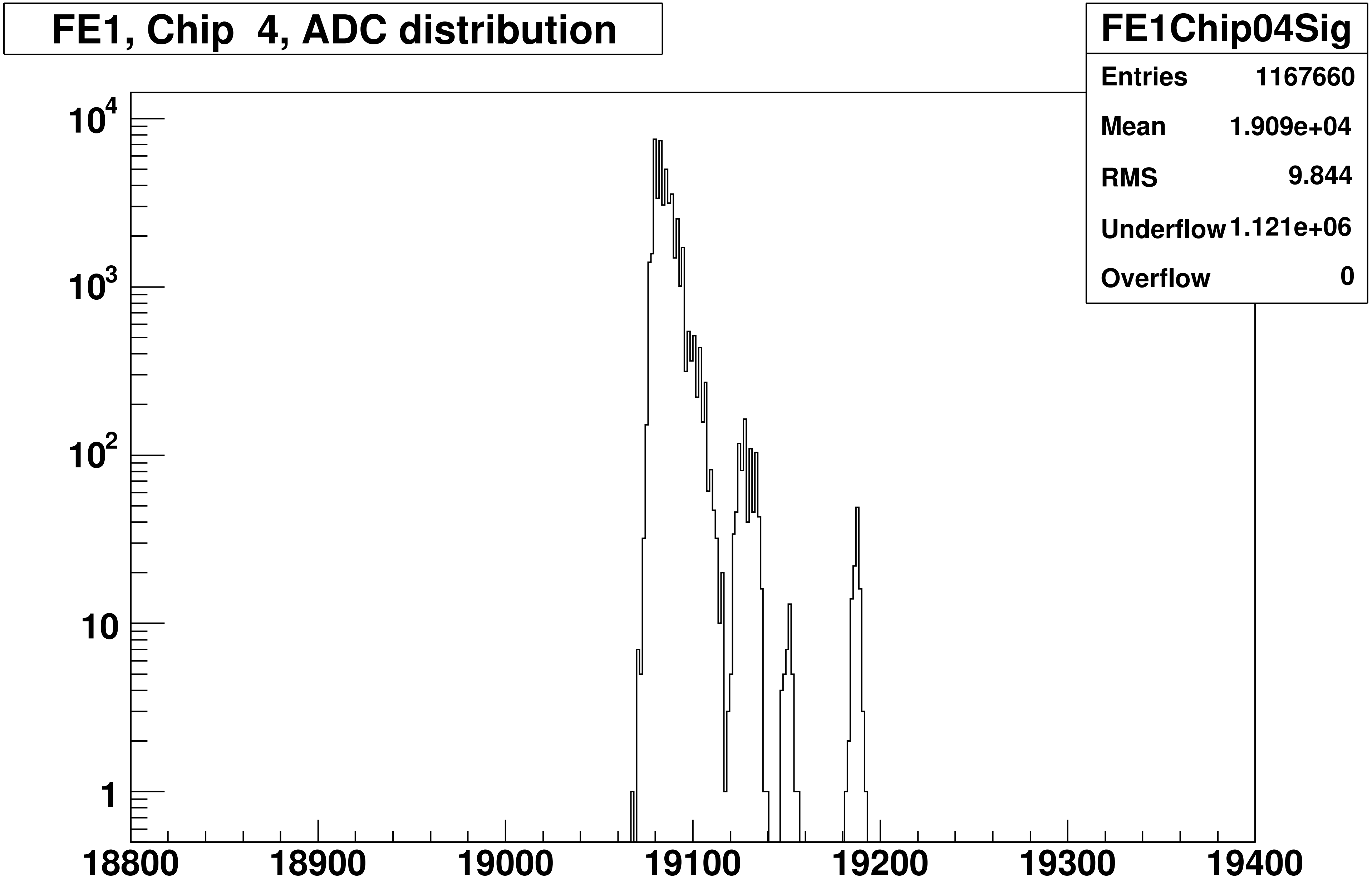
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 1167660
Mean -25.19
RMS 1.311
Underflow 0
Overflow 4.707e+04
10
10^2
10^3
10^4
10^5
10^6
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 1167660
Mean 1.909e+04
RMS 9.844
Underflow 1.121e+06
Overflow 0
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10^2
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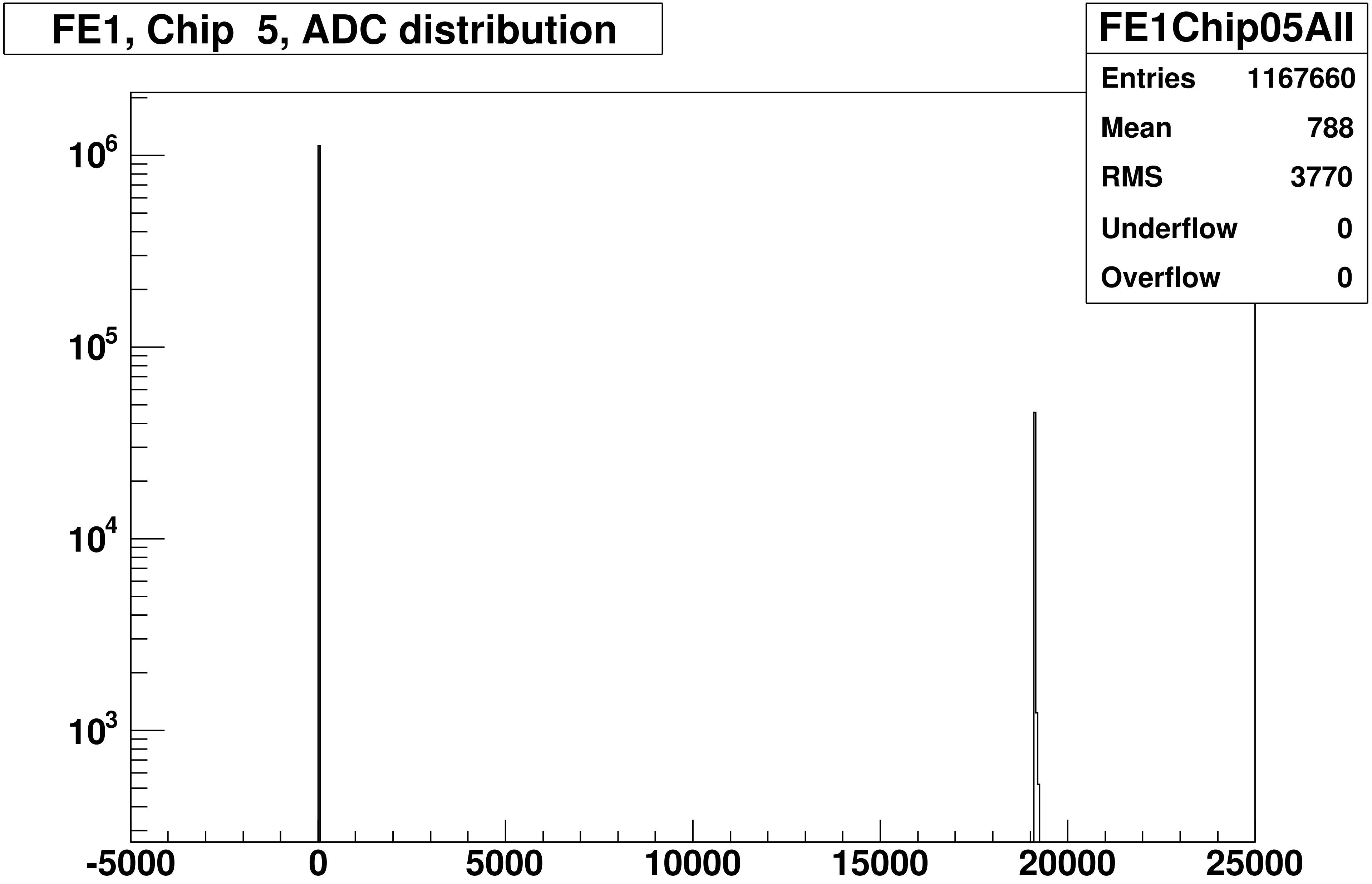
FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 1167660
Mean 788
RMS 3770
Underflow 0
Overflow 0
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10^5
10^4
10^3
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0
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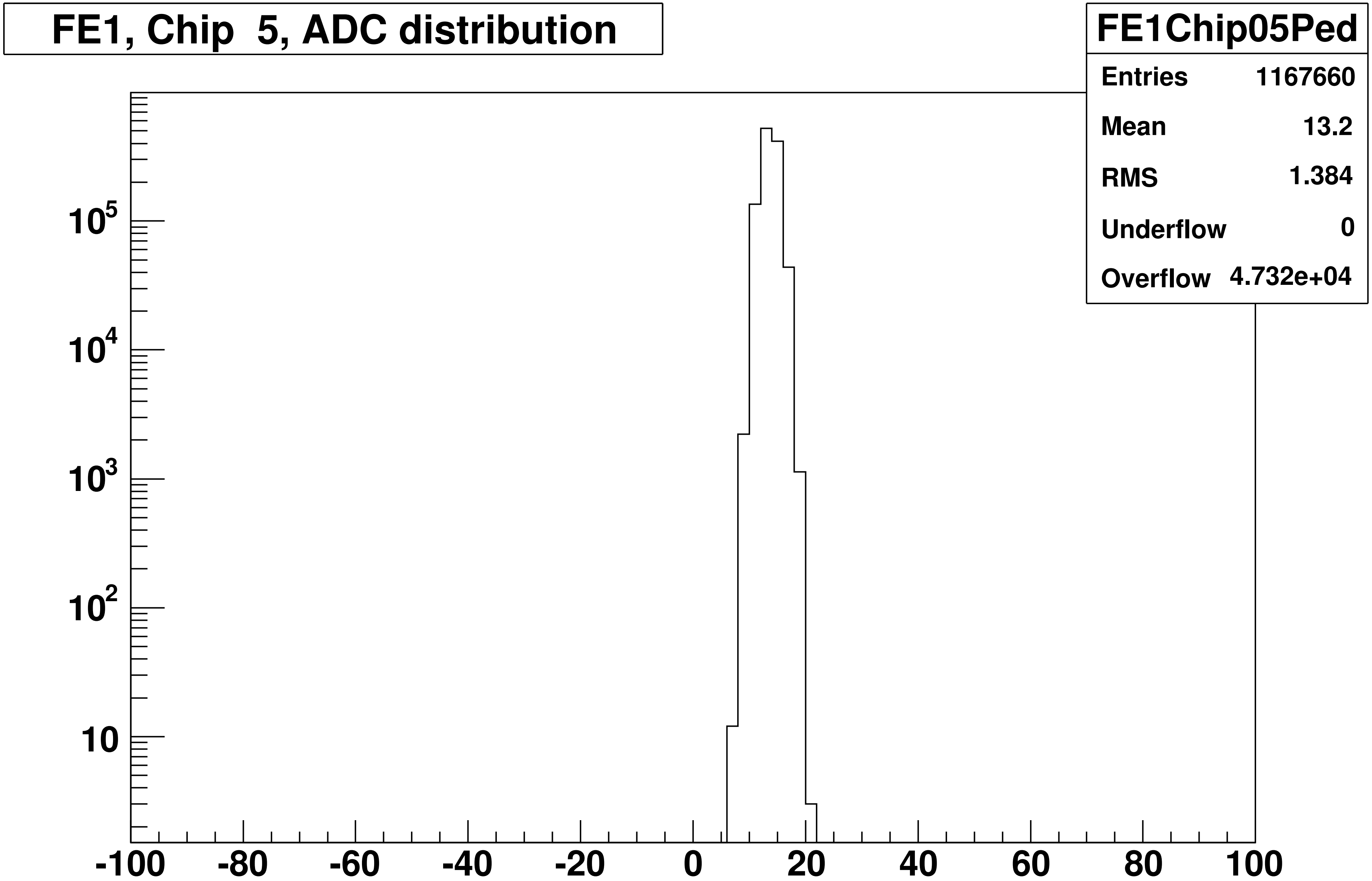
10000
15000
20000
25000


FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 1167660
Mean 13.2
RMS 1.384
Underflow 0
Overflow 4.732e+04
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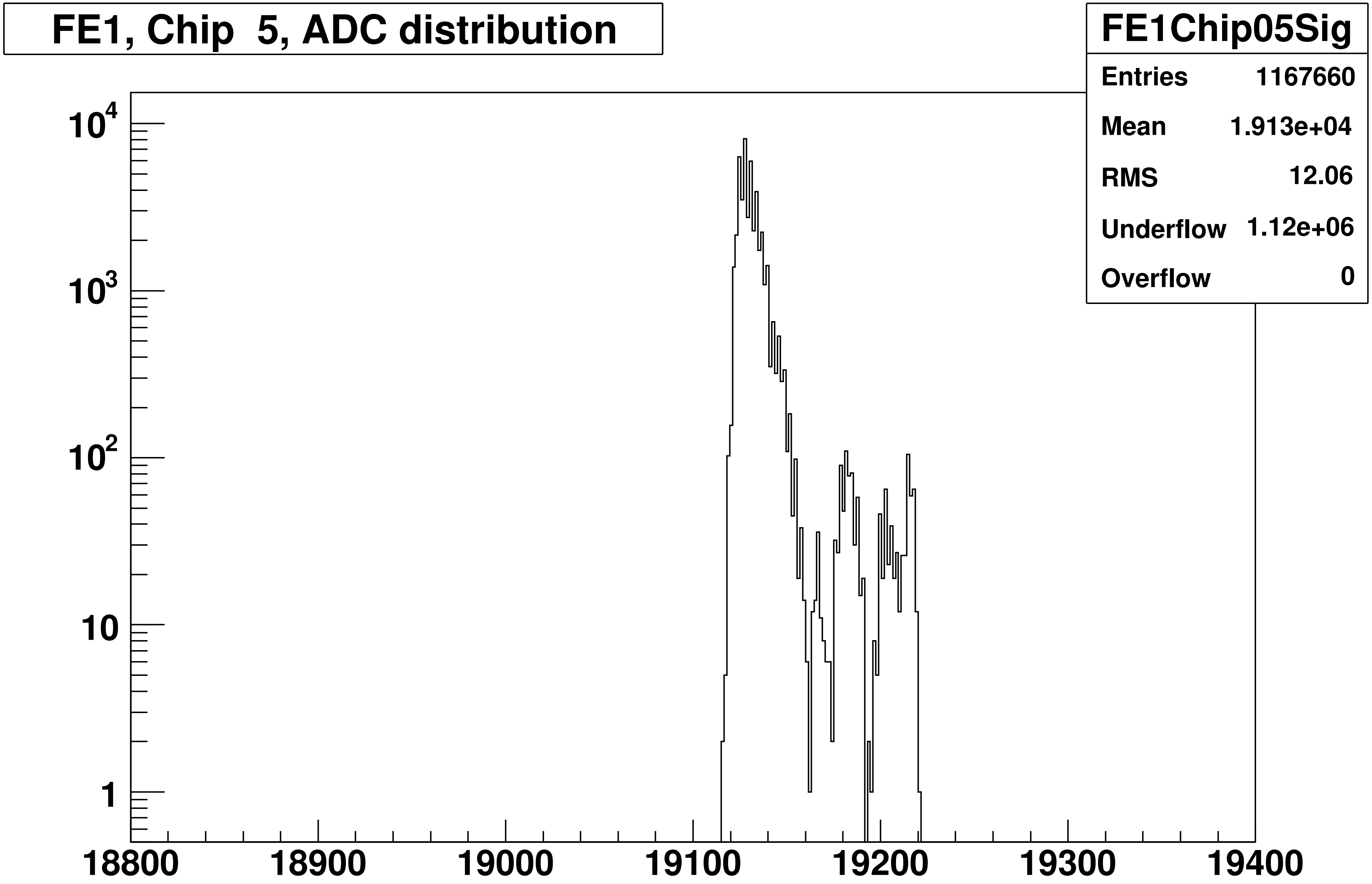
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 1167660
Mean 1.913e+04
RMS 12.06
Underflow 1.12e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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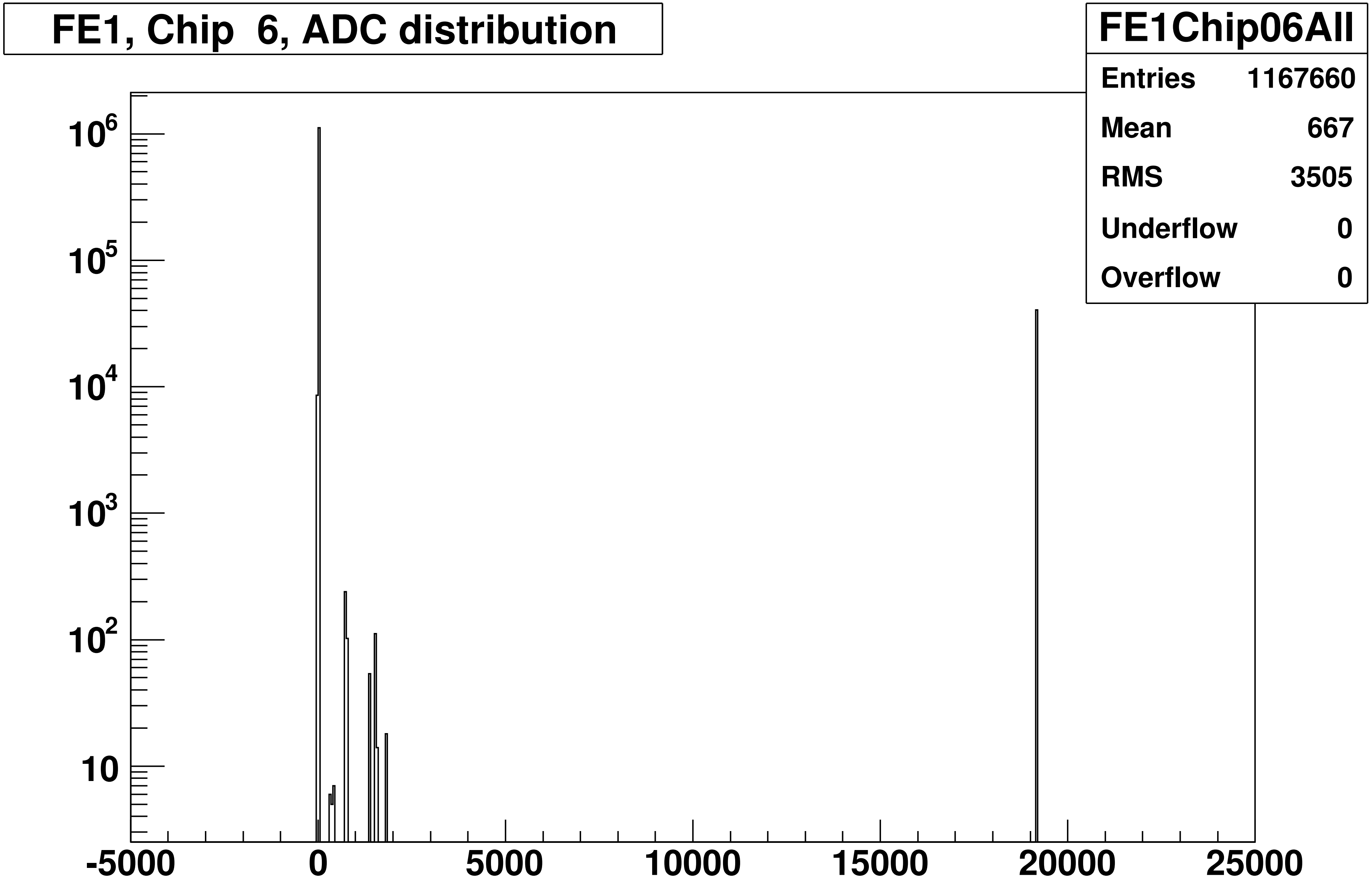
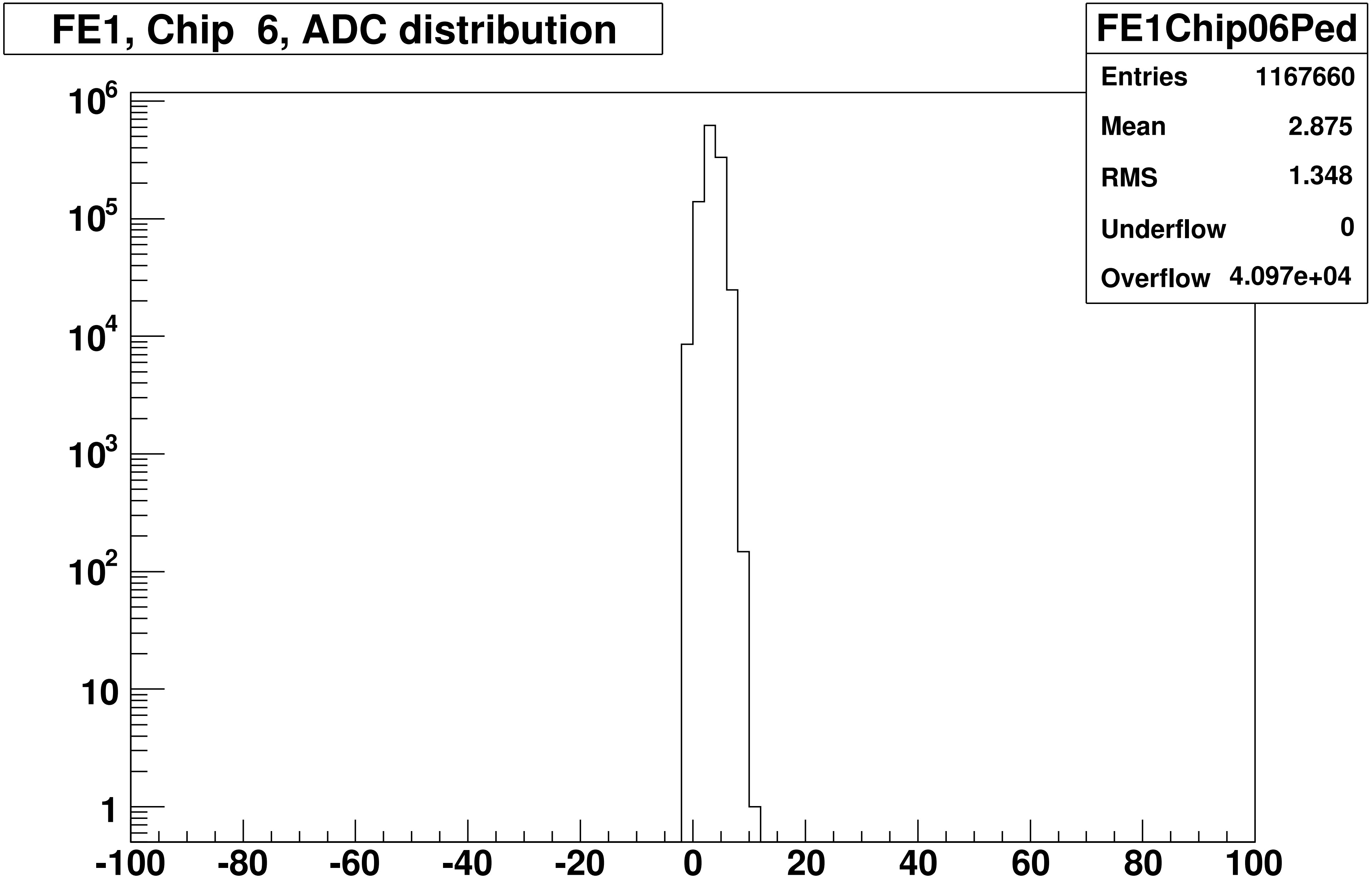
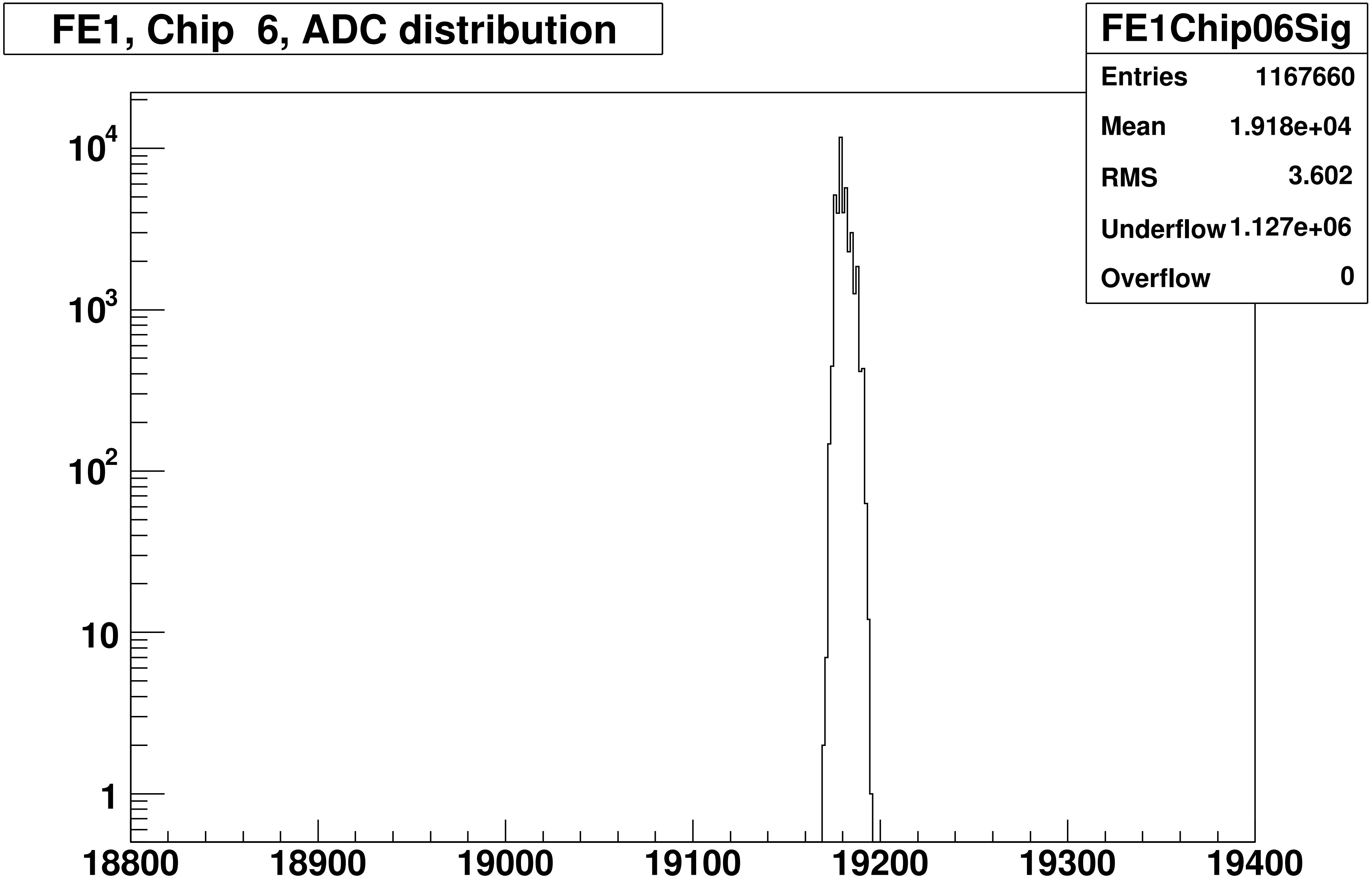
19100
19200
19300
19400


FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 1167660
Mean 667
RMS 3505
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 1167660
Mean 2.875
RMS 1.348
Underflow 0
Overflow 4.097e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 1167660
Mean 1.918e+04
RMS 3.602
Underflow 1.127e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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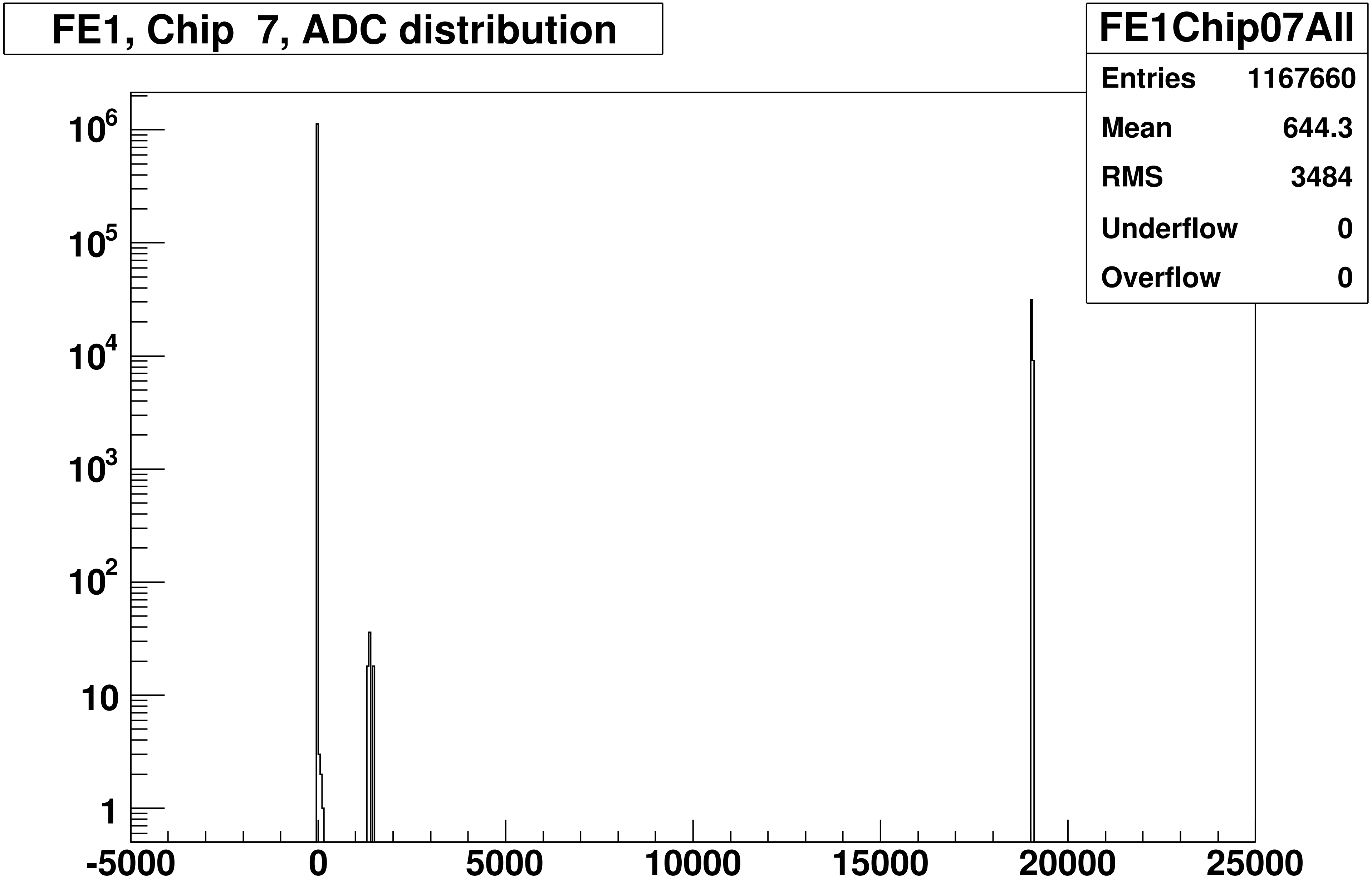
19100
19200
19300
19400



FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 1167660
Mean 644.3
RMS 3484
Underflow 0
Overflow 0
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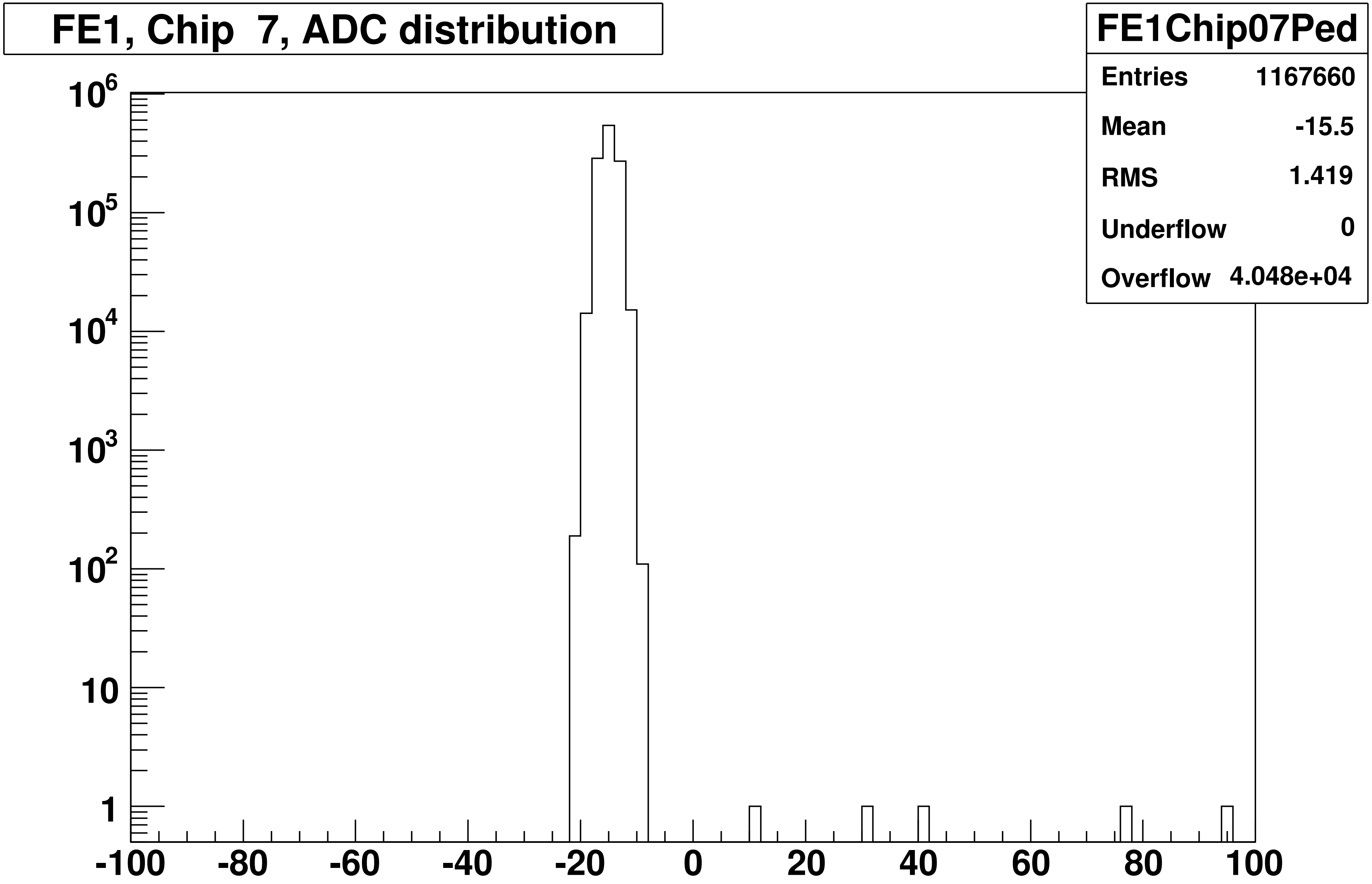
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 1167660
Mean -15.5
RMS 1.419
Underflow 0
Overflow 4.048e+04
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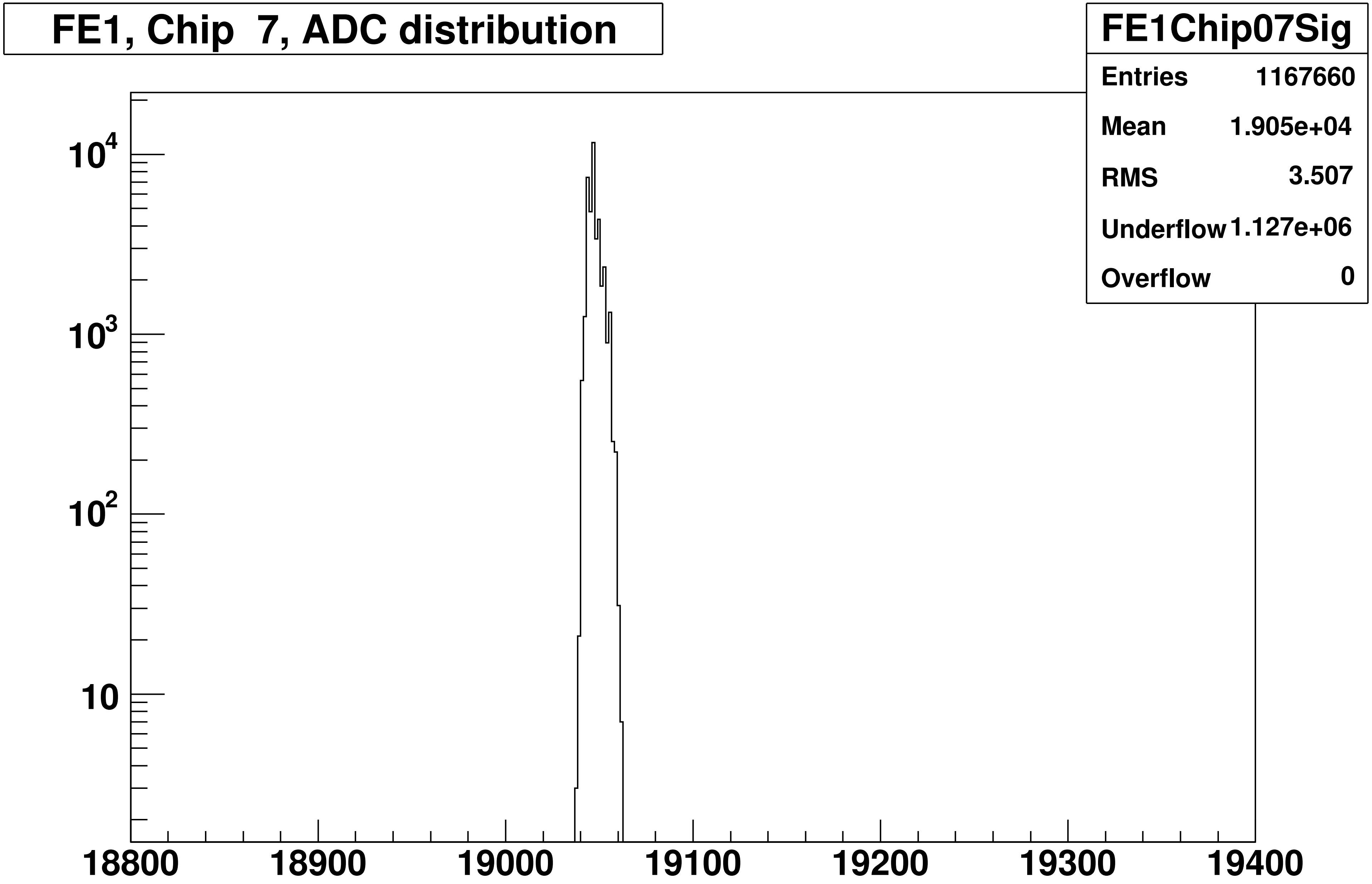
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 1167660
Mean 1.905e+04
RMS 3.507
Underflow 1.127e+06
Overflow 0
10^4
10^3
10^2
10
18800
18900
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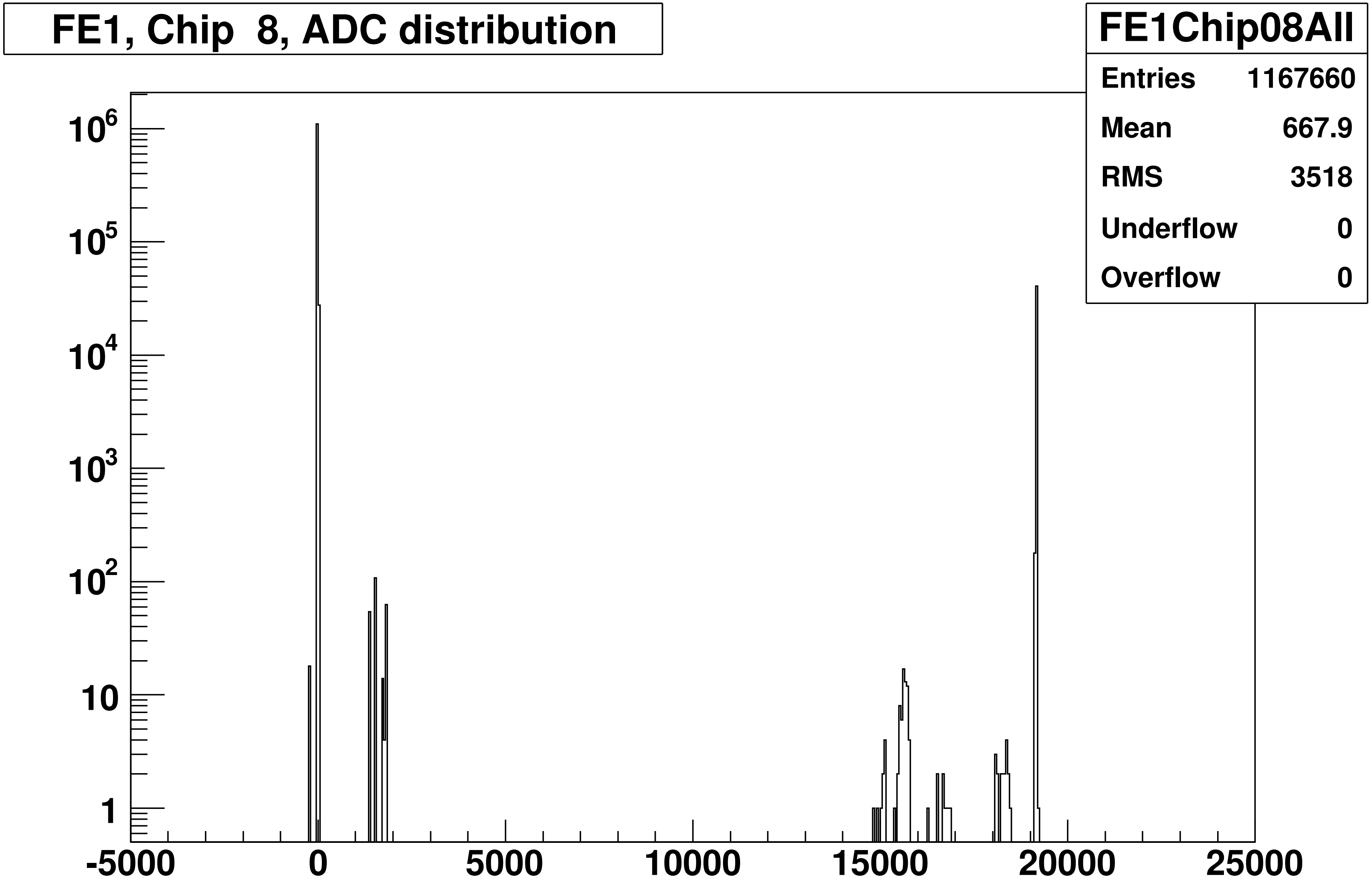
19100
19200
19300
19400



FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 1167660
Mean 667.9
RMS 3518
Underflow 0
Overflow 0
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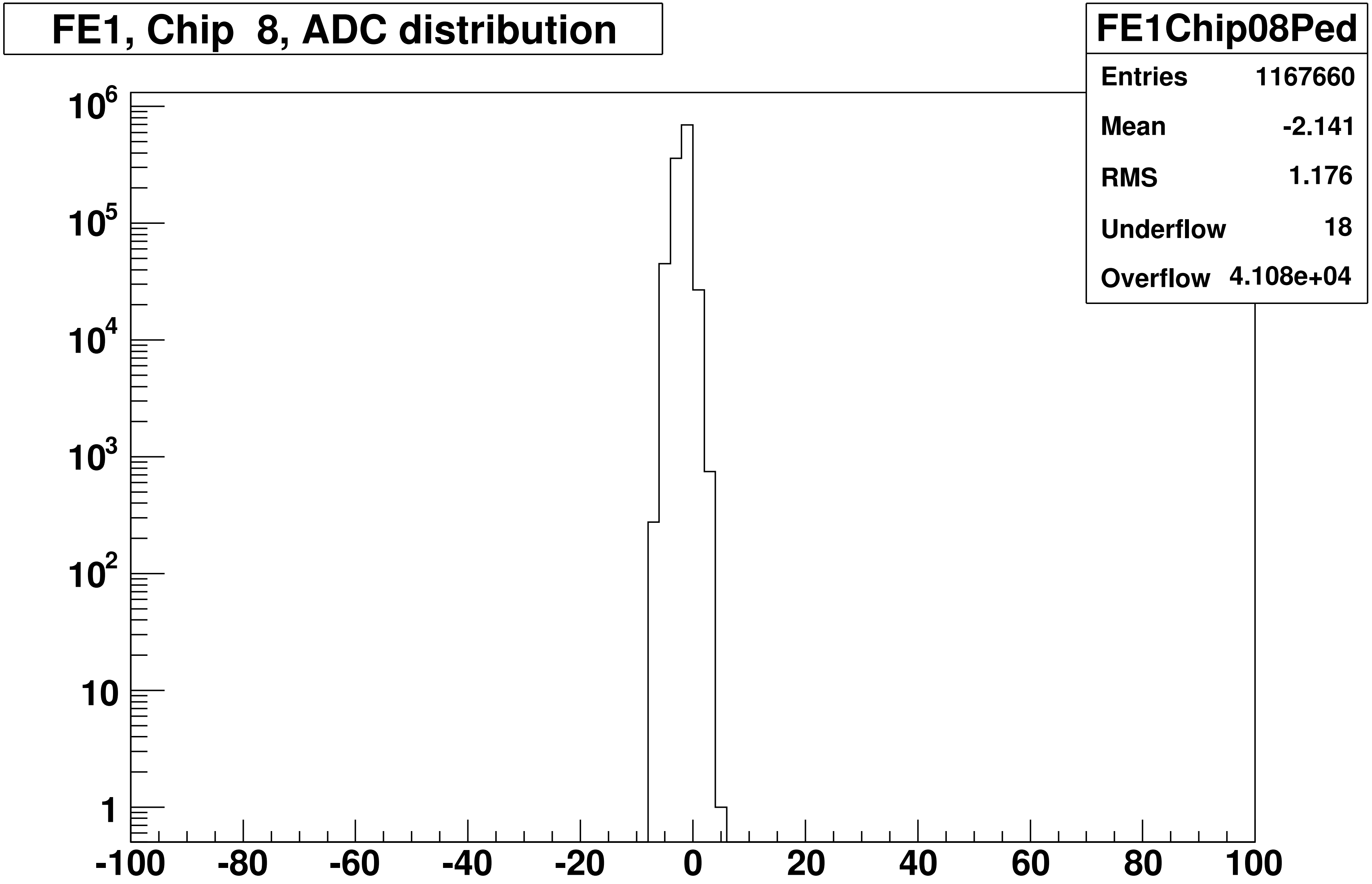
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 1167660
Mean -2.141
RMS 1.176
Underflow 18
Overflow 4.108e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
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-40
-20
0
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80
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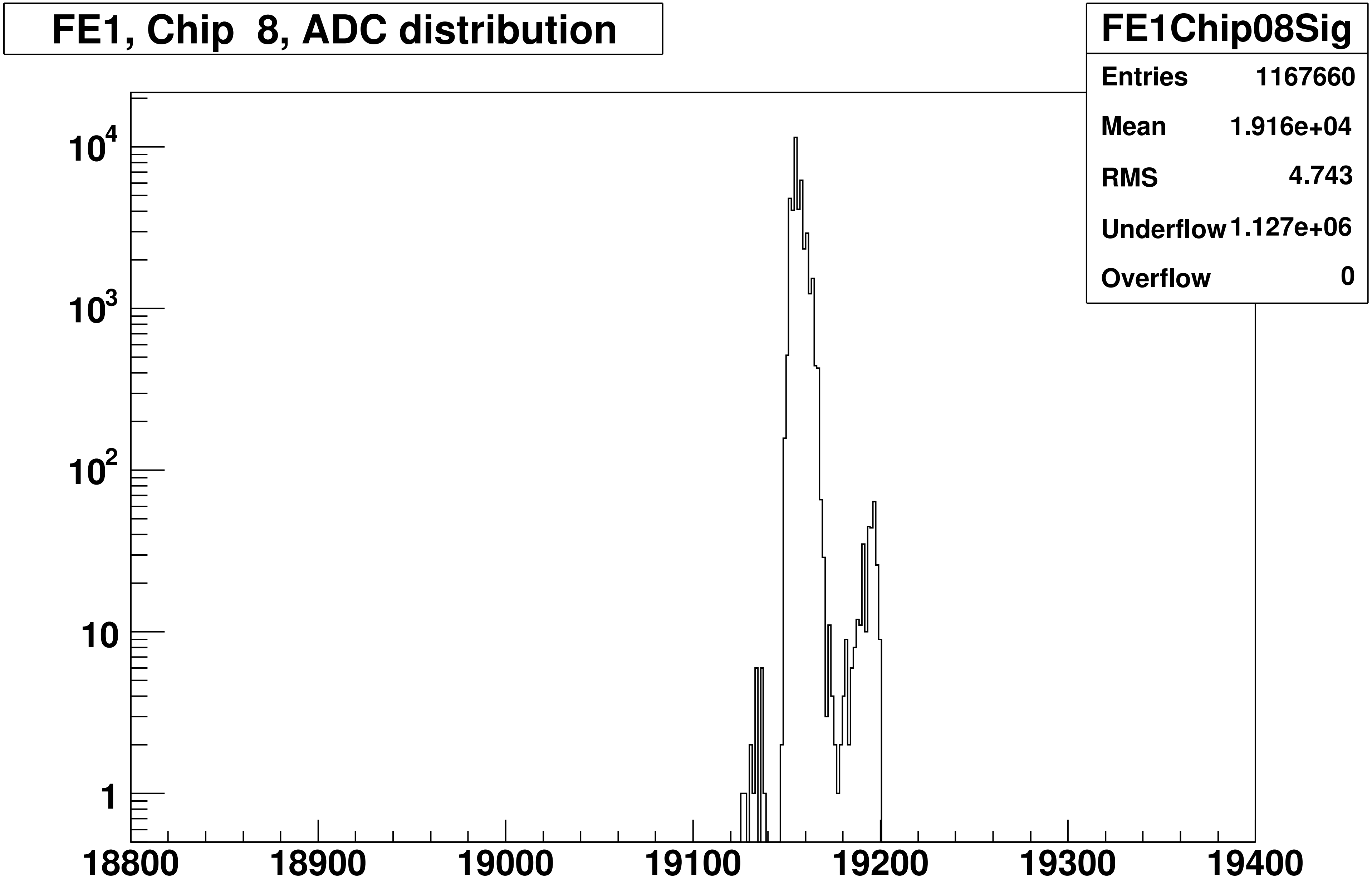
FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 1167660
Mean 1.916e+04
RMS 4.743
Underflow 1.127e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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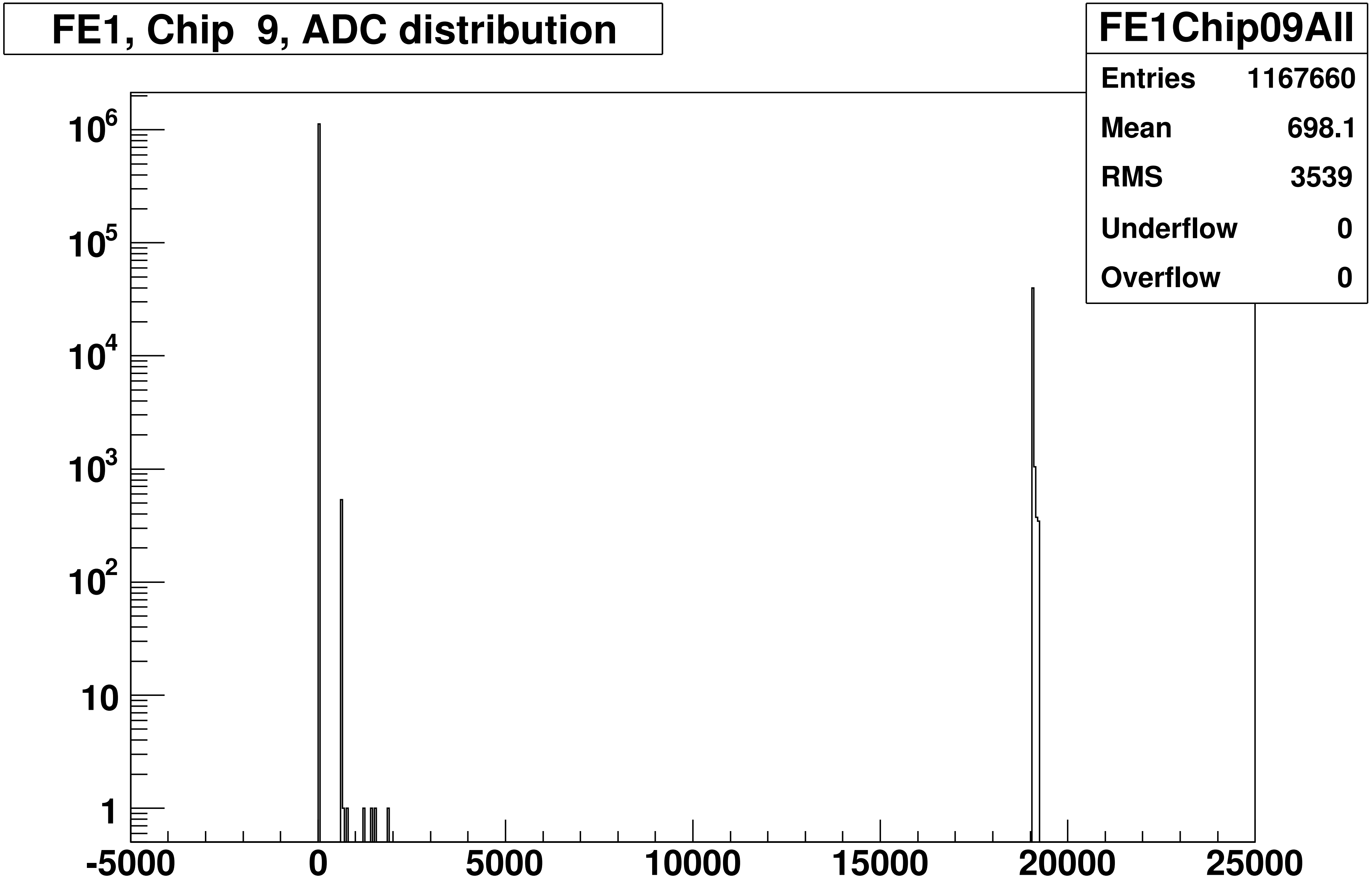
FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 1167660
Mean 698.1
RMS 3539
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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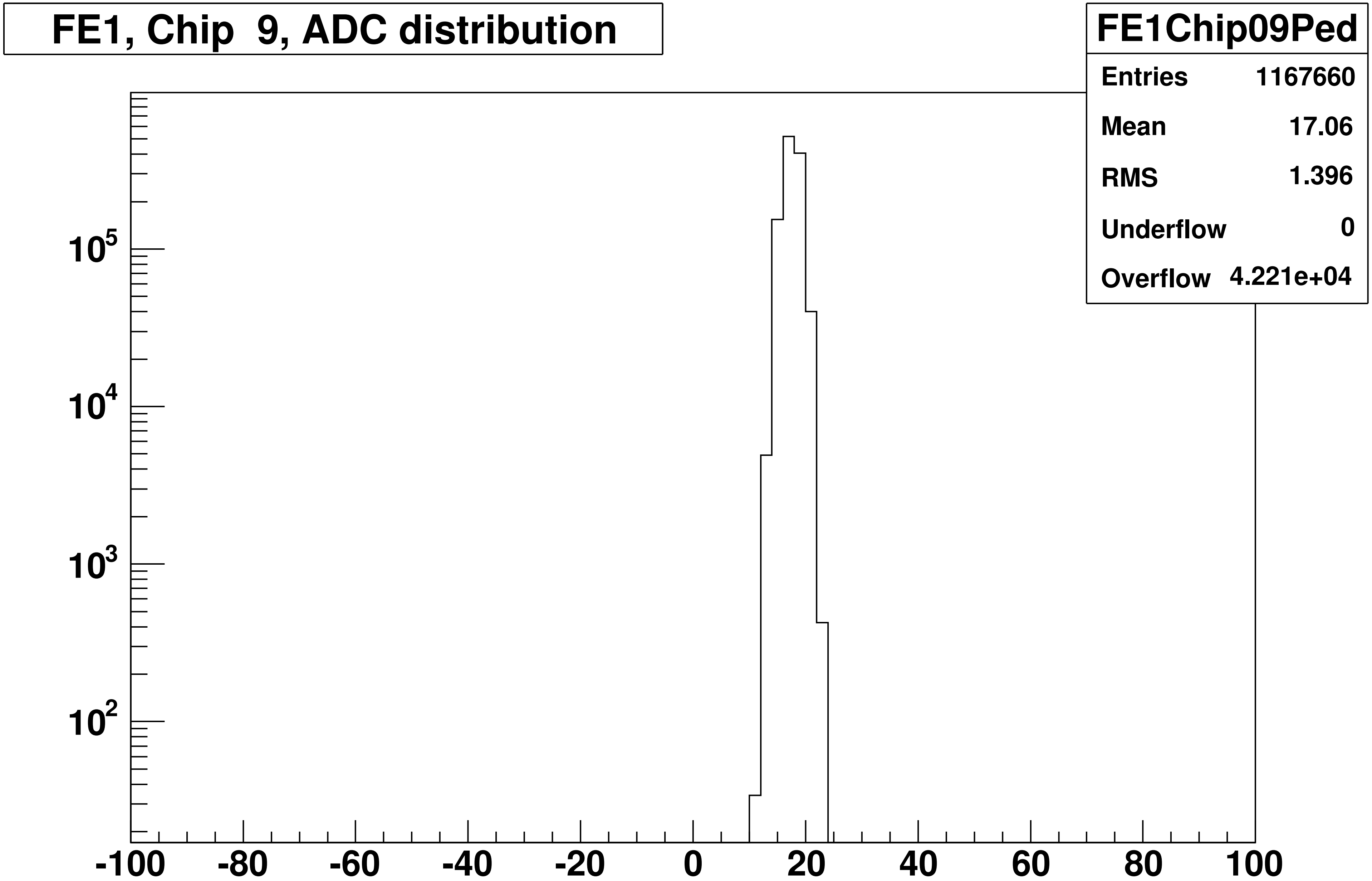
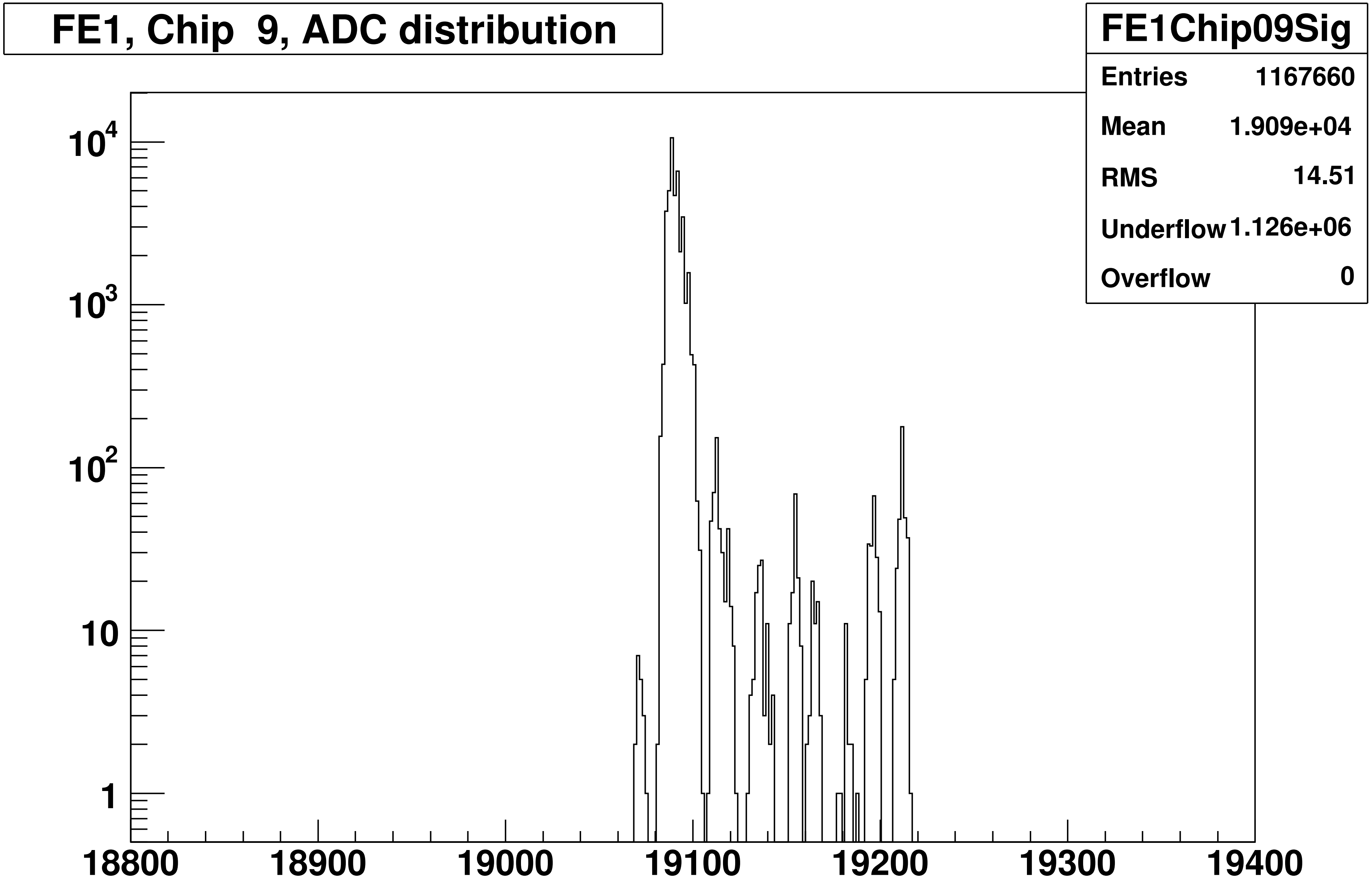
20000
25000


FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 1167660
Mean 17.06
RMS 1.396
Underflow 0
Overflow 4.221e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 1167660
Mean 1.909e+04
RMS 14.51
Underflow 1.126e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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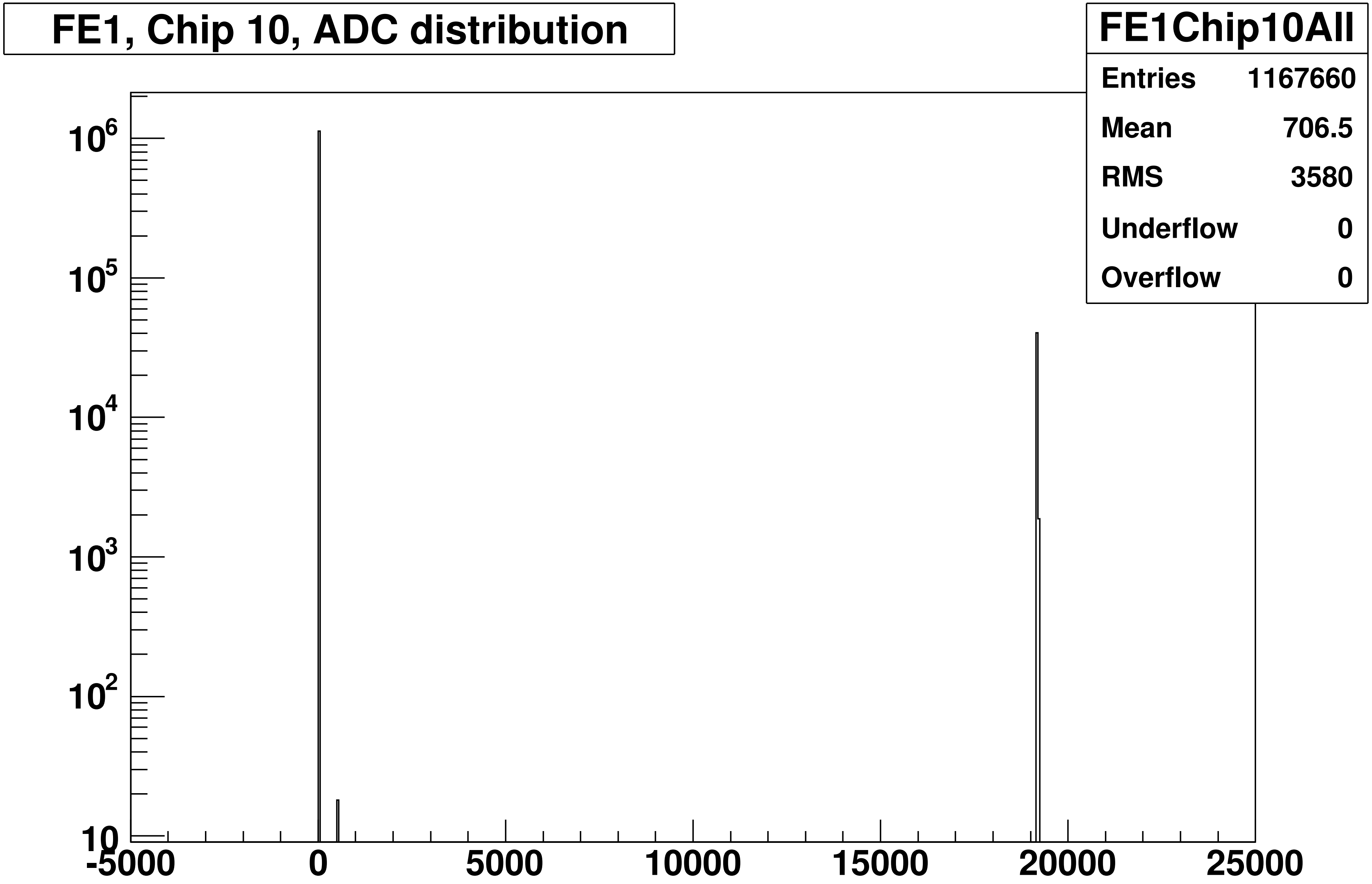
19200
19300
19400


FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 1167660
Mean 706.5
RMS 3580
Underflow 0
Overflow 0
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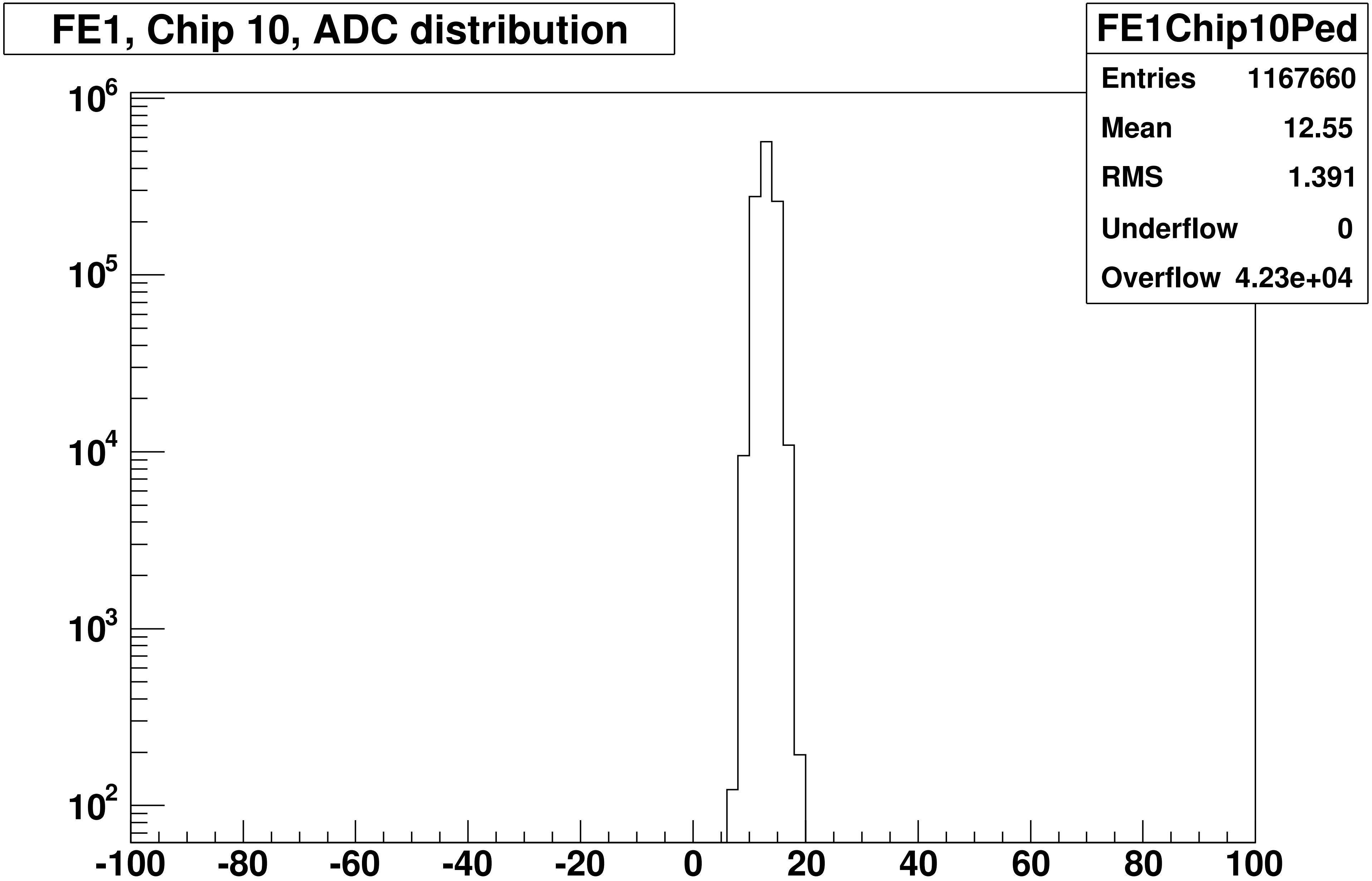
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 1167660
Mean 12.55
RMS 1.391
Underflow 0
Overflow 4.23e+04
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10^5
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10^3
10^2
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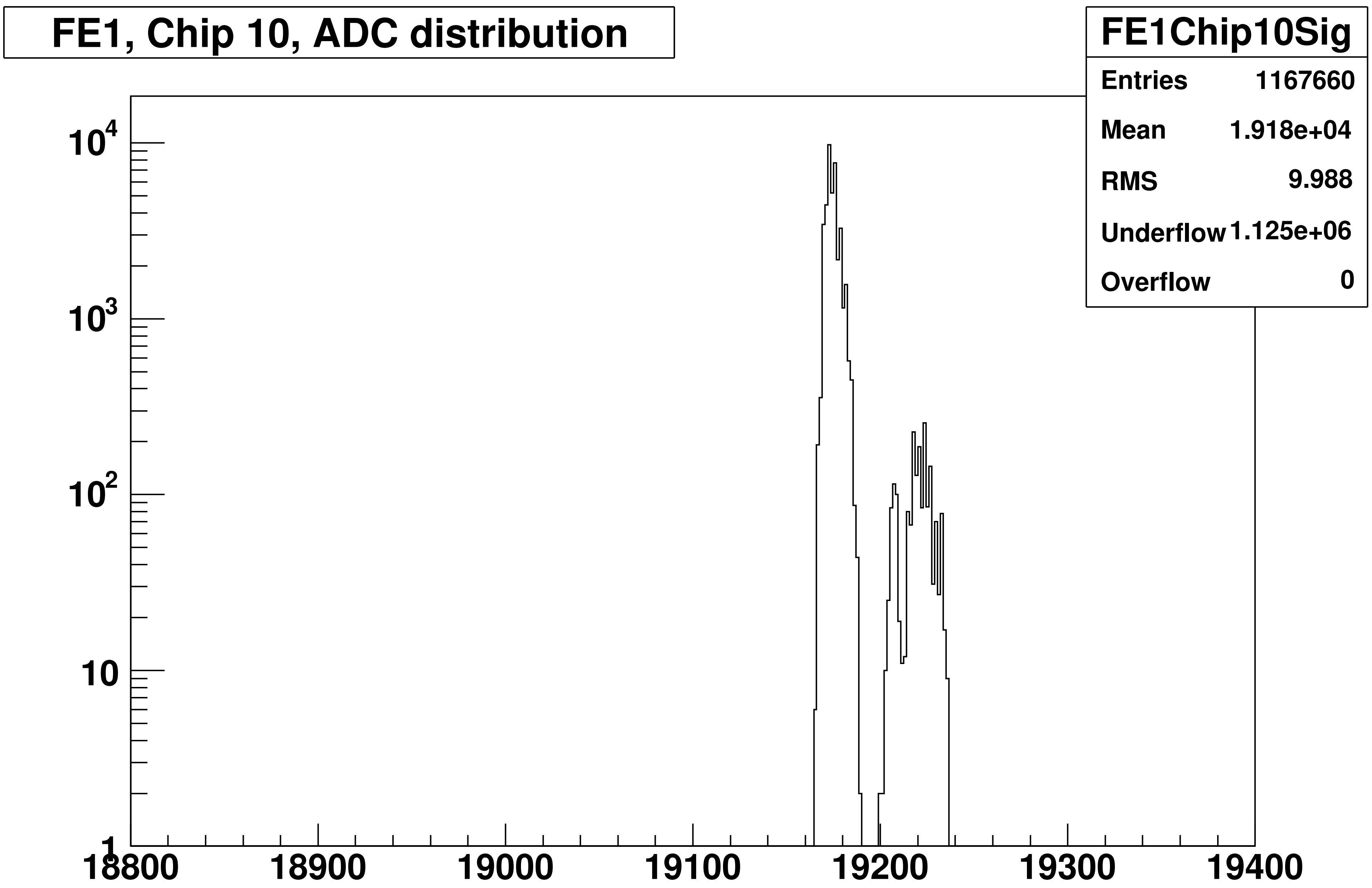
80
100


FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 1167660
Mean 1.918e+04
RMS 9.988
Underflow 1.125e+06
Overflow 0
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10^3
10^2
10
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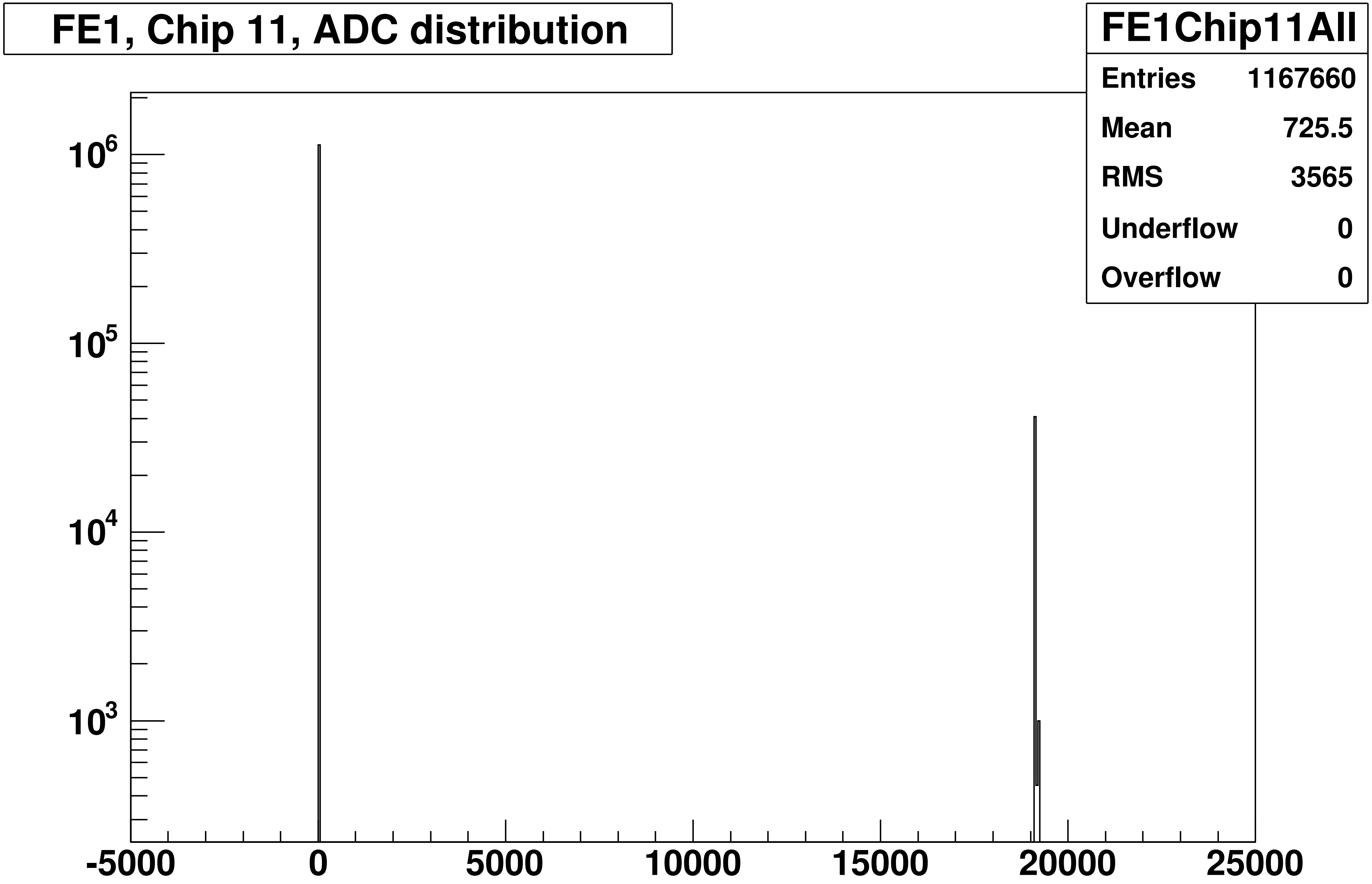
FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 1167660
Mean 725.5
RMS 3565
Underflow 0
Overflow 0
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10^5
10^4
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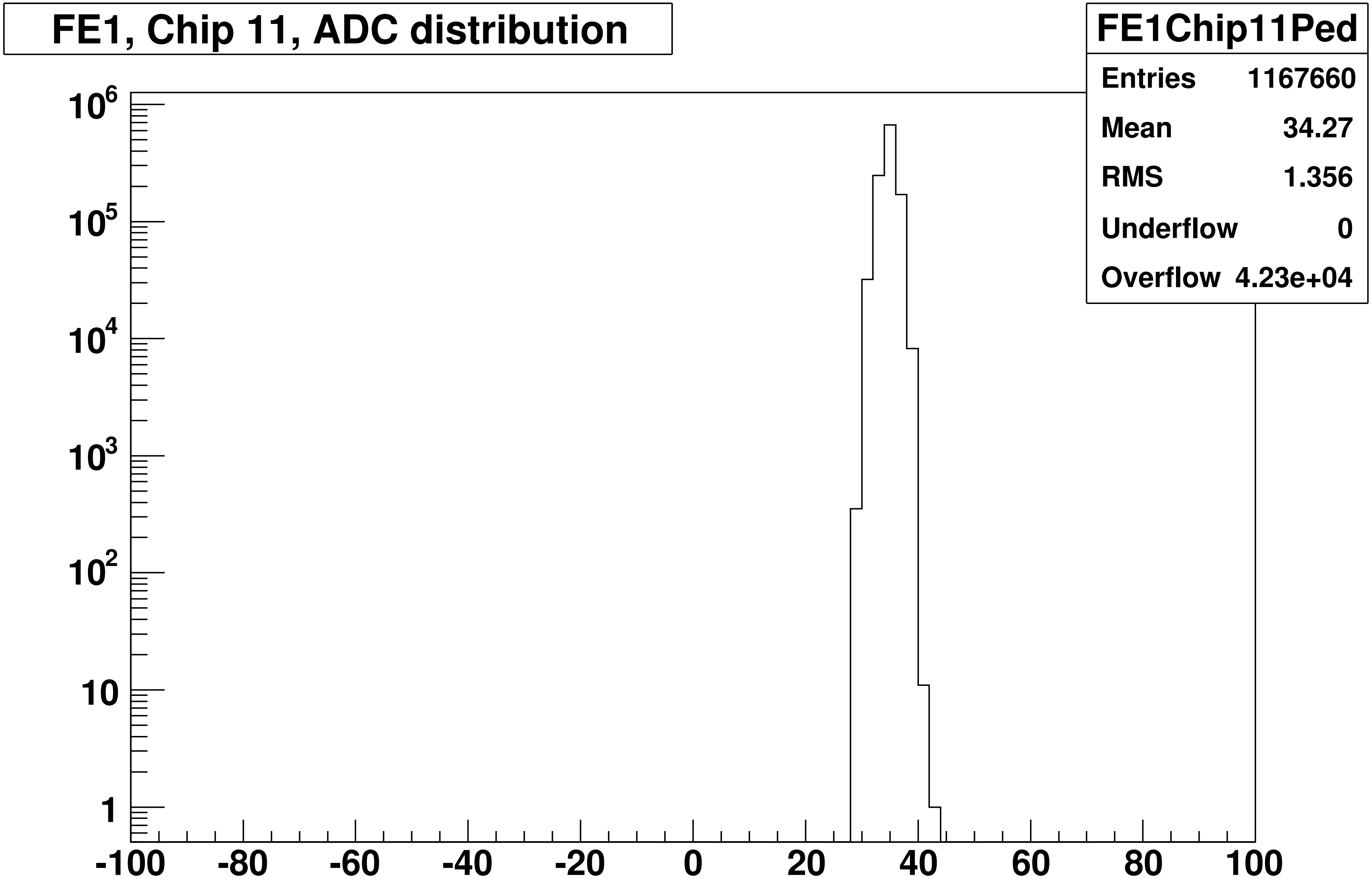
20000
25000


FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 1167660
Mean 34.27
RMS 1.356
Underflow 0
Overflow 4.23e+04
10^6
10^5
10^4
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80
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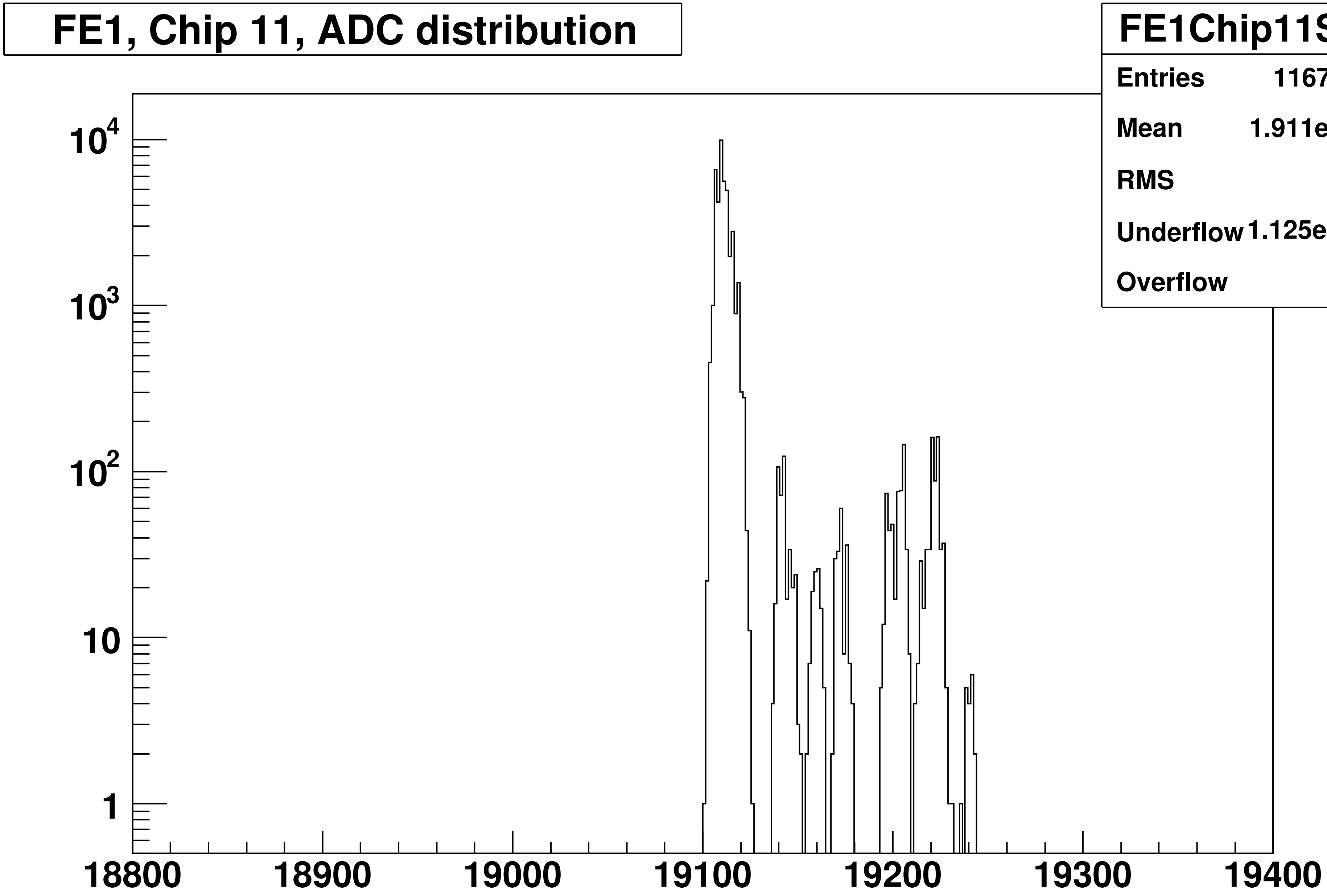
FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 1167660
Mean 1.911e+04
RMS 18
Underflow 1.125e+06
Overflow 0
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10^3
10^2
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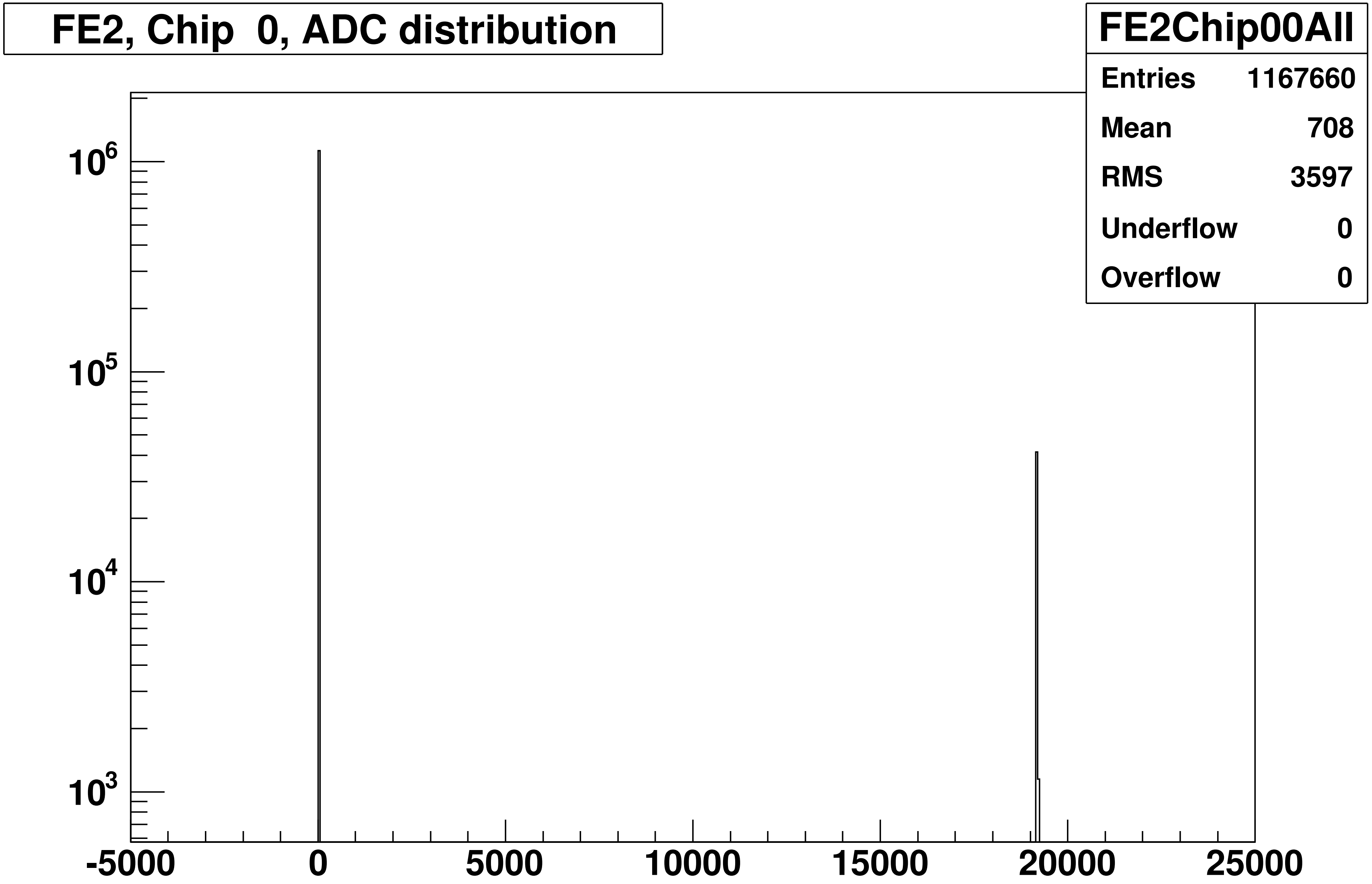
FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 1167660
Mean 708
RMS 3597
Underflow 0
Overflow 0
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10^5
10^4
10^3
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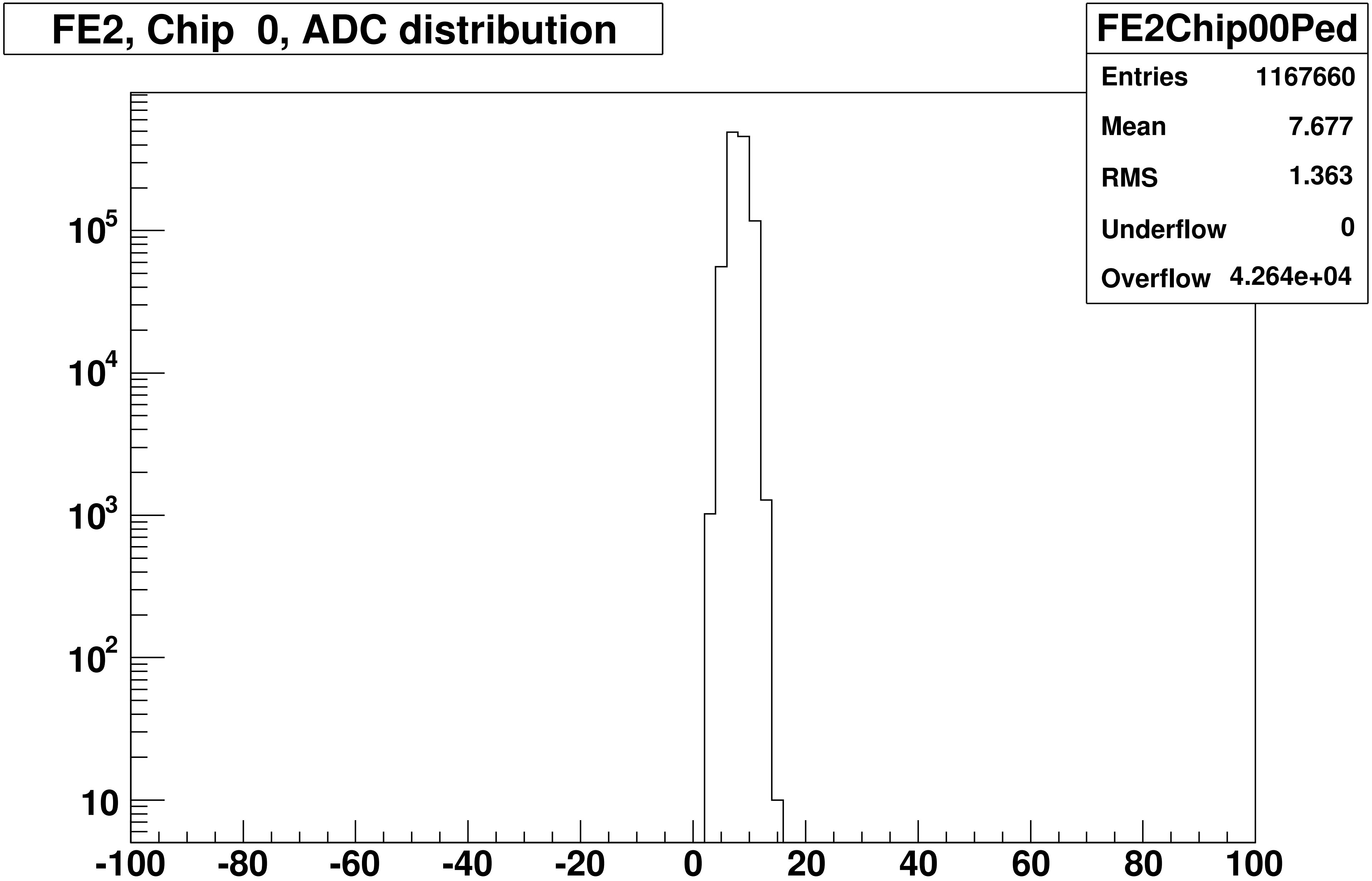
20000
25000



FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 1167660
Mean 7.677
RMS 1.363
Underflow 0
Overflow 4.264e+04
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80
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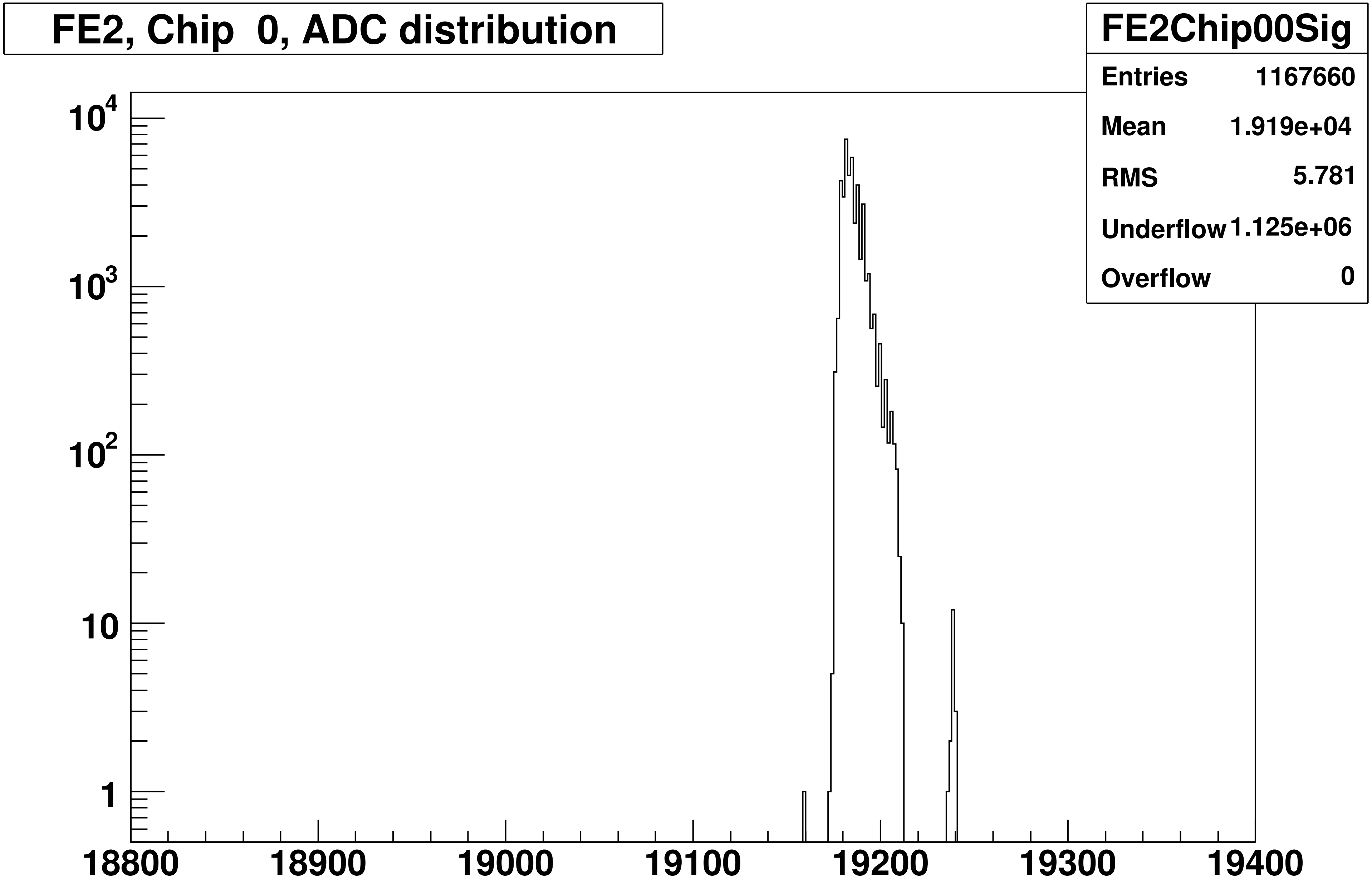
FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 1167660
Mean 1.919e+04
RMS 5.781
Underflow 1.125e+06
Overflow 0
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10^3
10^2
10
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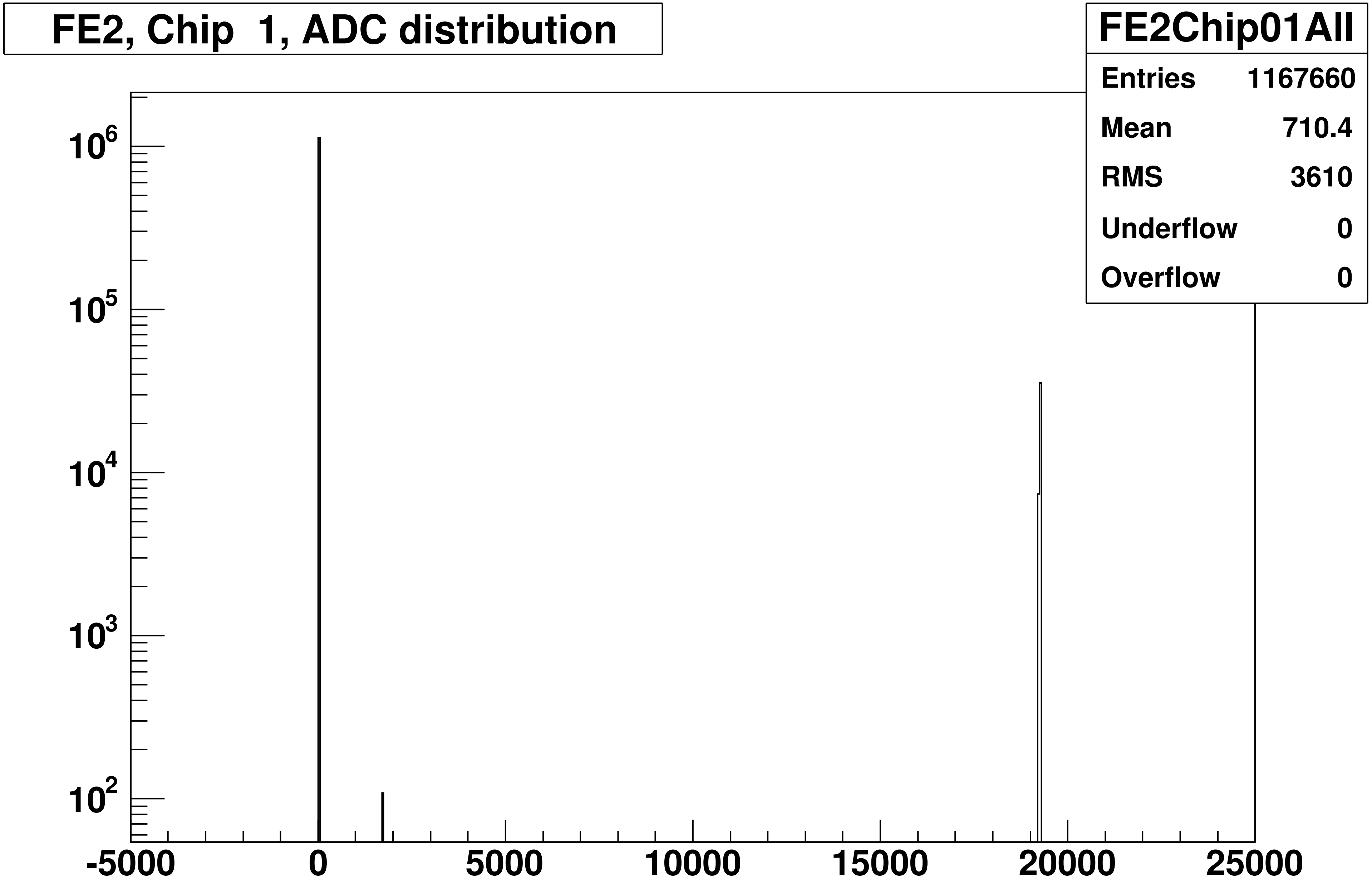

FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 1167660
Mean 710.4
RMS 3610
Underflow 0
Overflow 0
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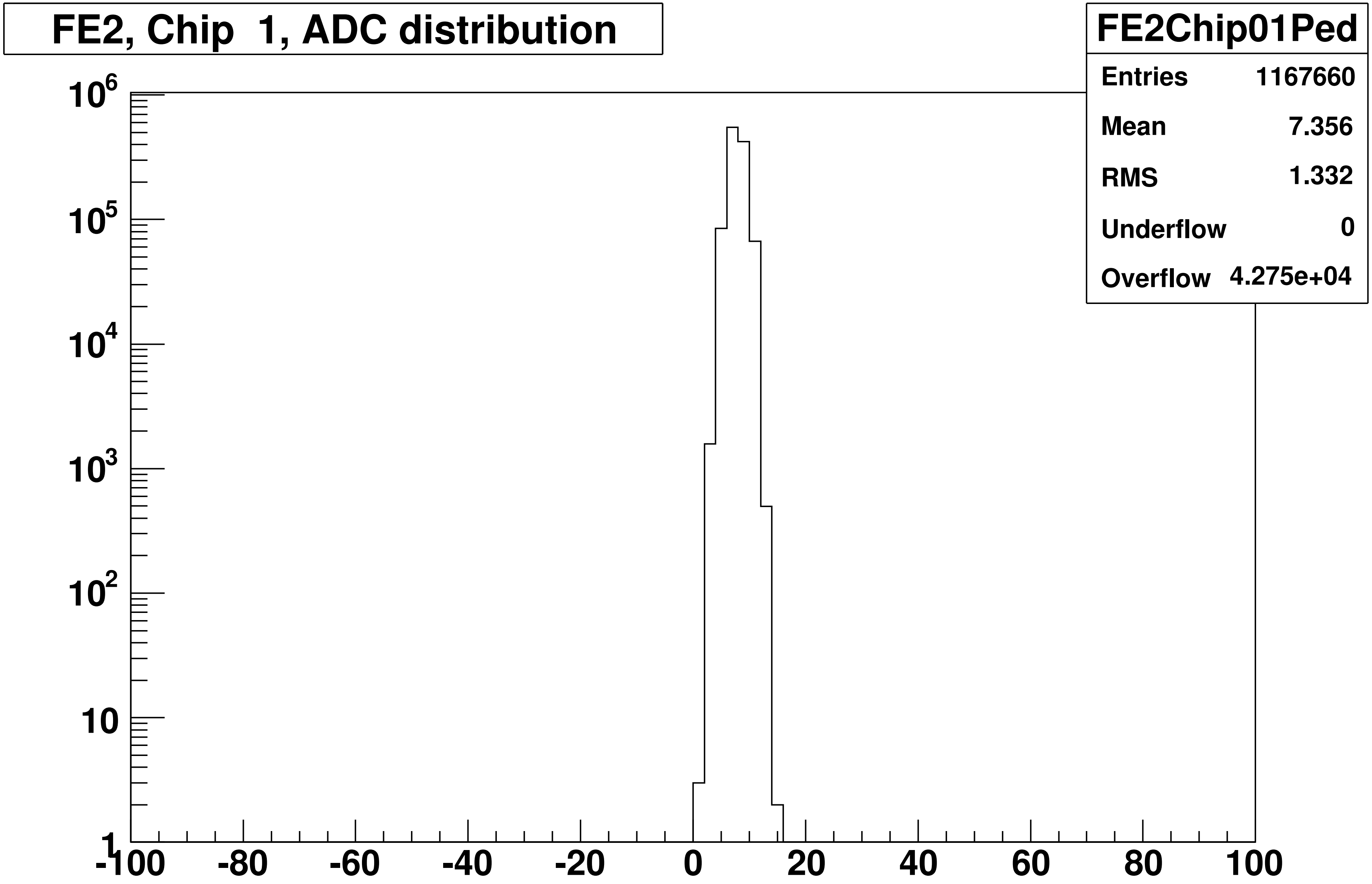
FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 1167660
Mean 7.356
RMS 1.332
Underflow 0
Overflow 4.275e+04
1
10
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80
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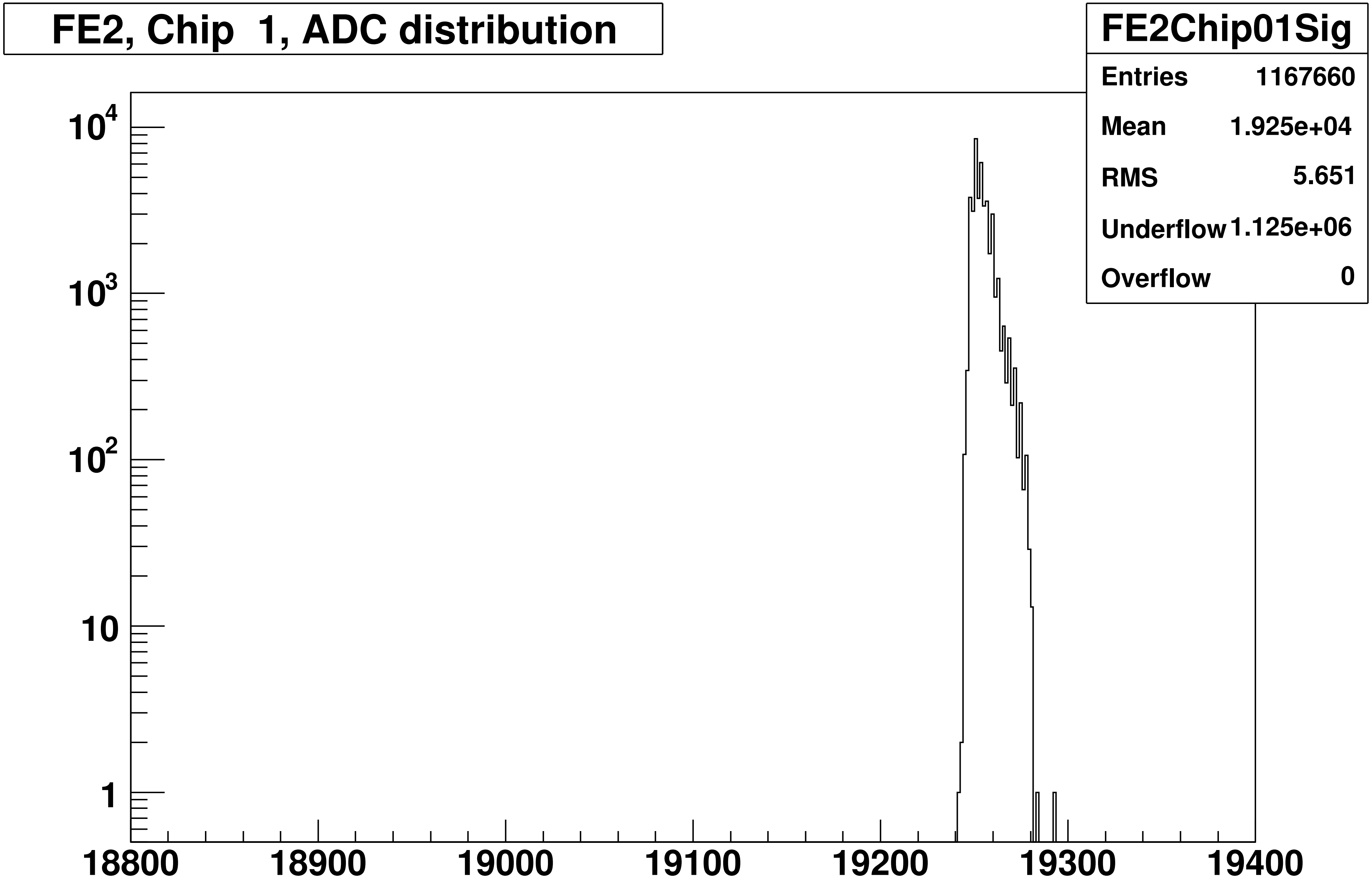
FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 1167660
Mean 1.925e+04
RMS 5.651
Underflow 1.125e+06
Overflow 0
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10^3
10^2
10
1
18800
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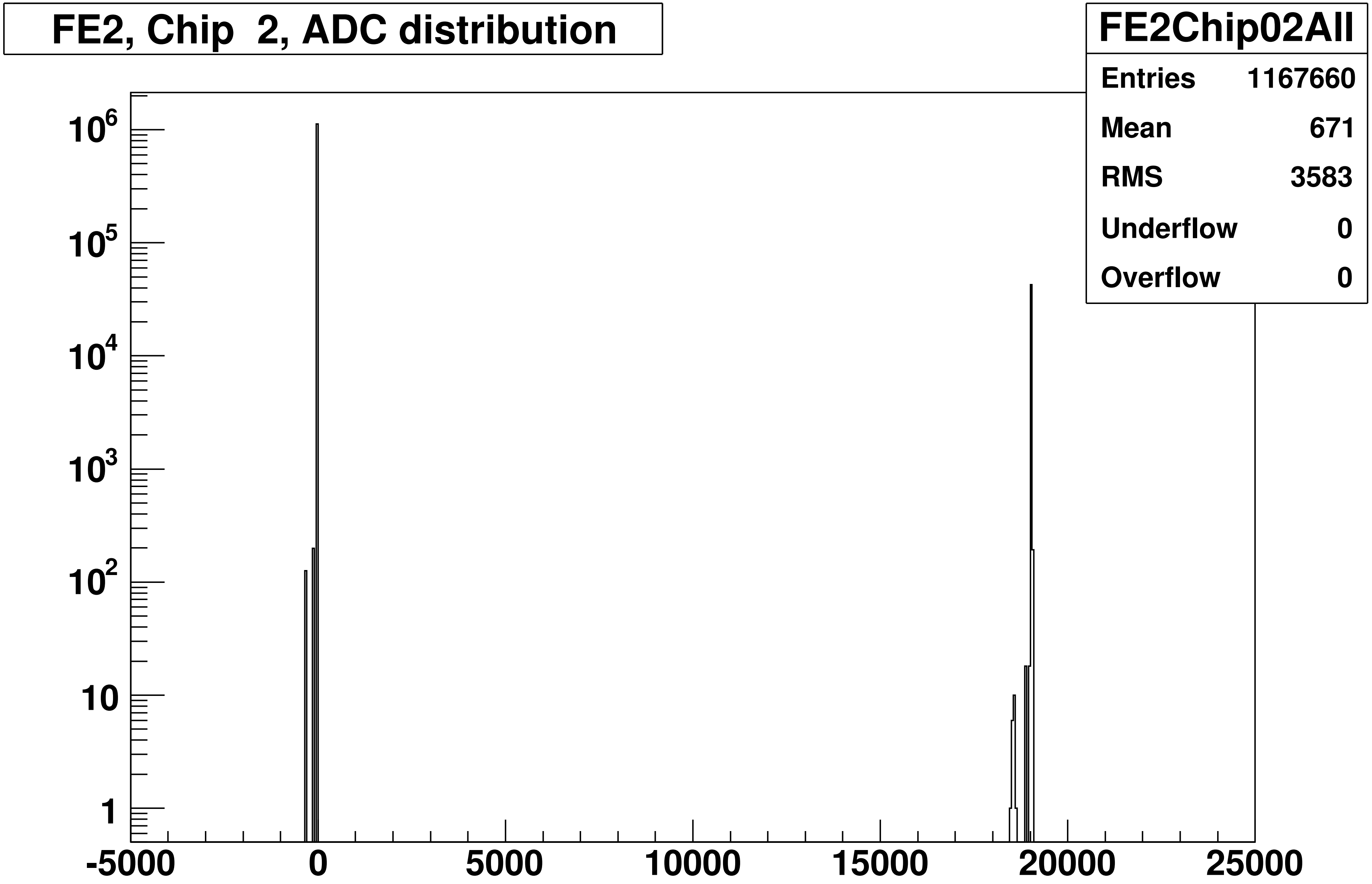
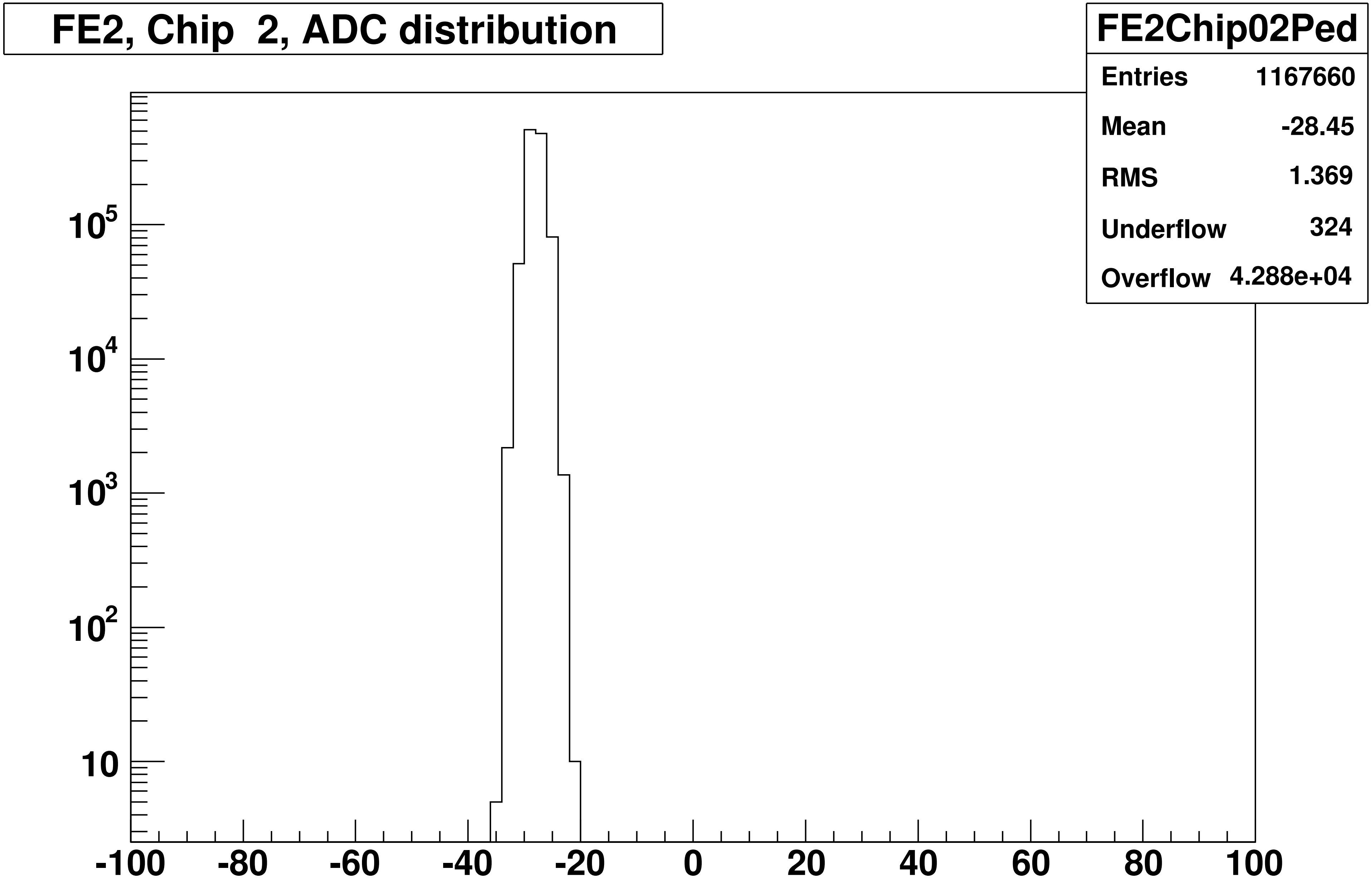
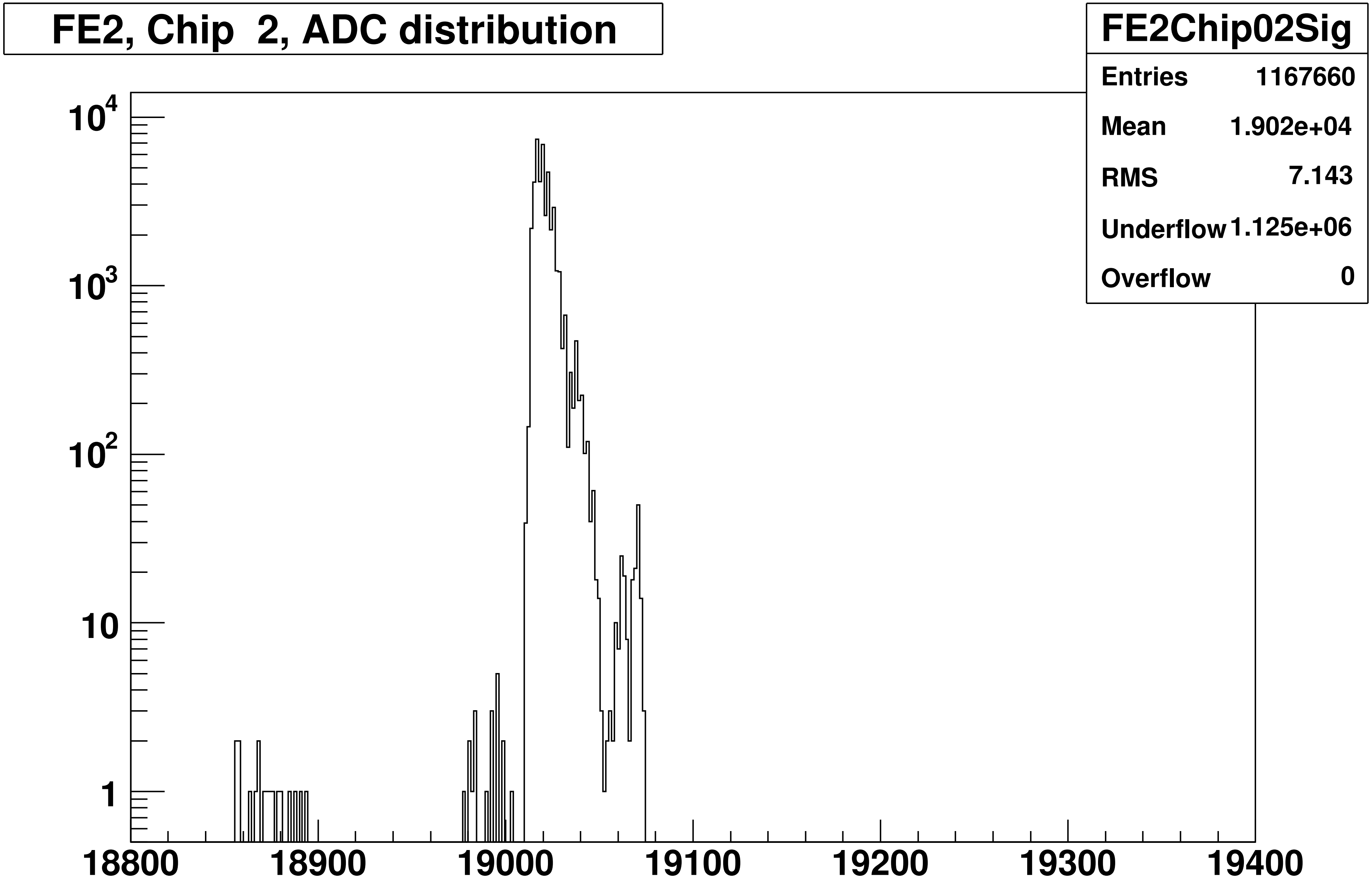
19400


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 1167660
Mean 671
RMS 3583
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 1167660
Mean -28.45
RMS 1.369
Underflow 324
Overflow 4.288e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 1167660
Mean 1.902e+04
RMS 7.143
Underflow 1.125e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
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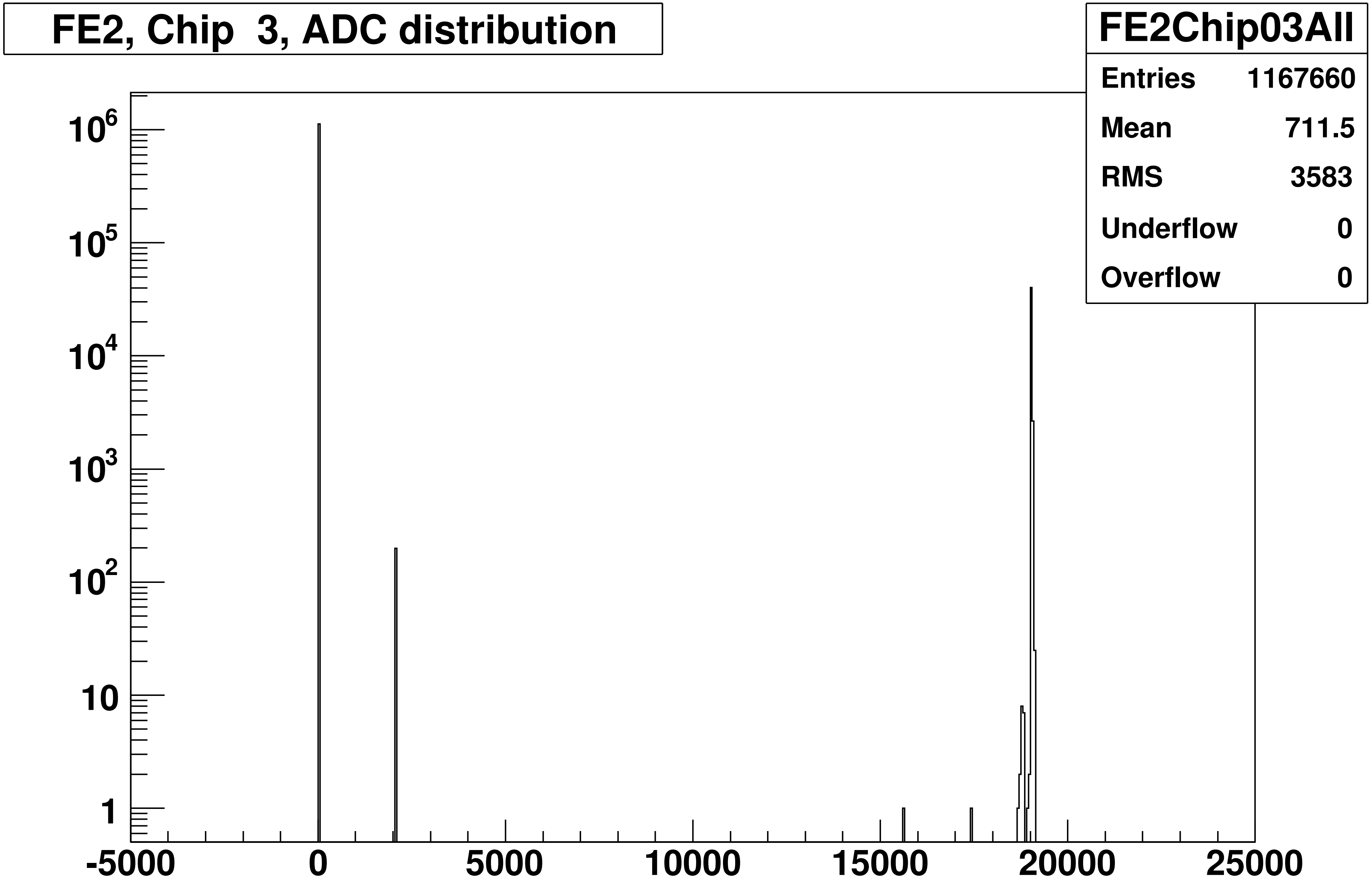
19400


FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 1167660
Mean 711.5
RMS 3583
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
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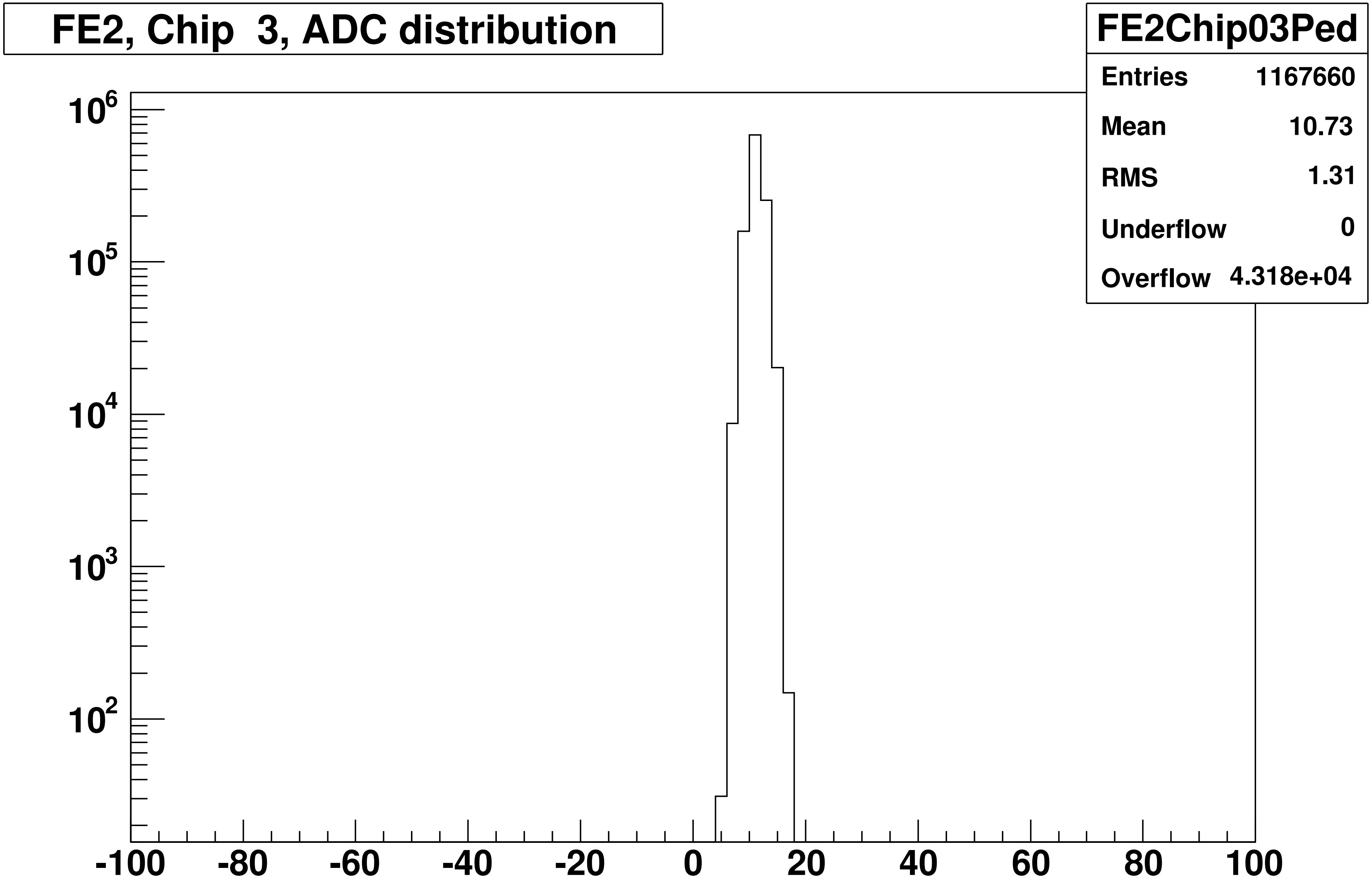
20000
25000


FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 1167660
Mean 10.73
RMS 1.31
Underflow 0
Overflow 4.318e+04
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10^5
10^4
10^3
10^2
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-80
-60
-40
-20
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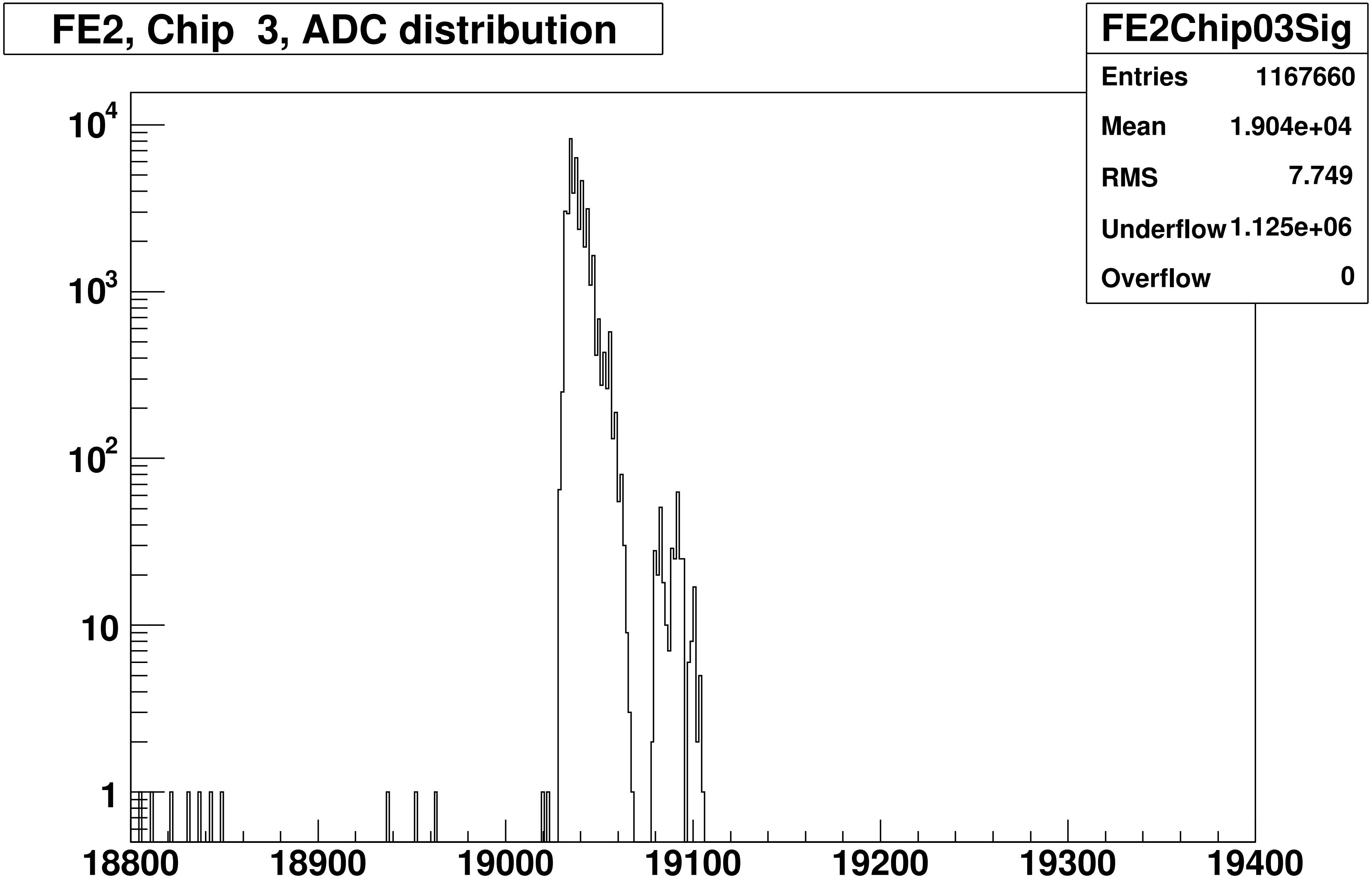
80
100


FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 1167660
Mean 1.904e+04
RMS 7.749
Underflow 1.125e+06
Overflow 0
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10^3
10^2
10
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18800
18900
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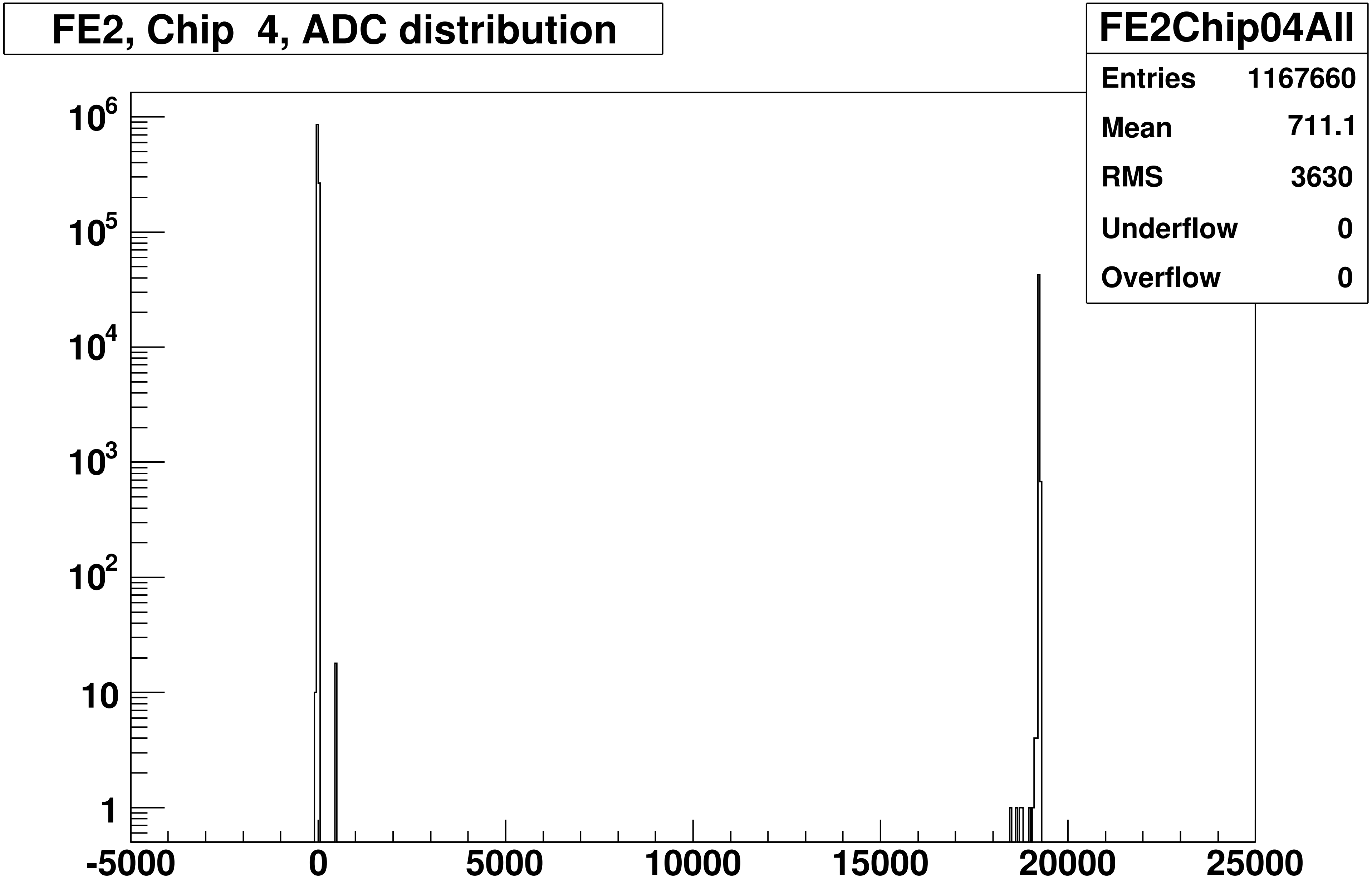
FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 1167660
Mean 711.1
RMS 3630
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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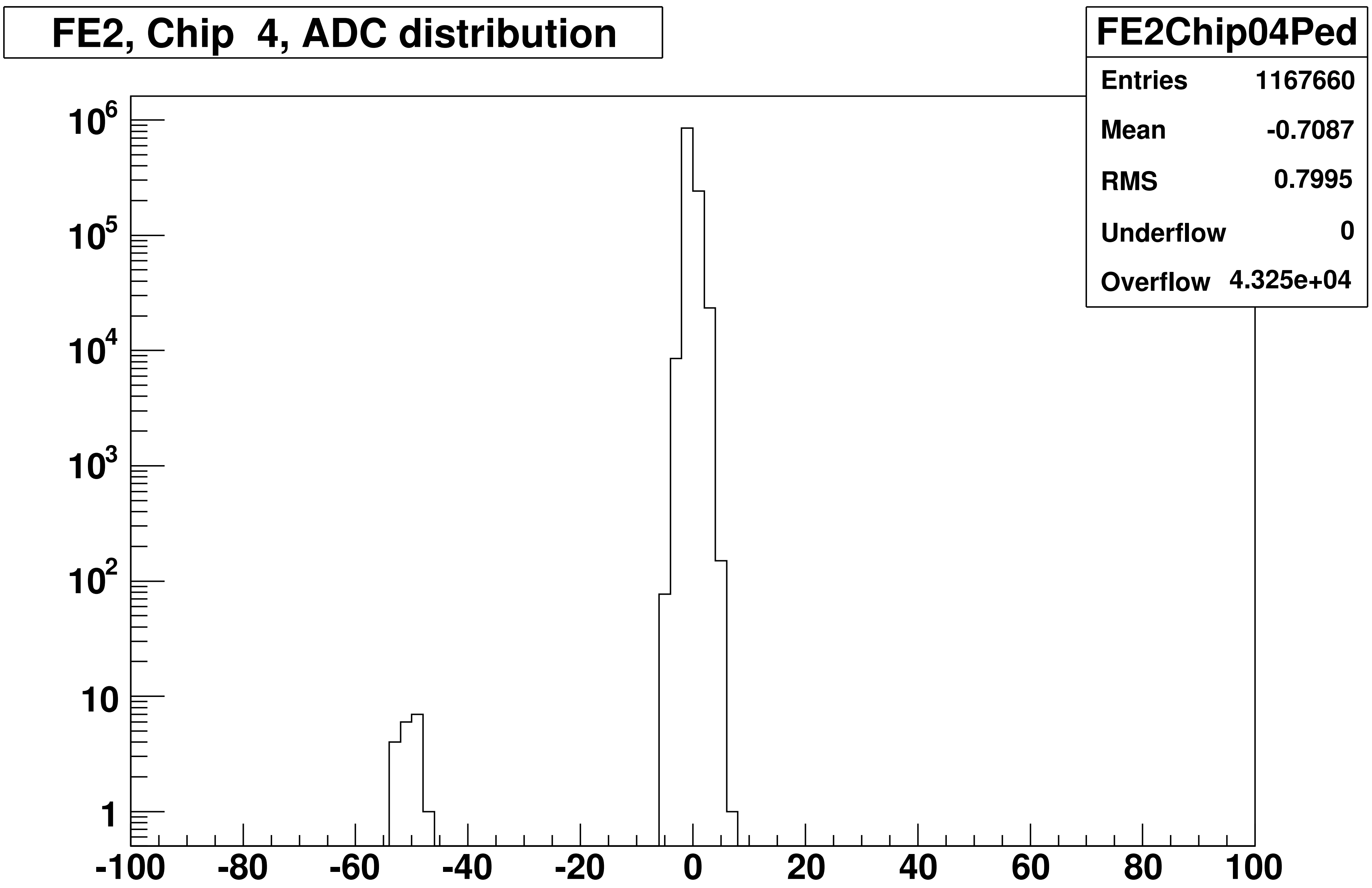
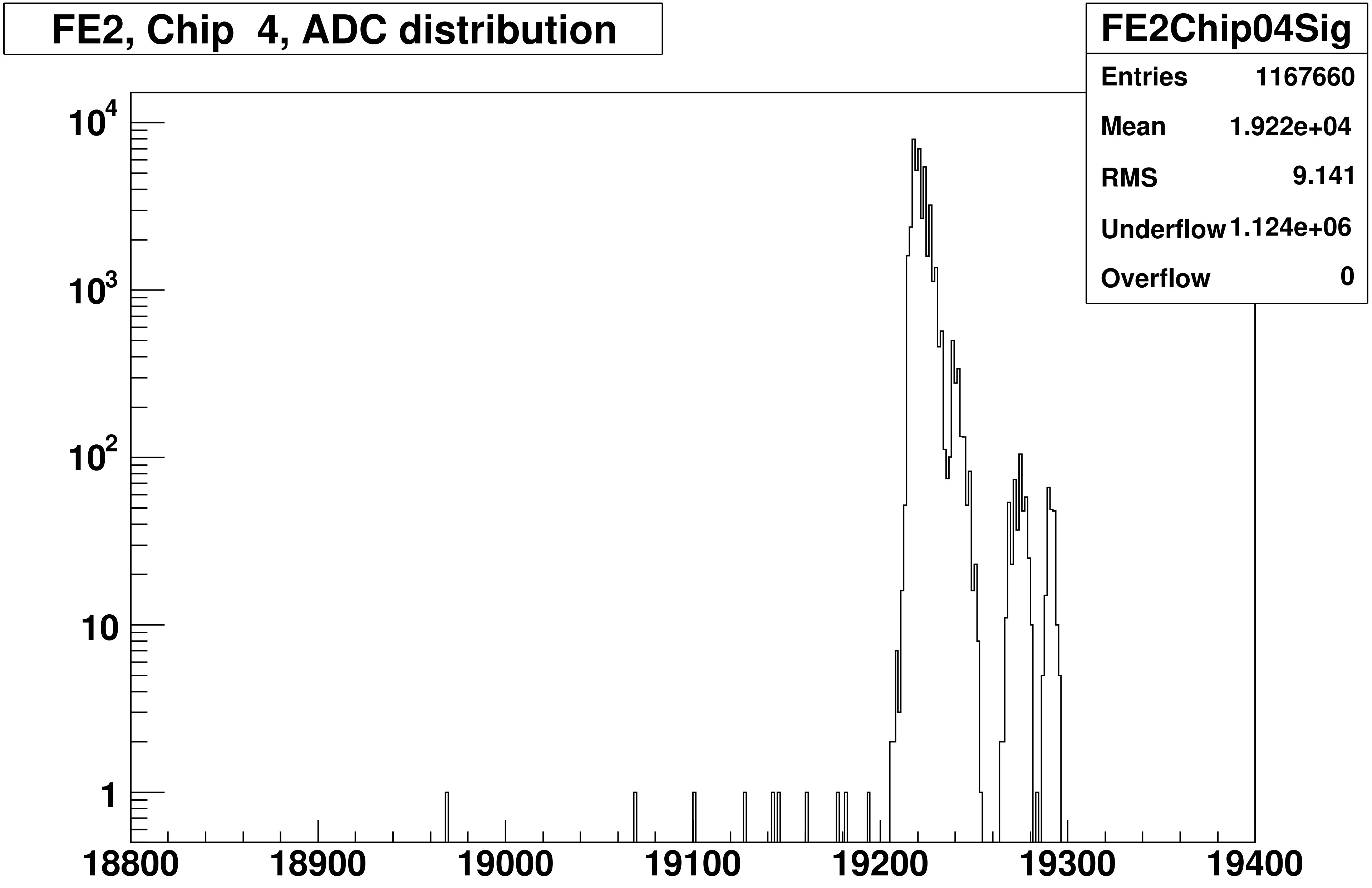
20000
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 1167660
Mean -0.7087
RMS 0.7995
Underflow 0
Overflow 4.325e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 1167660
Mean 1.922e+04
RMS 9.141
Underflow 1.124e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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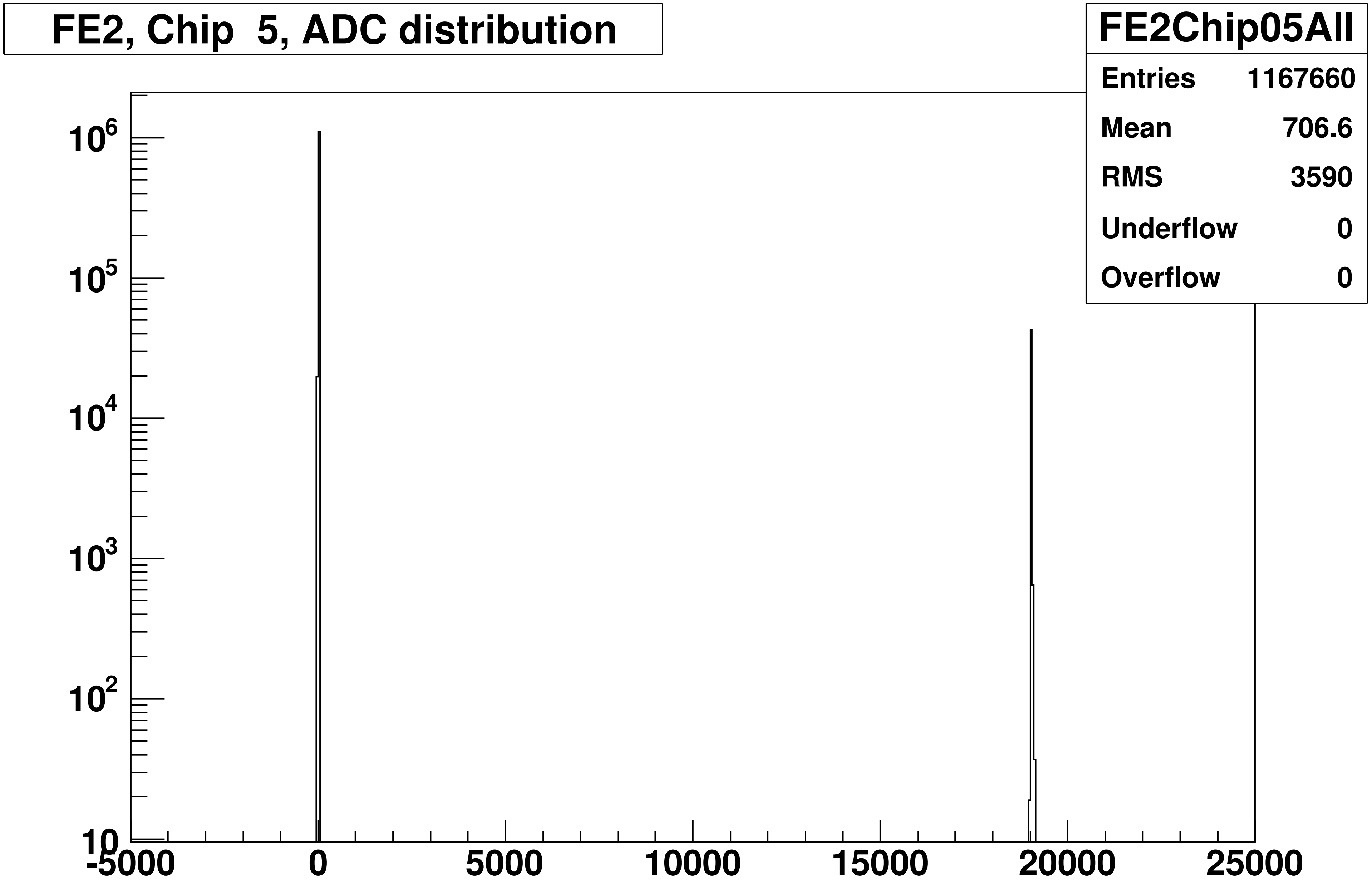
19100
19200
19300
19400


FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 1167660
Mean 706.6
RMS 3590
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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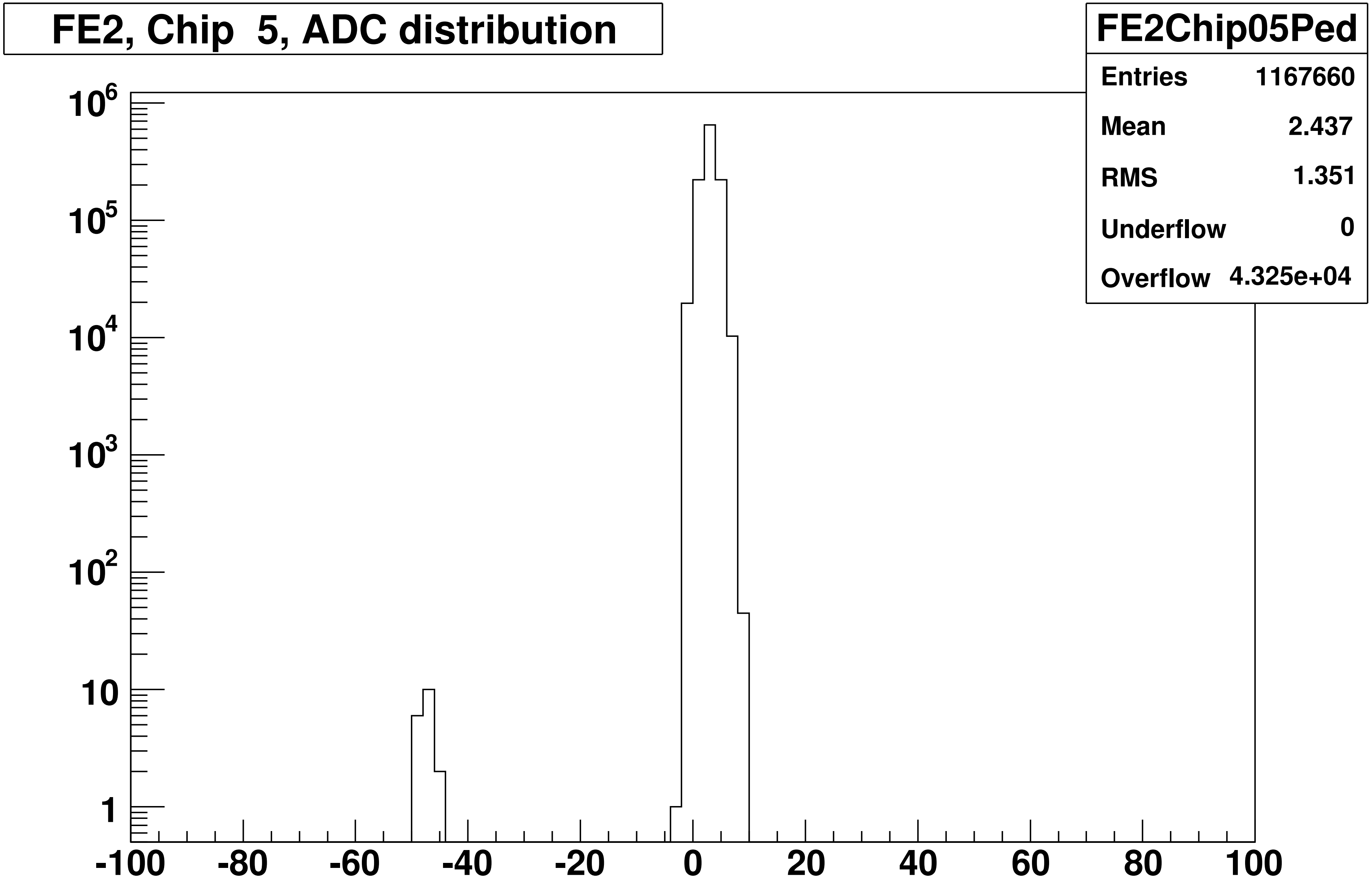
10000
15000
20000
25000


FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 1167660
Mean 2.437
RMS 1.351
Underflow 0
Overflow 4.325e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
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-40
-20
0
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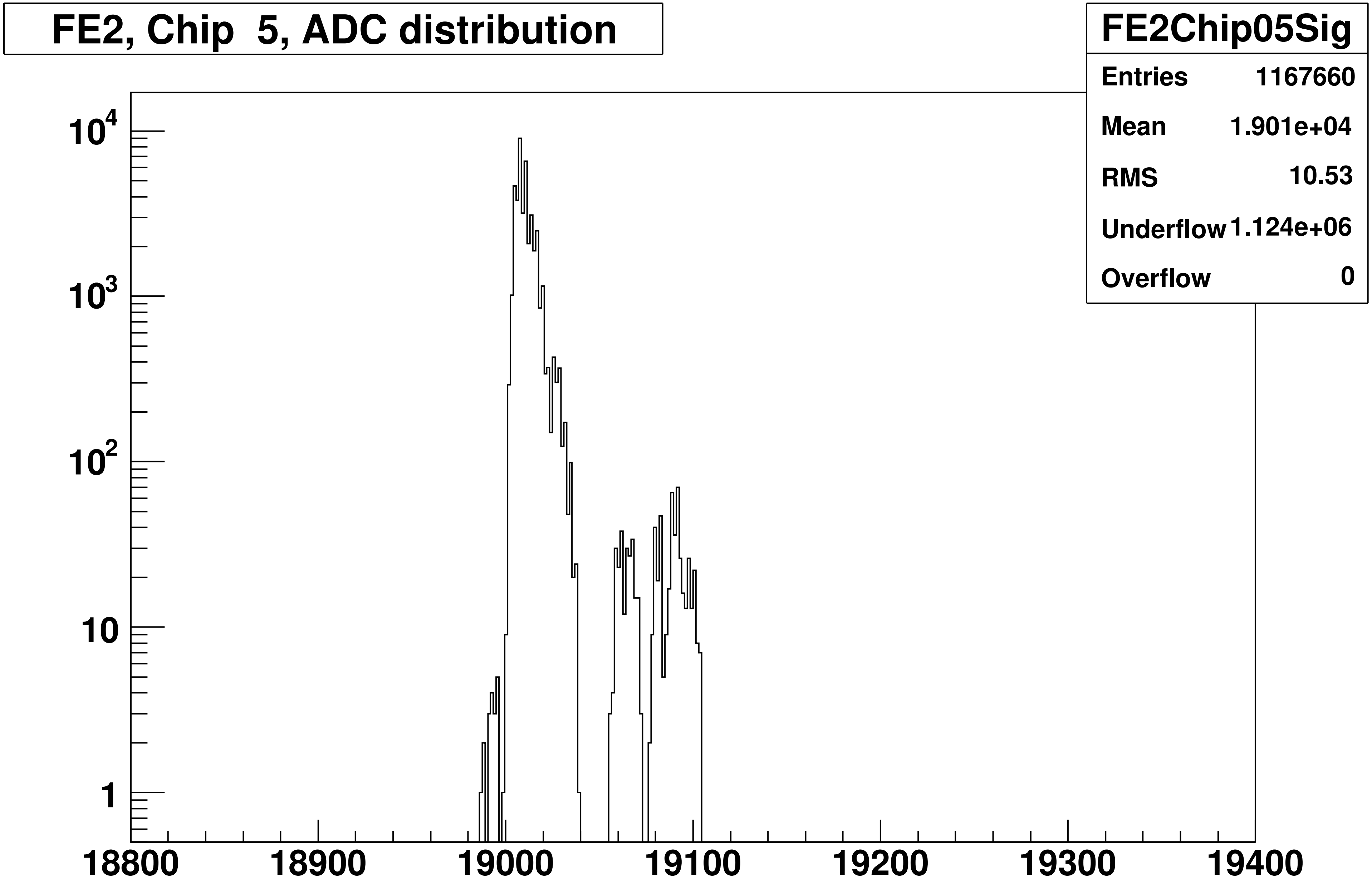
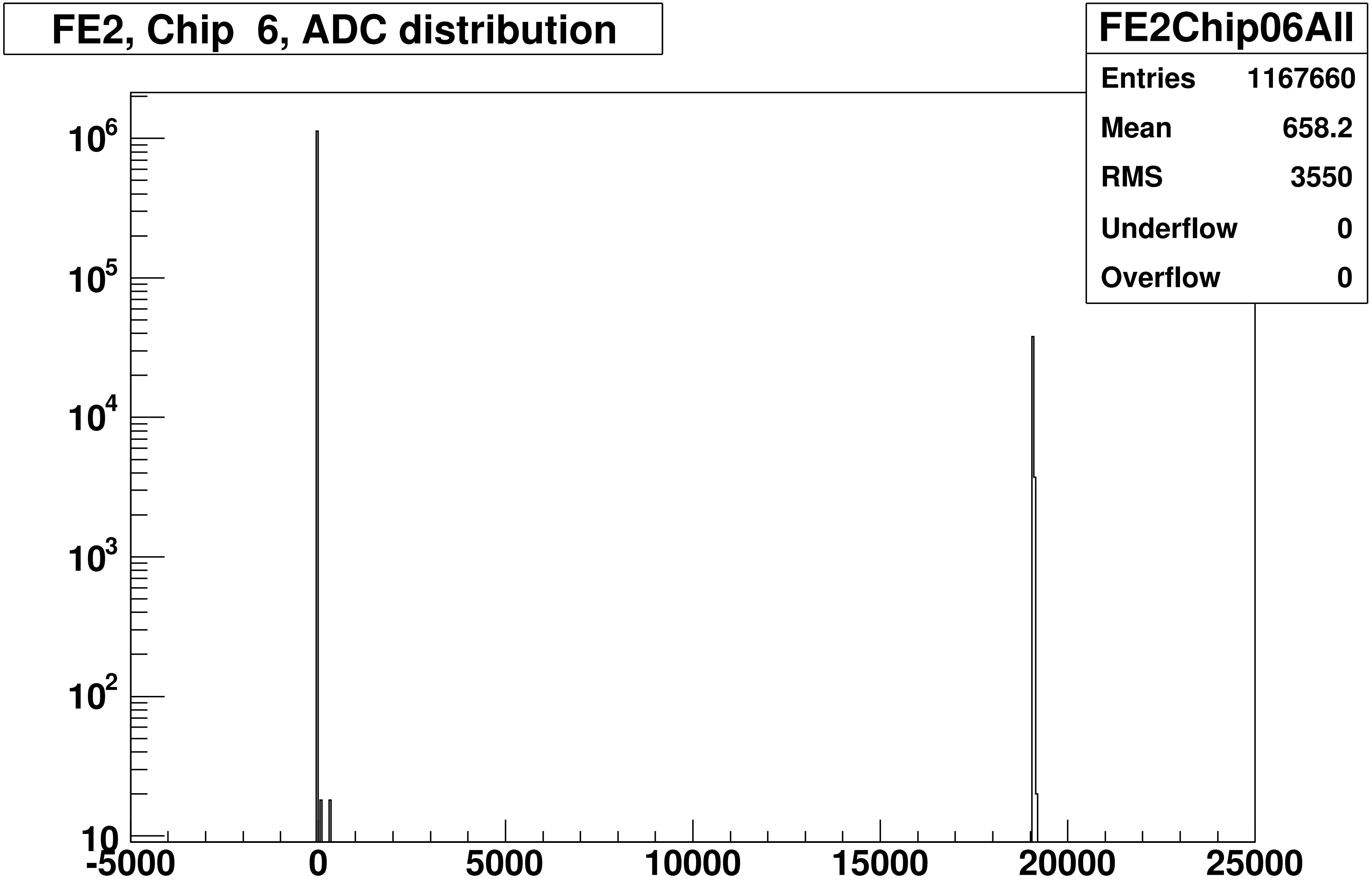
40
60
80
100



FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 1167660
Mean 1.901e+04
RMS 10.53
Underflow 1.124e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 1167660
Mean 658.2
RMS 3550
Underflow 0
Overflow 0
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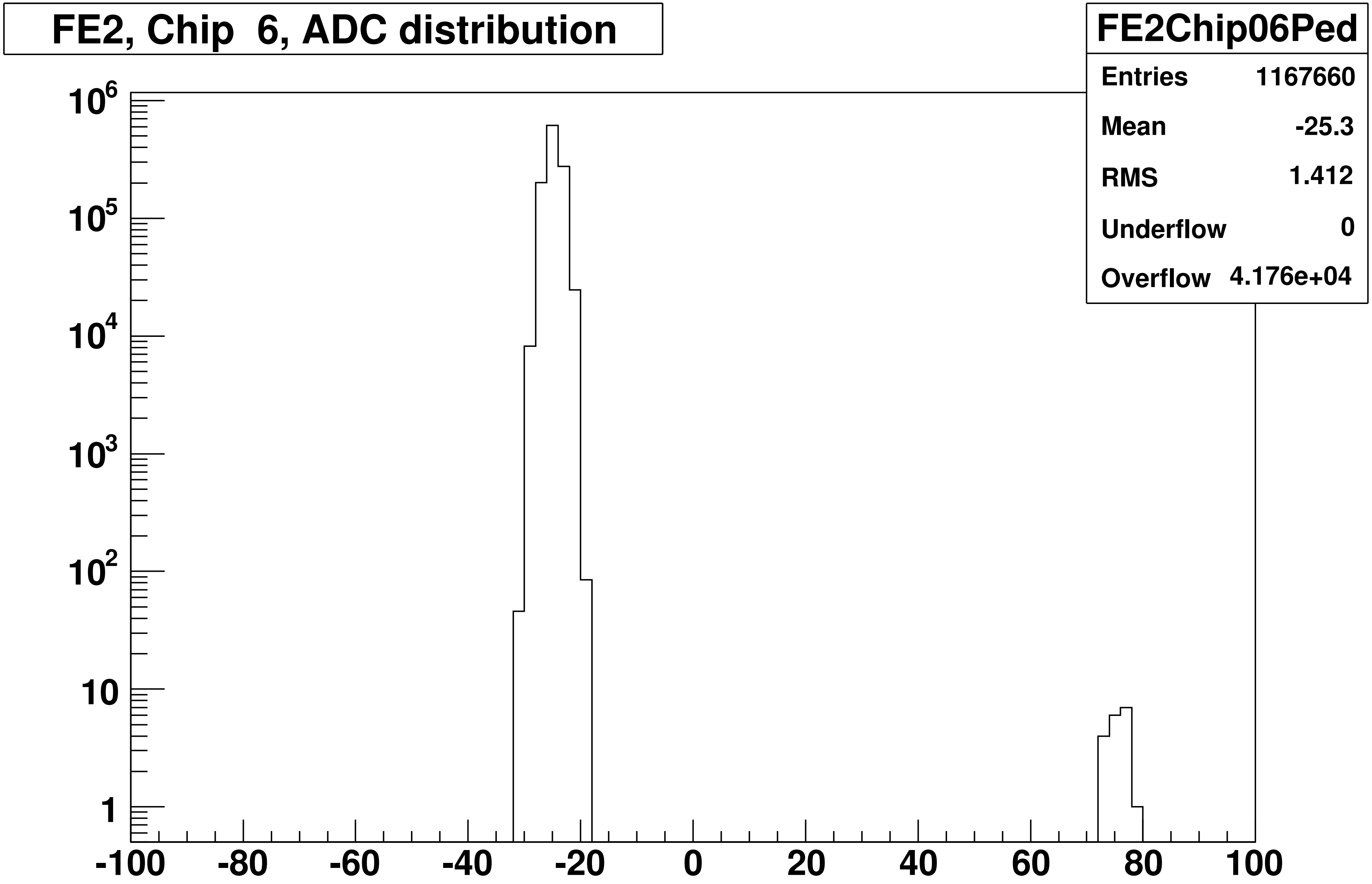
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 1167660
Mean -25.3
RMS 1.412
Underflow 0
Overflow 4.176e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
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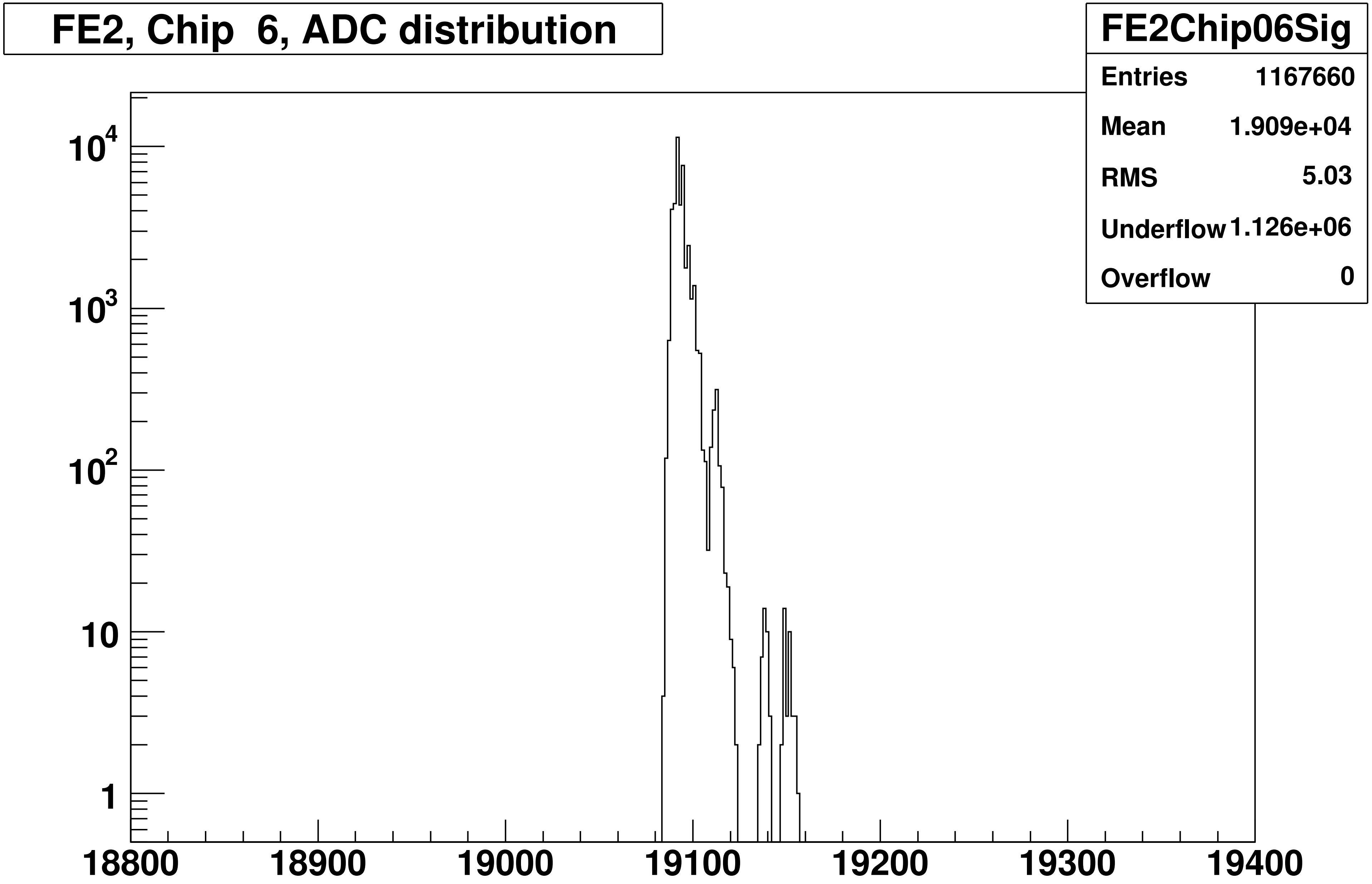
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 1167660
Mean 1.909e+04
RMS 5.03
Underflow 1.126e+06
Overflow 0
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10^3
10^2
10
1
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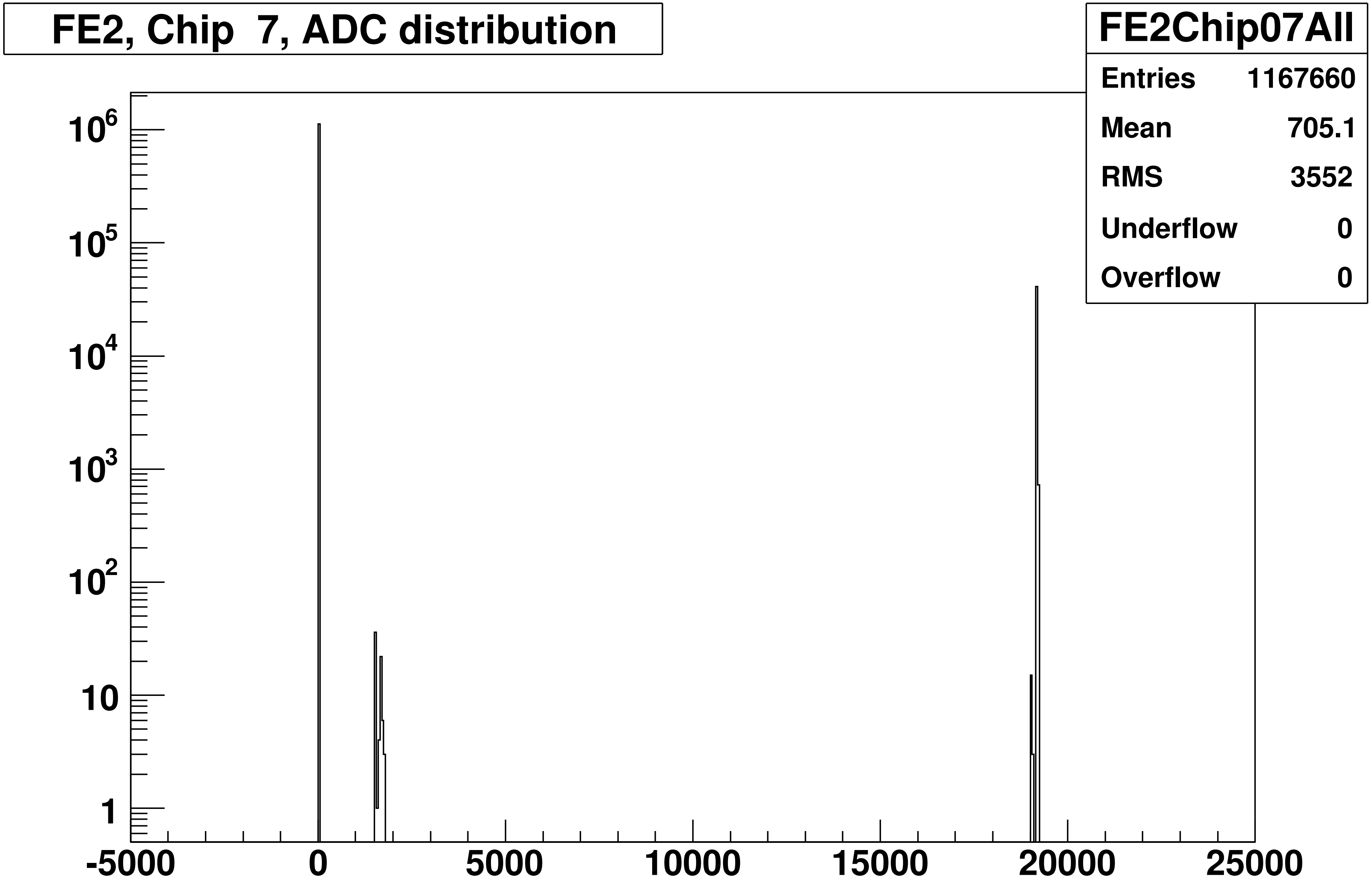
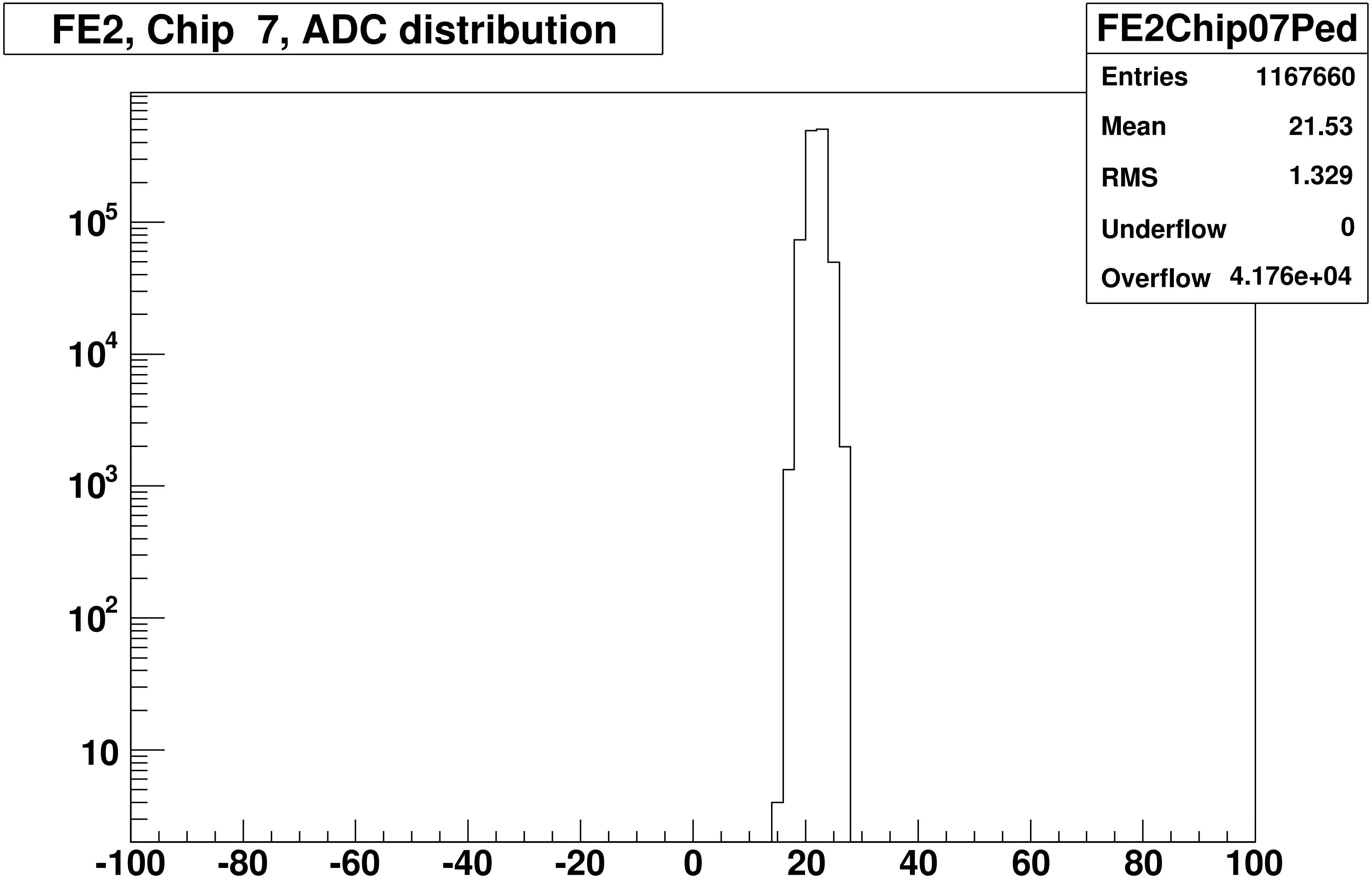
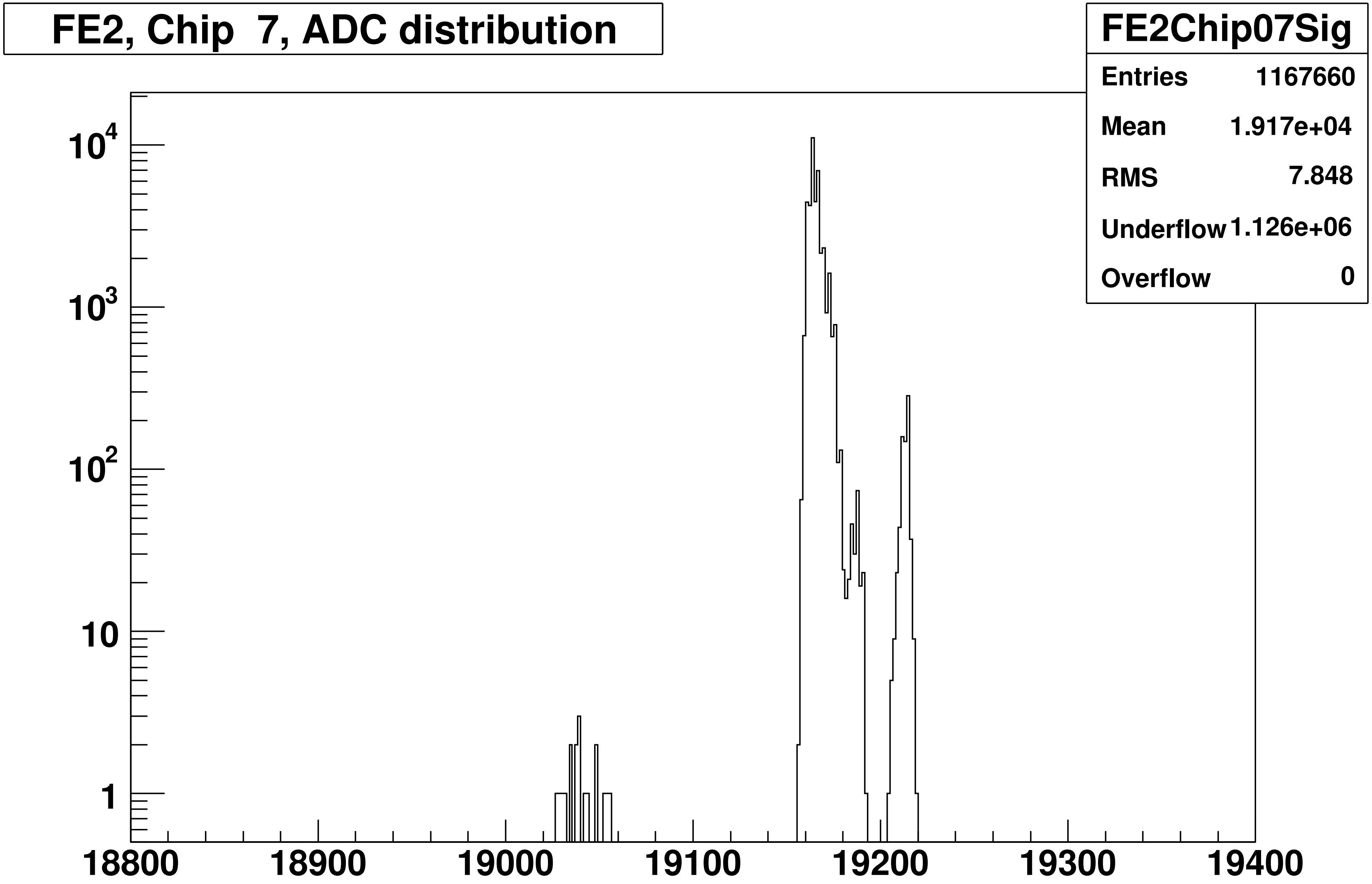
18900
19000
19100
19200
19300
19400


FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 1167660
Mean 705.1
RMS 3552
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 1167660
Mean 21.53
RMS 1.329
Underflow 0
Overflow 4.176e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 1167660
Mean 1.917e+04
RMS 7.848
Underflow 1.126e+06
Overflow 0
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10^3
10^2
10
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18800
18900
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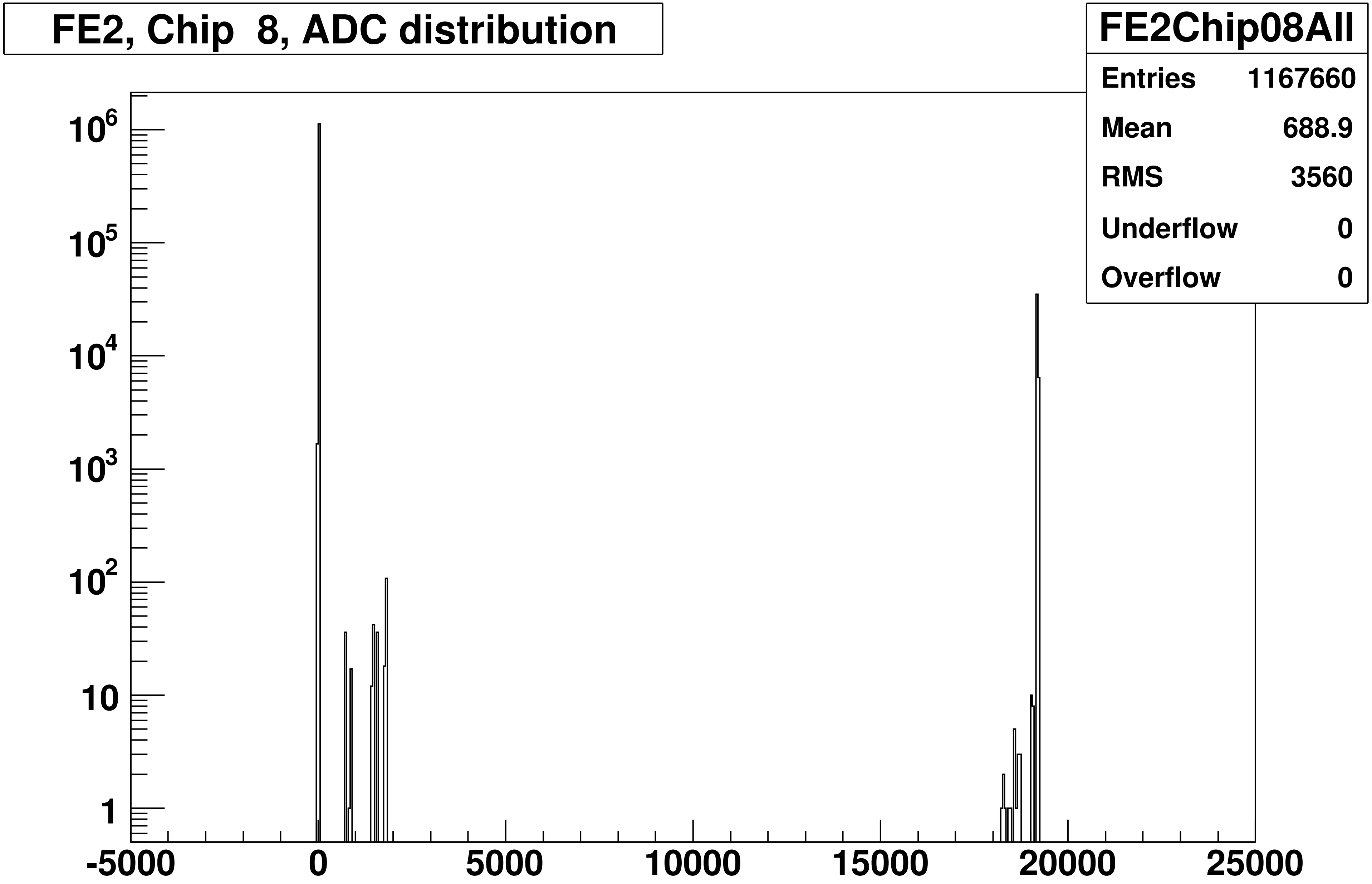
19100
19200
19300
19400



FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 1167660
Mean 688.9
RMS 3560
Underflow 0
Overflow 0
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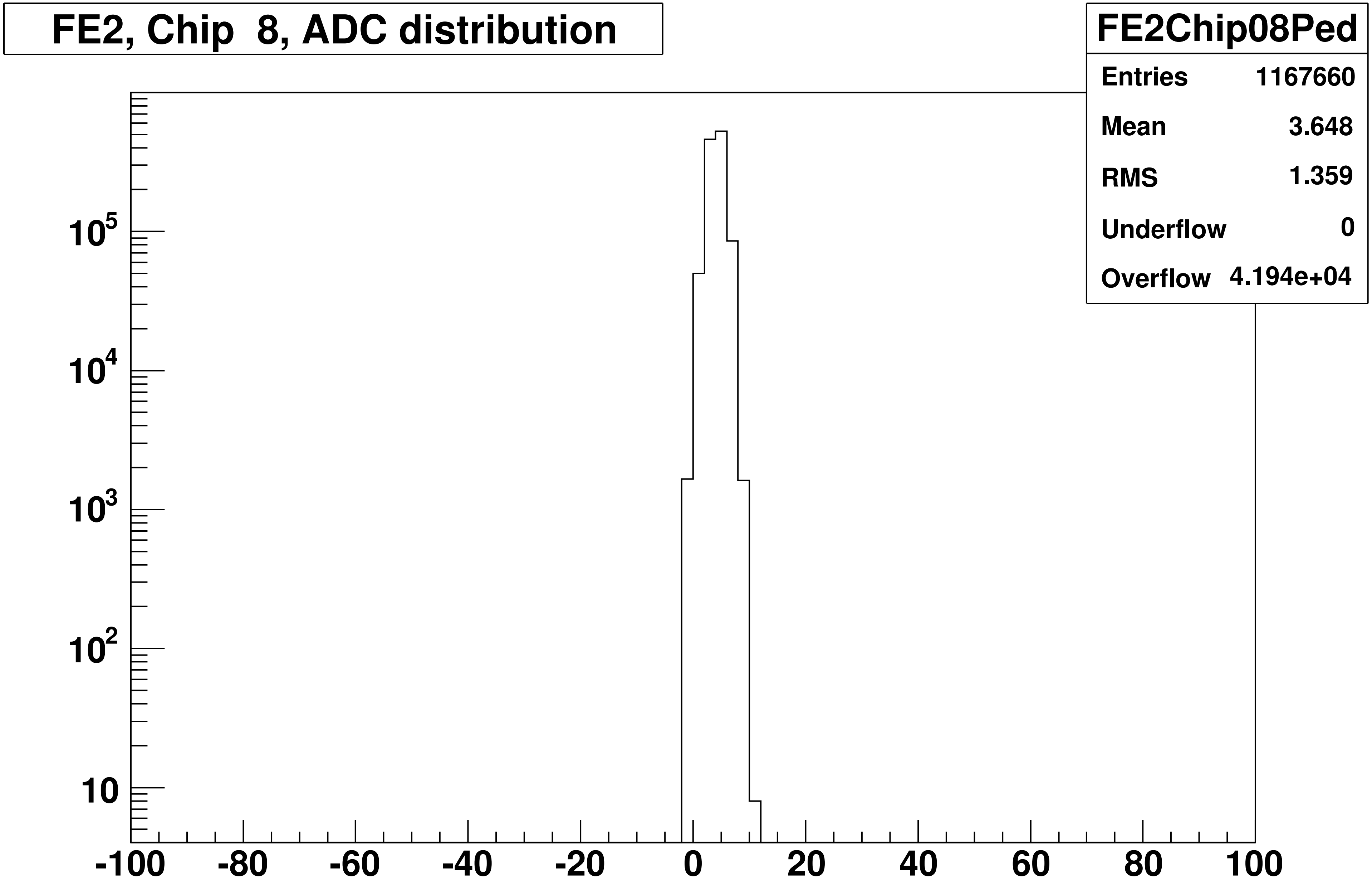
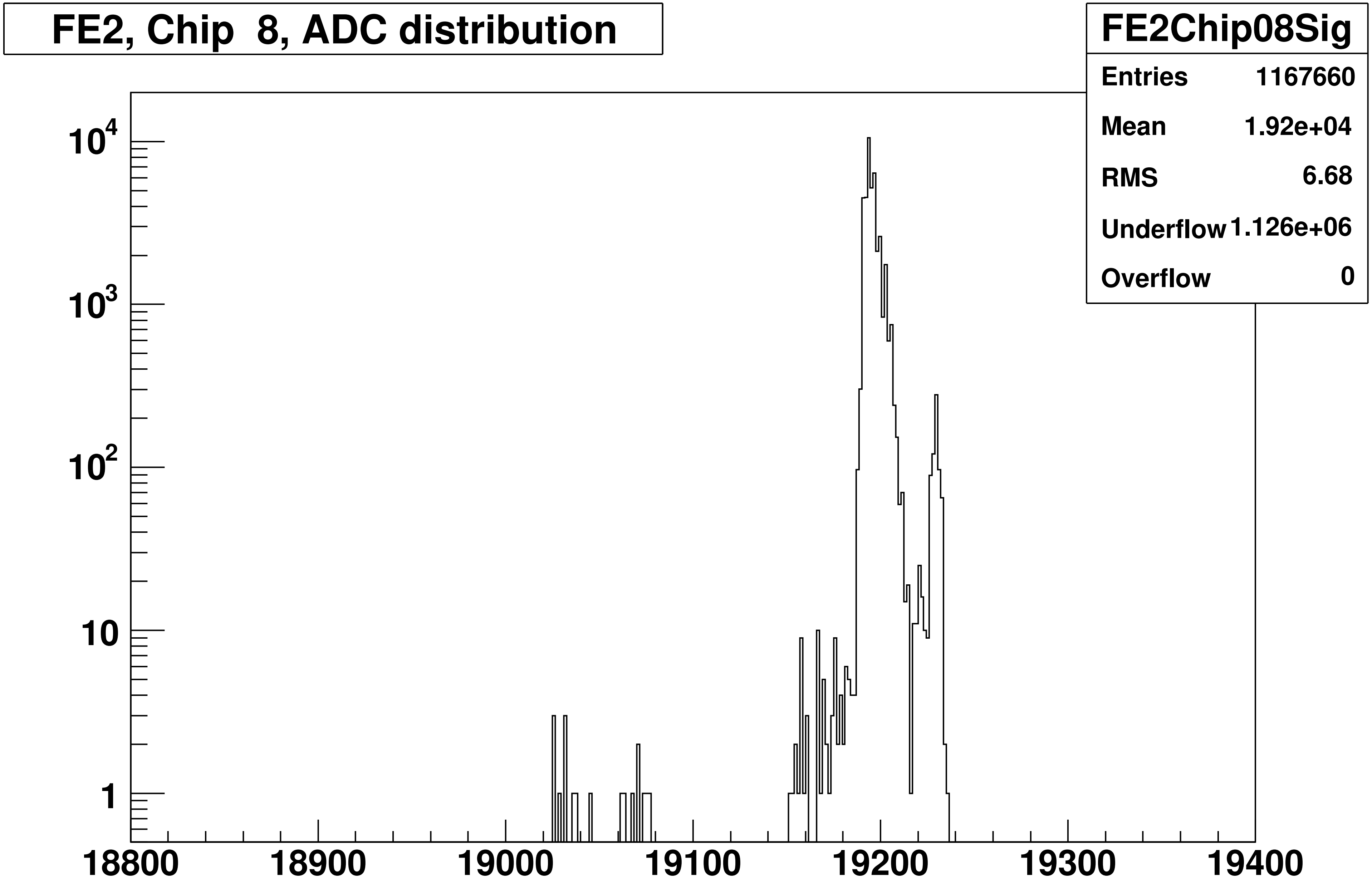
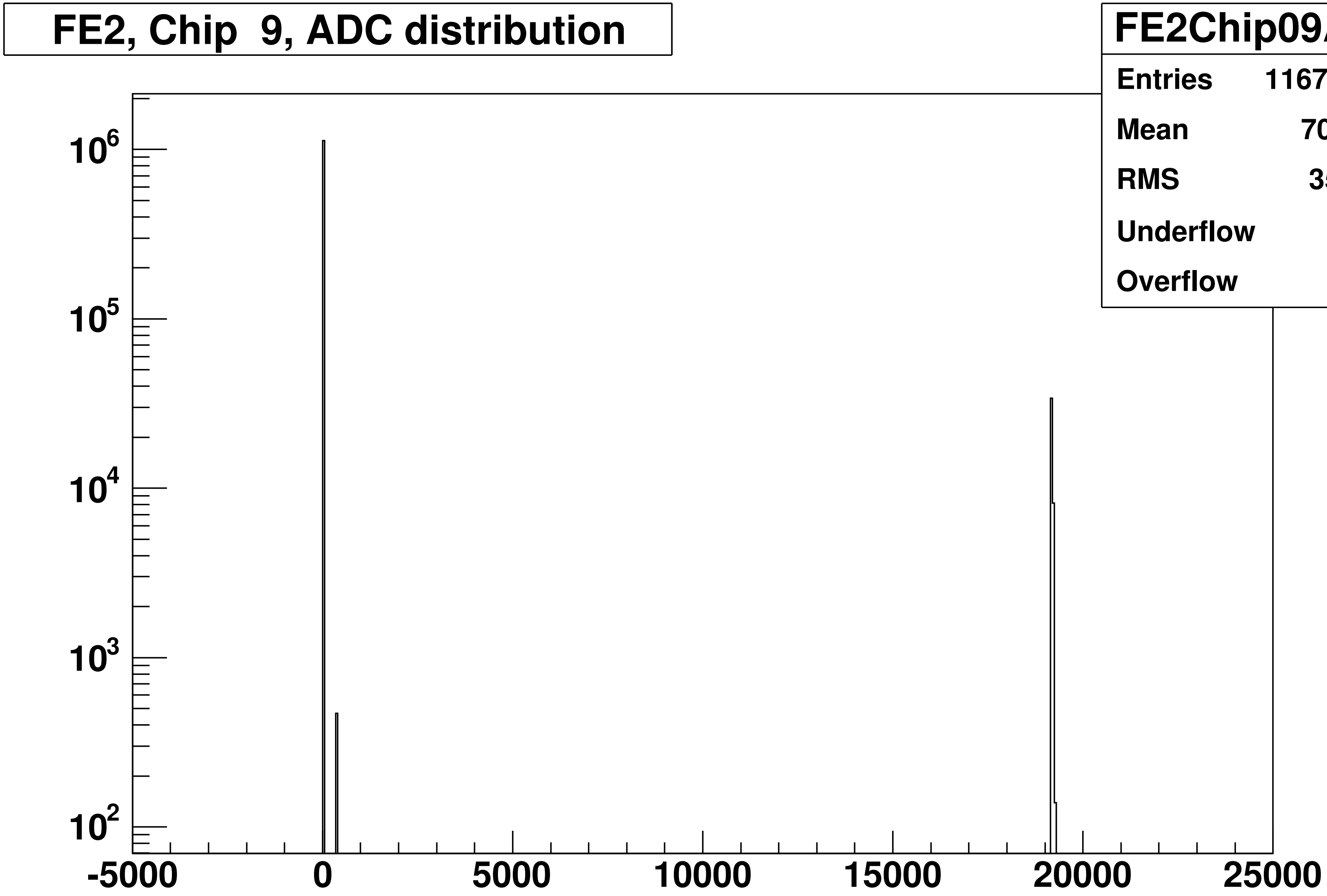
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

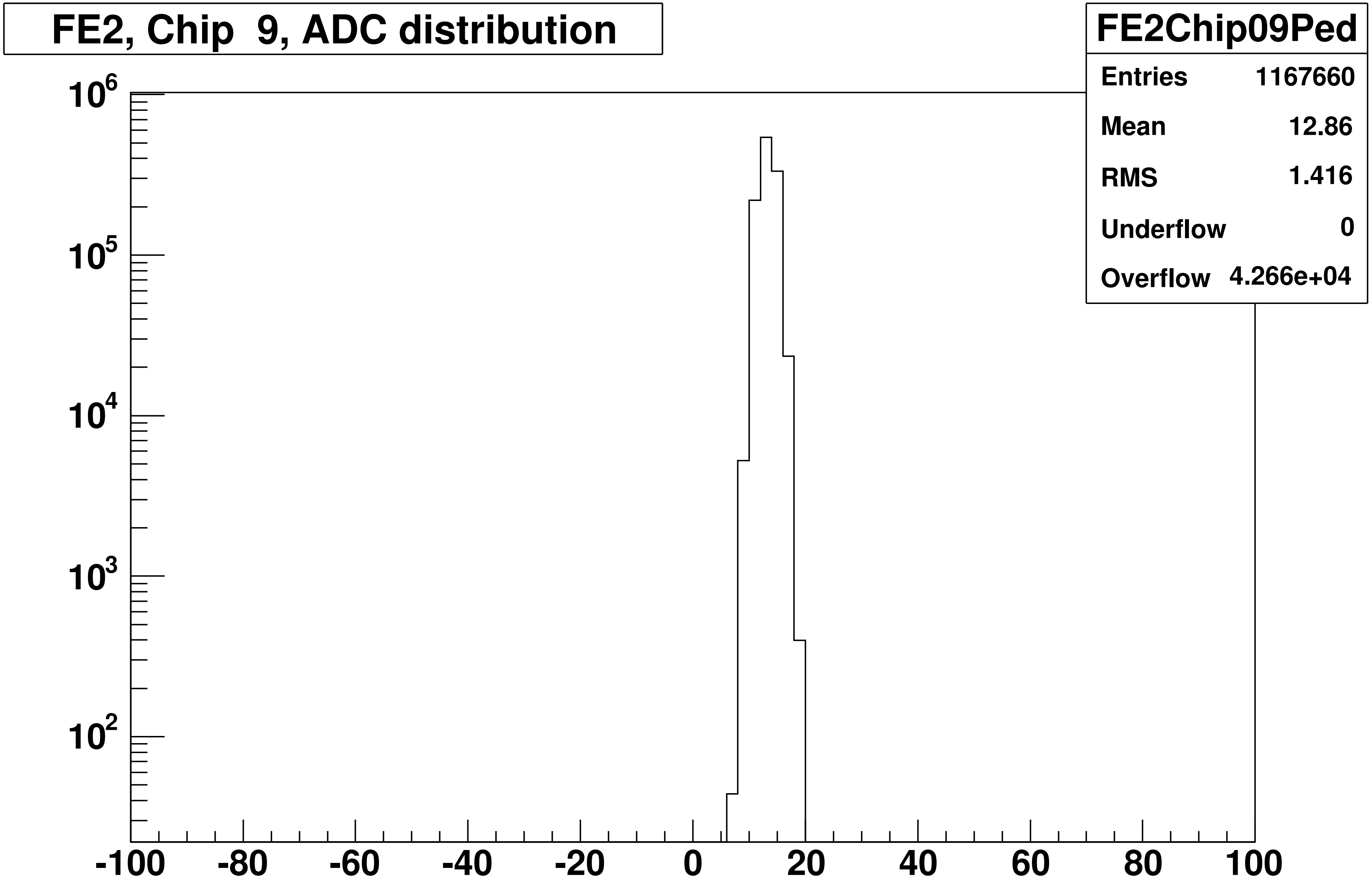

FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 1167660
Mean 3.648
RMS 1.359
Underflow 0
Overflow 4.194e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 1167660
Mean 1.92e+04
RMS 6.68
Underflow 1.126e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 1167660
Mean 706.2
RMS 3580
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 1167660
Mean 12.86
RMS 1.416
Underflow 0
Overflow 4.266e+04
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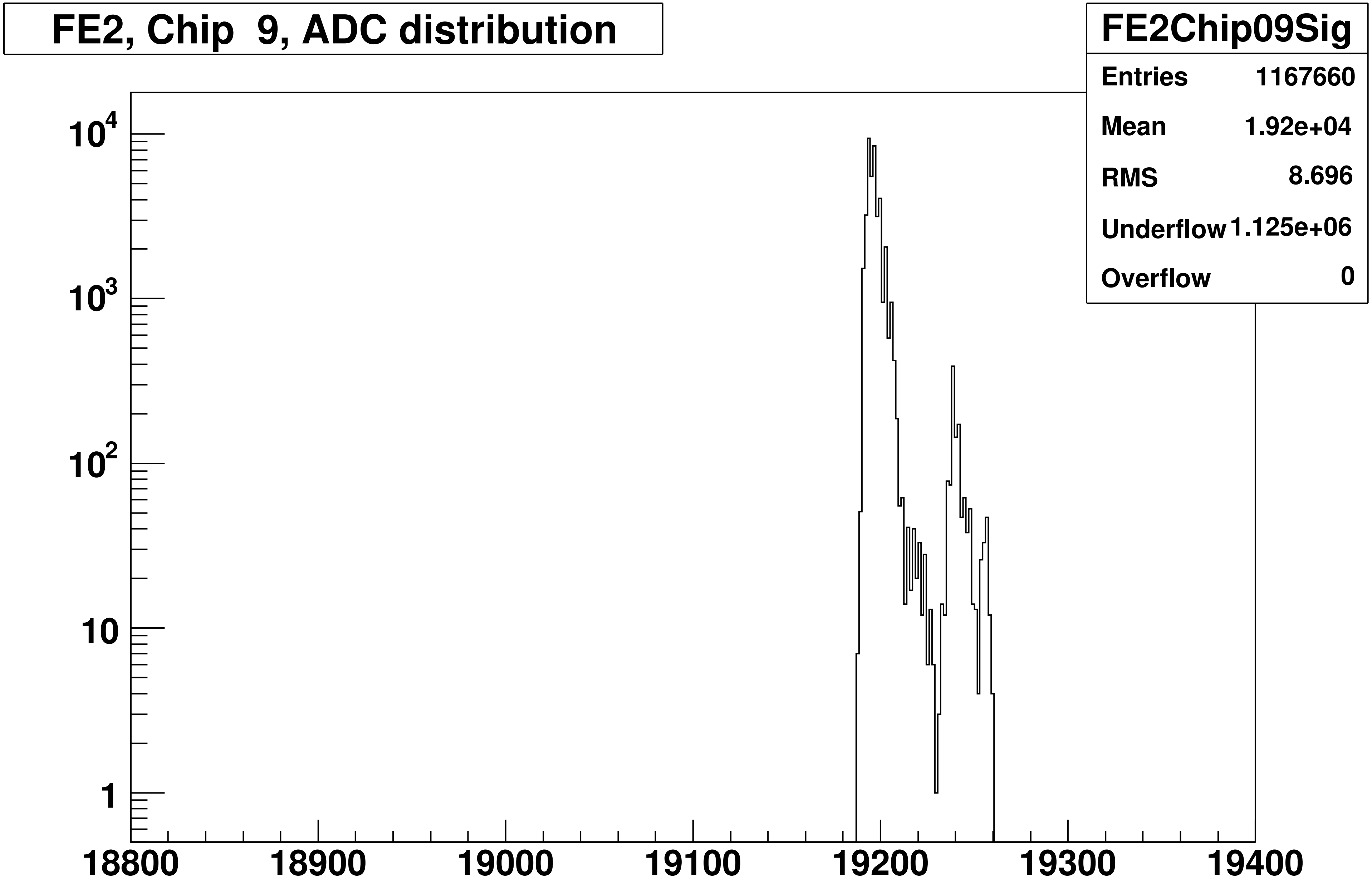
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 1167660
Mean 1.92e+04
RMS 8.696
Underflow 1.125e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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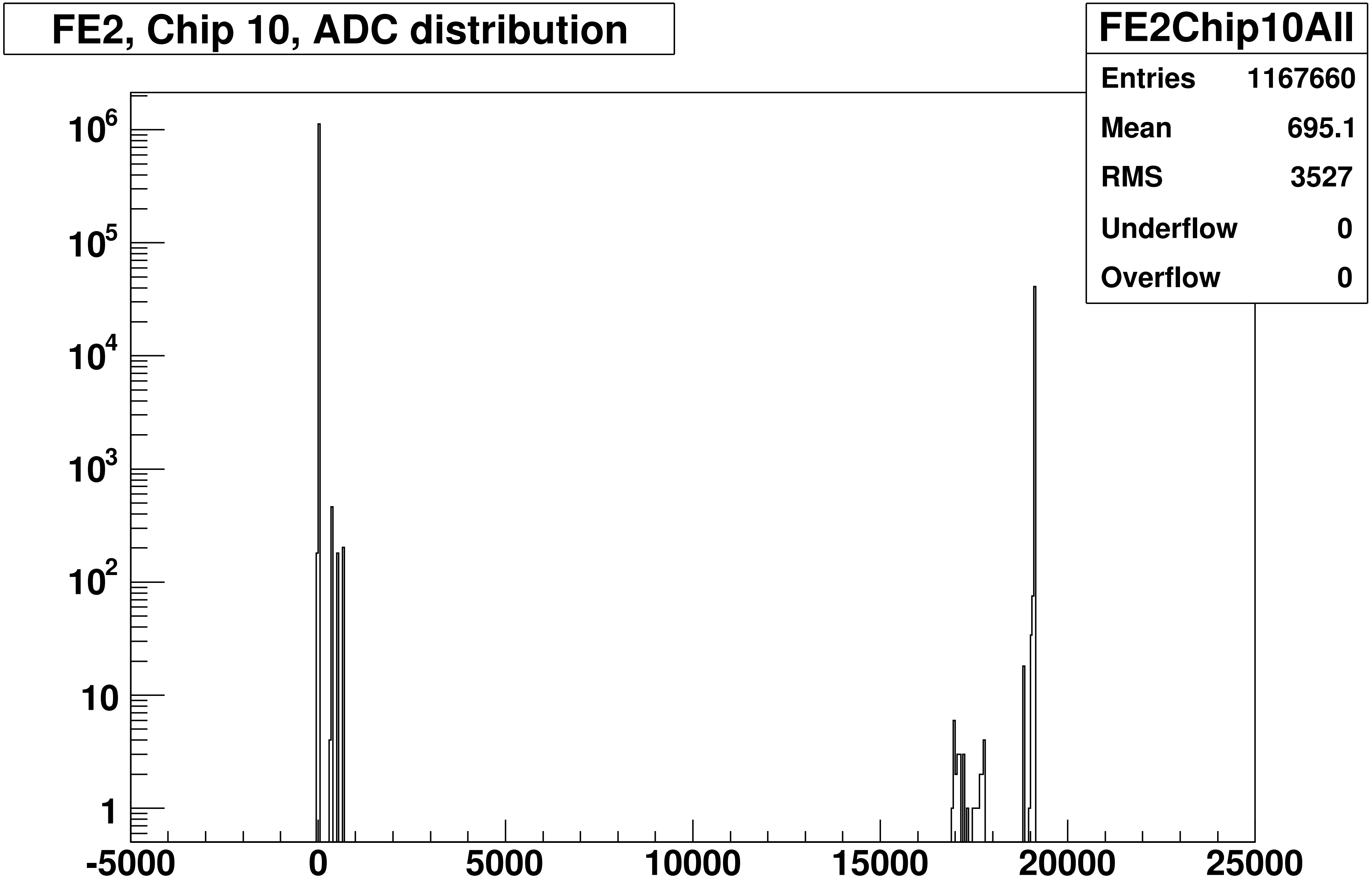
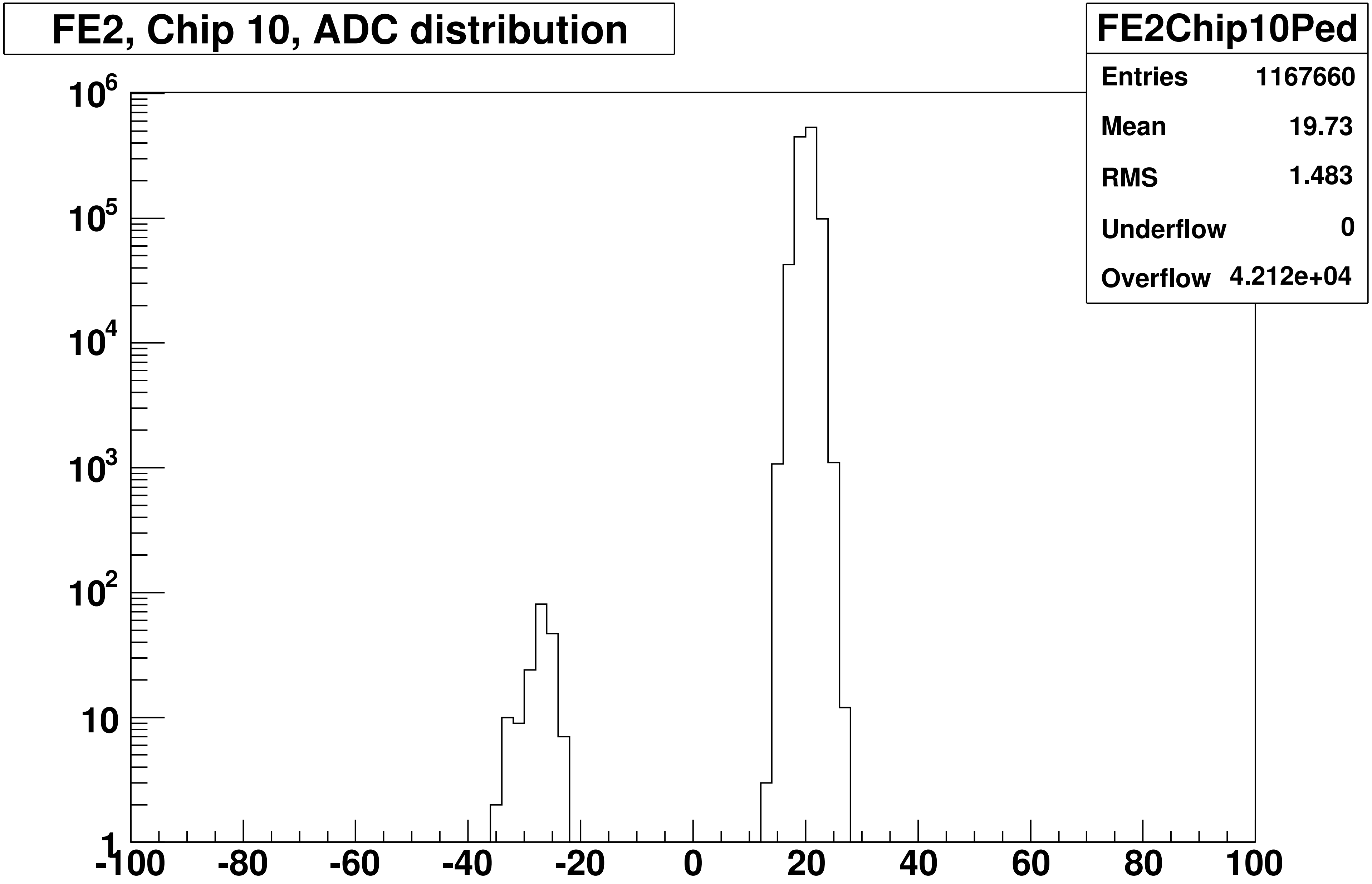
19100
19200
19300
19400


FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 1167660
Mean 695.1
RMS 3527
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 1167660
Mean 19.73
RMS 1.483
Underflow 0
Overflow 4.212e+04
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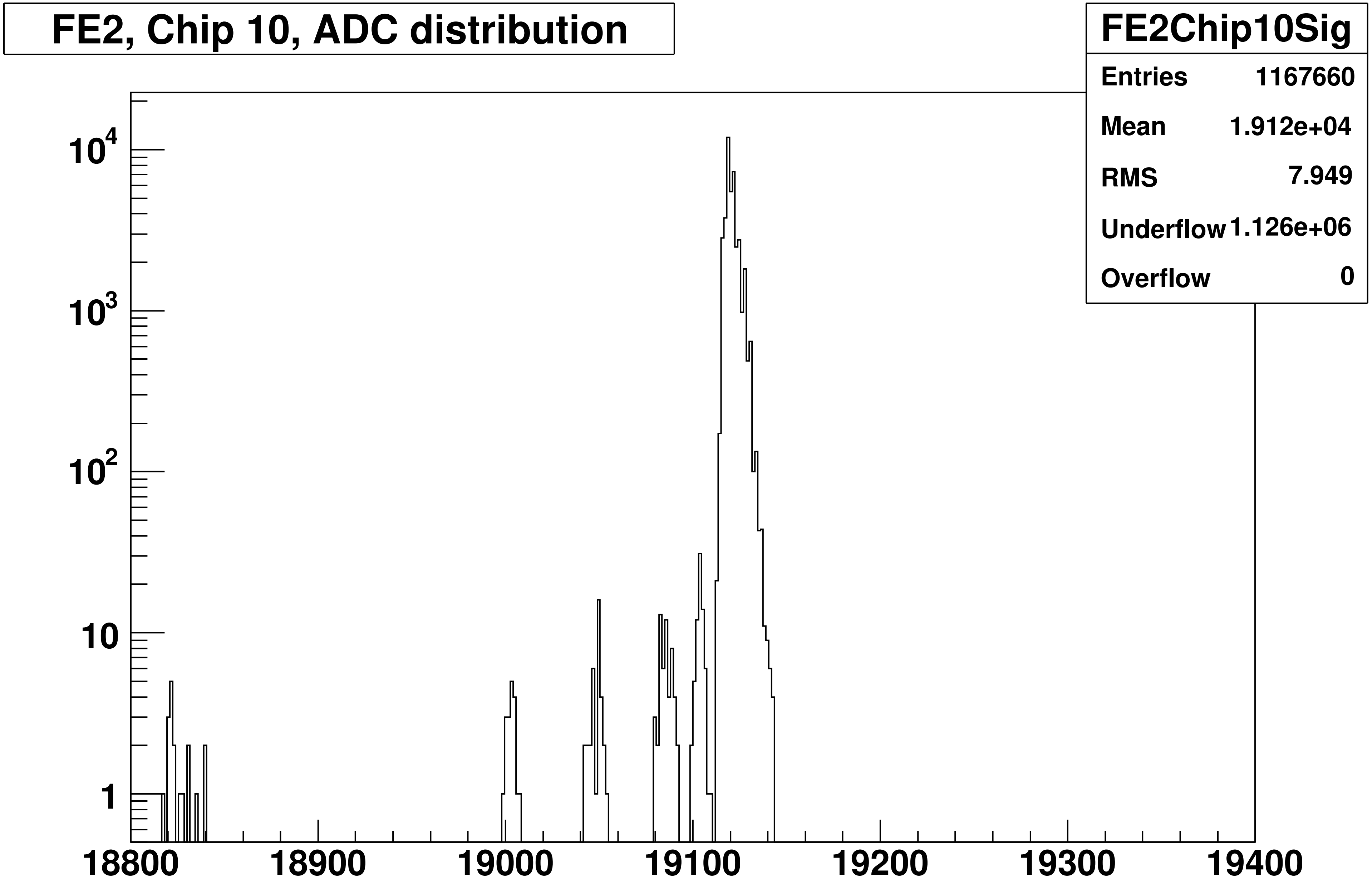
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 1167660
Mean 1.912e+04
RMS 7.949
Underflow 1.126e+06
Overflow 0
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10^3
10^2
10
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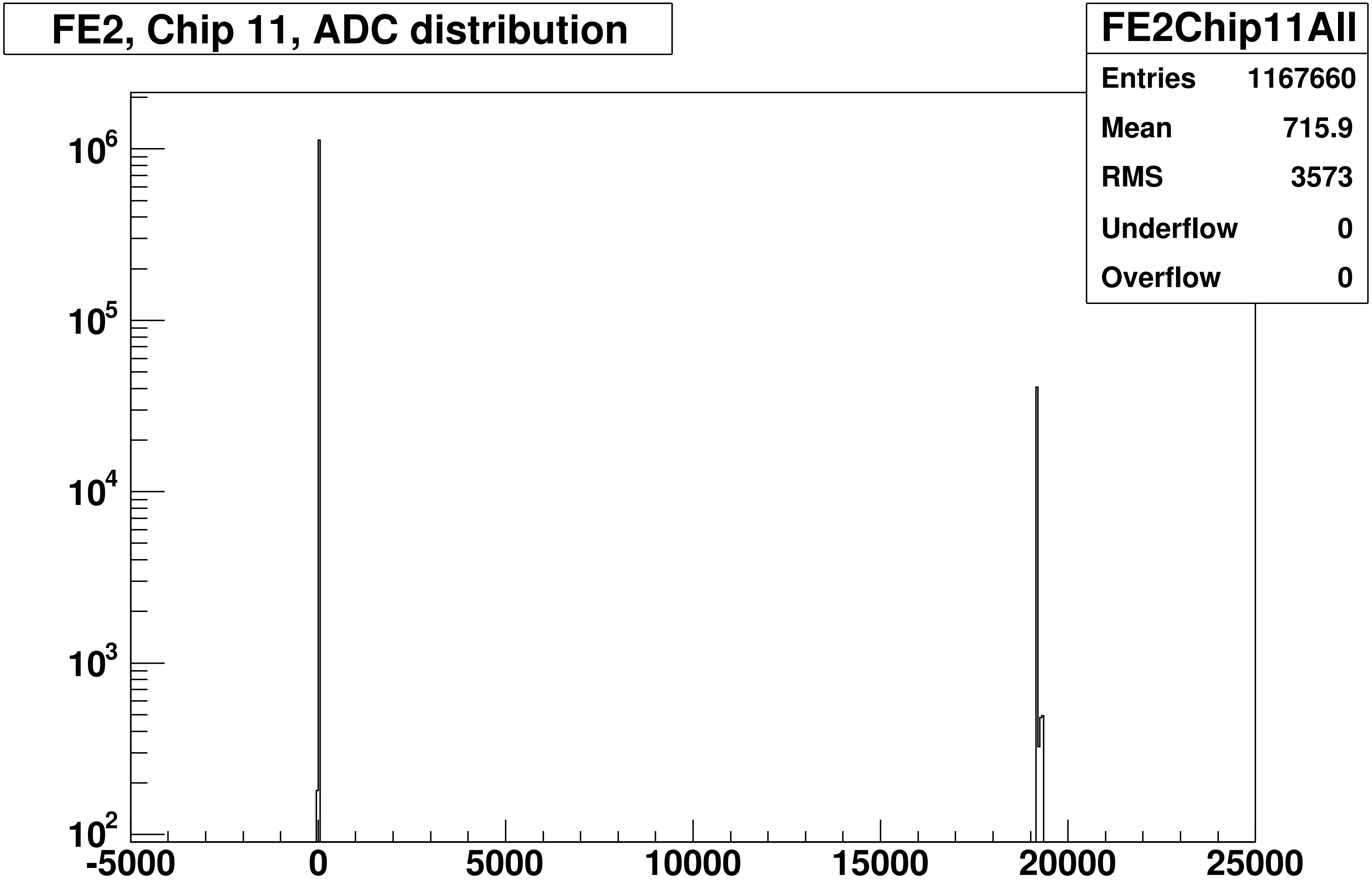
18800
18900
19000
19100
19200
19300
19400



FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 1167660
Mean 715.9
RMS 3573
Underflow 0
Overflow 0
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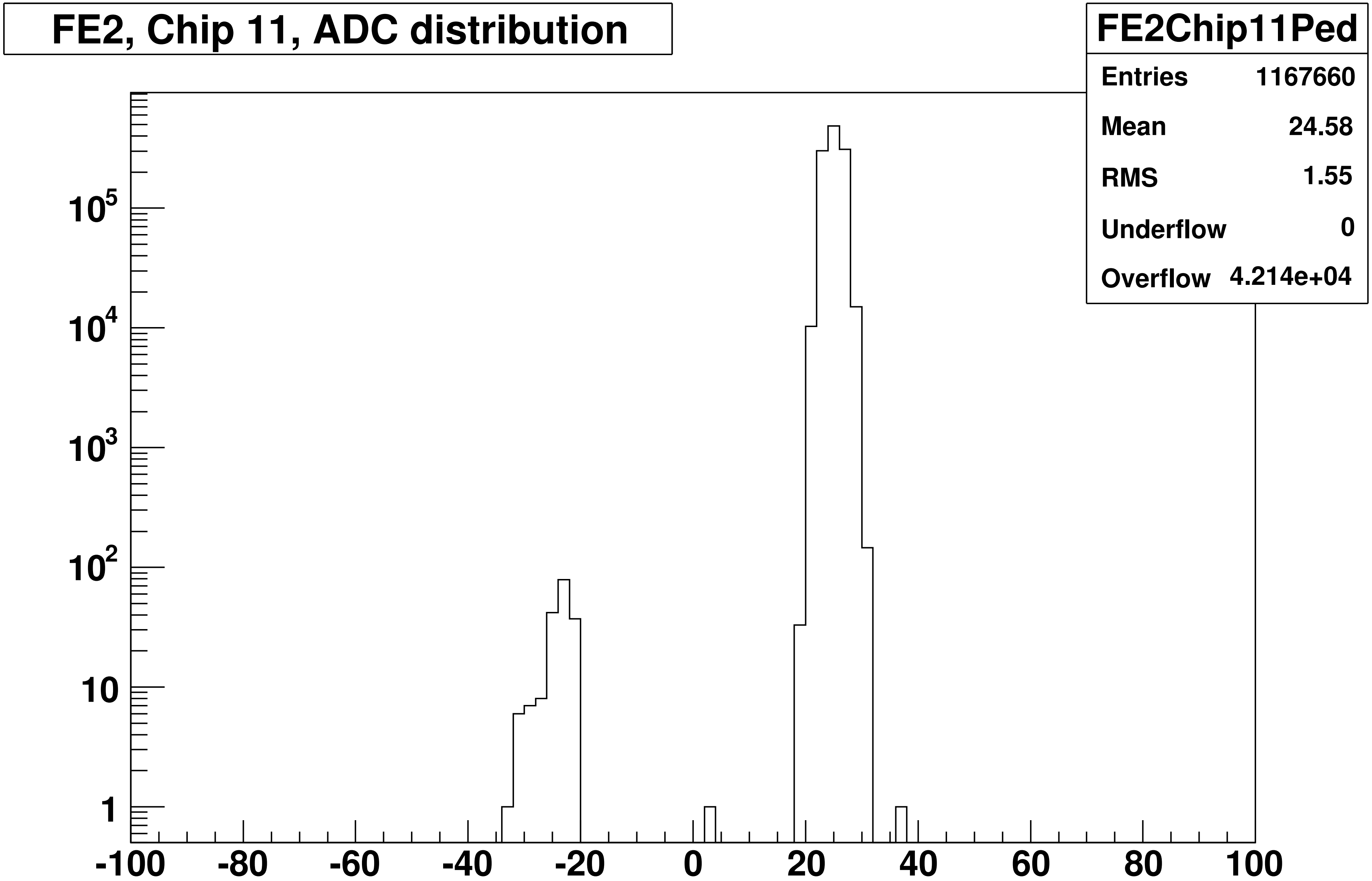
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 1167660
Mean 24.58
RMS 1.55
Underflow 0
Overflow 4.214e+04
10^5
10^4
10^3
10^2
10
1
-100
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-60
-40
-20
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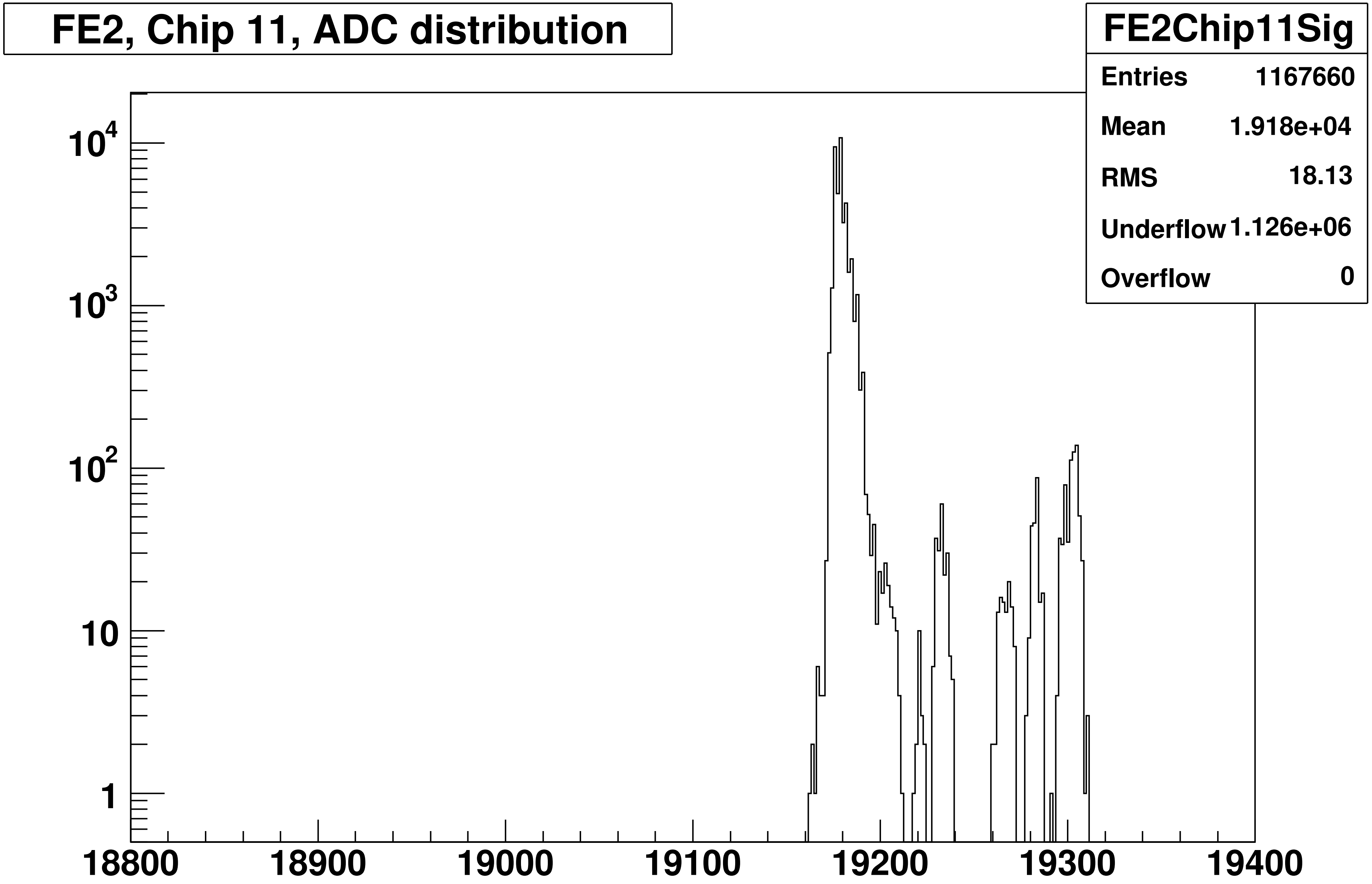
20
40
60
80
100



FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 1167660
Mean 1.918e+04
RMS 18.13
Underflow 1.126e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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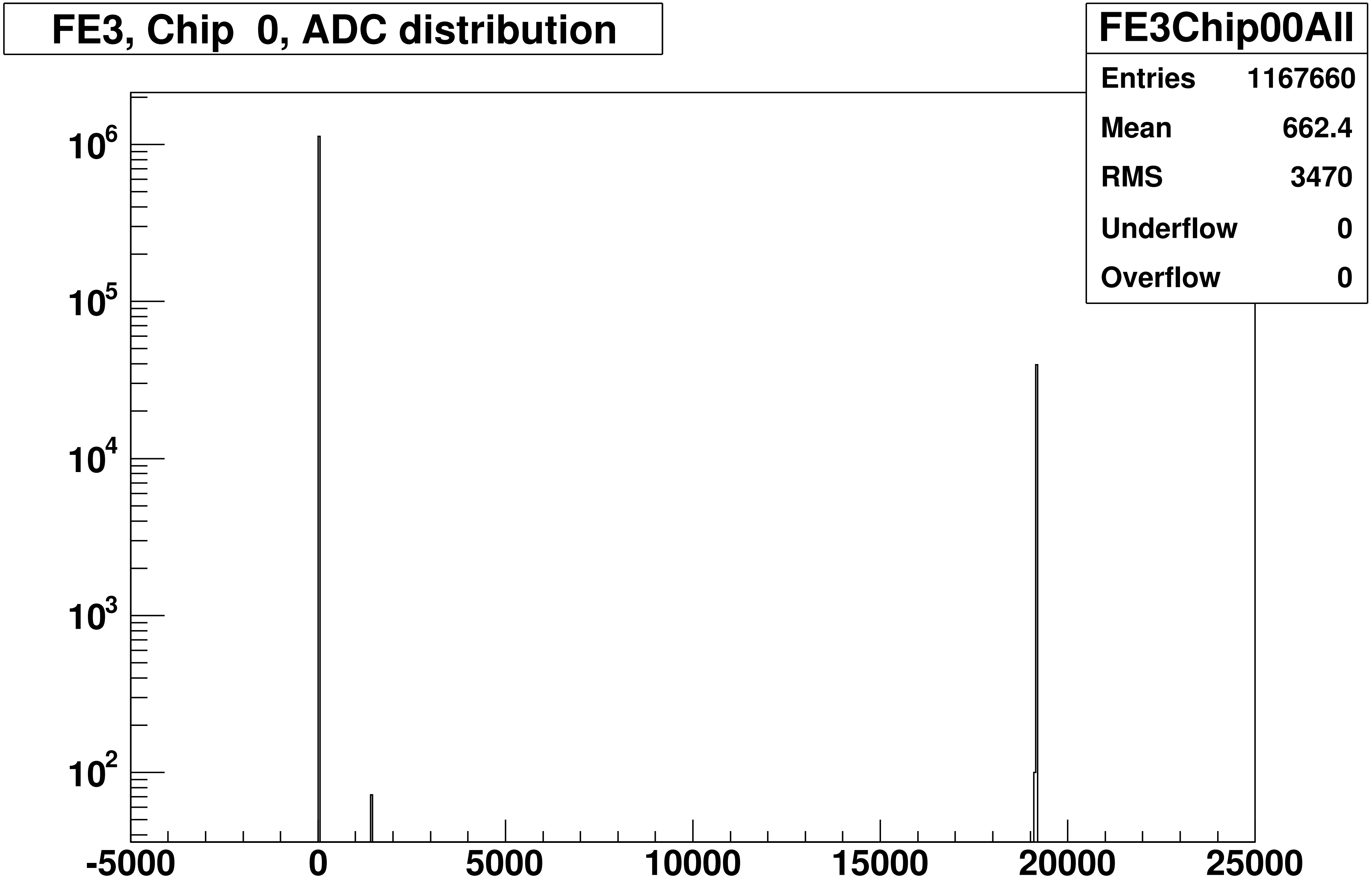
19400


FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 1167660
Mean 662.4
RMS 3470
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
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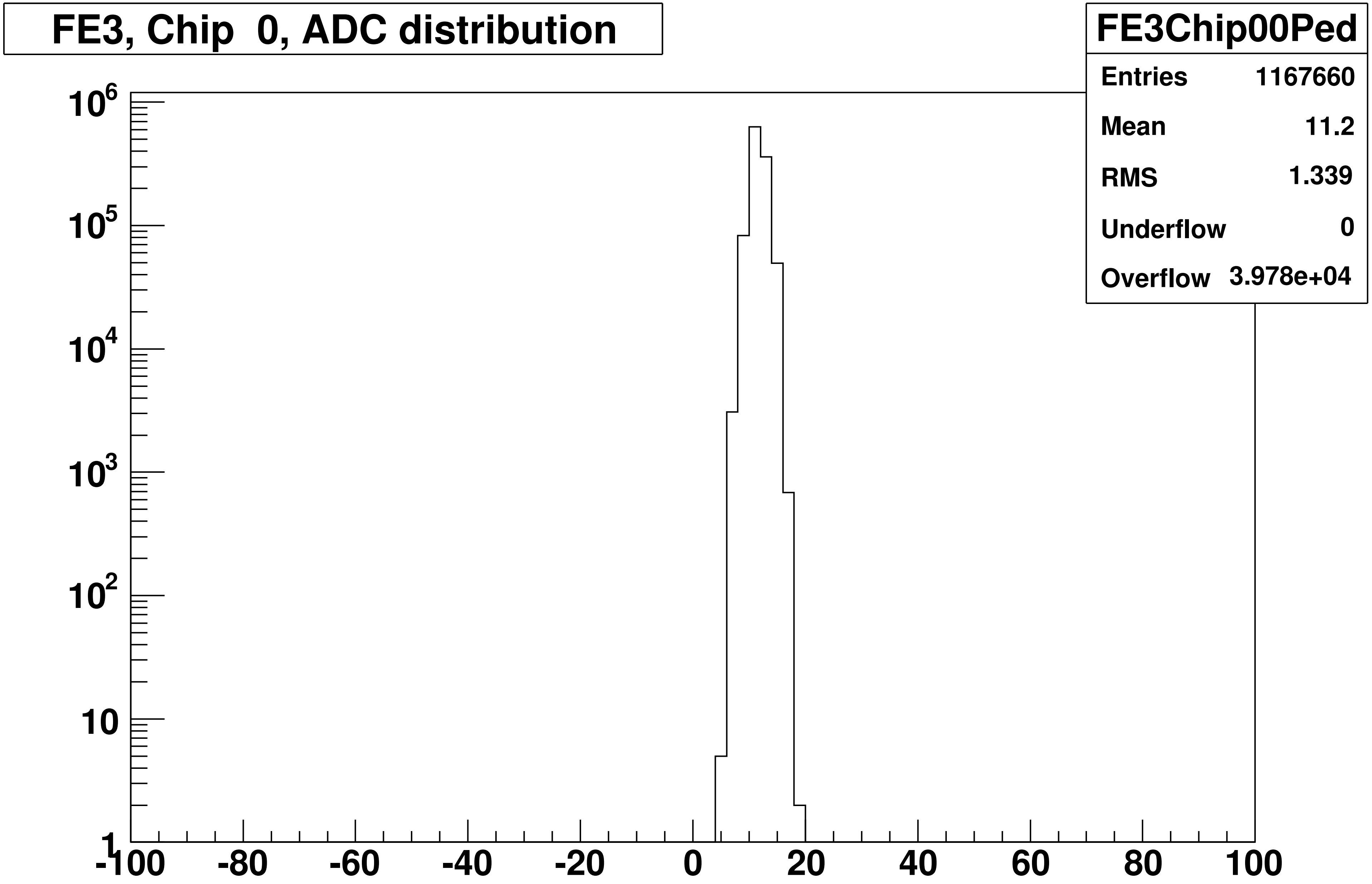
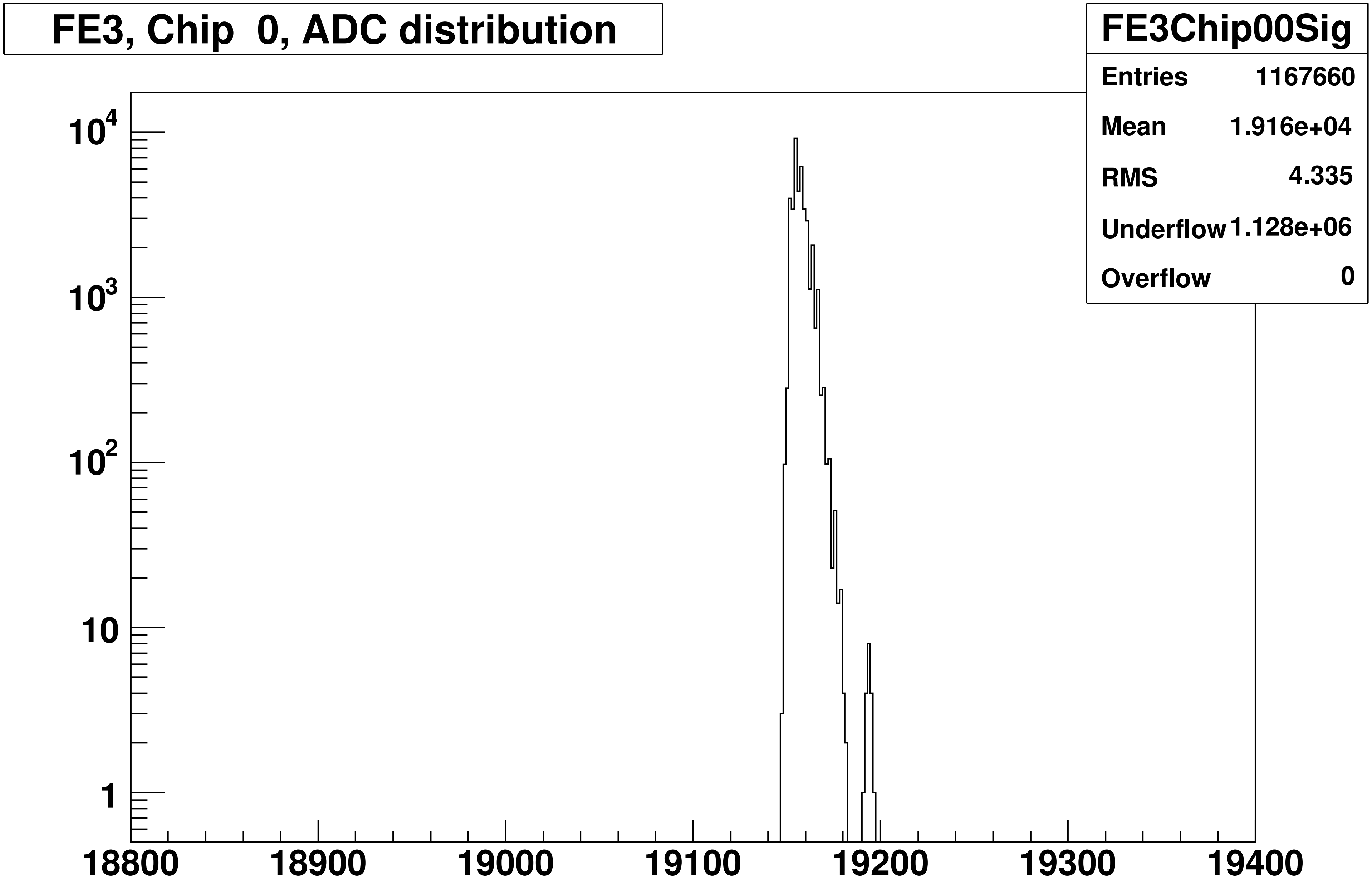
20000
25000


FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 1167660
Mean 11.2
RMS 1.339
Underflow 0
Overflow 3.978e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 1167660
Mean 1.916e+04
RMS 4.335
Underflow 1.128e+06
Overflow 0
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10^3
10^2
10
1
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18900
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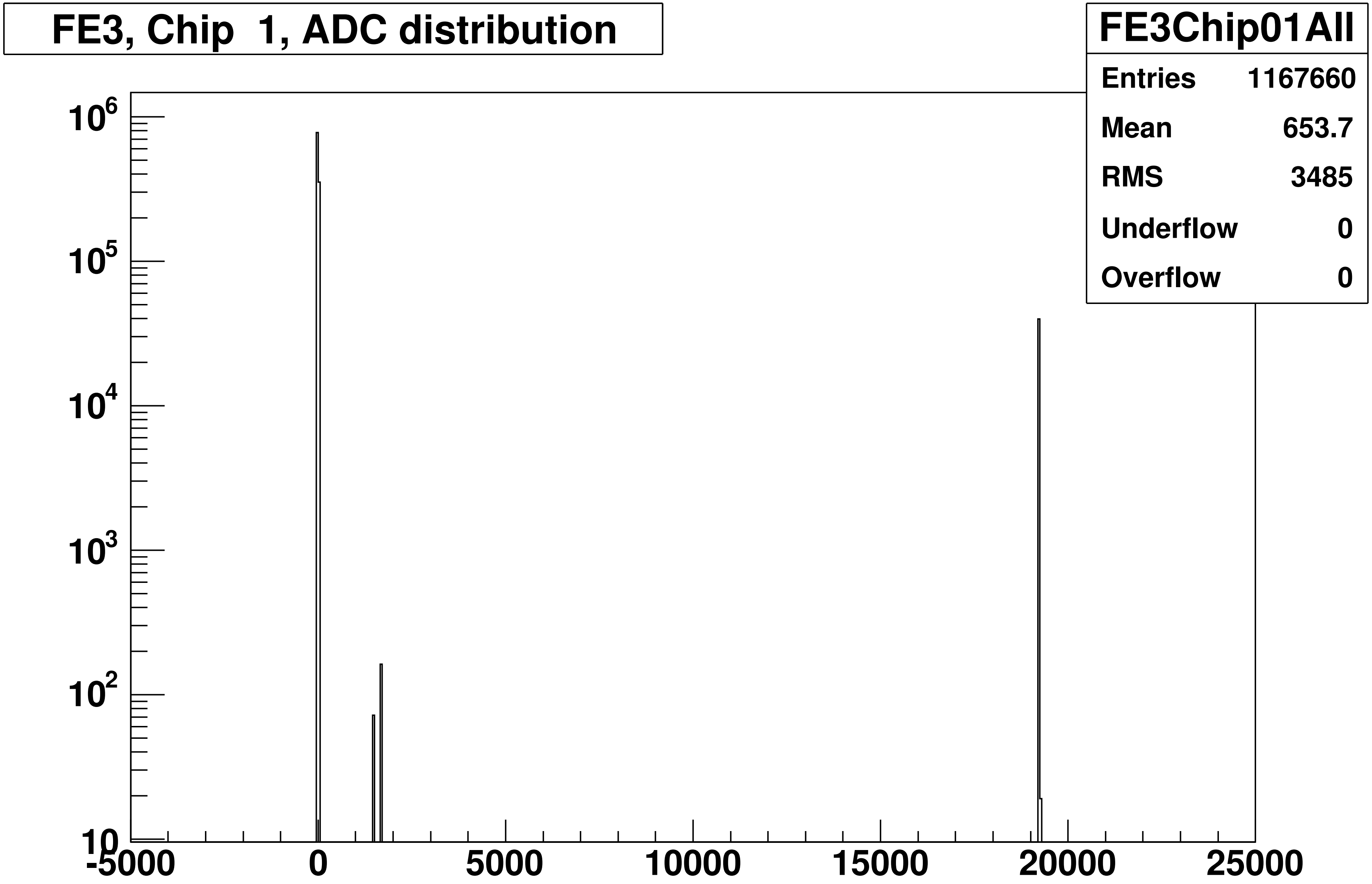
19400


FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 1167660
Mean 653.7
RMS 3485
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
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10000
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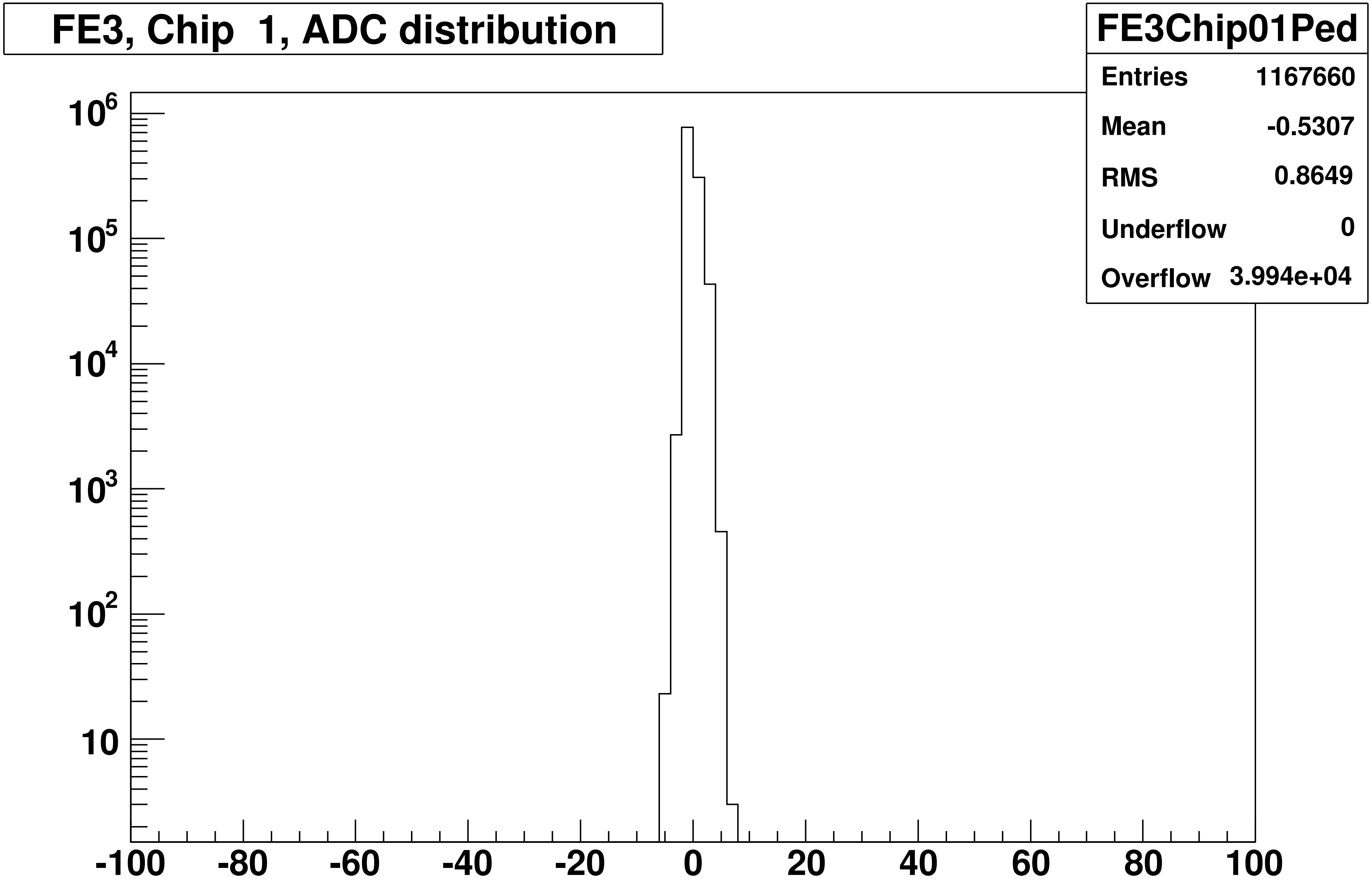
20000
25000


FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 1167660
Mean -0.5307
RMS 0.8649
Underflow 0
Overflow 3.994e+04
10^6
10^5
10^4
10^3
10^2
10
-100
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-40
-20
0
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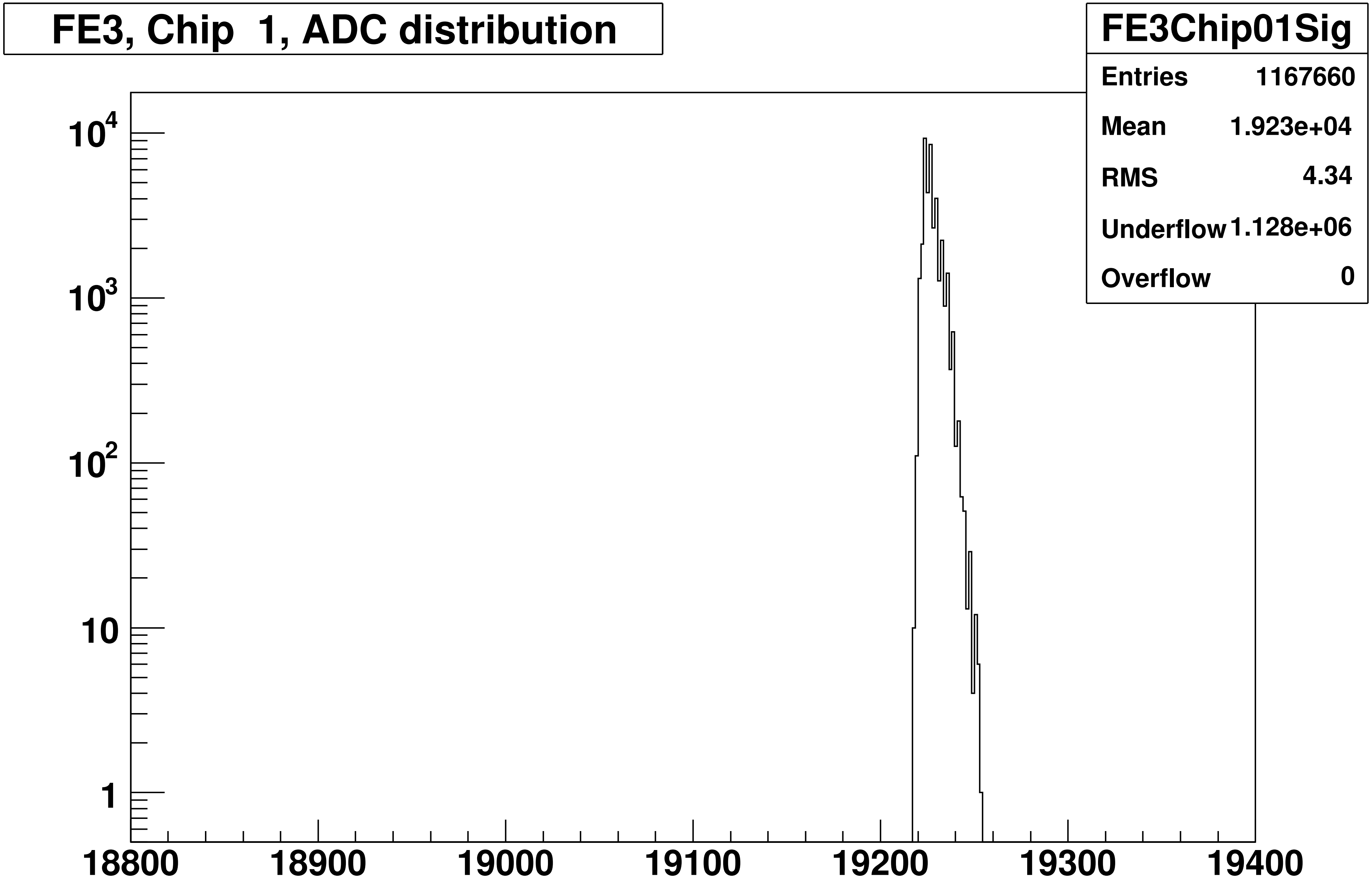
40
60
80
100


FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 1167660
Mean 1.923e+04
RMS 4.34
Underflow 1.128e+06
Overflow 0
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10^3
10^2
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18900
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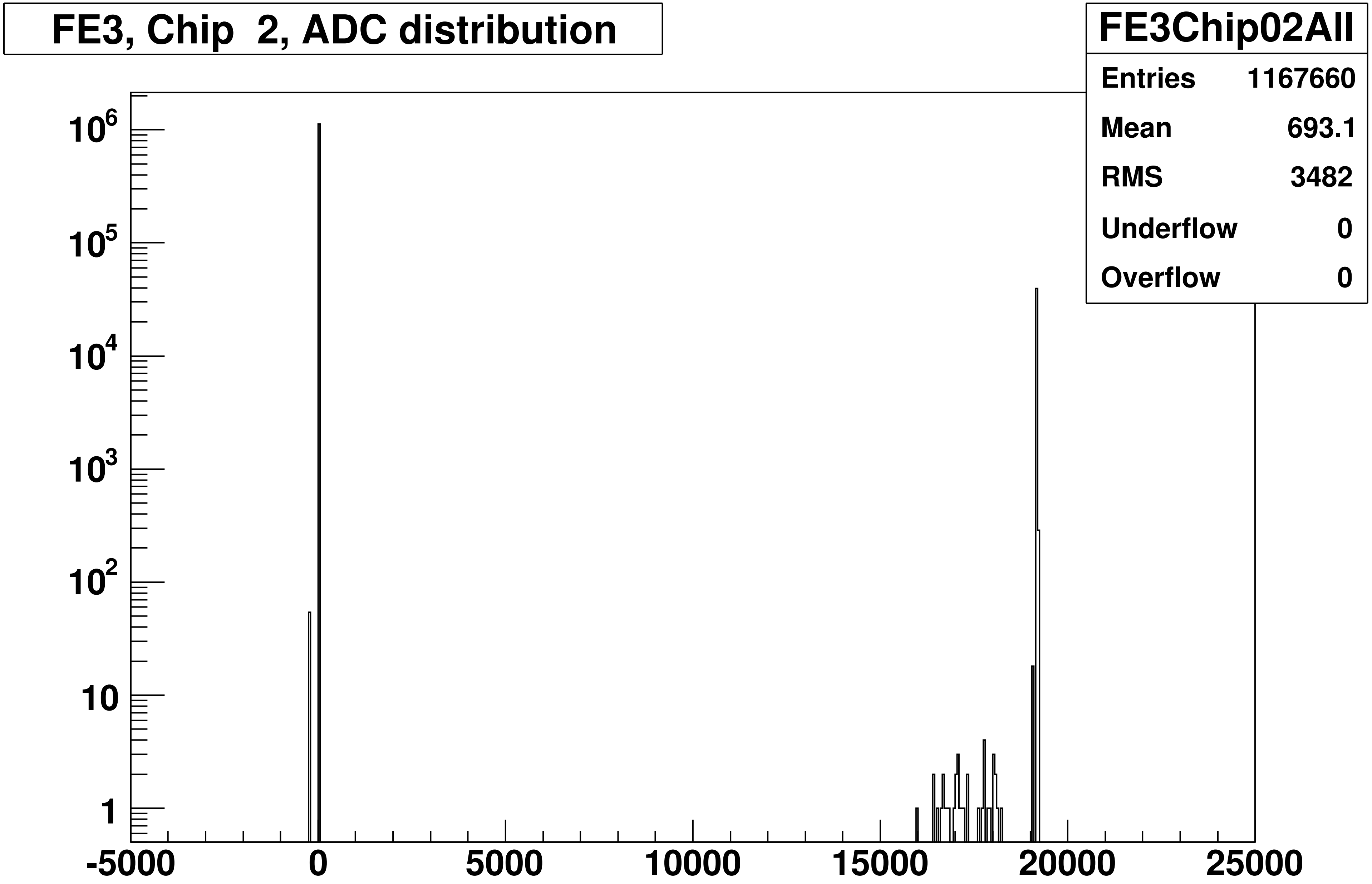
19400



FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 1167660
Mean 693.1
RMS 3482
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
5000
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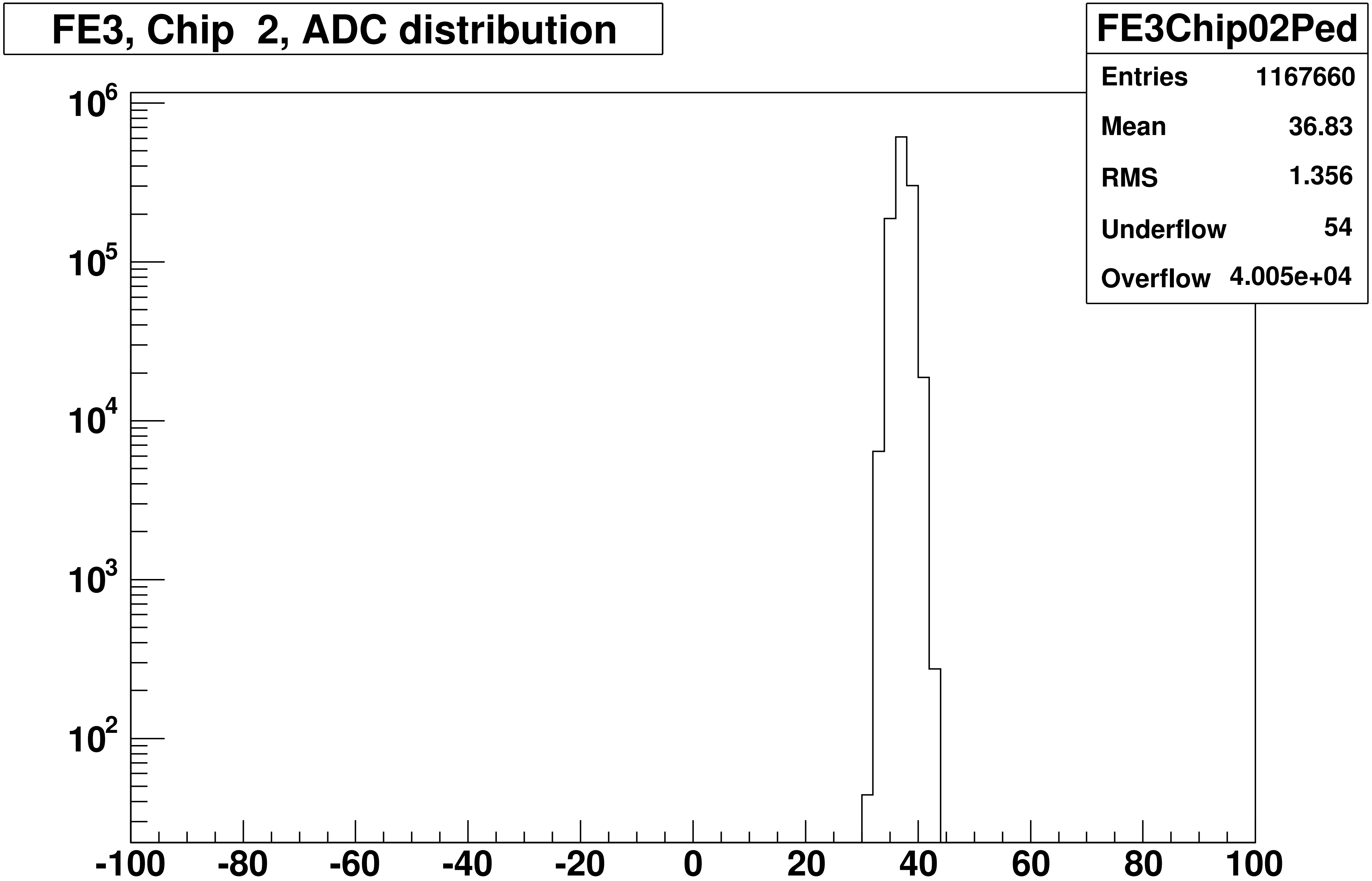
15000
20000
25000


FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 1167660
Mean 36.83
RMS 1.356
Underflow 54
Overflow 4.005e+04
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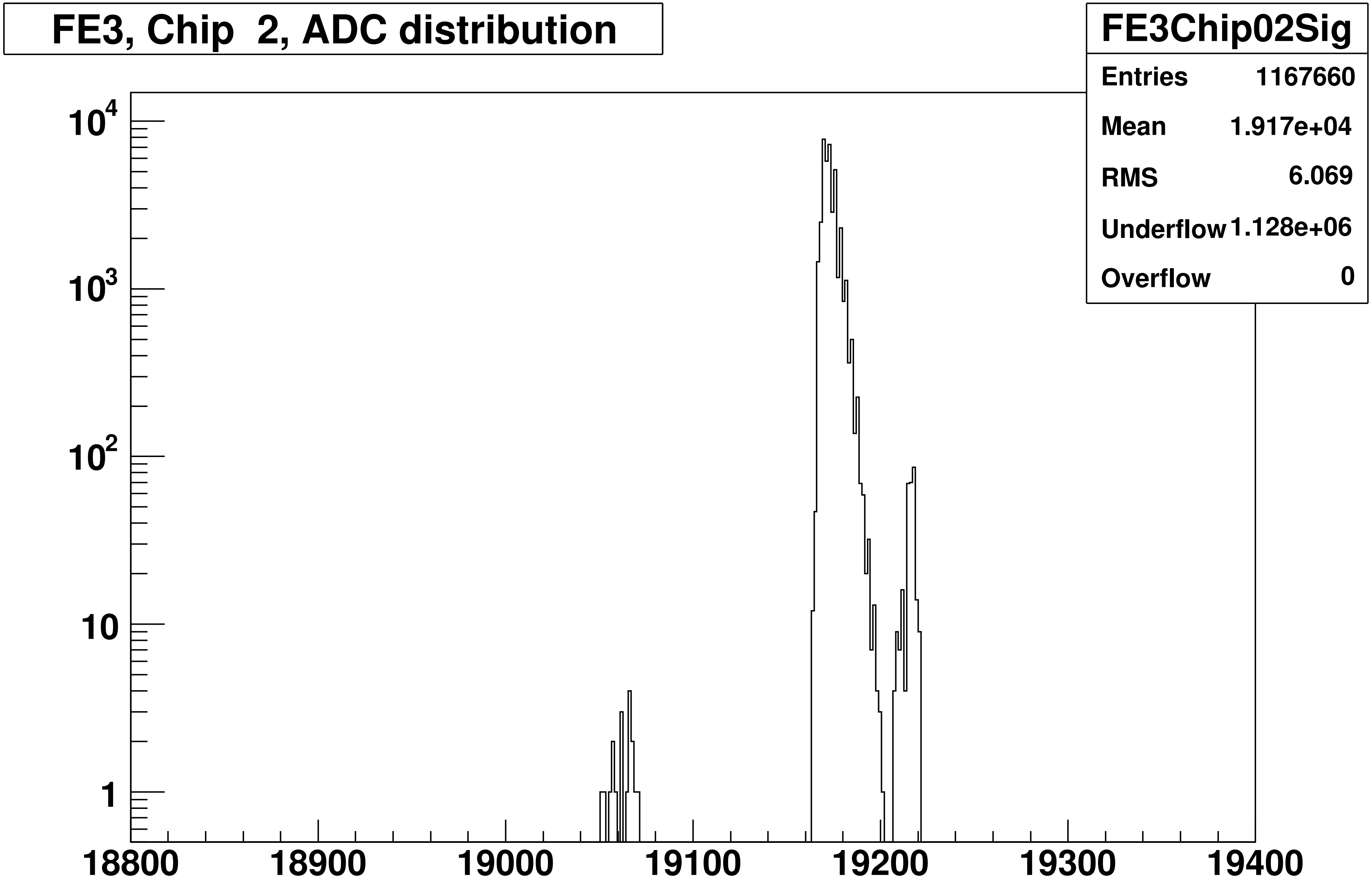
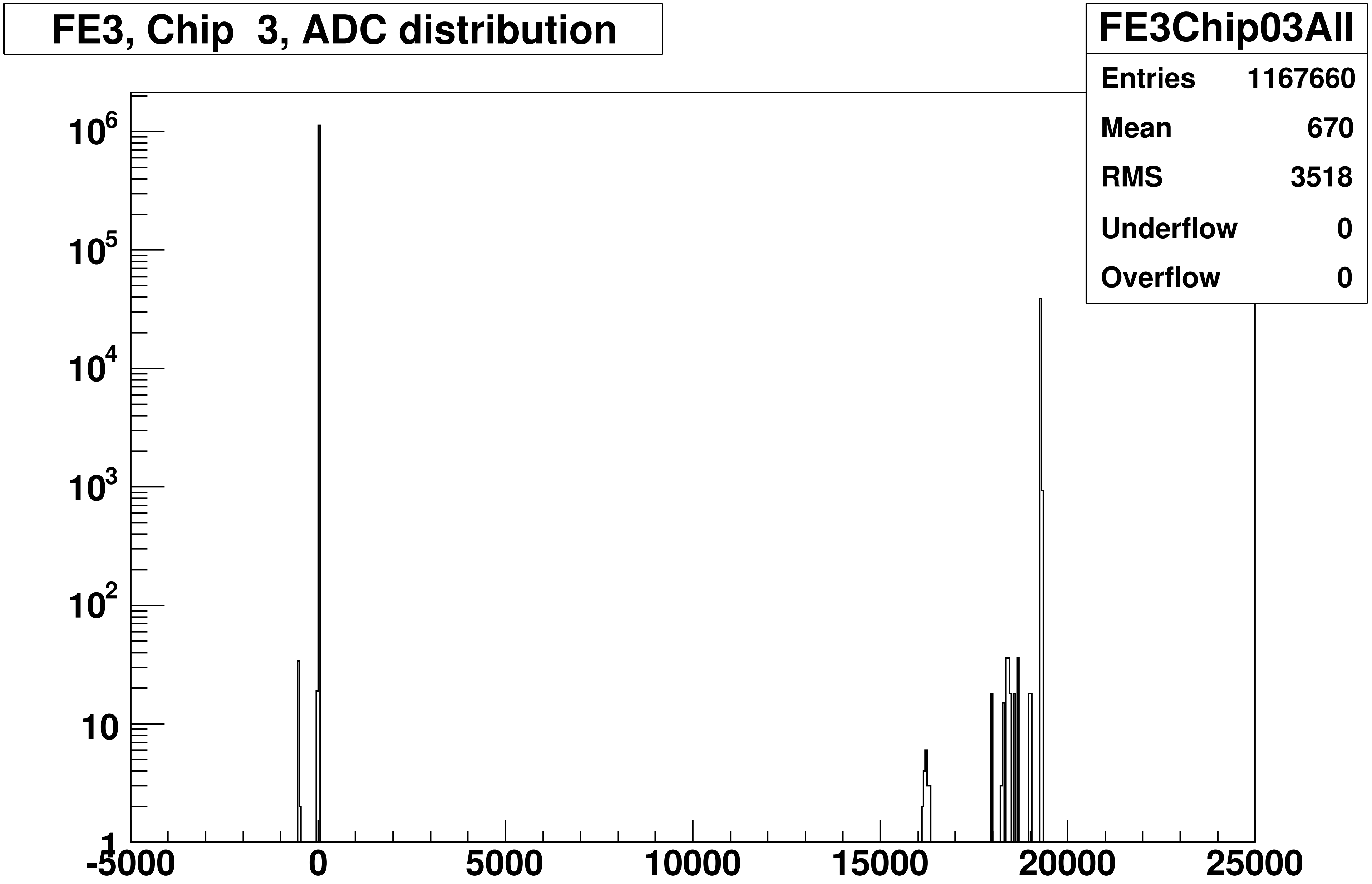
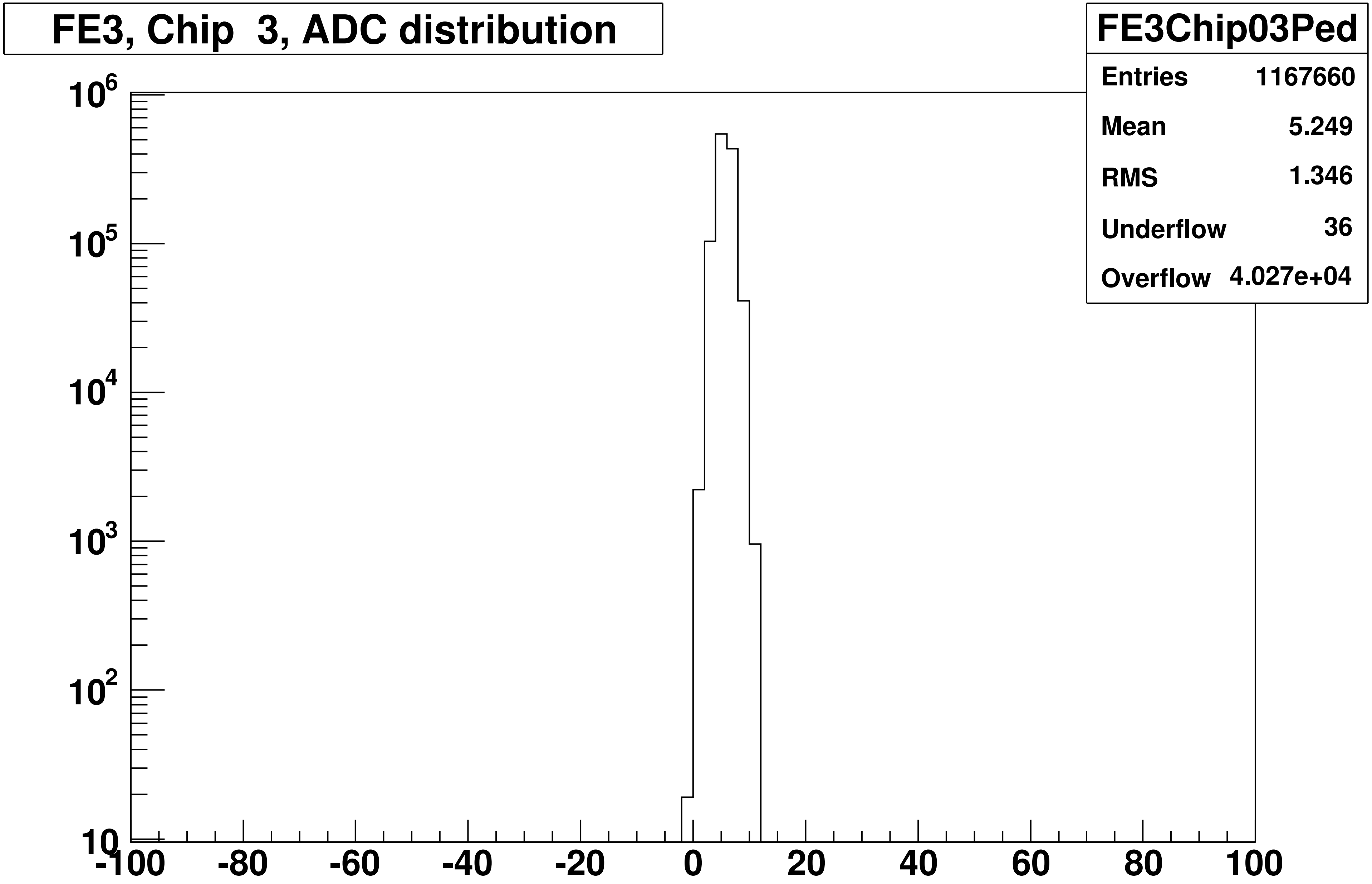
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

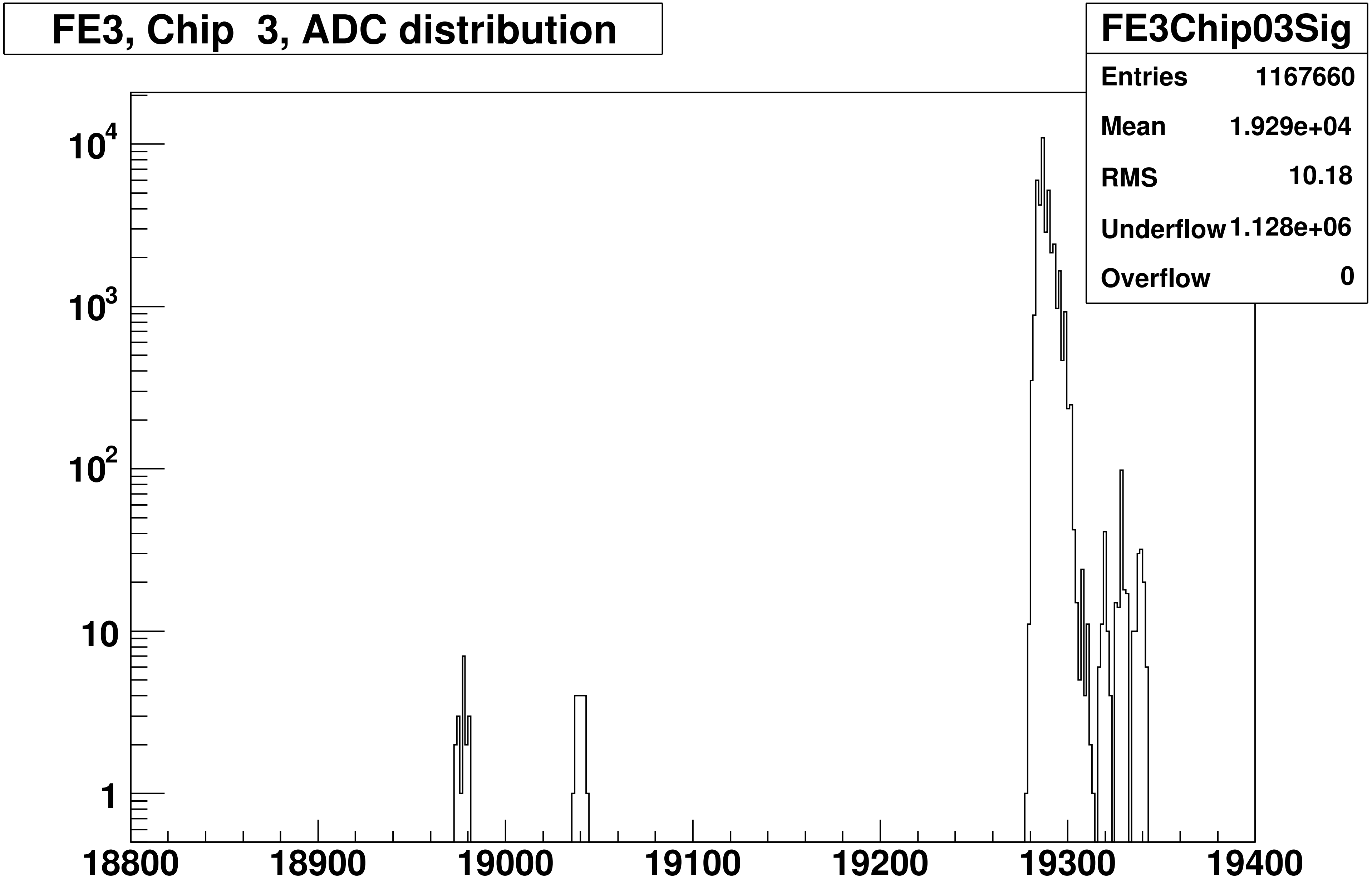


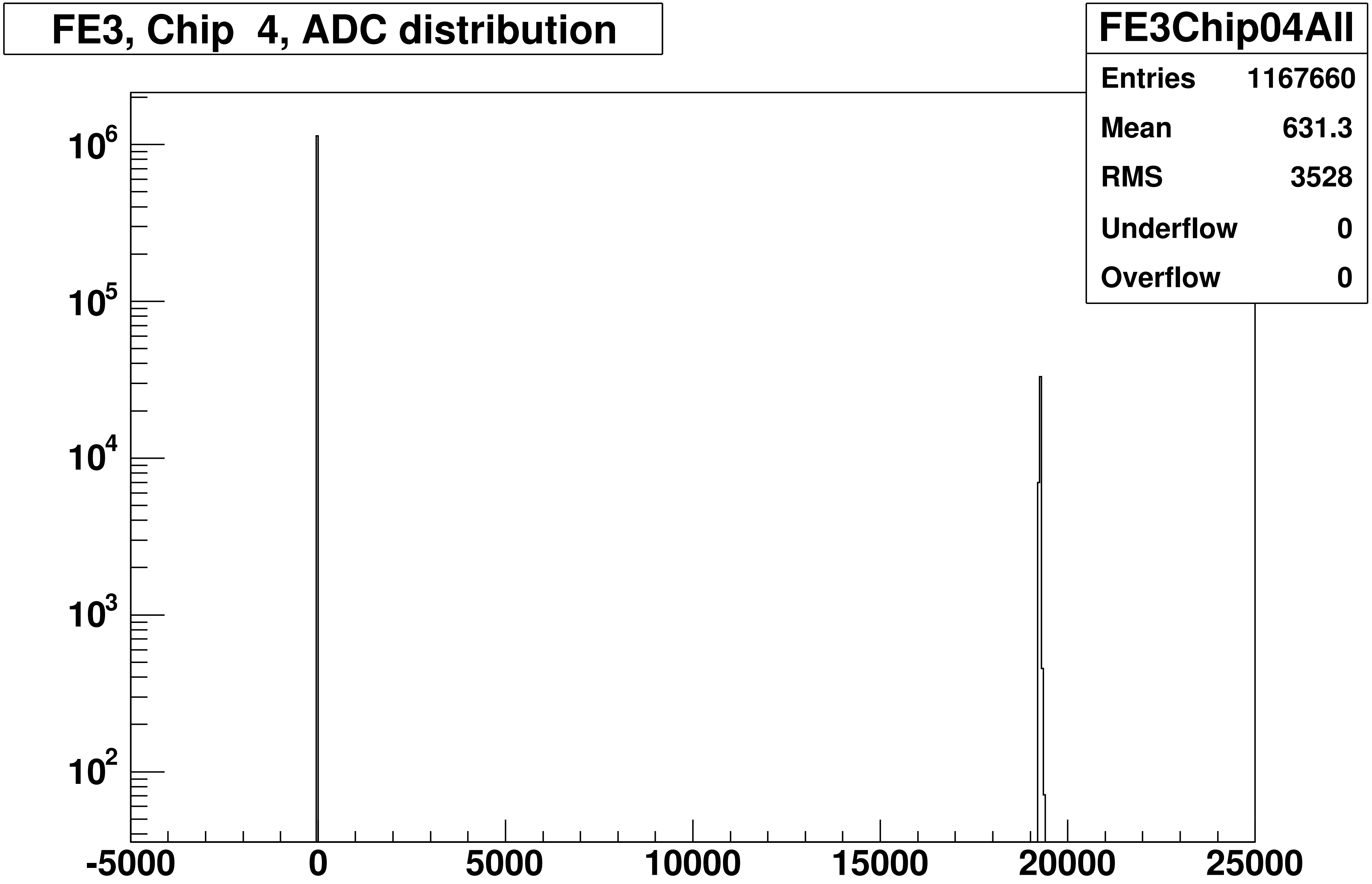
FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 1167660
Mean 1.917e+04
RMS 6.069
Underflow 1.128e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 1167660
Mean 670
RMS 3518
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 1167660
Mean 5.249
RMS 1.346
Underflow 36
Overflow 4.027e+04
10
10^2
10^3
10^4
10^5
10^6
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 1167660
Mean 1.929e+04
RMS 10.18
Underflow 1.128e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 1167660
Mean 631.3
RMS 3528
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
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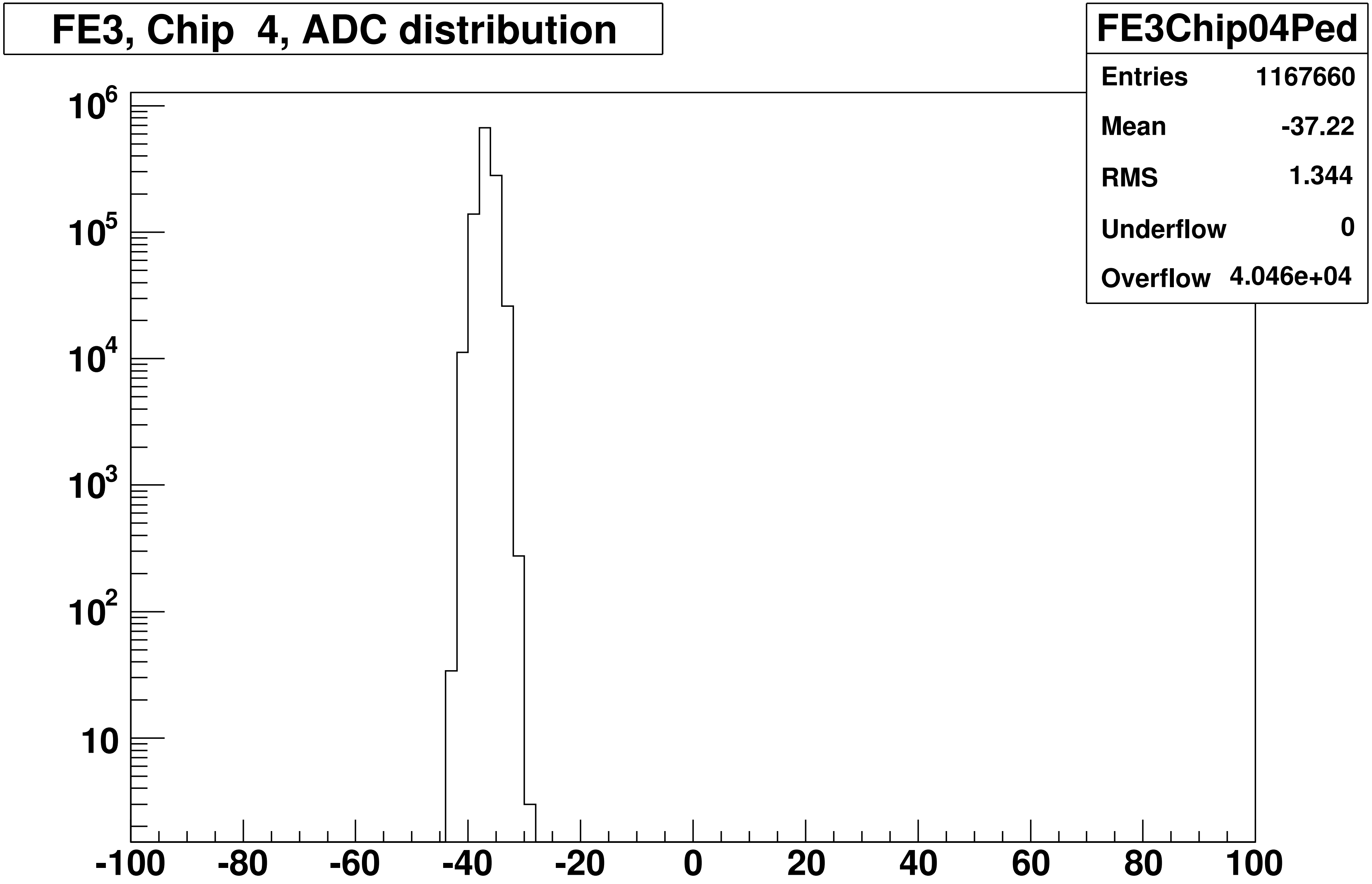
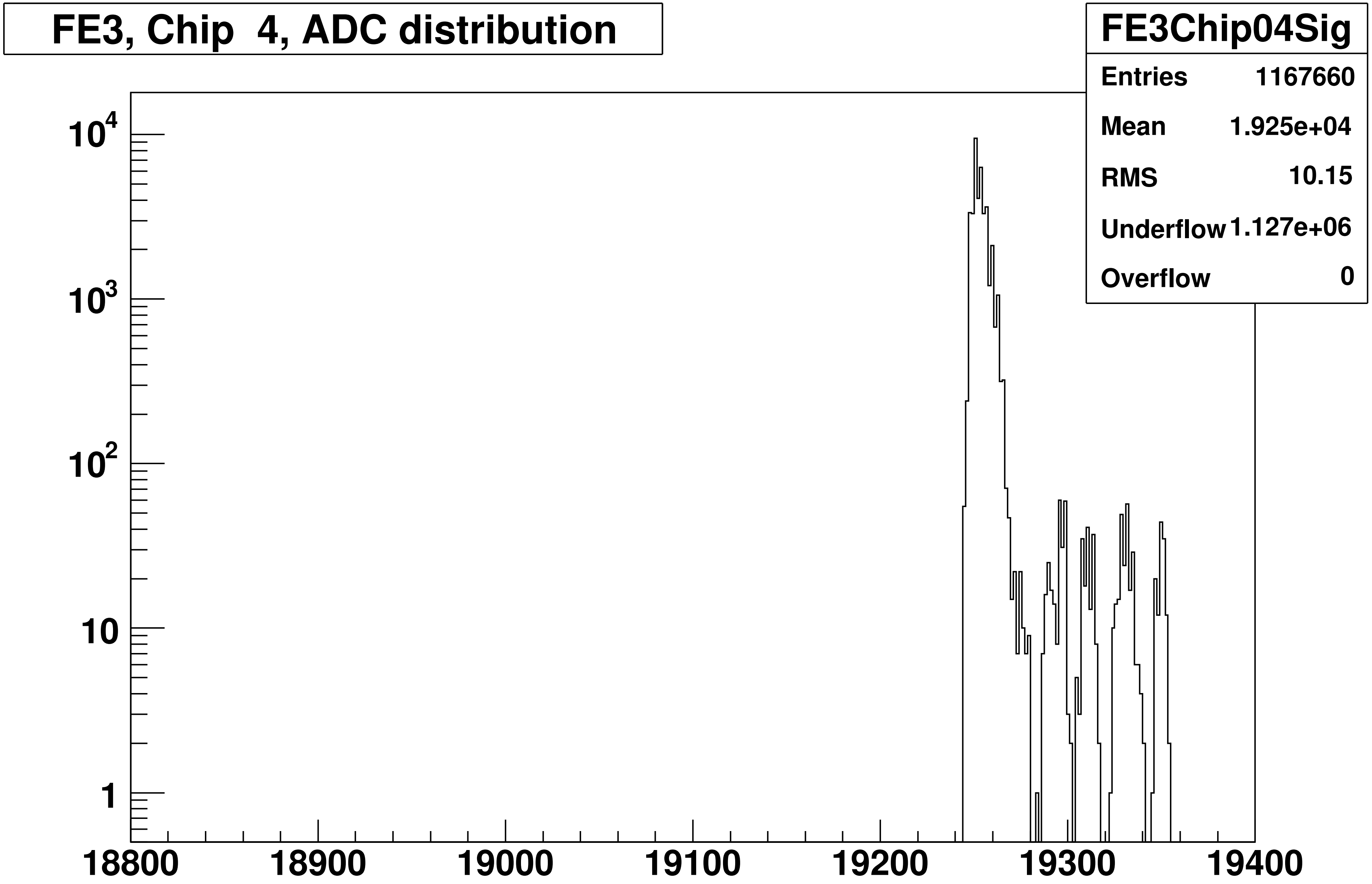
10000
15000
20000
25000


FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 1167660
Mean -37.22
RMS 1.344
Underflow 0
Overflow 4.046e+04
10^6
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 1167660
Mean 1.925e+04
RMS 10.15
Underflow 1.127e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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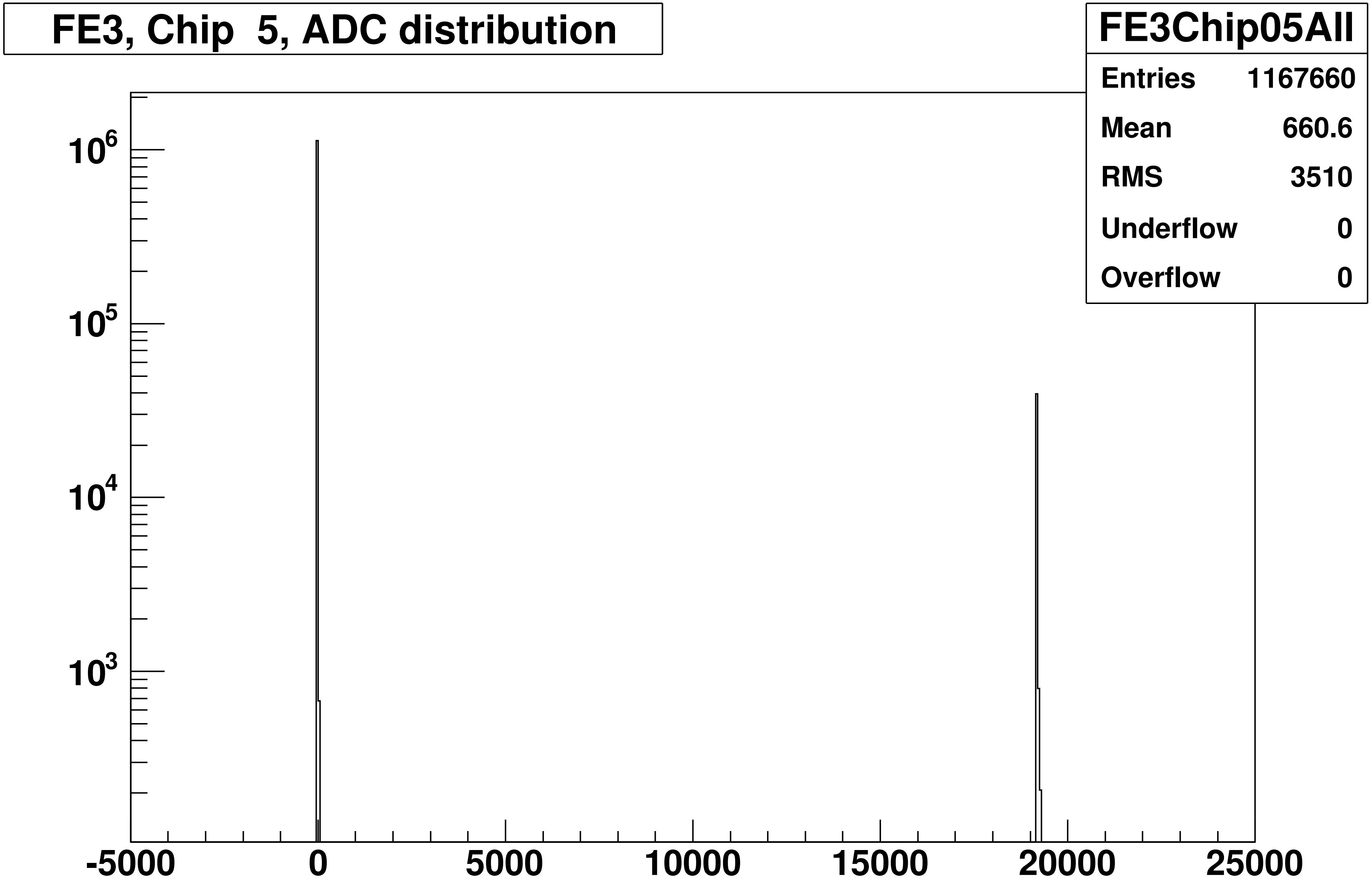
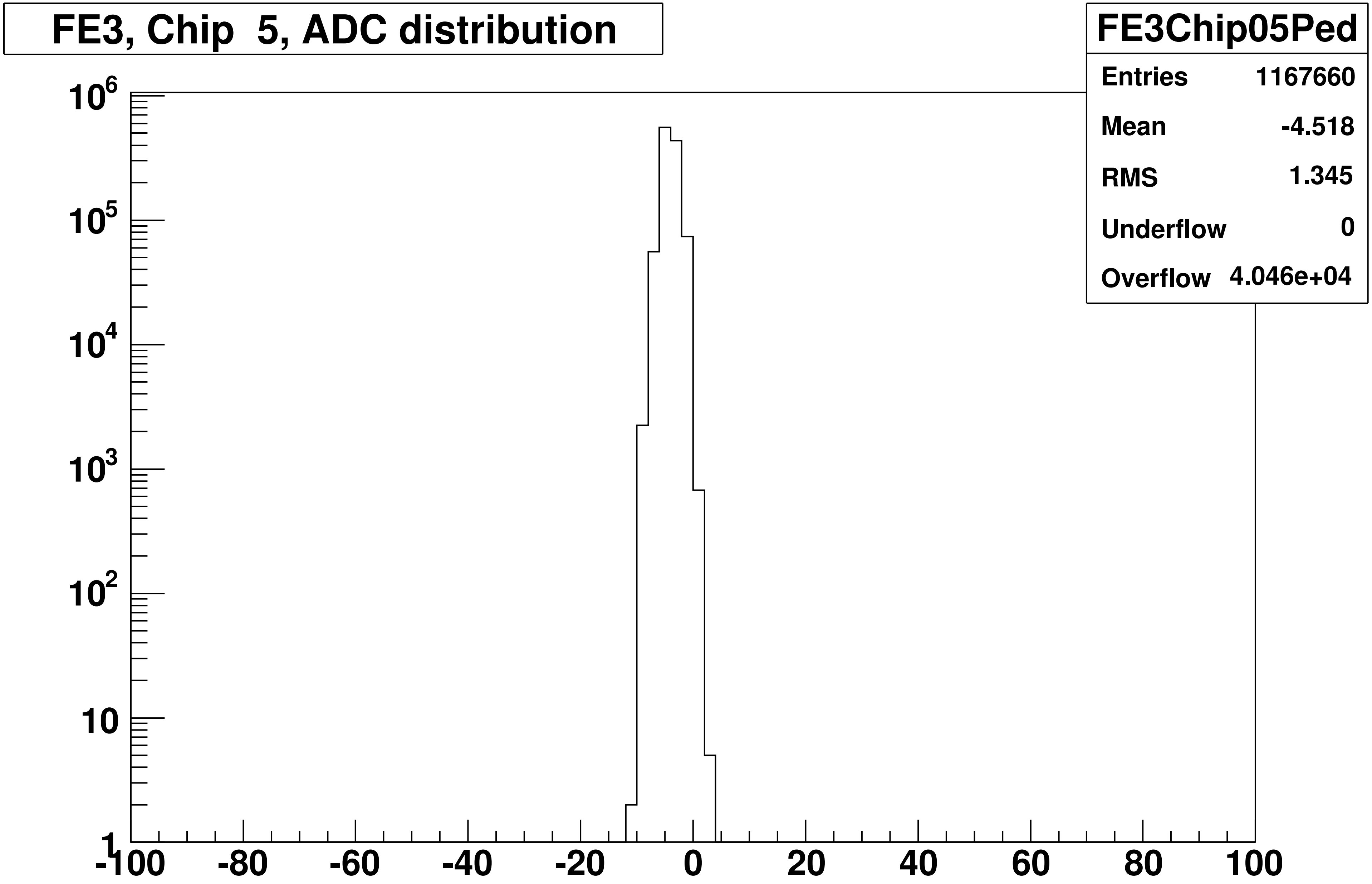

FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 1167660
Mean 660.6
RMS 3510
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 1167660
Mean -4.518
RMS 1.345
Underflow 0
Overflow 4.046e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
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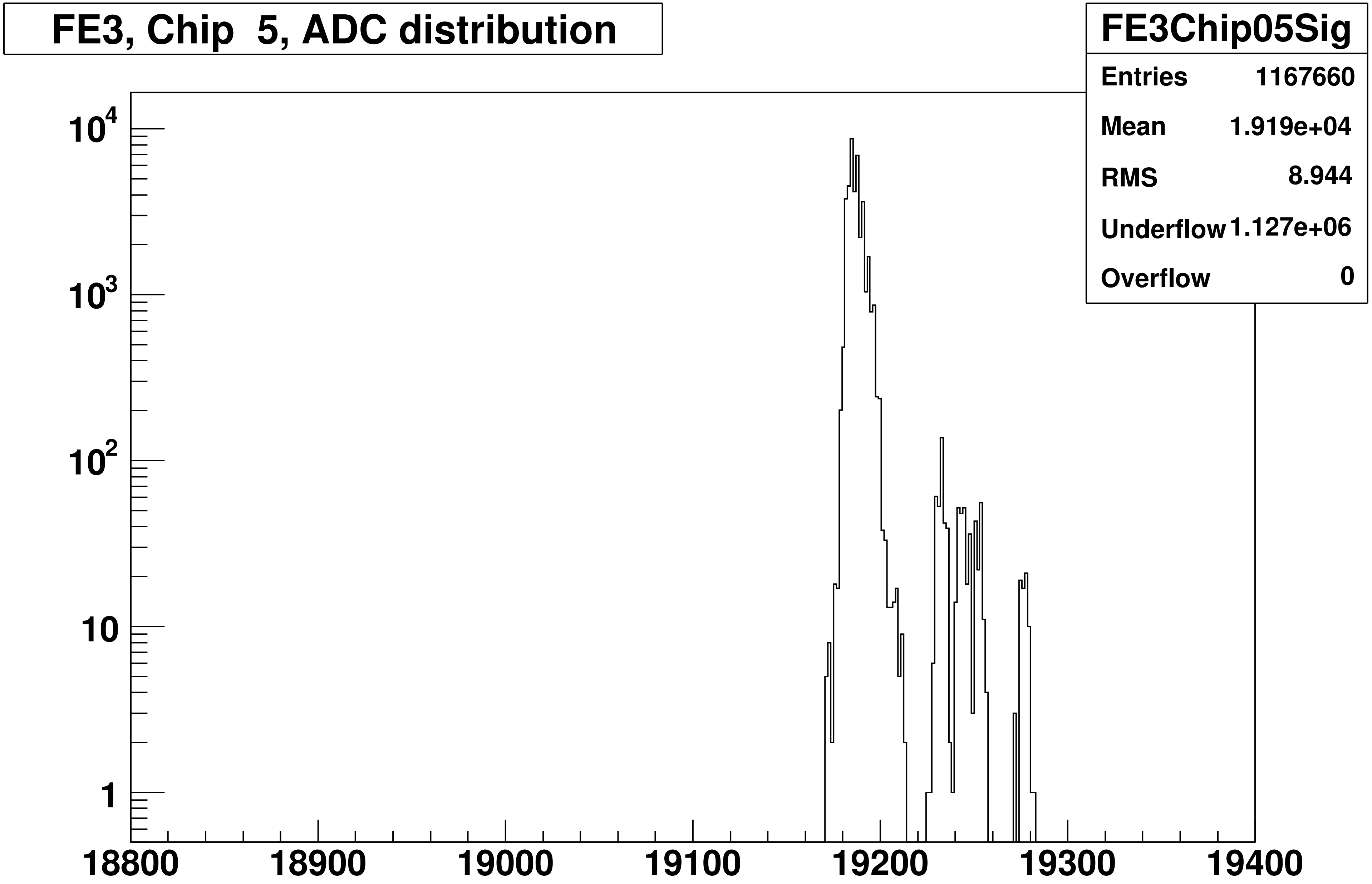
-40
-20
0
20
40
60
80
100


FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 1167660
Mean 1.919e+04
RMS 8.944
Underflow 1.127e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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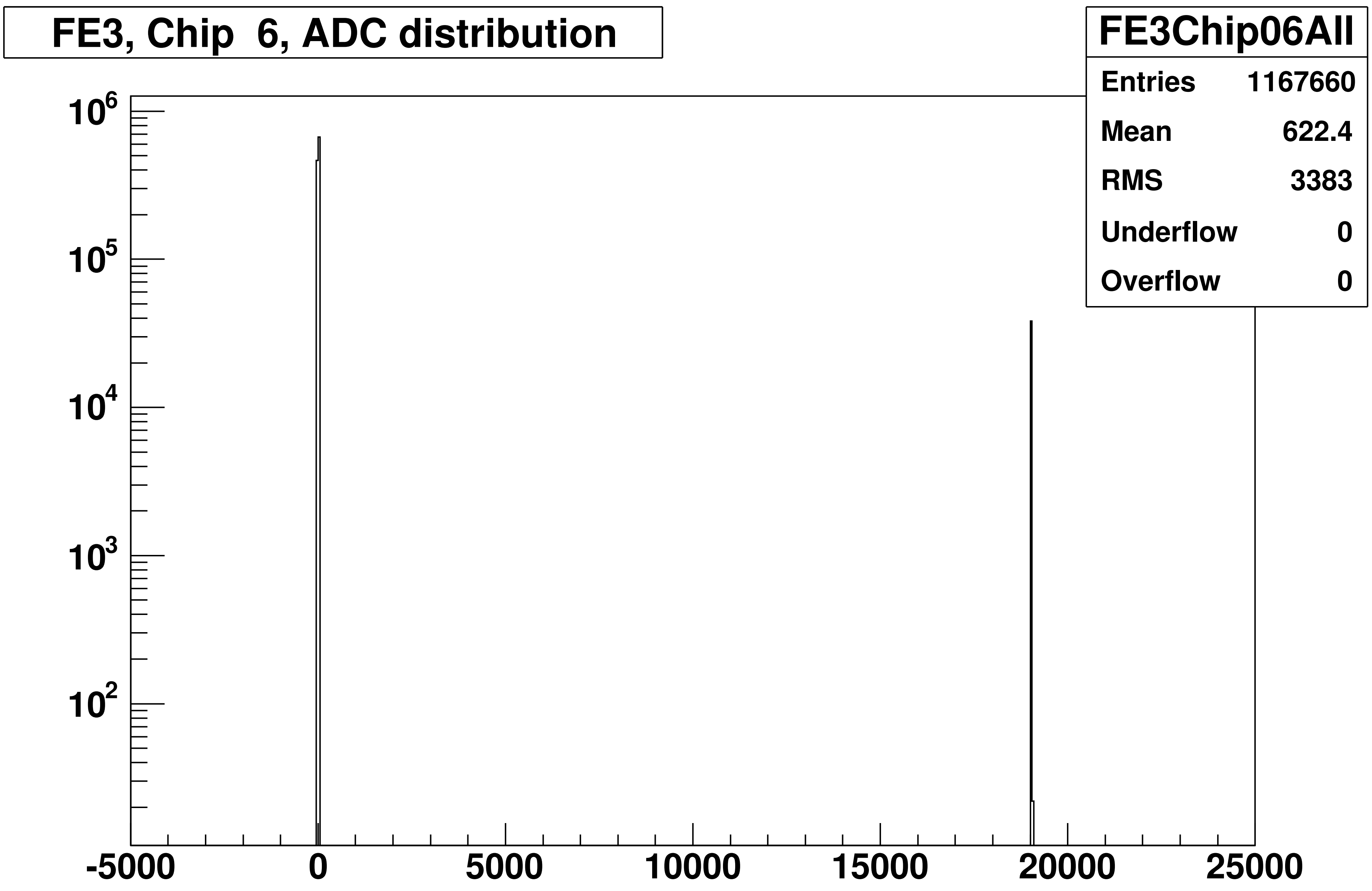
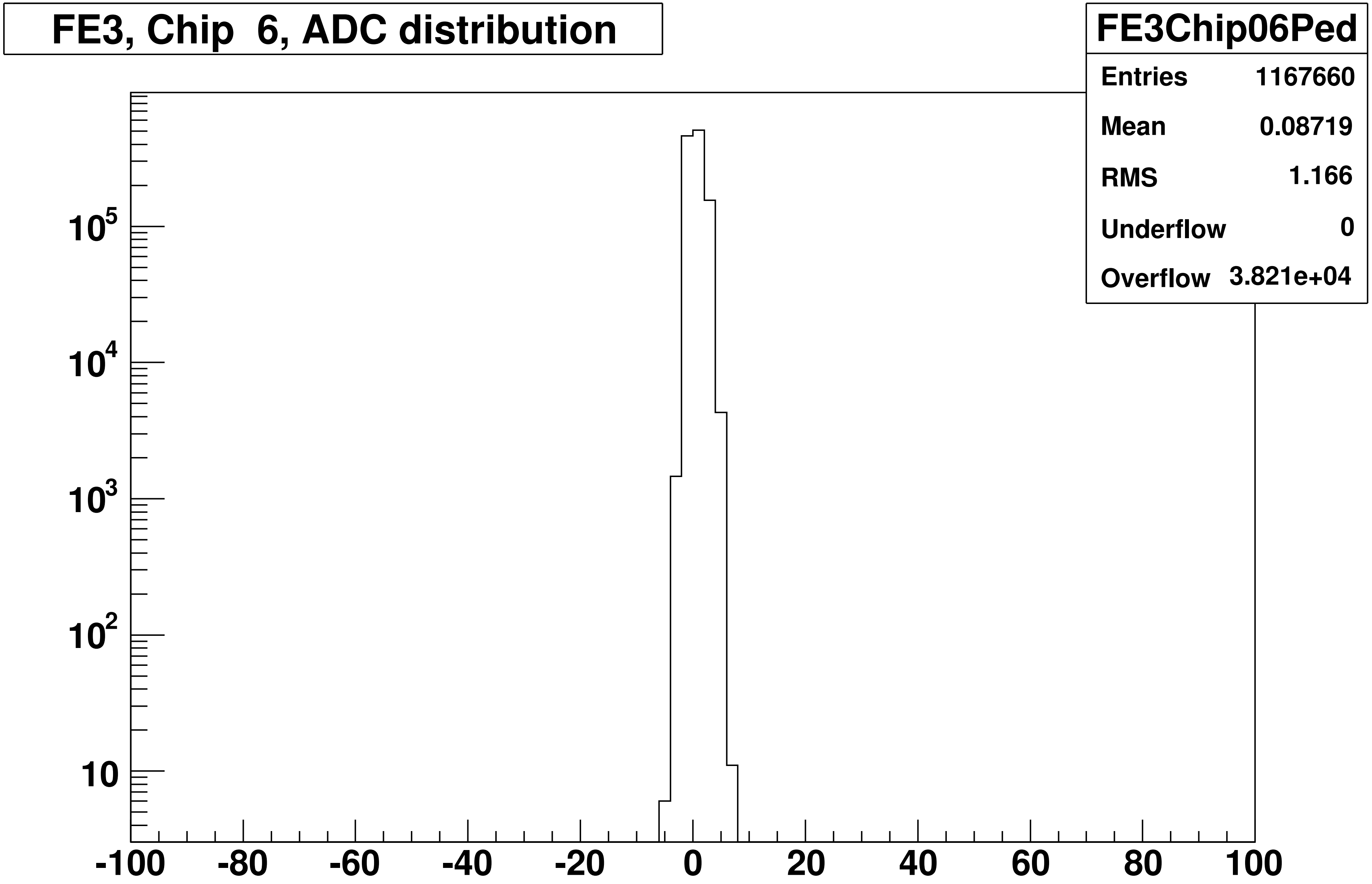
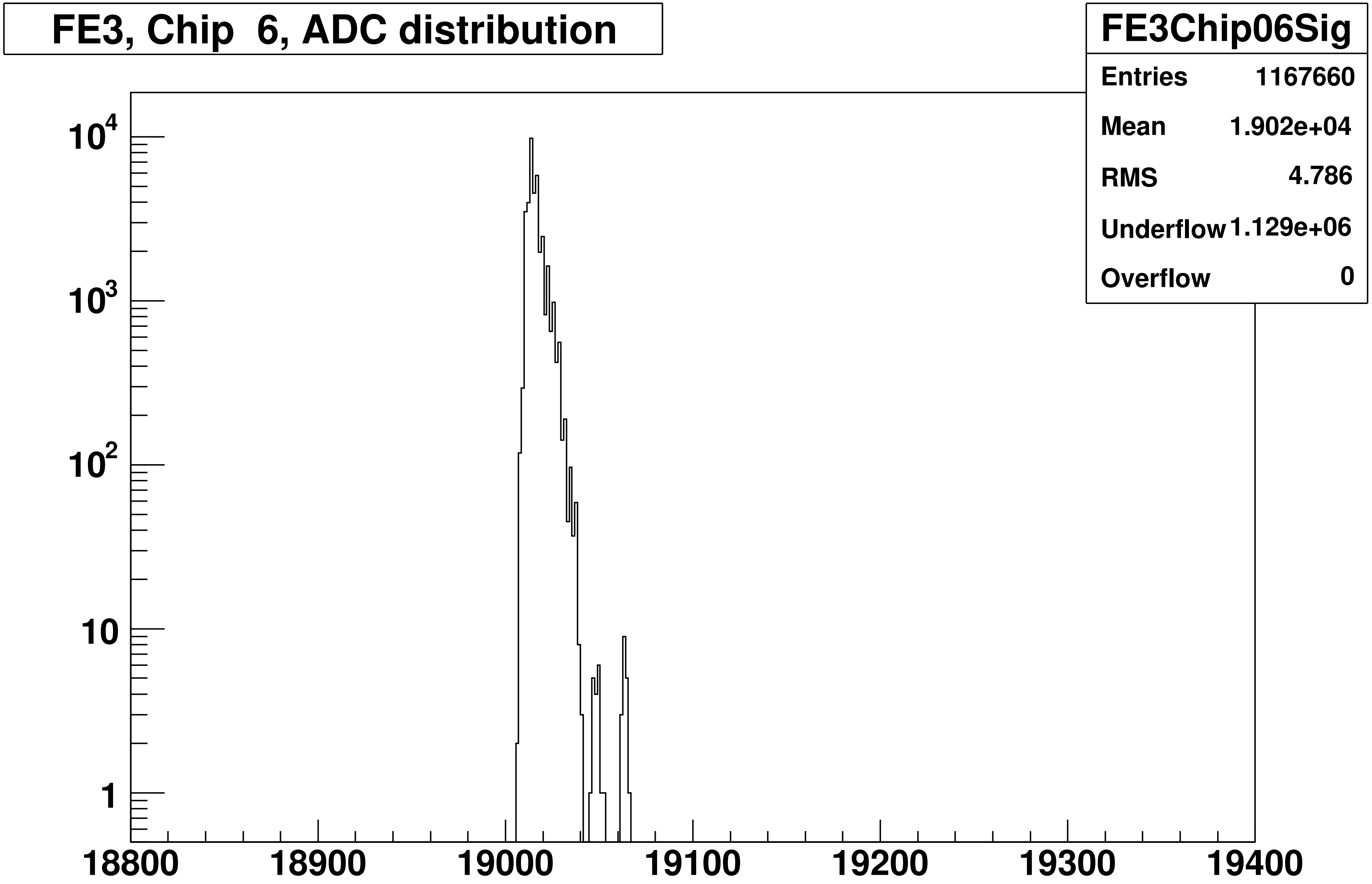
19200
19300
19400


FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 1167660
Mean 622.4
RMS 3383
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 1167660
Mean 0.08719
RMS 1.166
Underflow 0
Overflow 3.821e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 1167660
Mean 1.902e+04
RMS 4.786
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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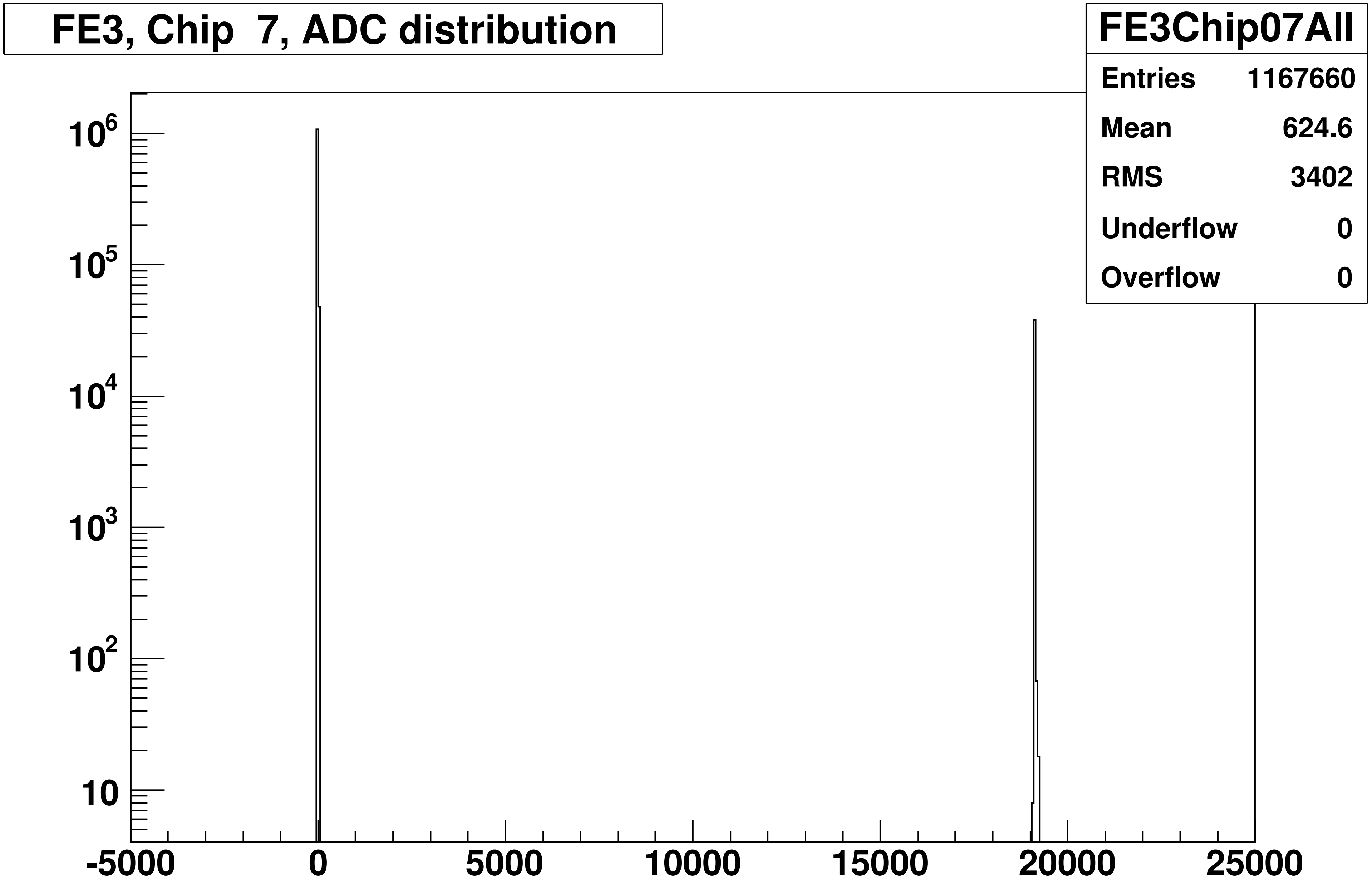
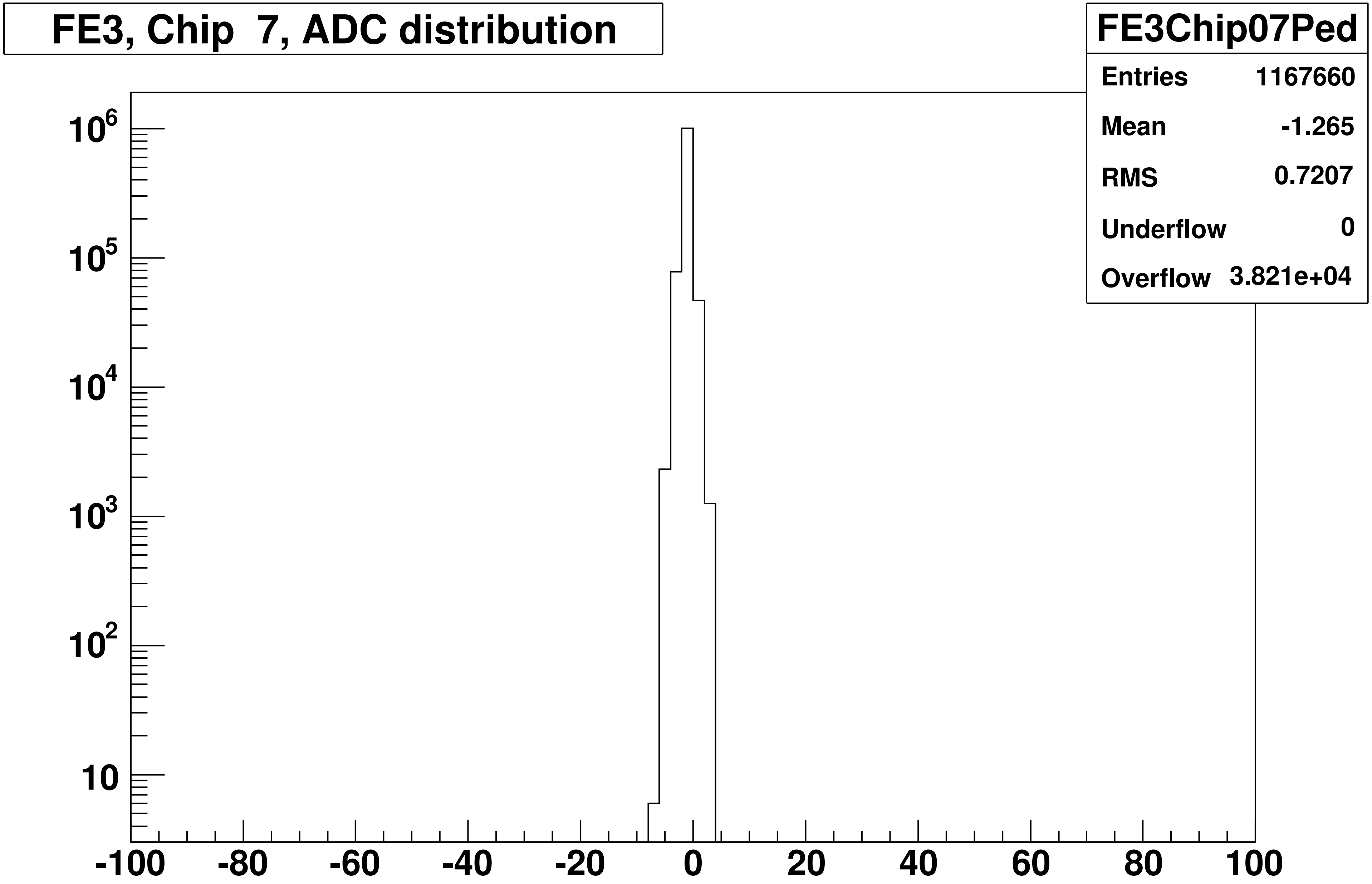
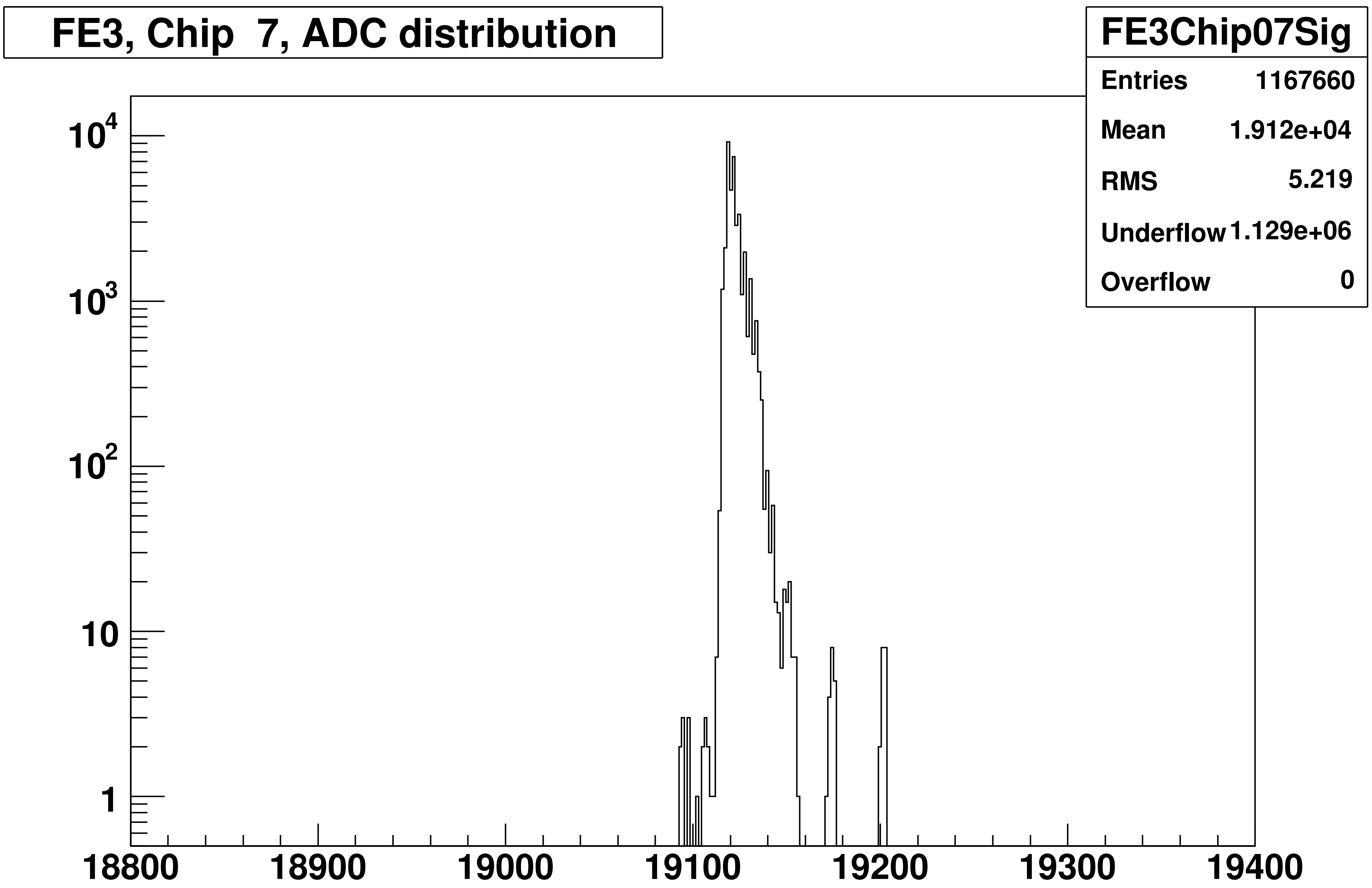
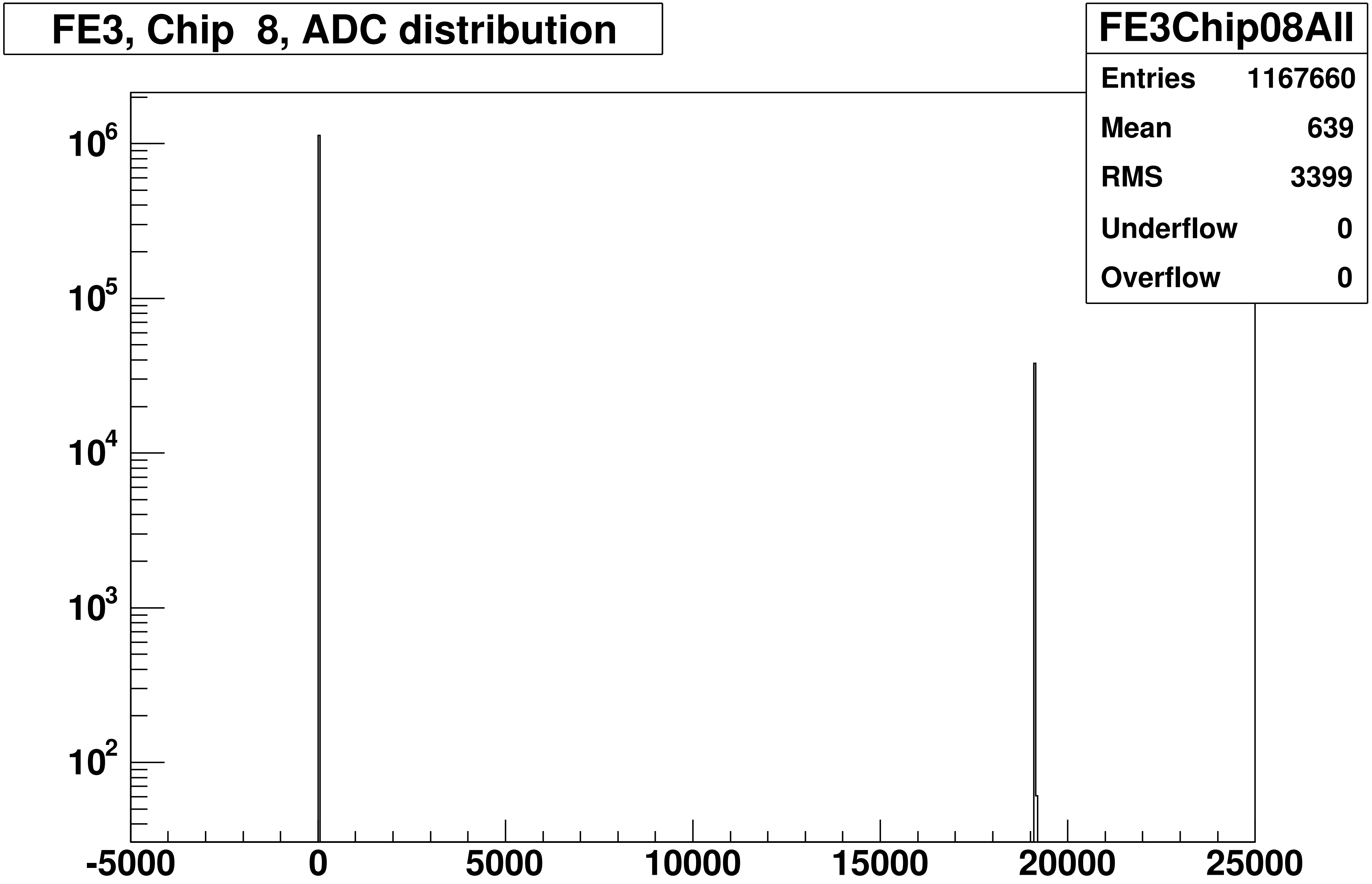
19300
19400


FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 1167660
Mean 624.6
RMS 3402
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

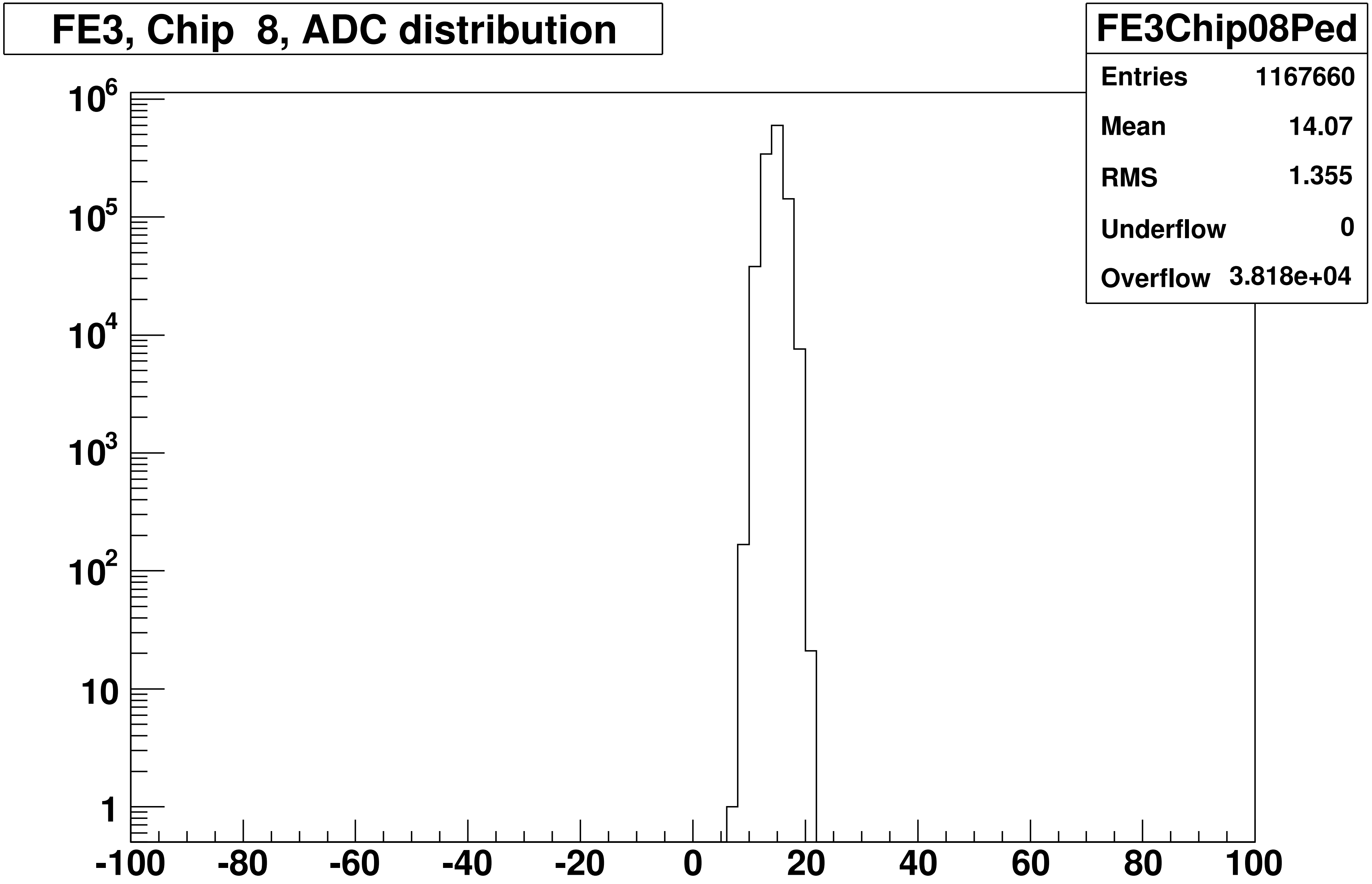


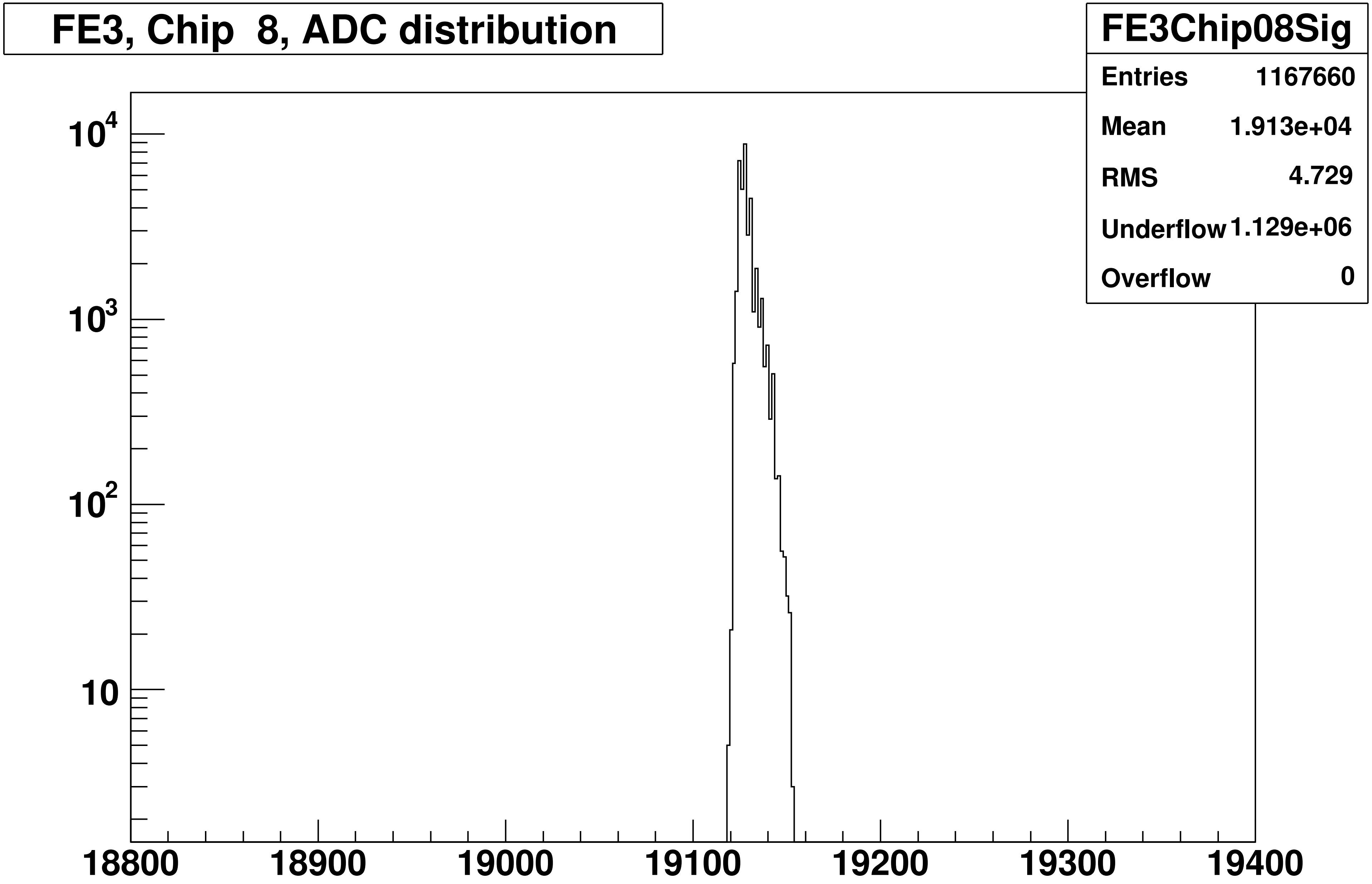
FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 1167660
Mean -1.265
RMS 0.7207
Underflow 0
Overflow 3.821e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 1167660
Mean 1.912e+04
RMS 5.219
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

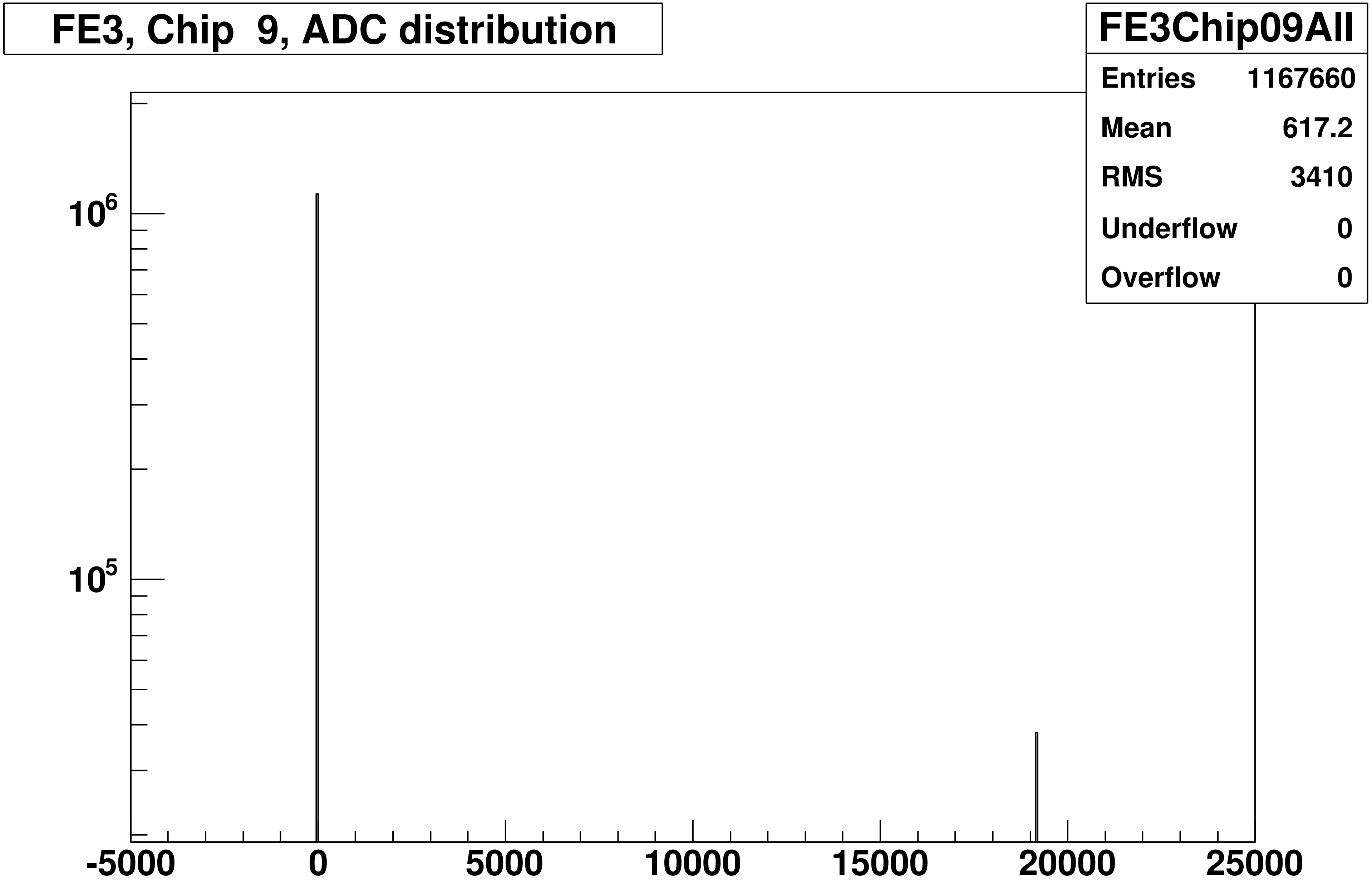


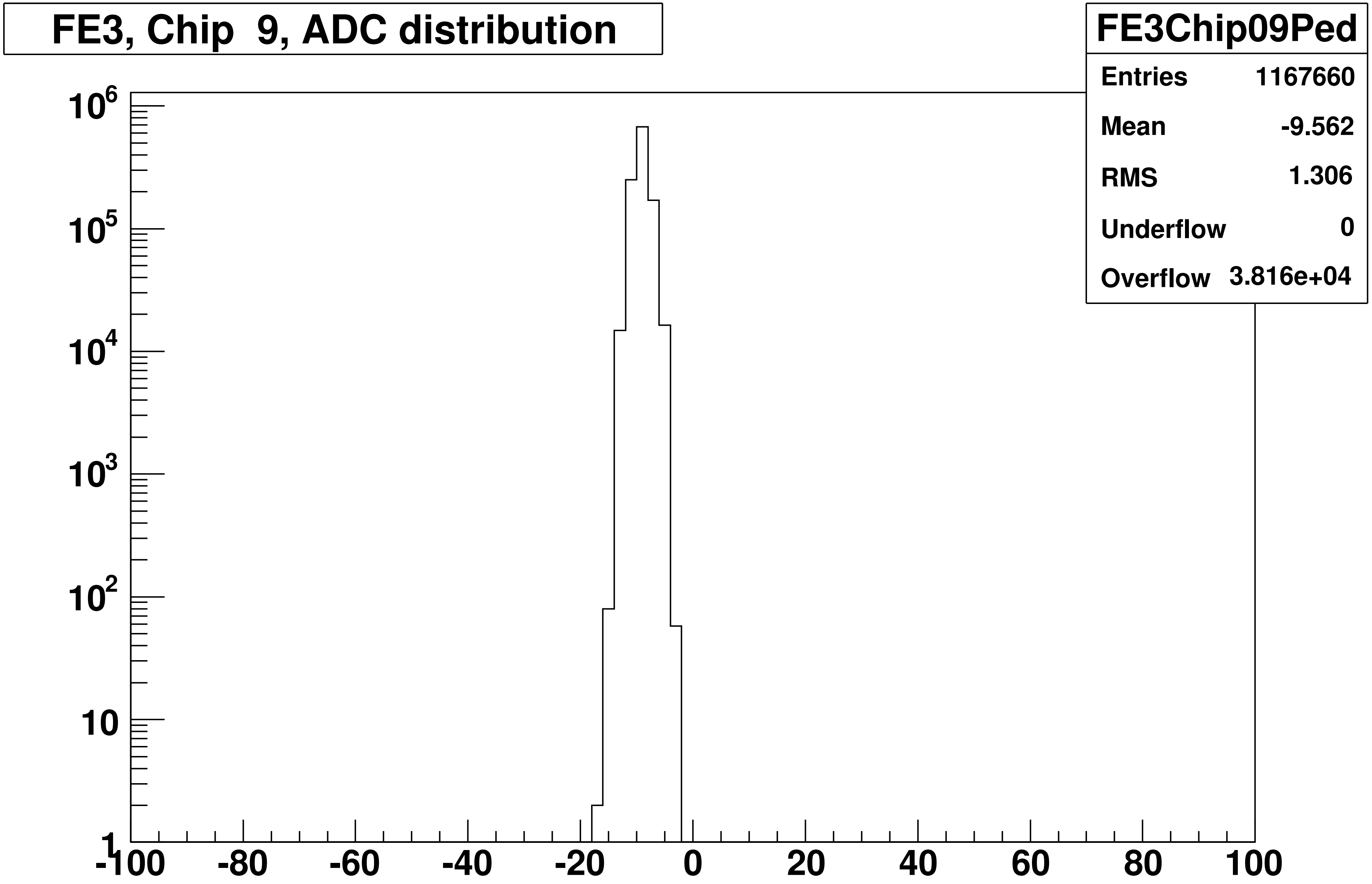
FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 1167660
Mean 639
RMS 3399
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 1167660
Mean 14.07
RMS 1.355
Underflow 0
Overflow 3.818e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

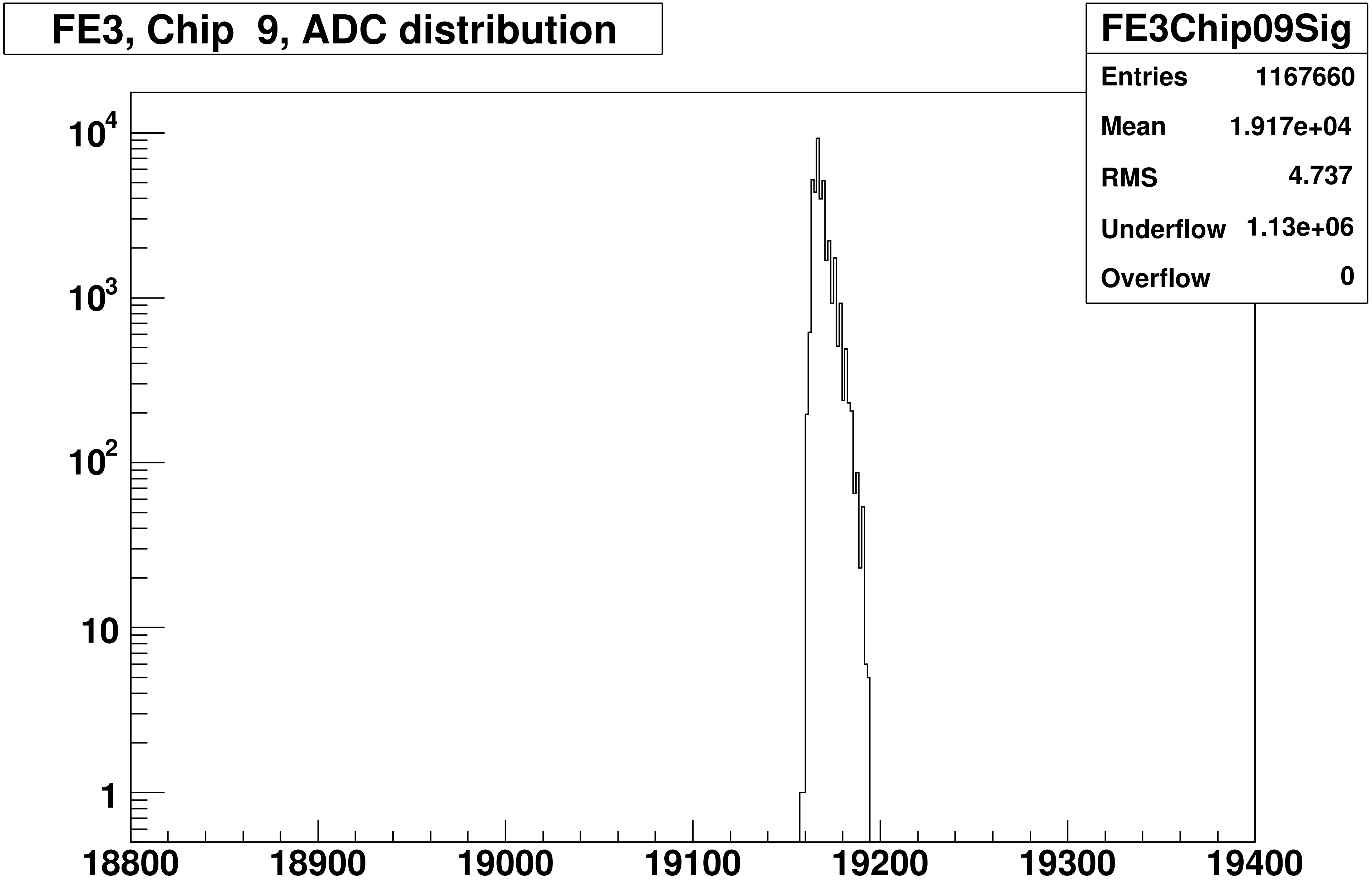


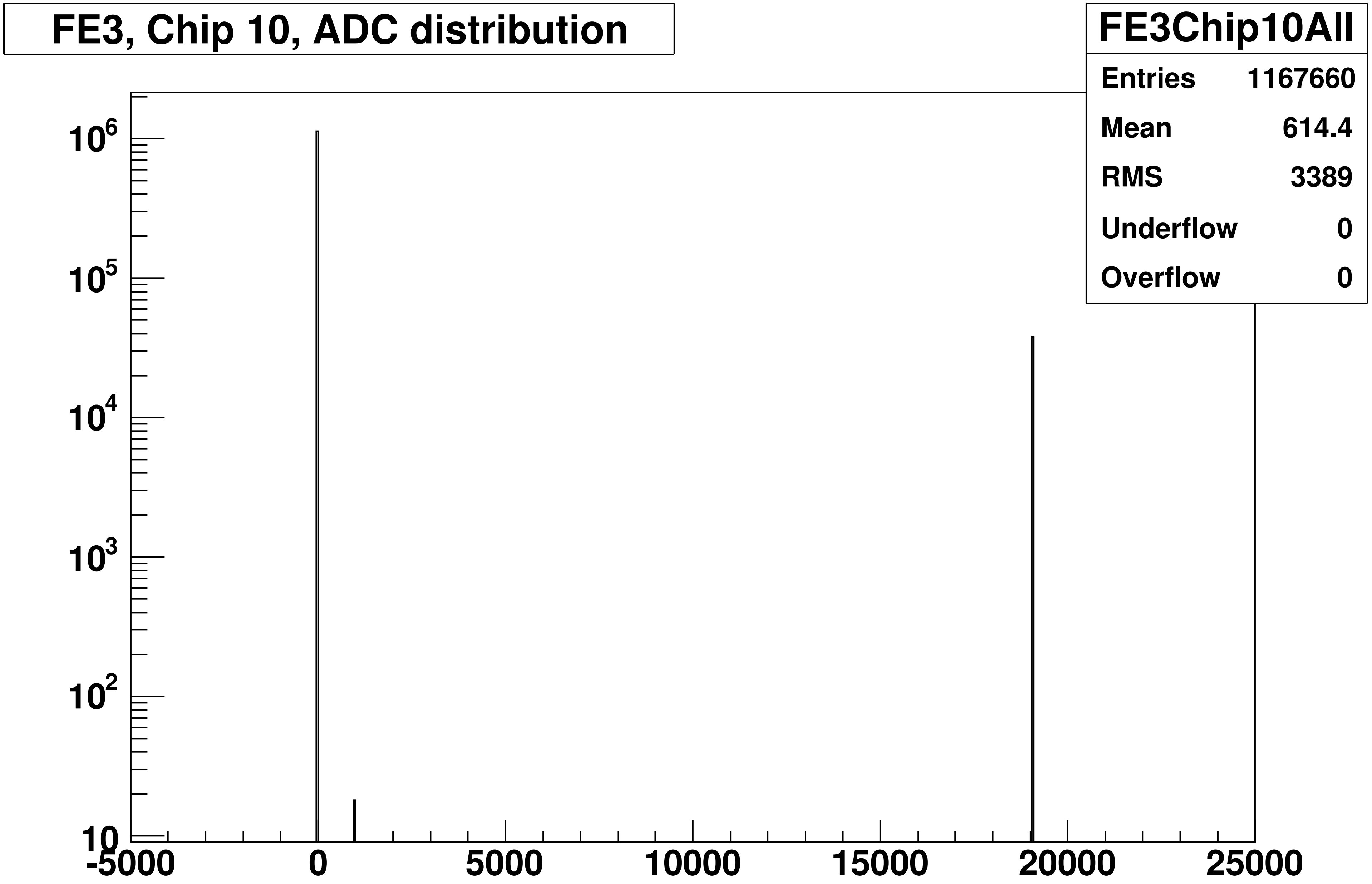
FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 1167660
Mean 1.913e+04
RMS 4.729
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 1167660
Mean 617.2
RMS 3410
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000

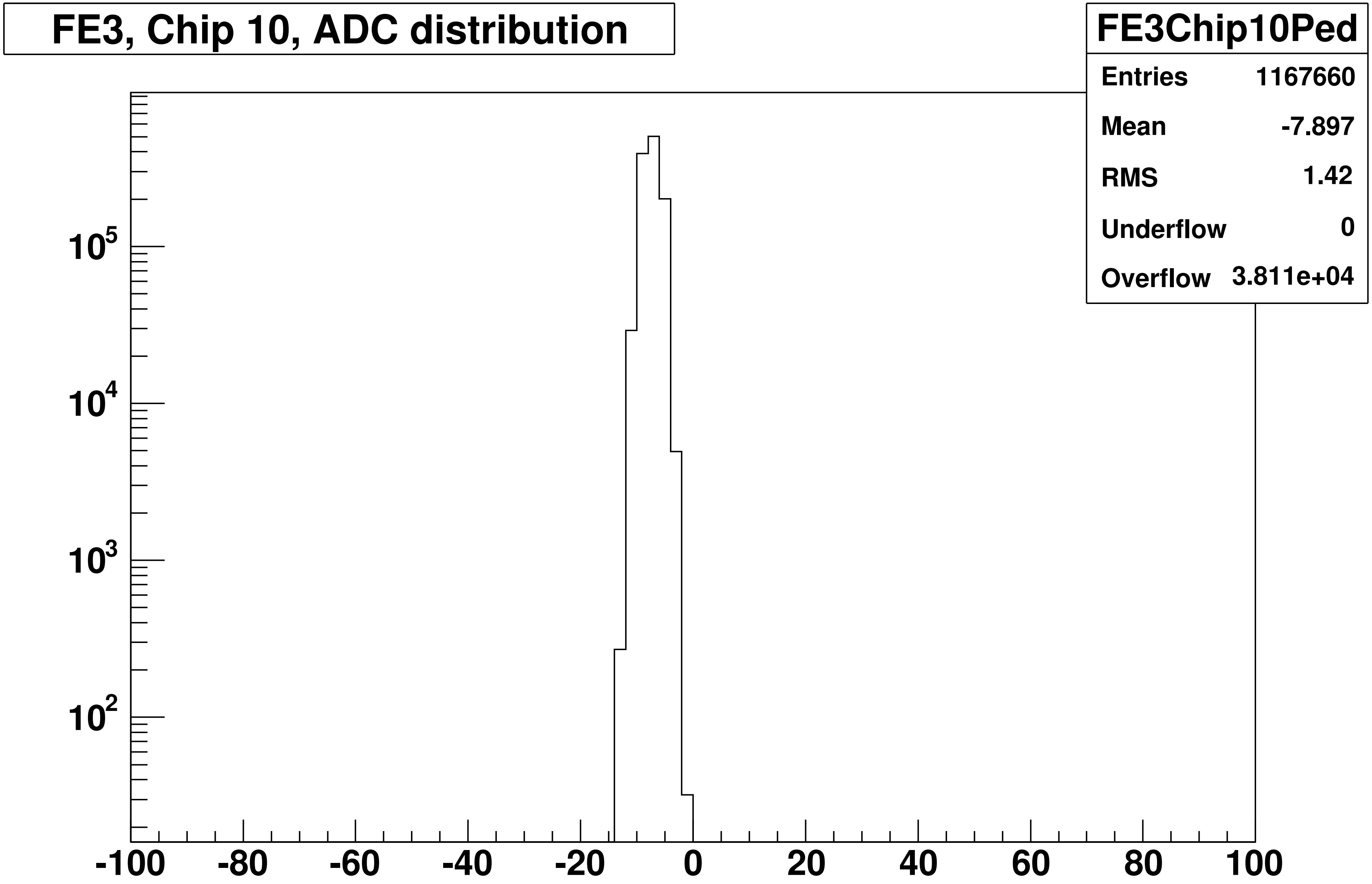


FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 1167660
Mean -9.562
RMS 1.306
Underflow 0
Overflow 3.816e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

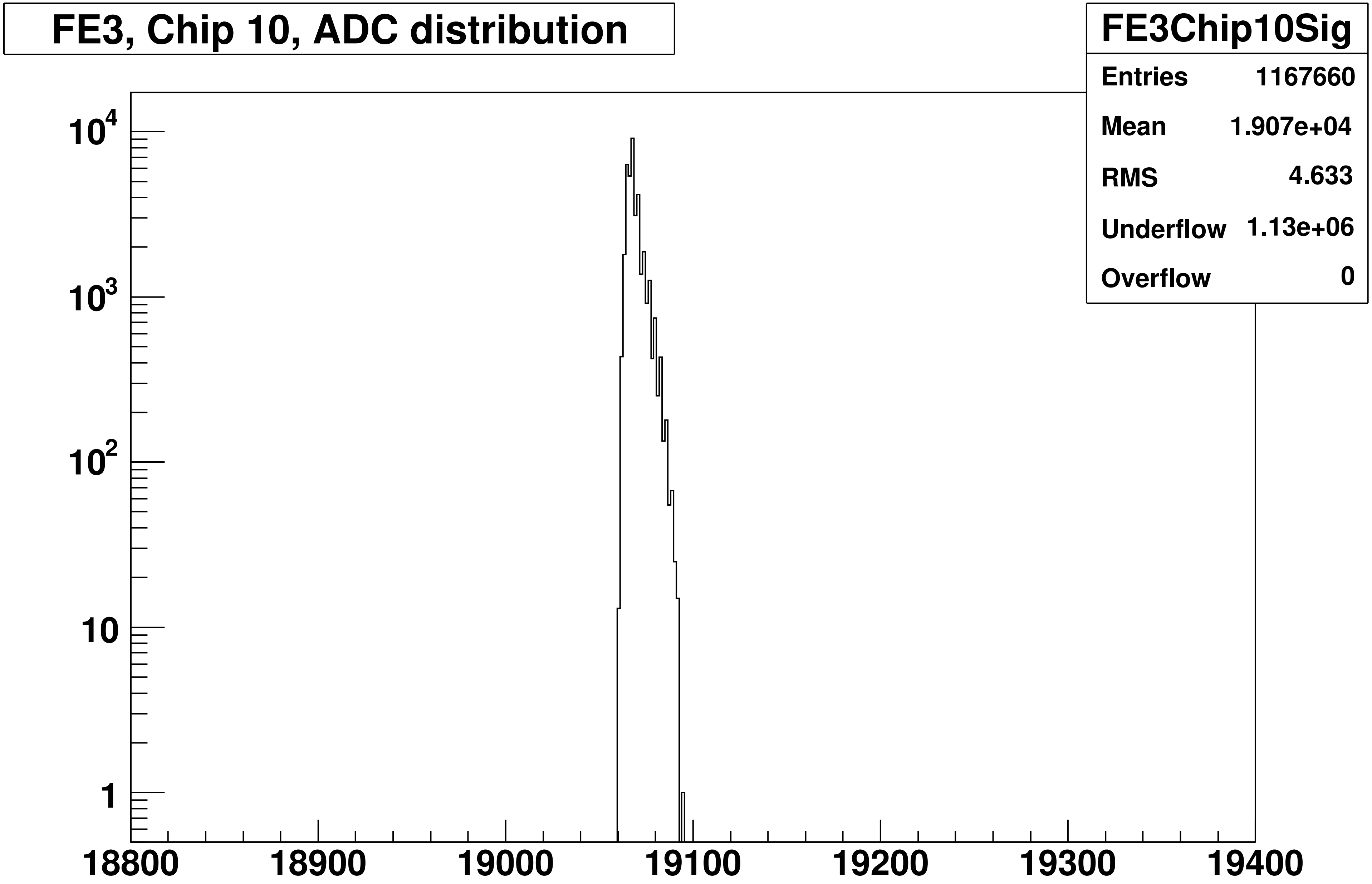

FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 1167660
Mean 1.917e+04
RMS 4.737
Underflow 1.13e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

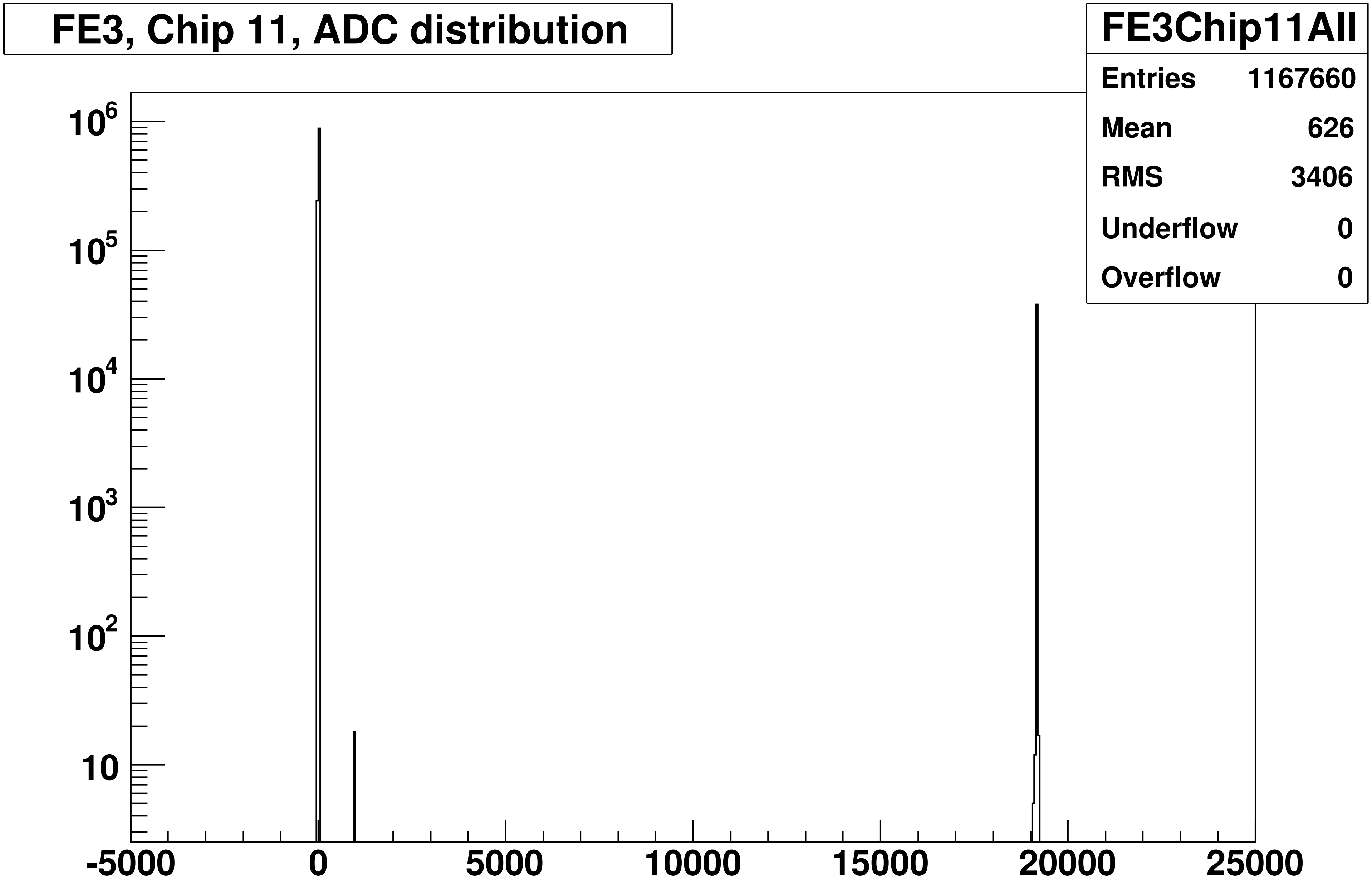


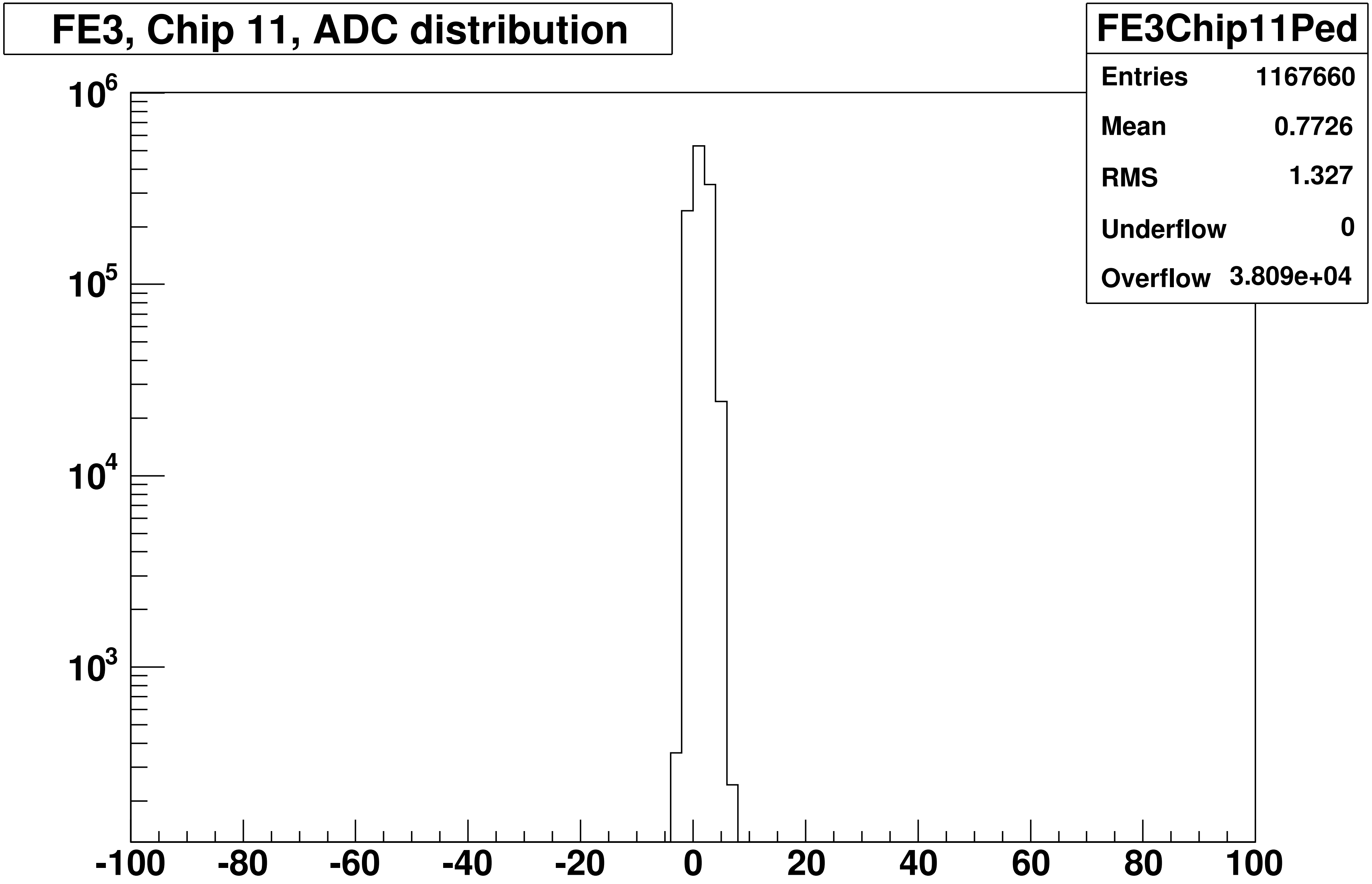
FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 1167660
Mean 614.4
RMS 3389
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000



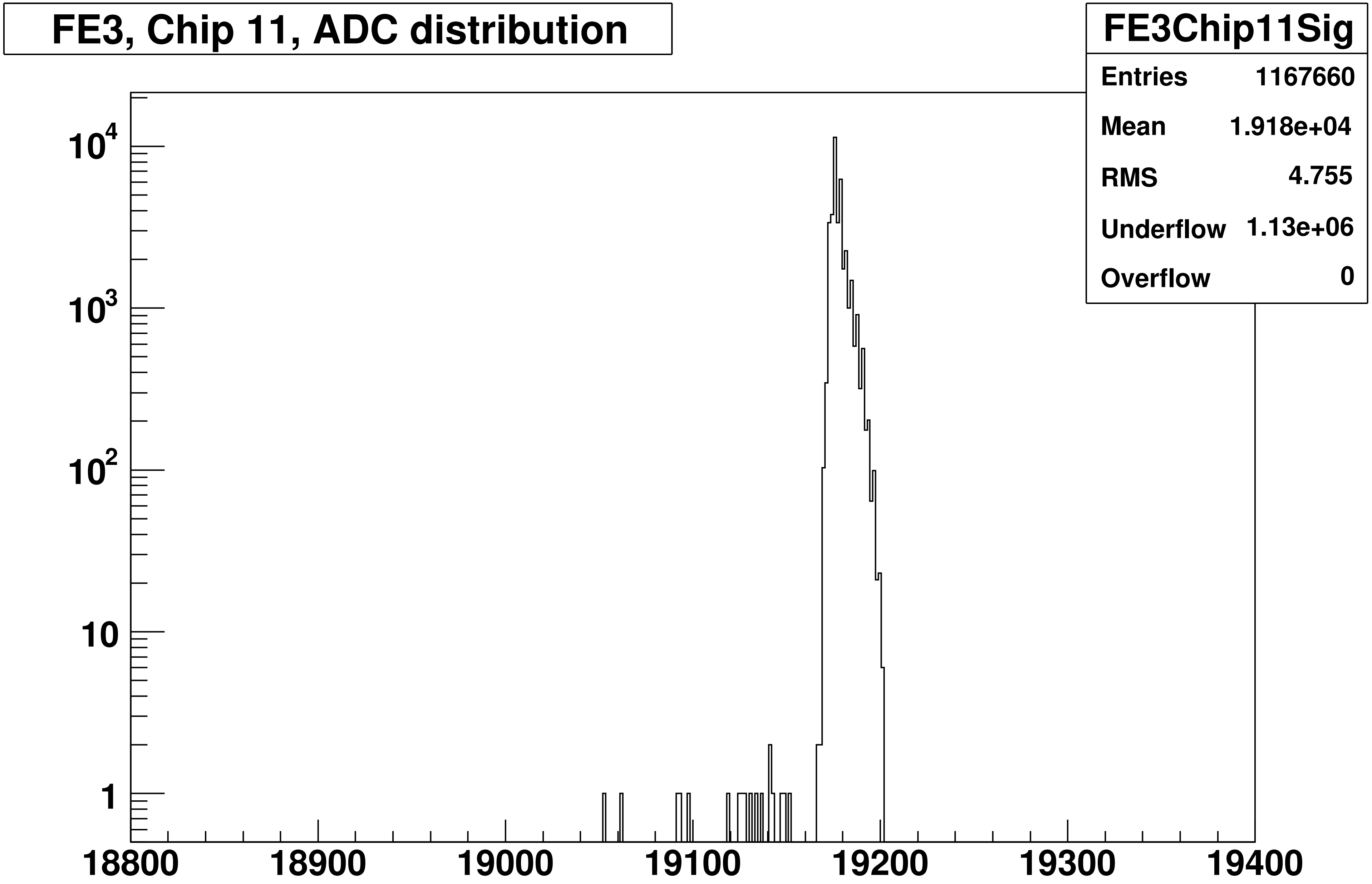
FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 1167660
Mean -7.897
RMS 1.42
Underflow 0
Overflow 3.811e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 1167660
Mean 1.907e+04
RMS 4.633
Underflow 1.13e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

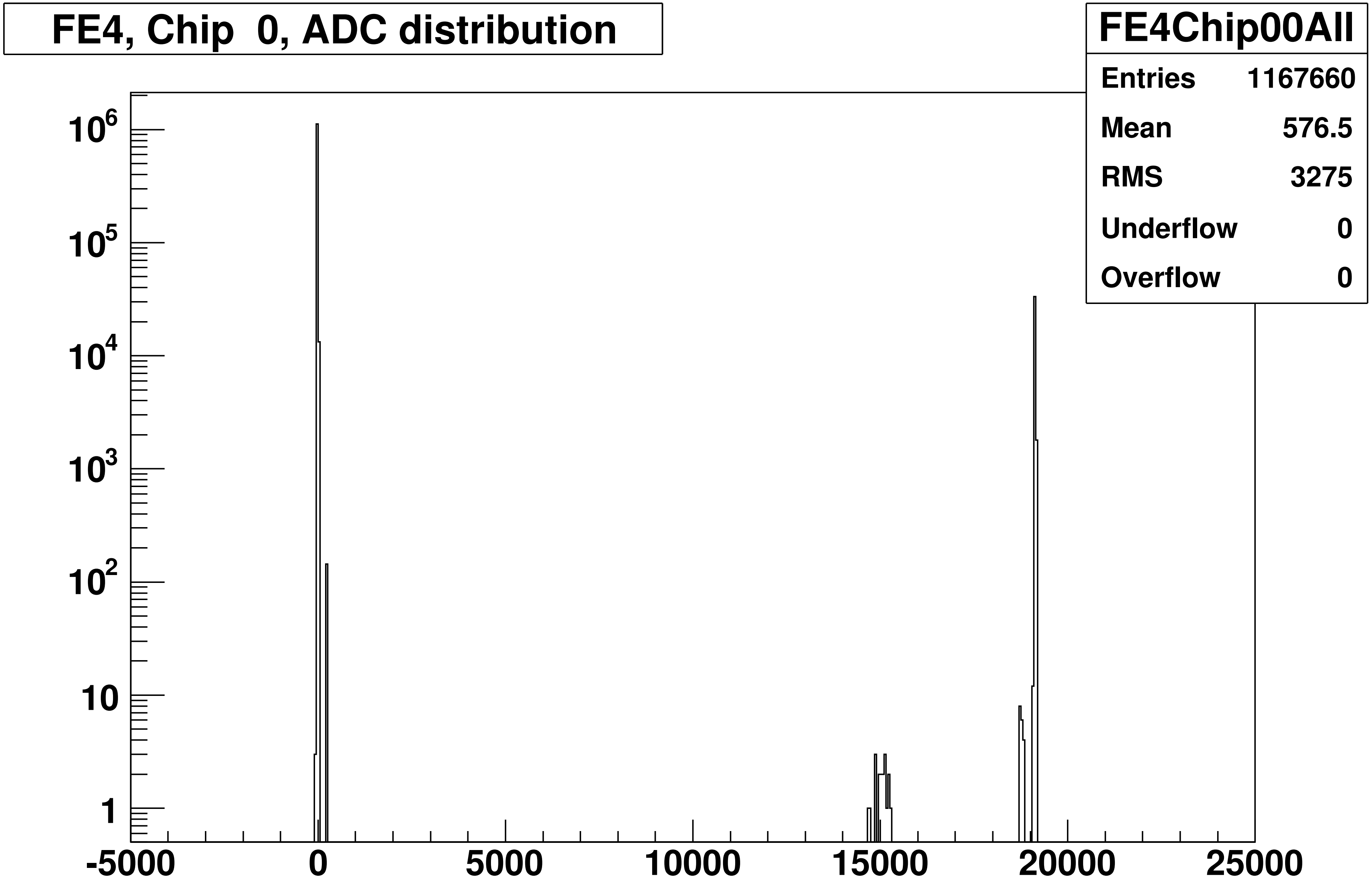

FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 1167660
Mean 626
RMS 3406
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



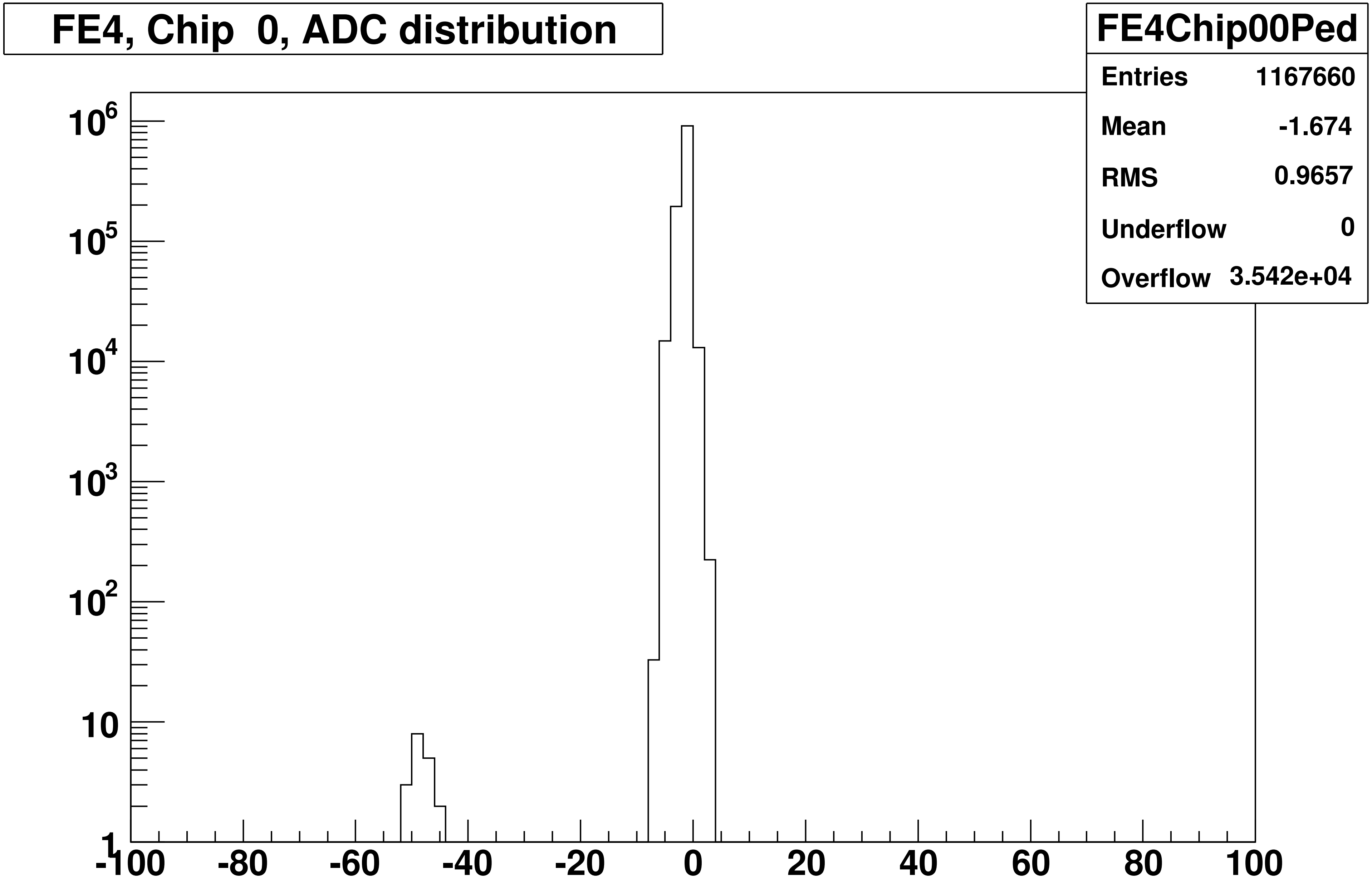
FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 1167660
Mean 0.7726
RMS 1.327
Underflow 0
Overflow 3.809e+04
10^6
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100

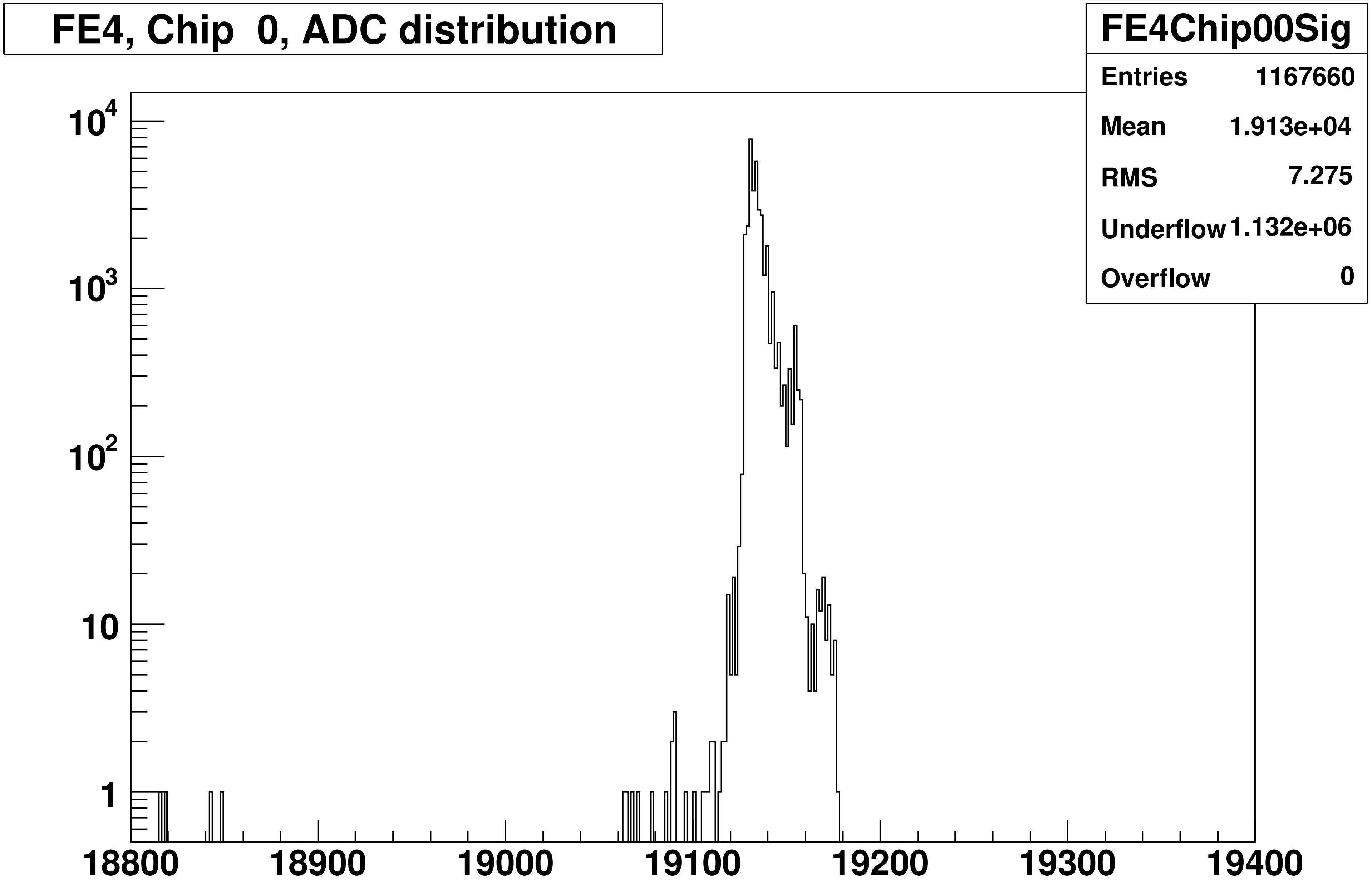


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 1167660
Mean 1.918e+04
RMS 4.755
Underflow 1.13e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

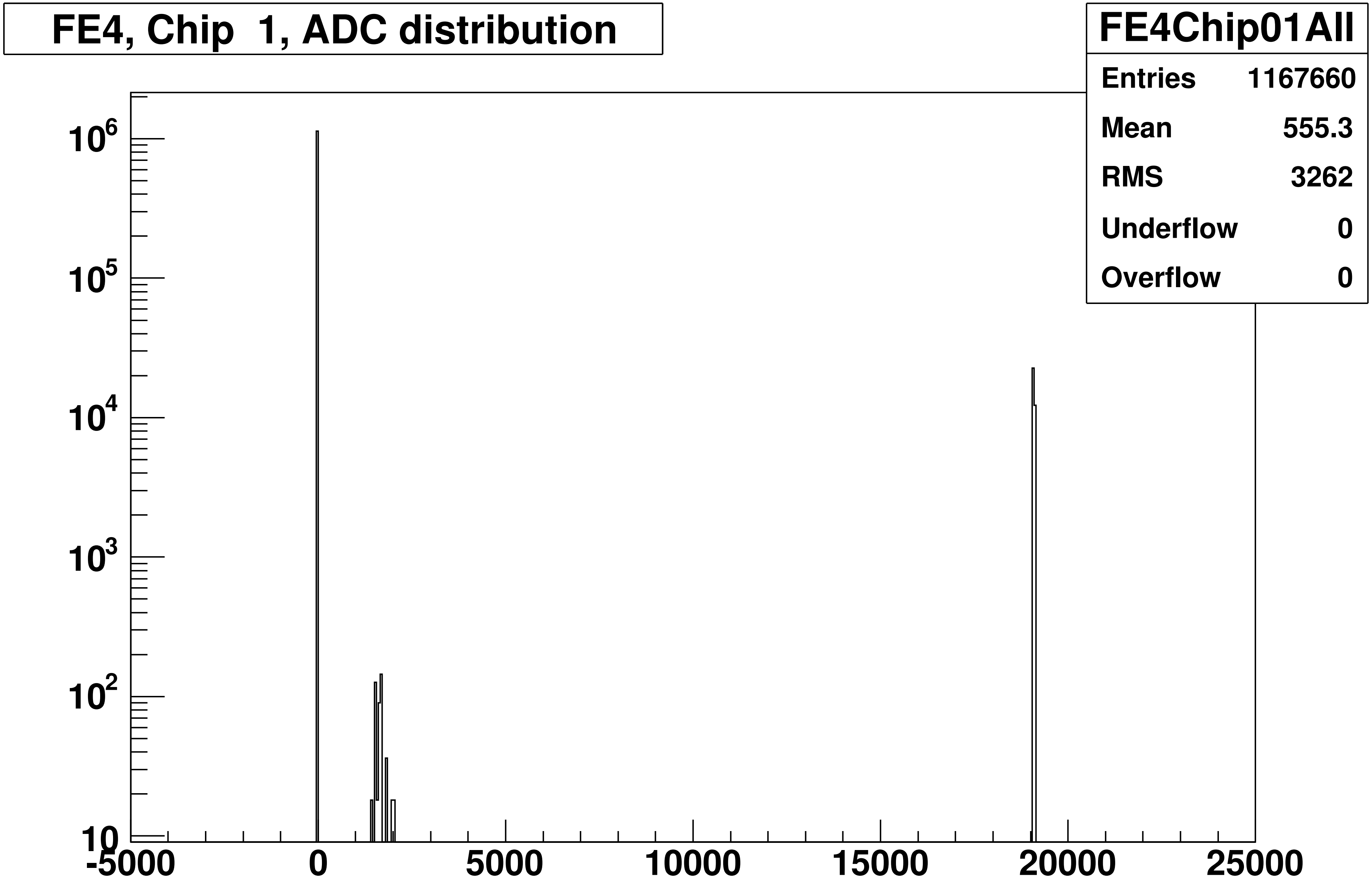


FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 1167660
Mean 576.5
RMS 3275
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 1167660
Mean -1.674
RMS 0.9657
Underflow 0
Overflow 3.542e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 1167660
Mean 1.913e+04
RMS 7.275
Underflow 1.132e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 1167660
Mean 555.3
RMS 3262
Underflow 0
Overflow 0
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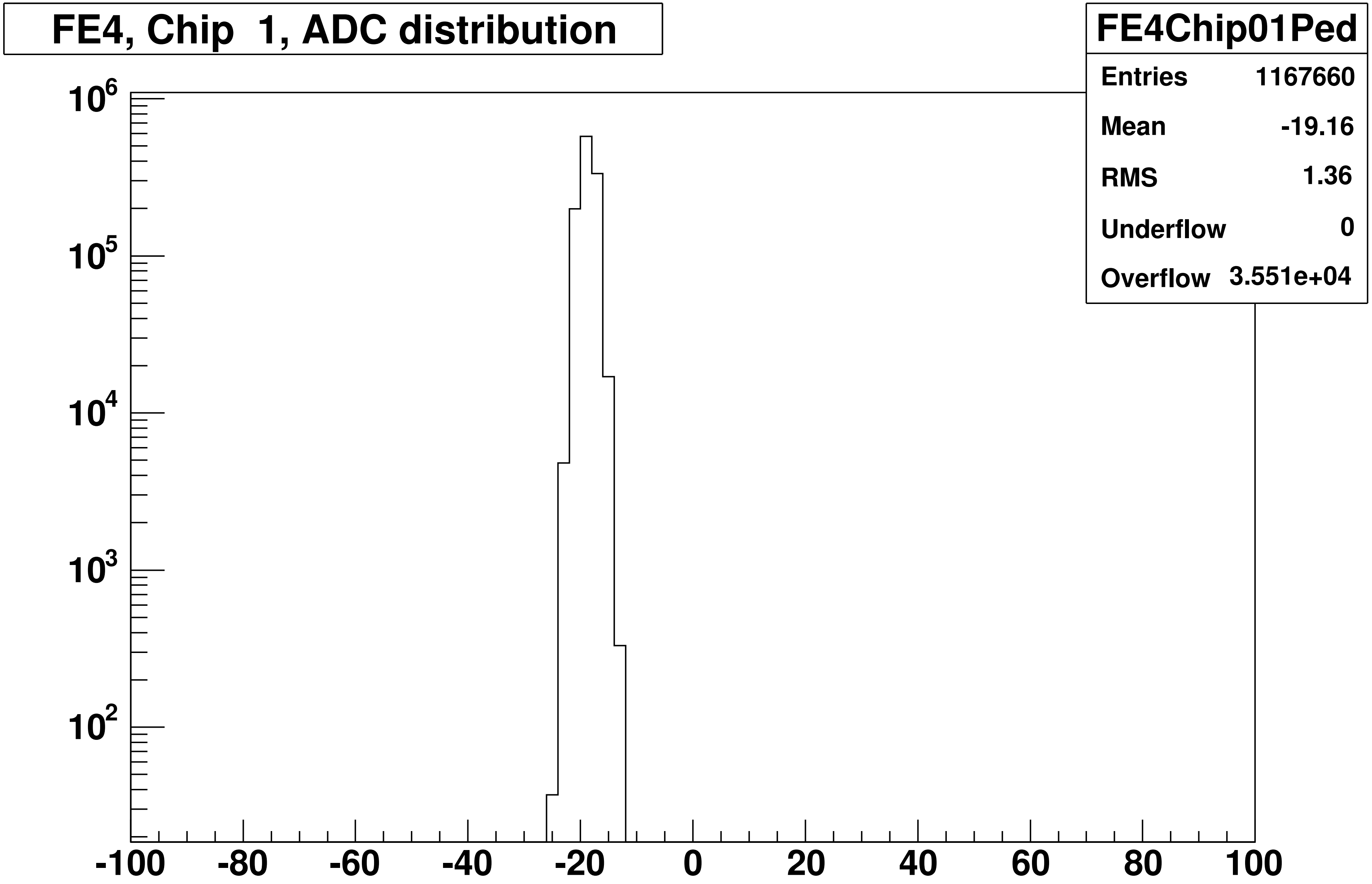
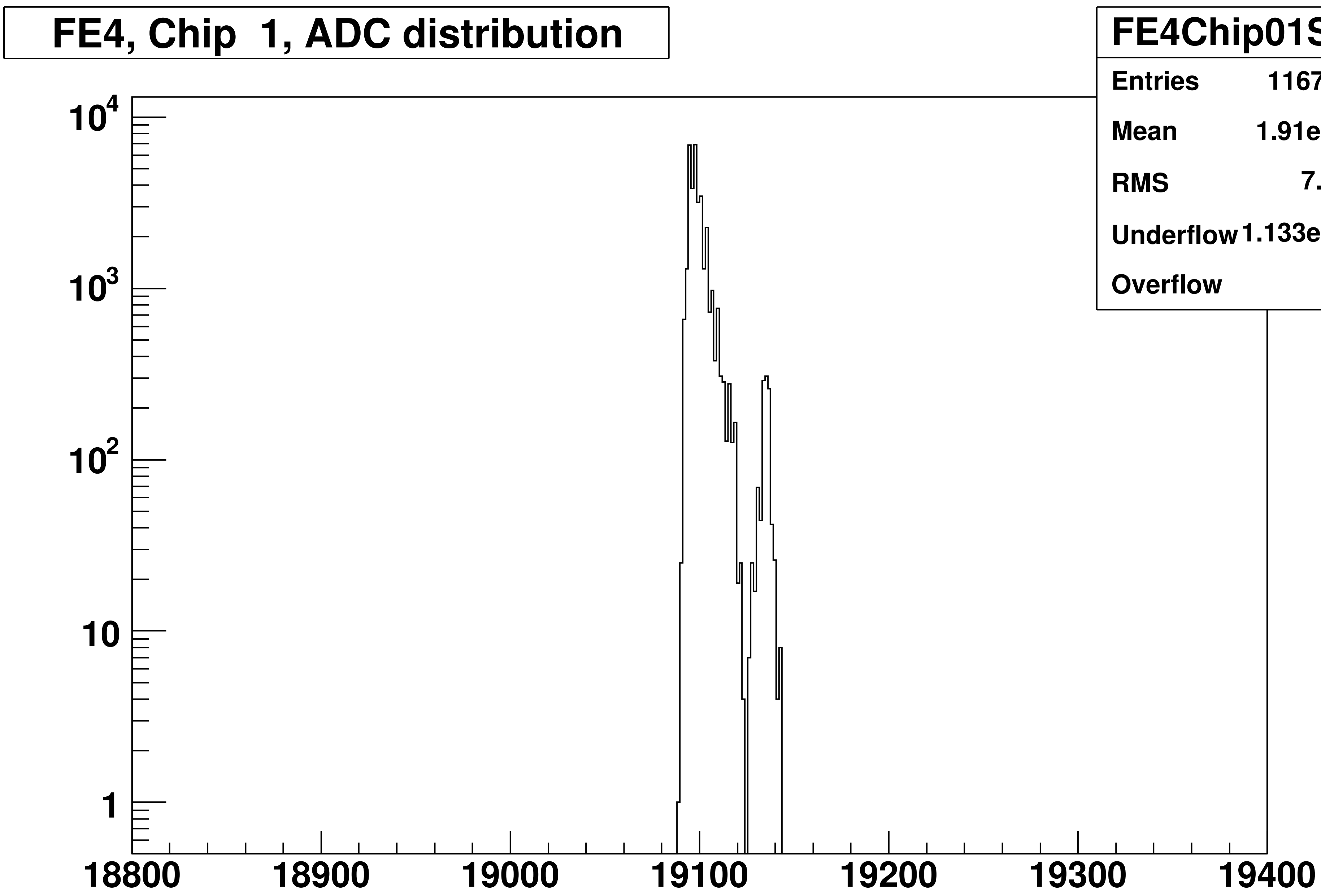
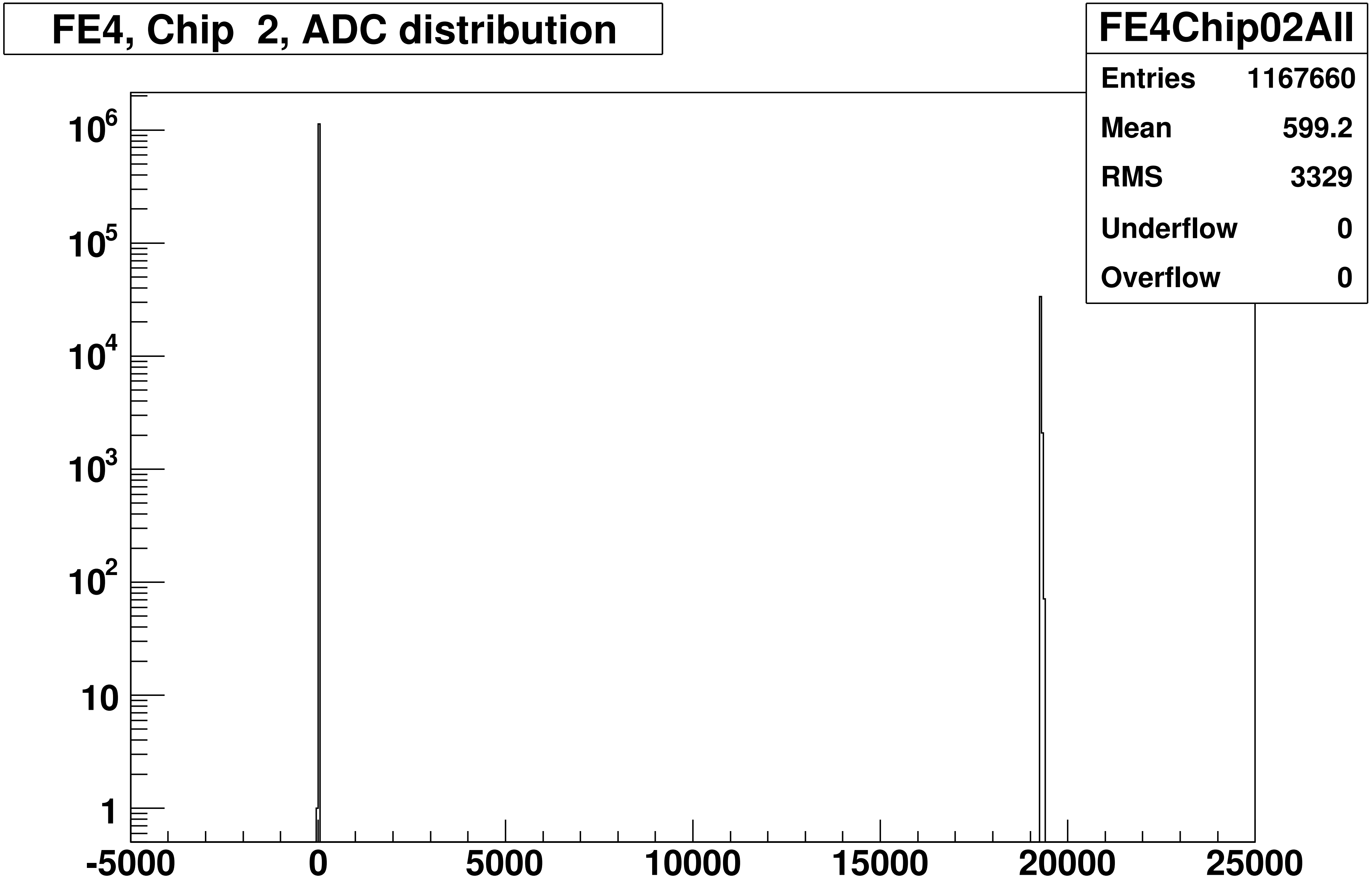
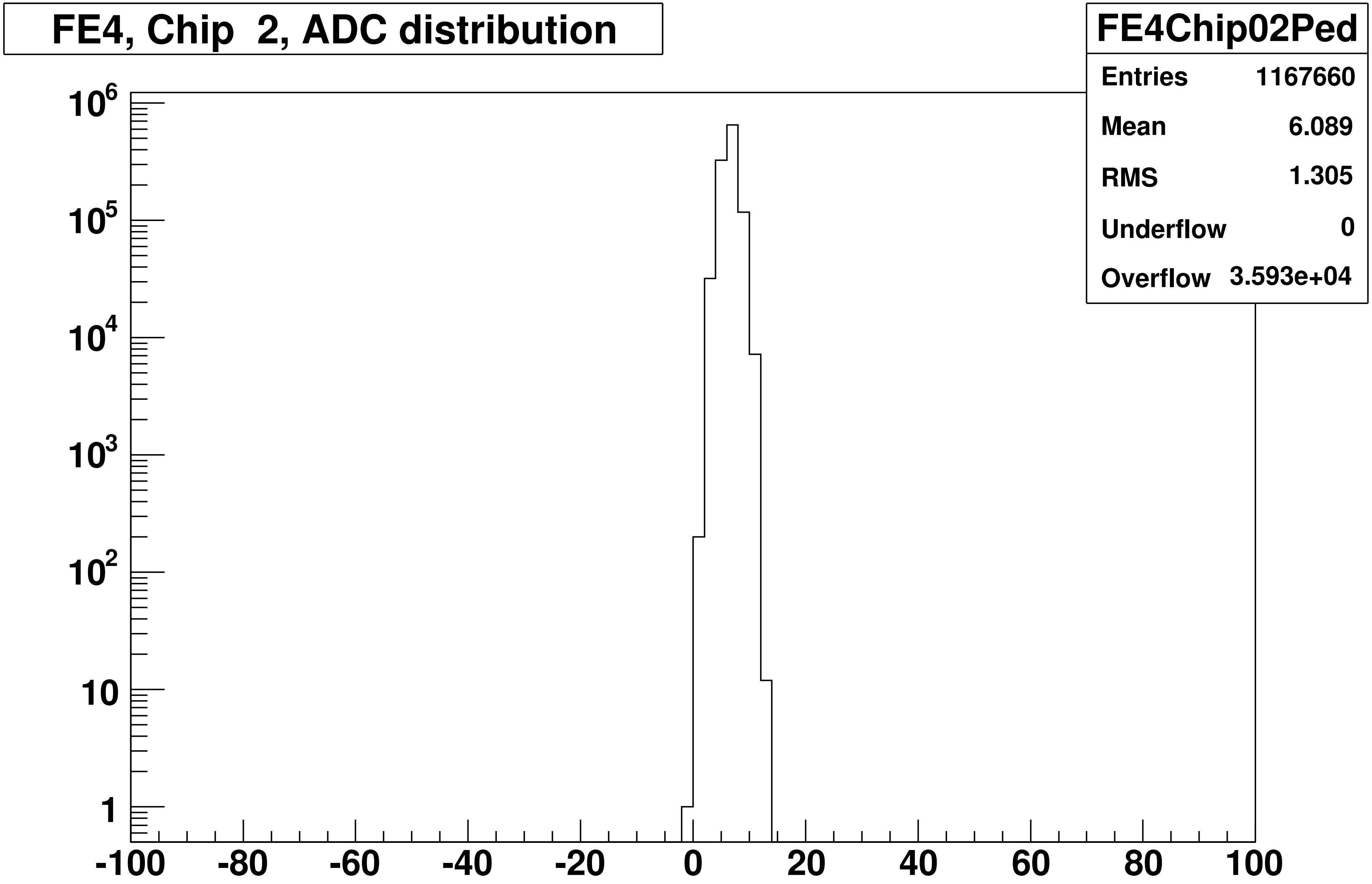
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



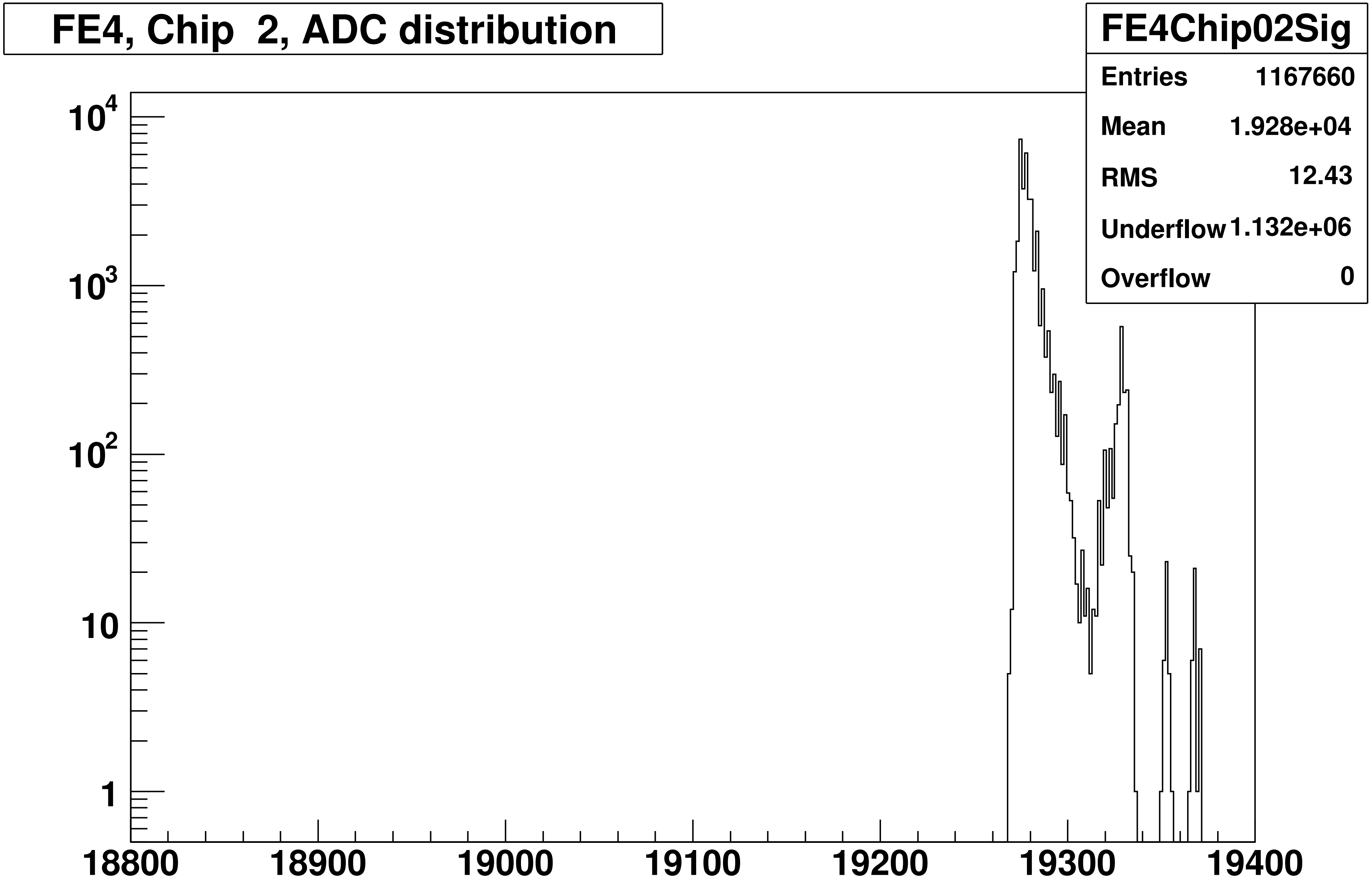
FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 1167660
Mean -19.16
RMS 1.36
Underflow 0
Overflow 3.551e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 1167660
Mean 1.91e+04
RMS 7.936
Underflow 1.133e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

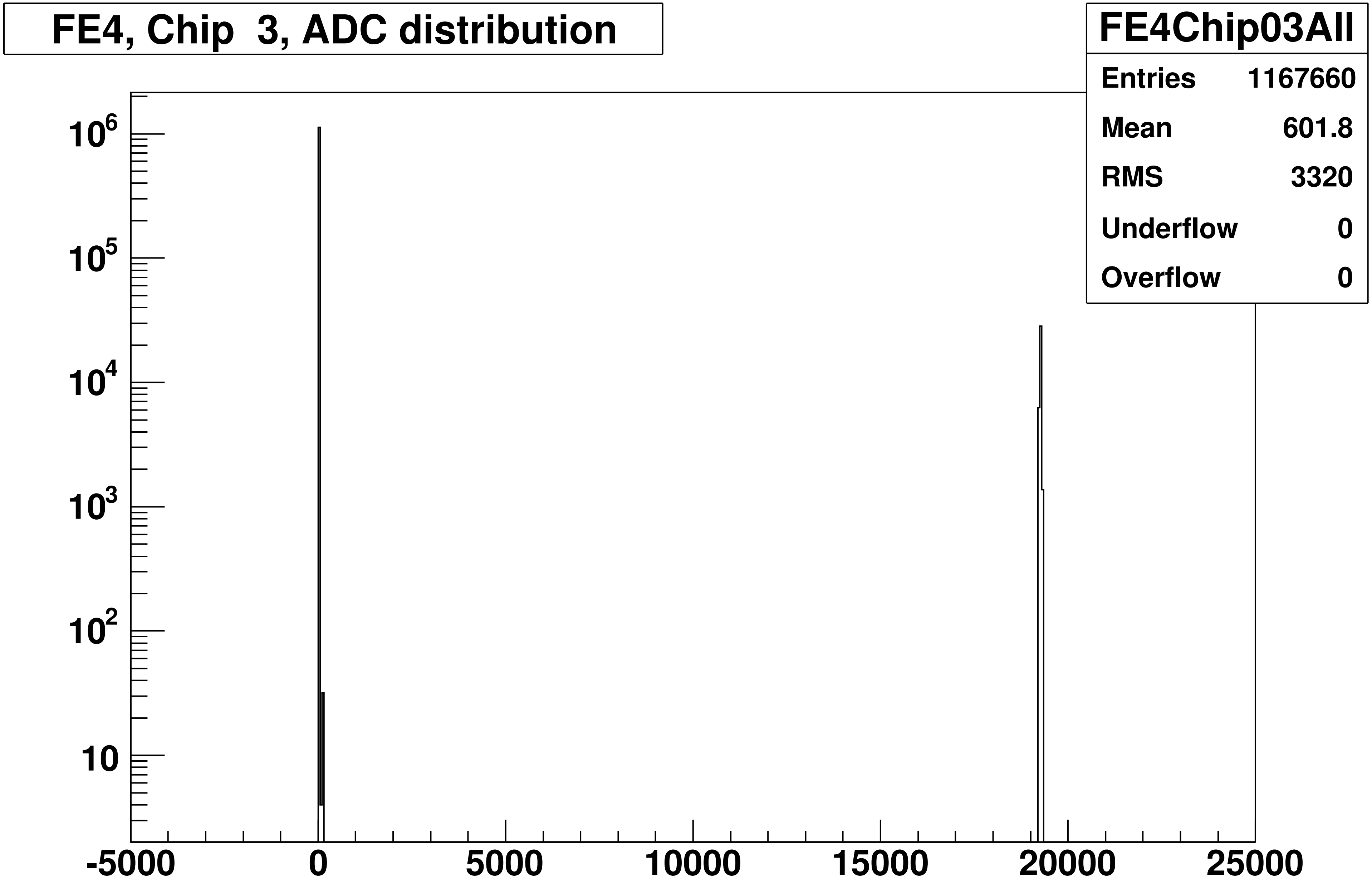

FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 1167660
Mean 599.2
RMS 3329
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



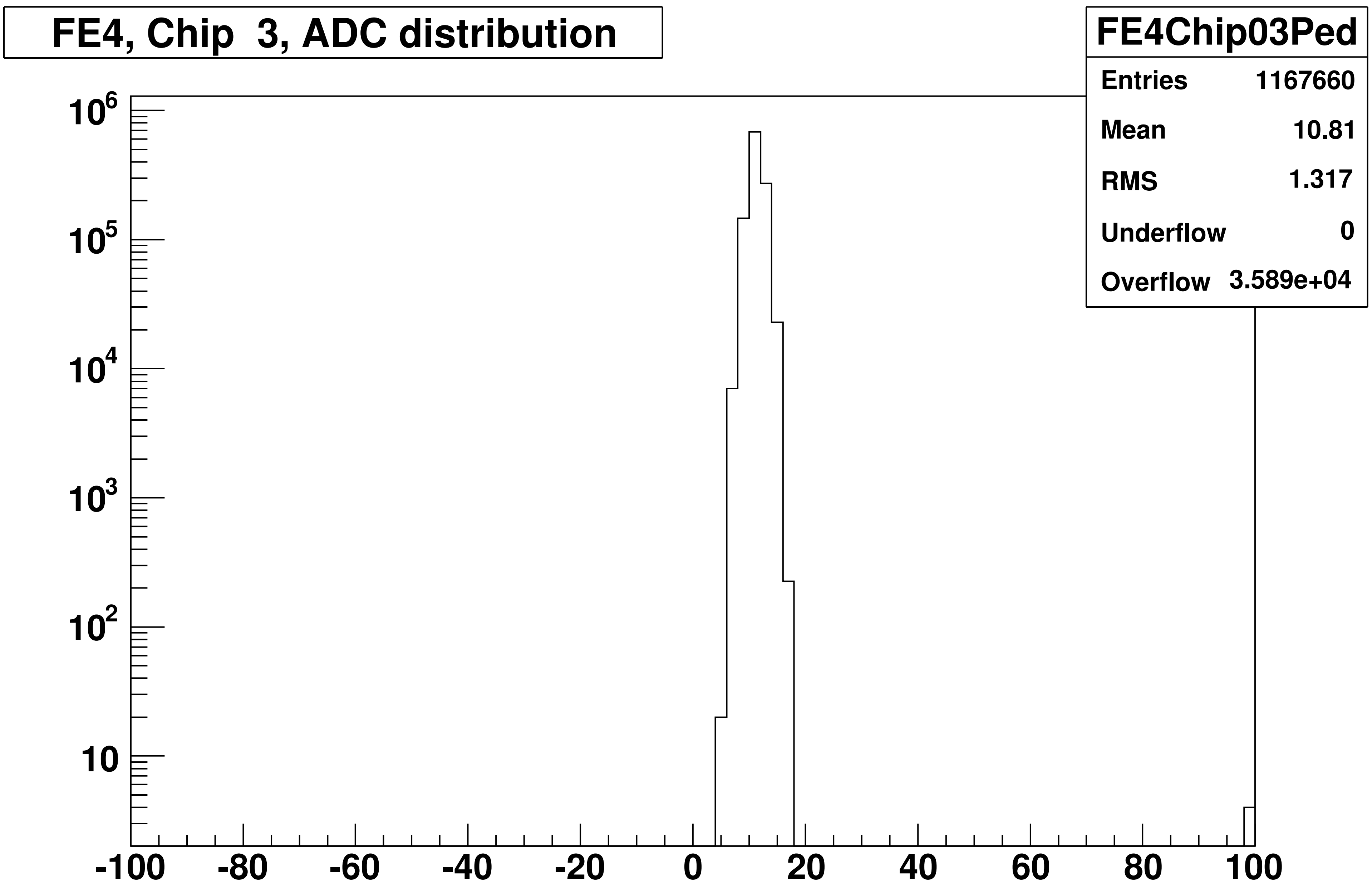
FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 1167660
Mean 6.089
RMS 1.305
Underflow 0
Overflow 3.593e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



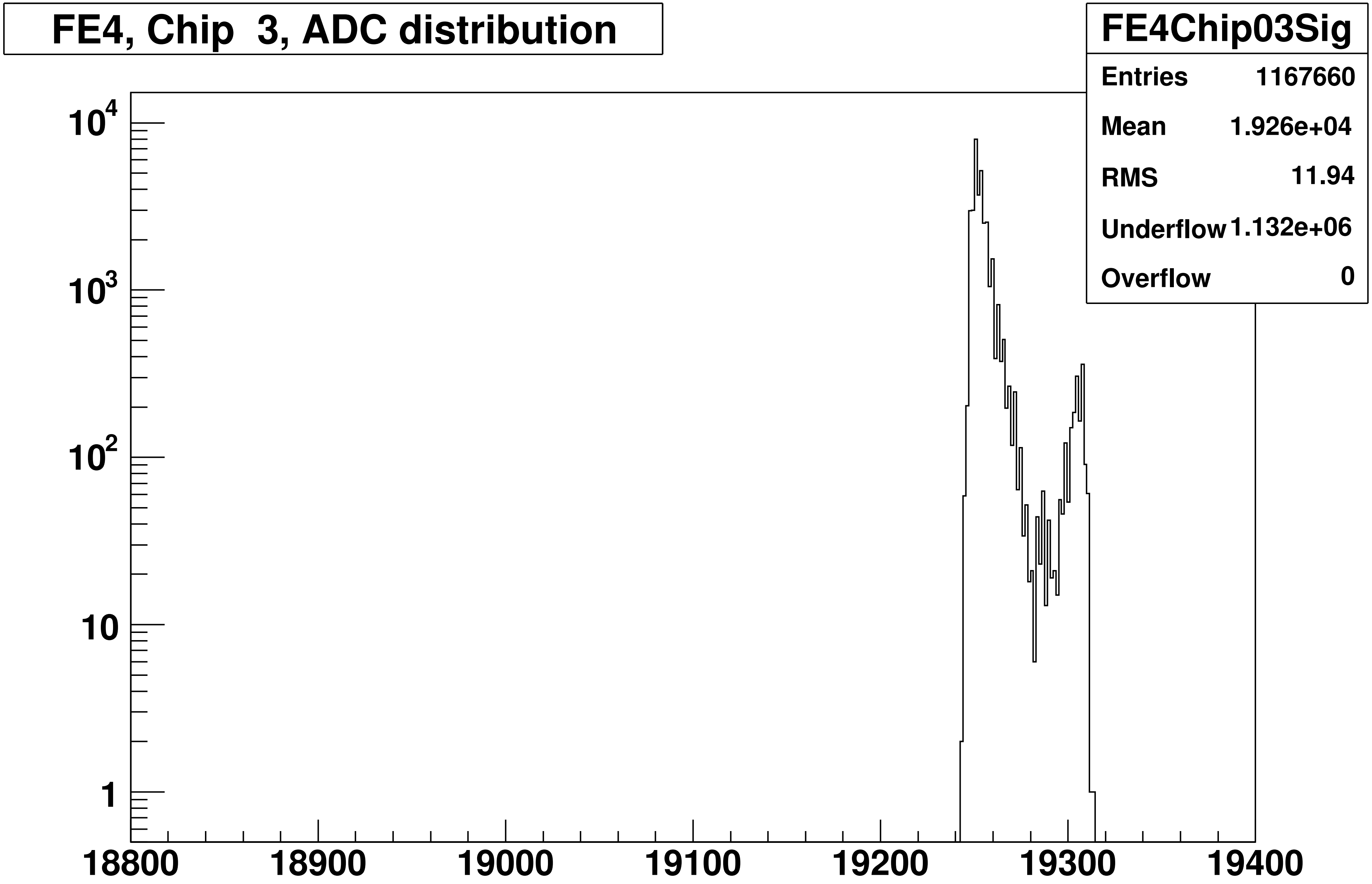
FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 1167660
Mean 1.928e+04
RMS 12.43
Underflow 1.132e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

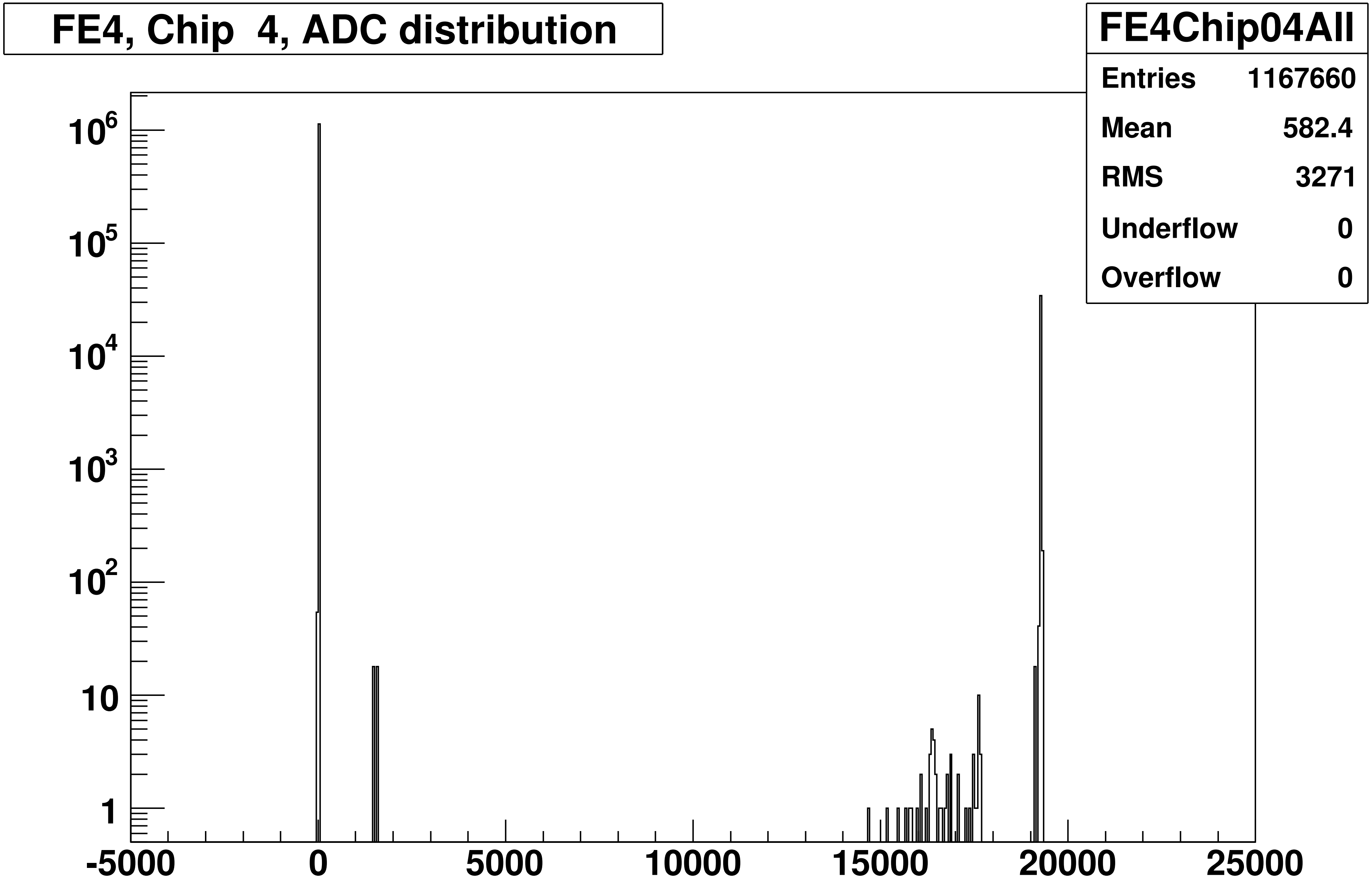


FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 1167660
Mean 601.8
RMS 3320
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 1167660
Mean 10.81
RMS 1.317
Underflow 0
Overflow 3.589e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 1167660
Mean 1.926e+04
RMS 11.94
Underflow 1.132e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 1167660
Mean 582.4
RMS 3271
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
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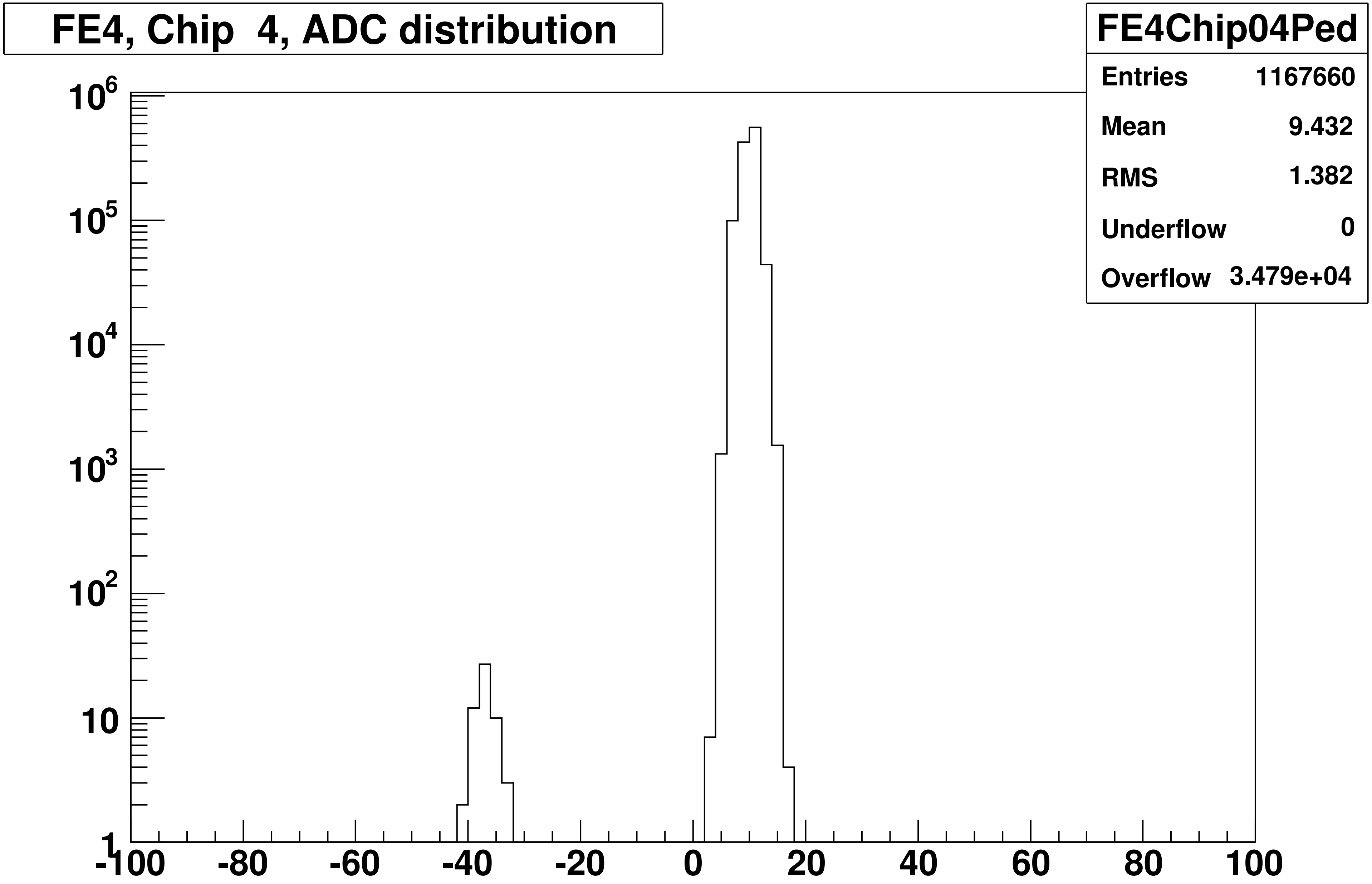
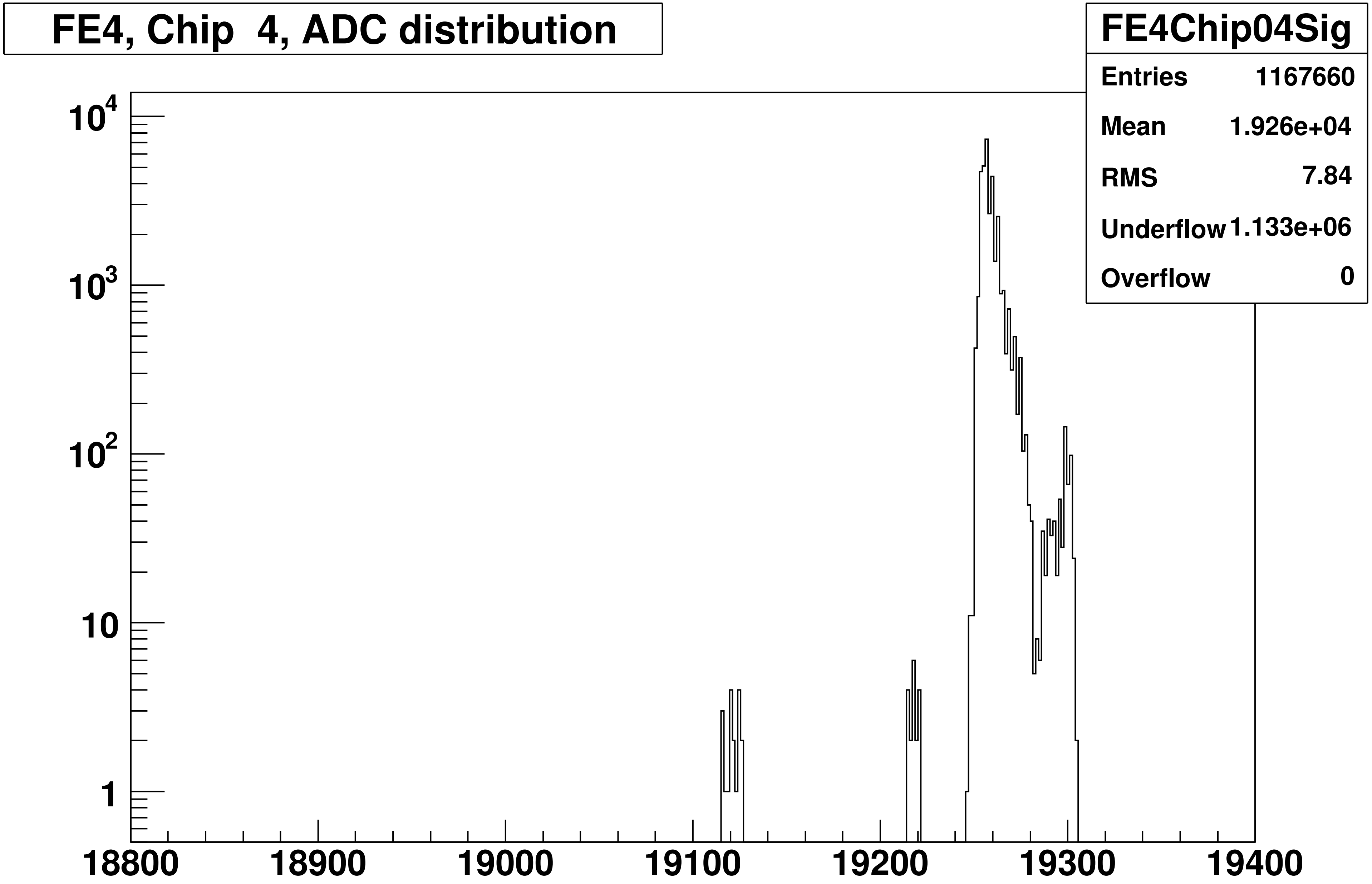
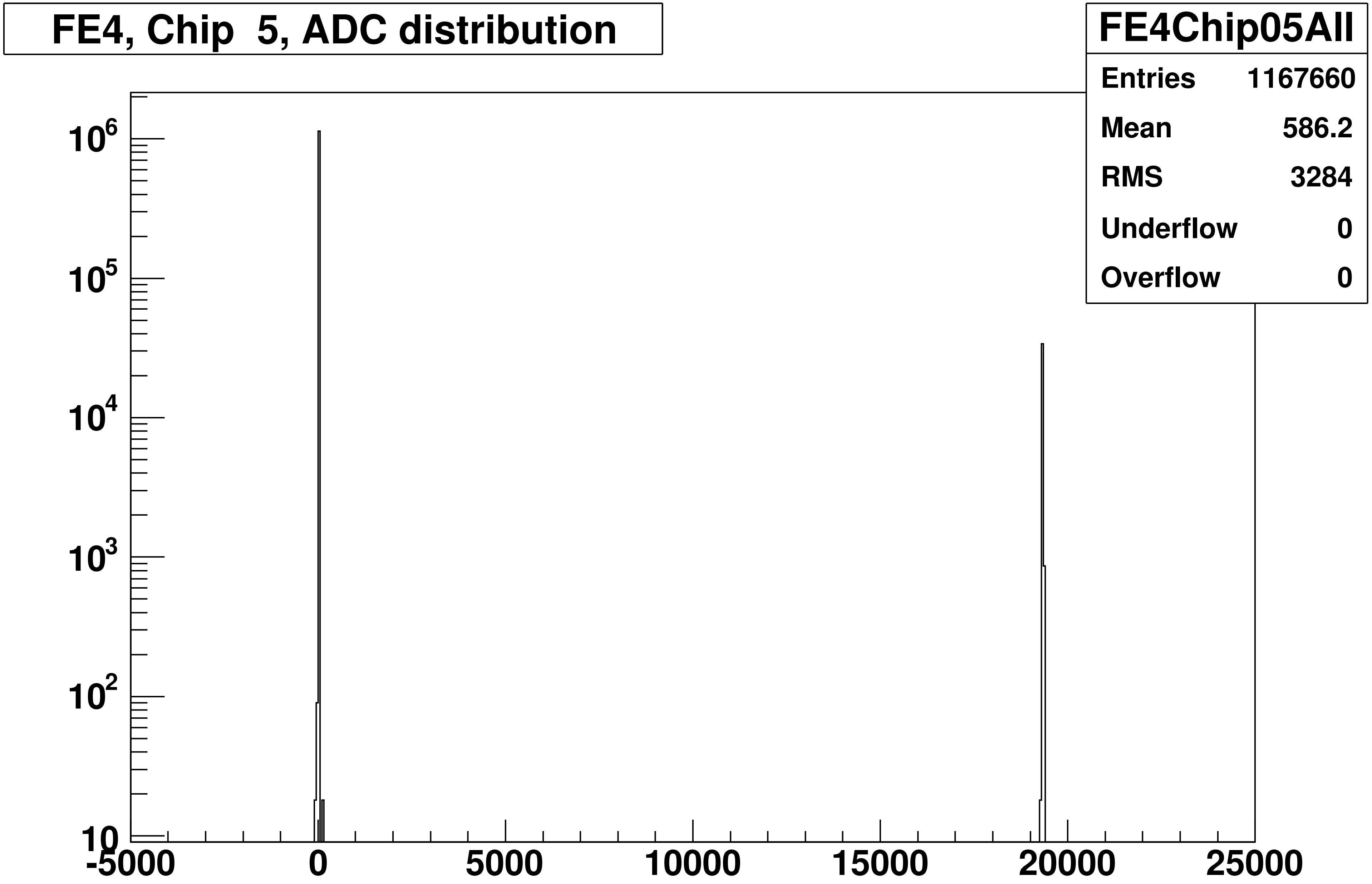
10000
15000
20000
25000



FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 1167660
Mean 9.432
RMS 1.382
Underflow 0
Overflow 3.479e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 1167660
Mean 1.926e+04
RMS 7.84
Underflow 1.133e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 1167660
Mean 586.2
RMS 3284
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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10000
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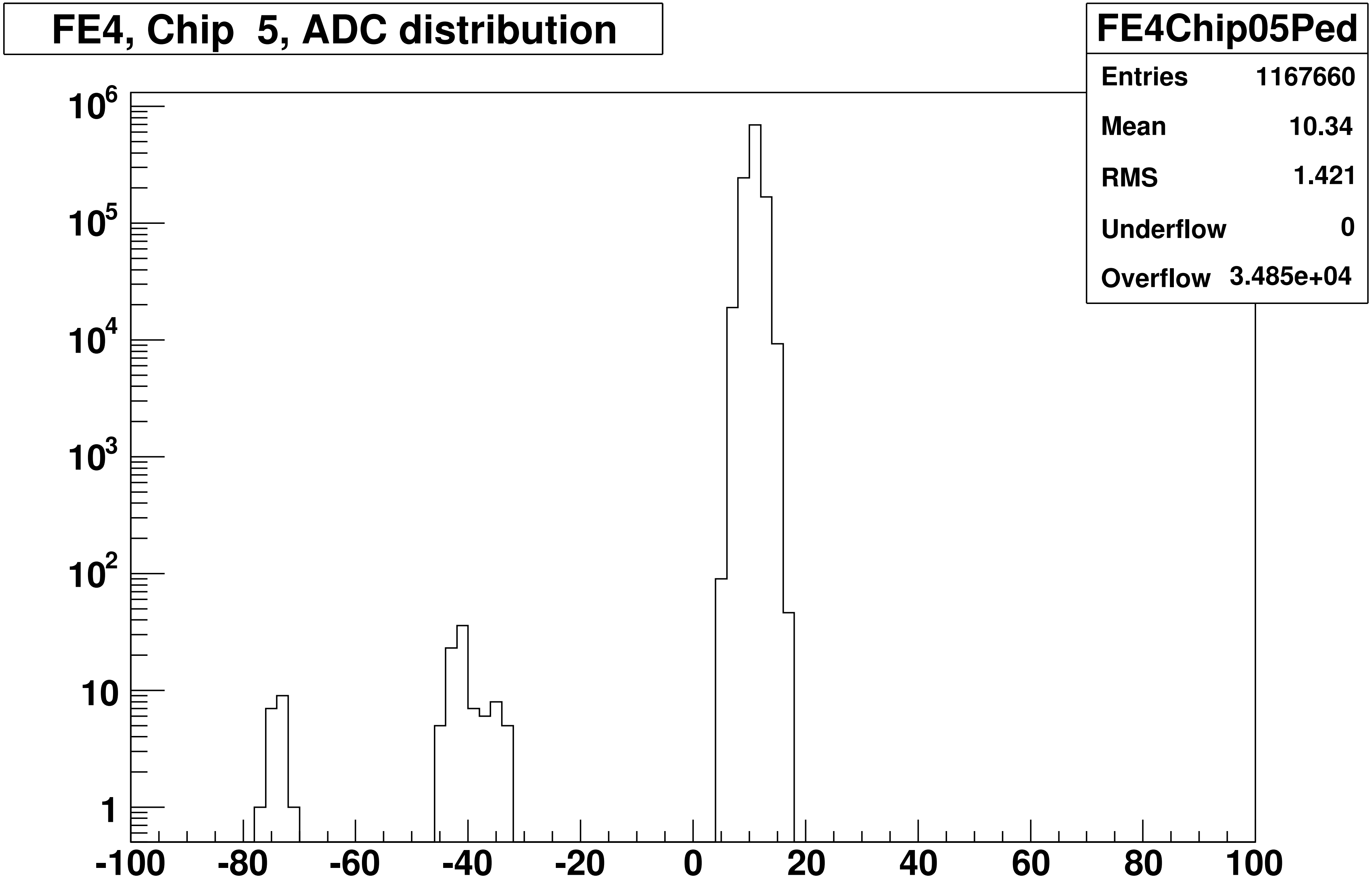
20000
25000



FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 1167660
Mean 10.34
RMS 1.421
Underflow 0
Overflow 3.485e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
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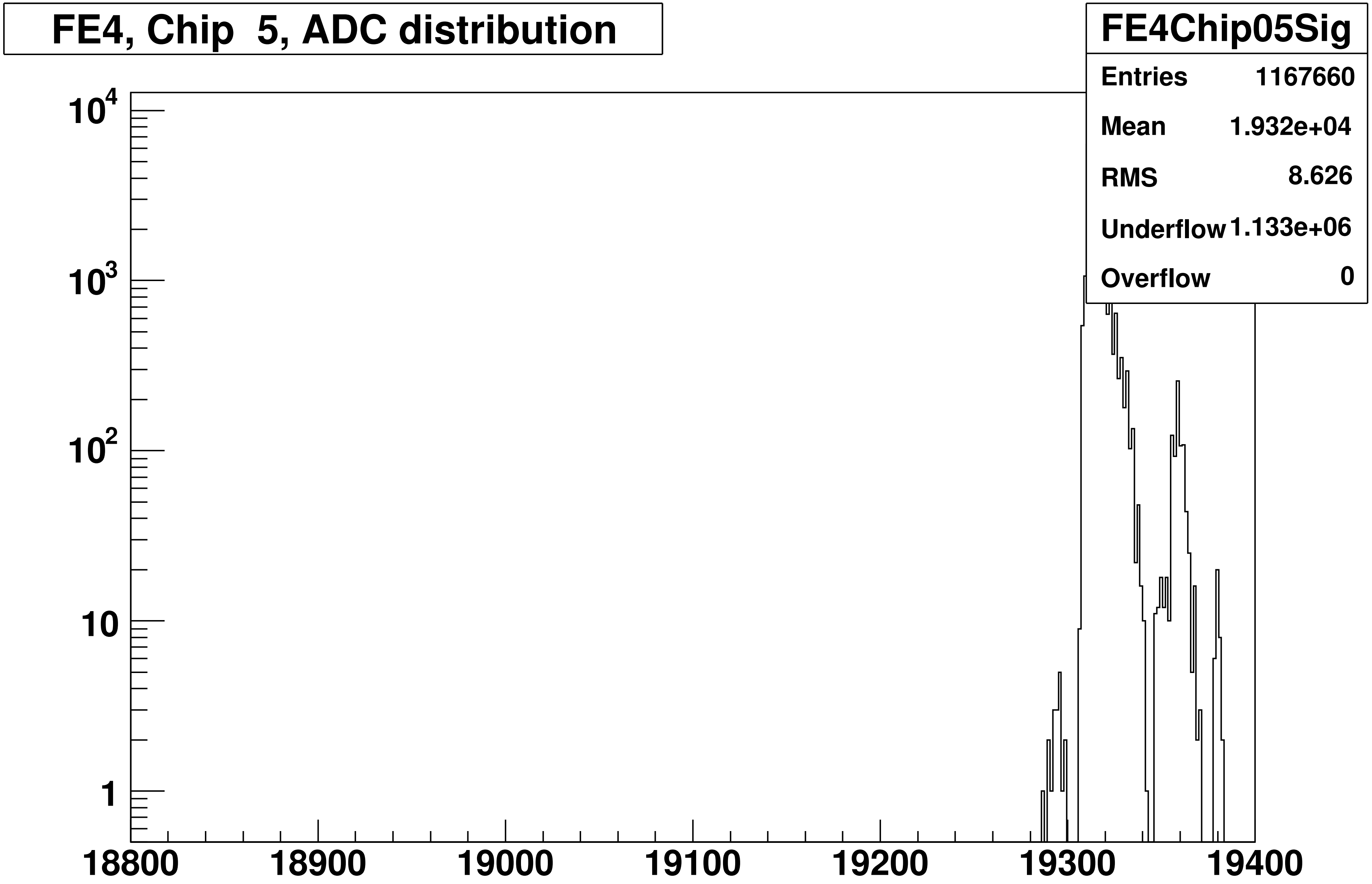
60
80
100


FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 1167660
Mean 1.932e+04
RMS 8.626
Underflow 1.133e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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19100
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19300
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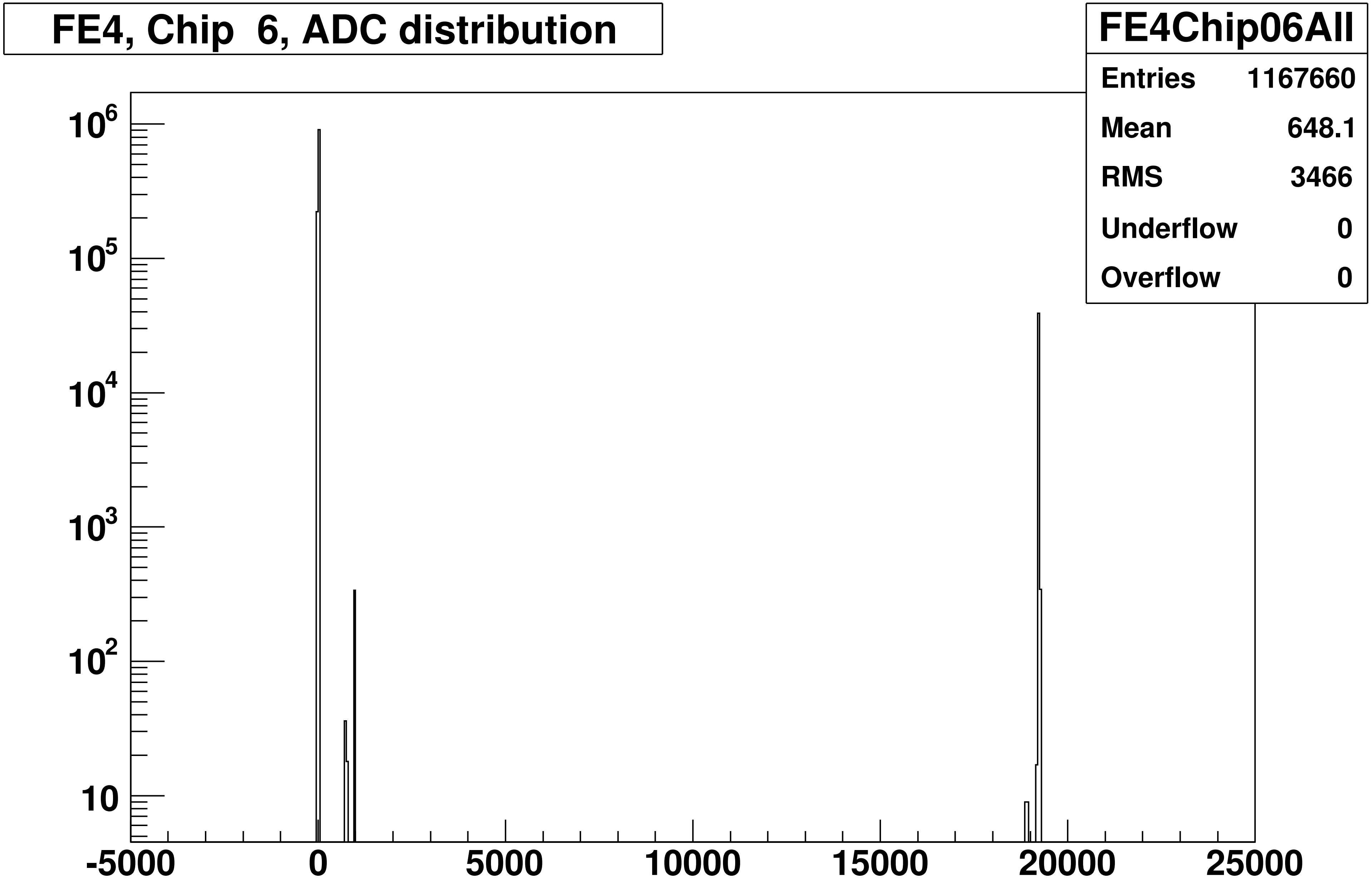
FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 1167660
Mean 648.1
RMS 3466
Underflow 0
Overflow 0
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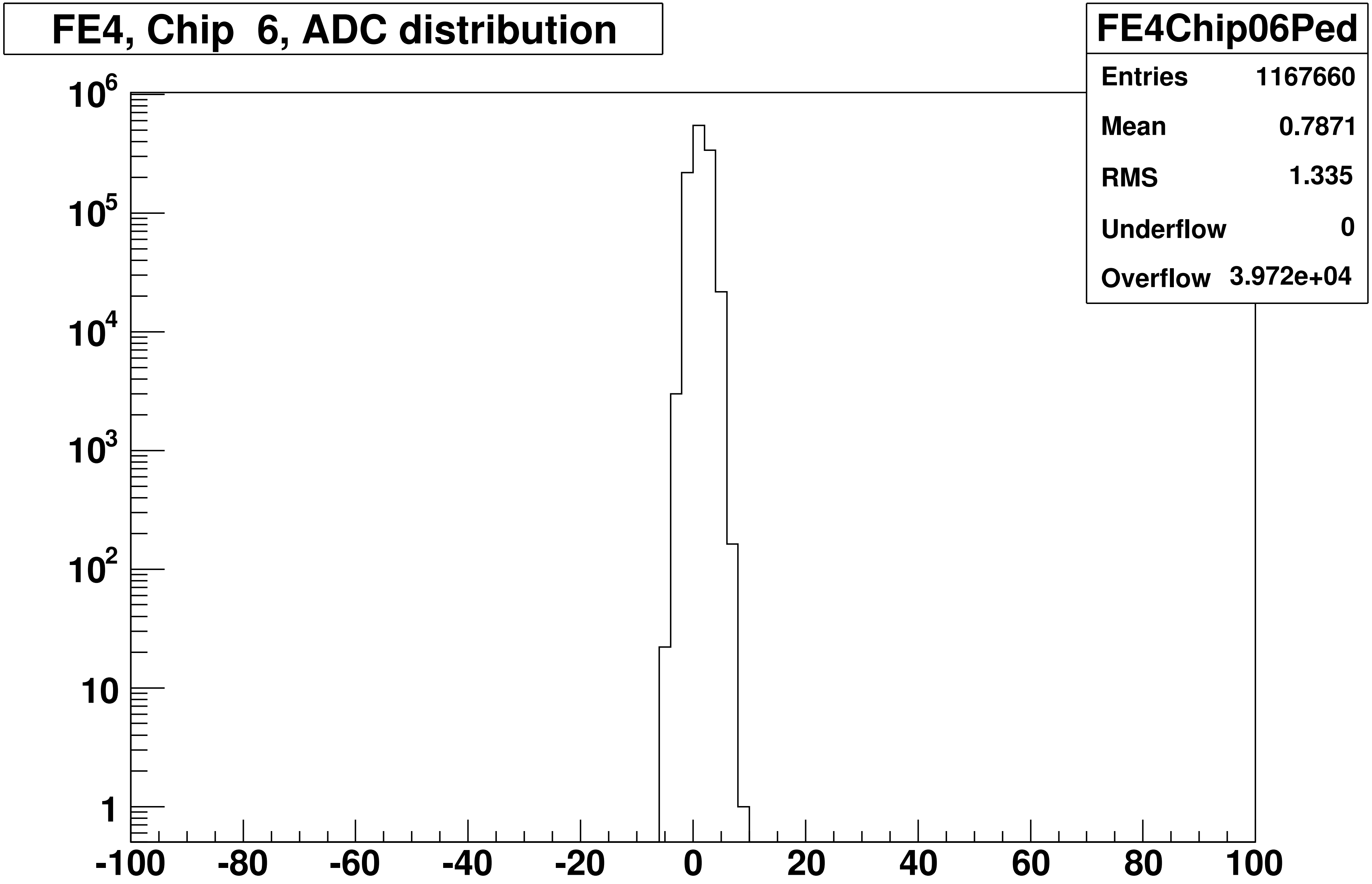
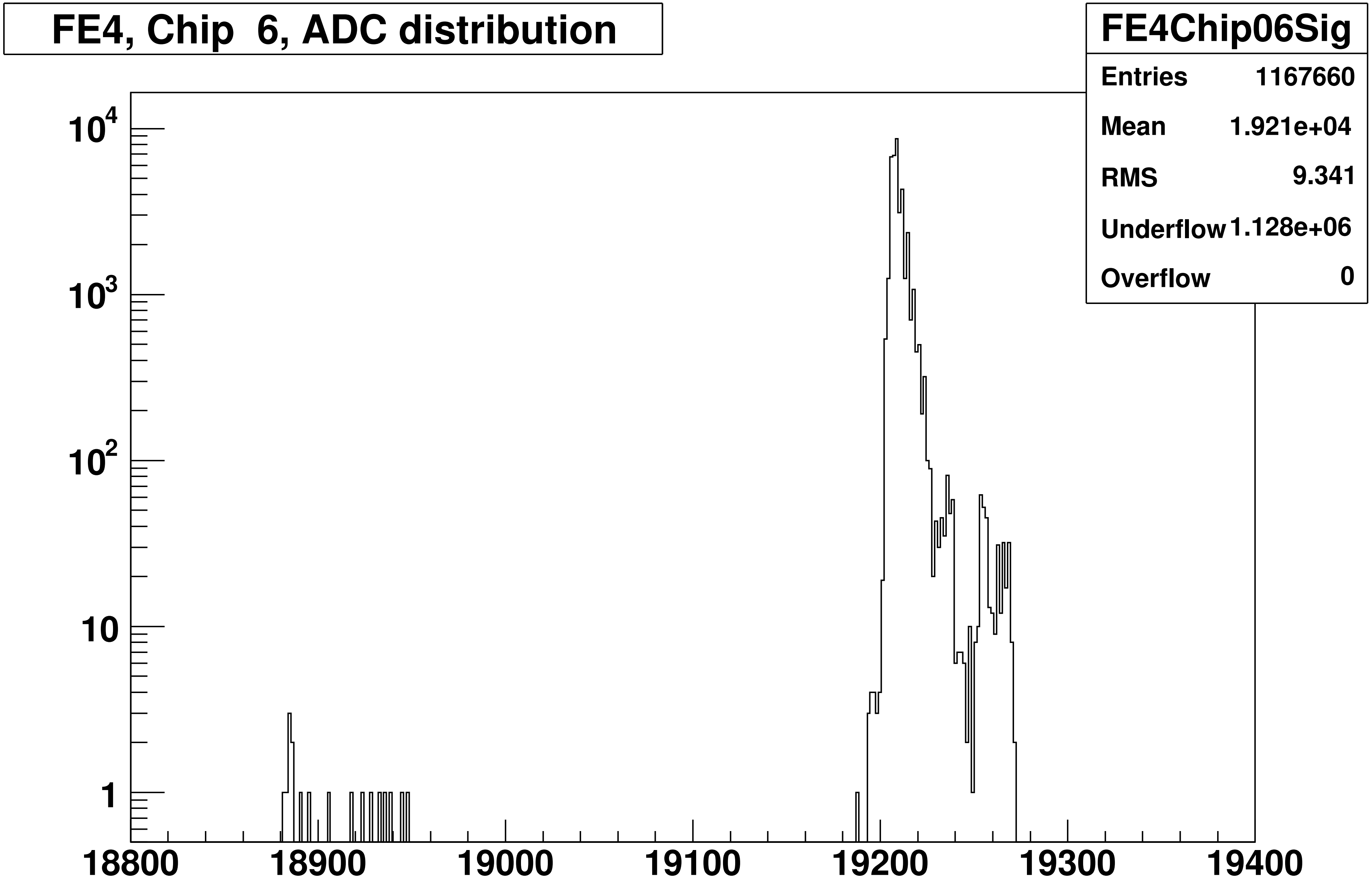
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 1167660
Mean 0.7871
RMS 1.335
Underflow 0
Overflow 3.972e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 1167660
Mean 1.921e+04
RMS 9.341
Underflow 1.128e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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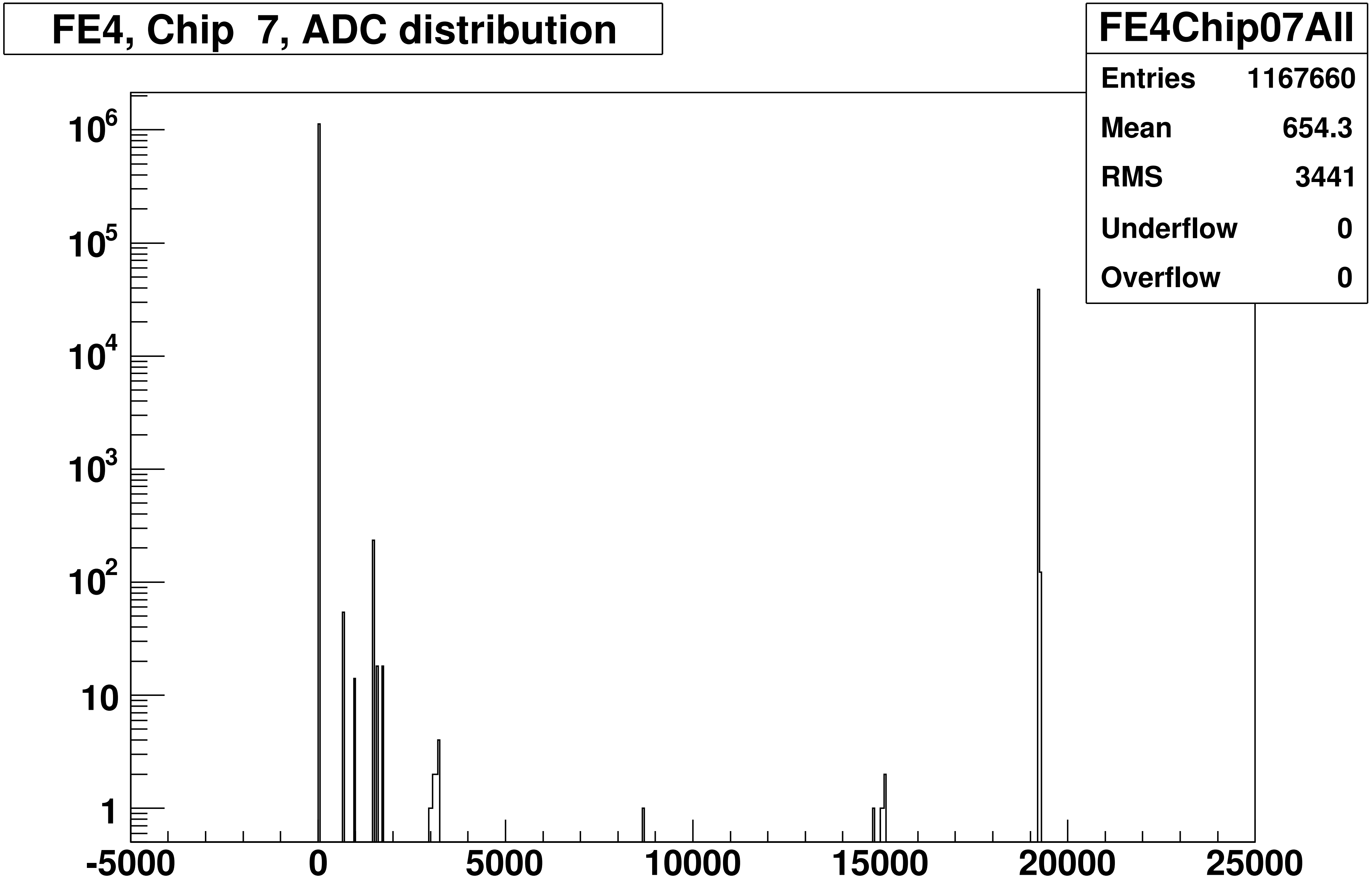
19100
19200
19300
19400


FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 1167660
Mean 654.3
RMS 3441
Underflow 0
Overflow 0
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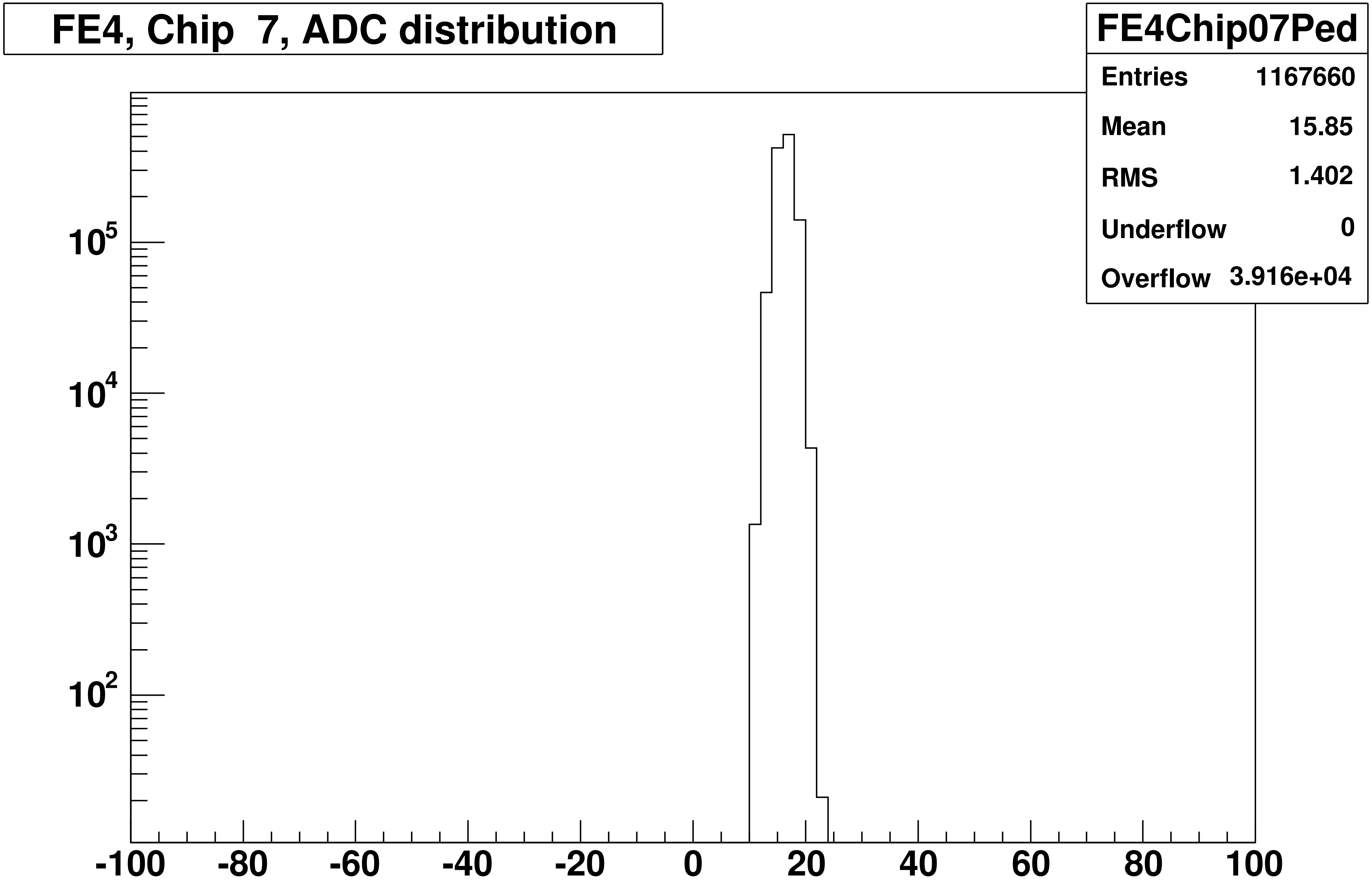
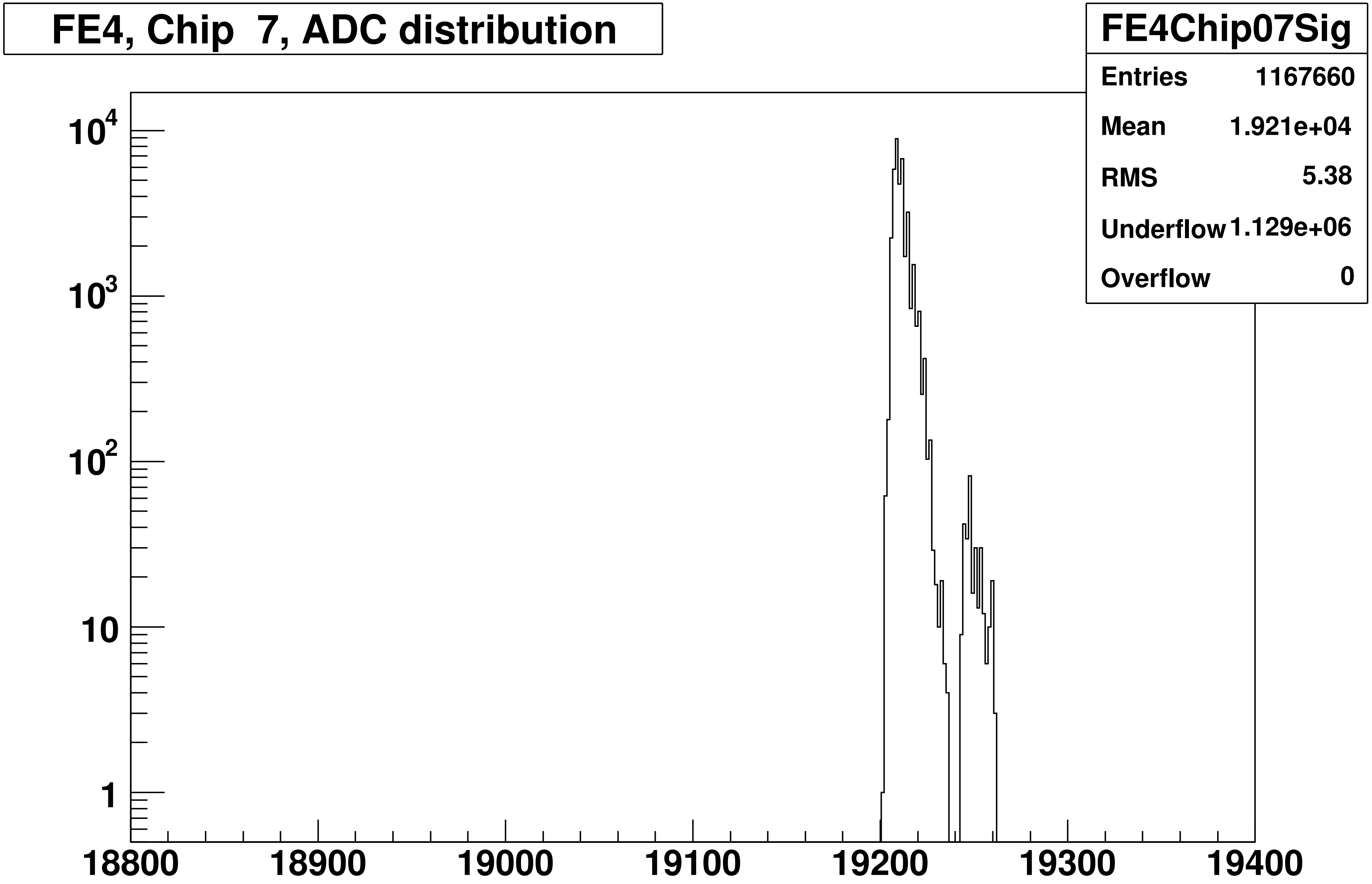
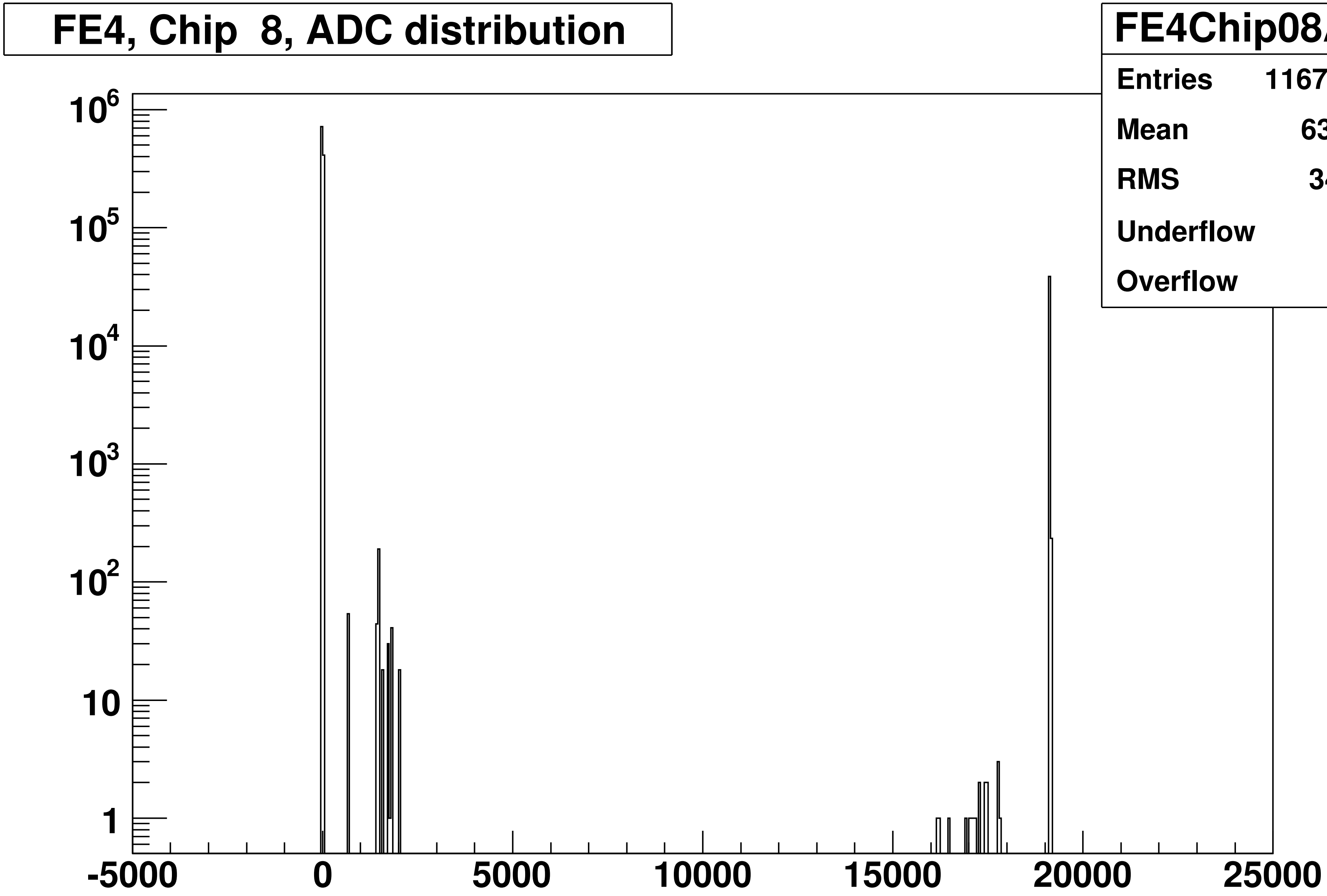
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 1167660
Mean 15.85
RMS 1.402
Underflow 0
Overflow 3.916e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 1167660
Mean 1.921e+04
RMS 5.38
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 1167660
Mean 634.8
RMS 3426
Underflow 0
Overflow 0
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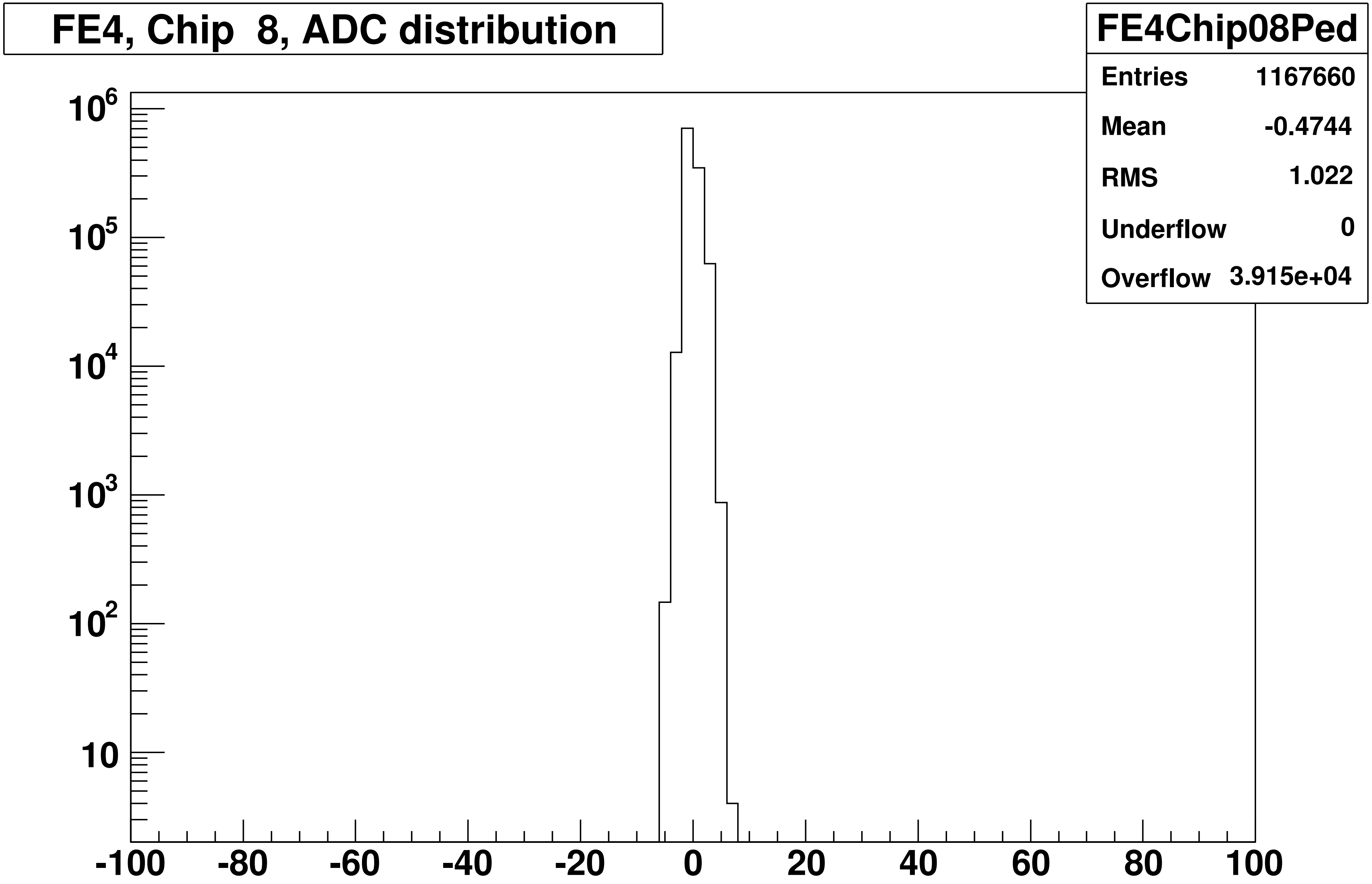
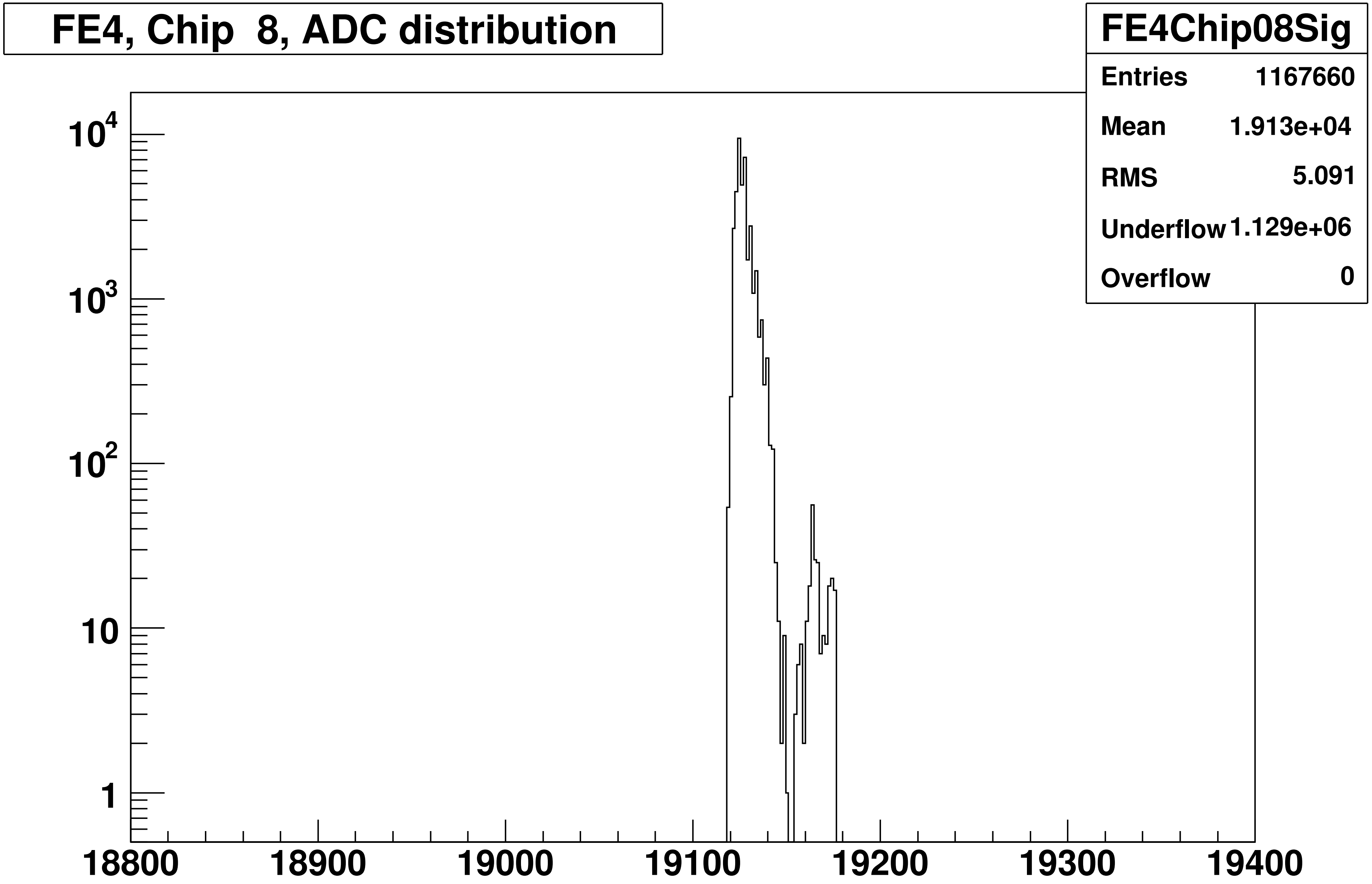
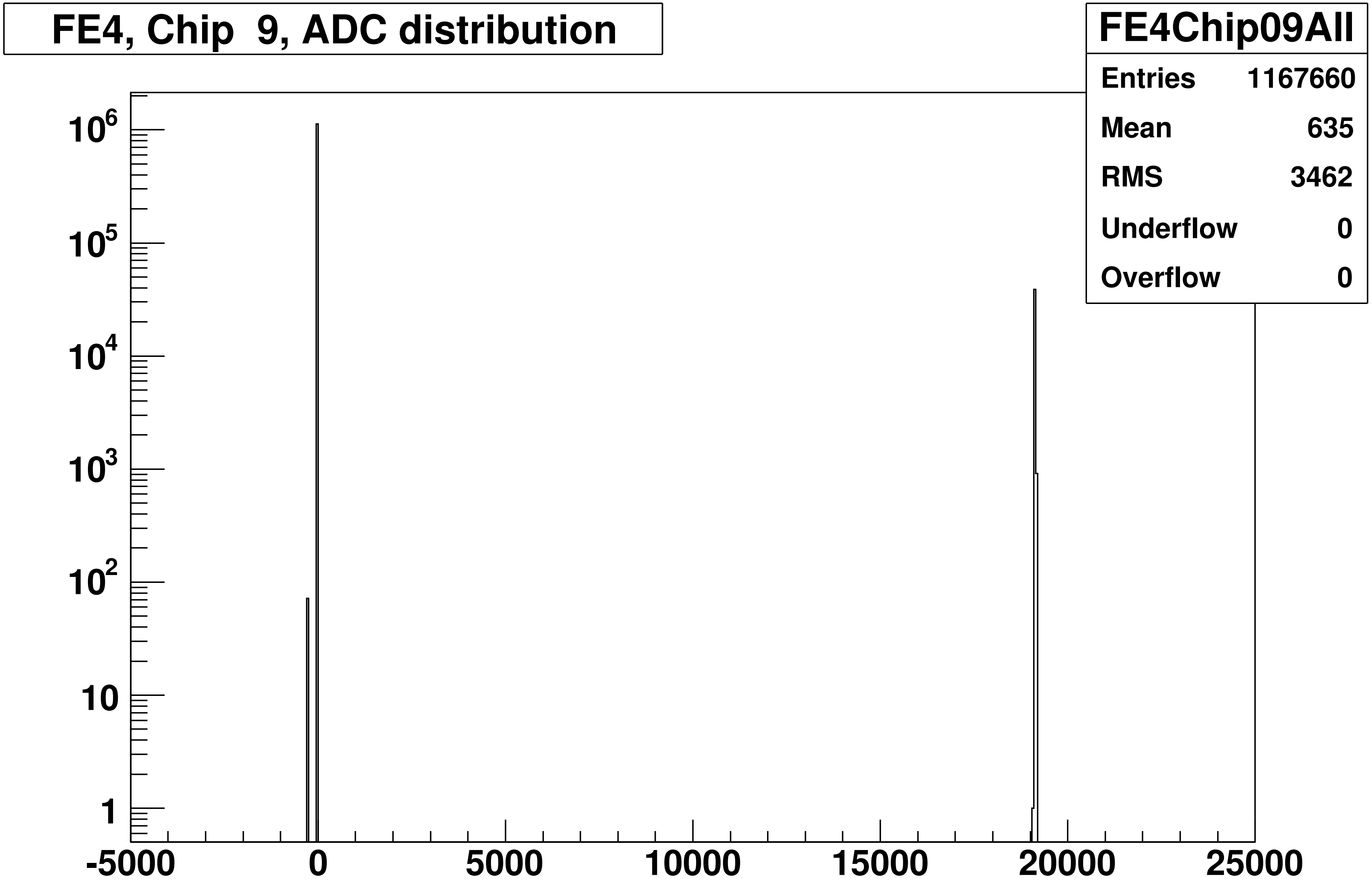
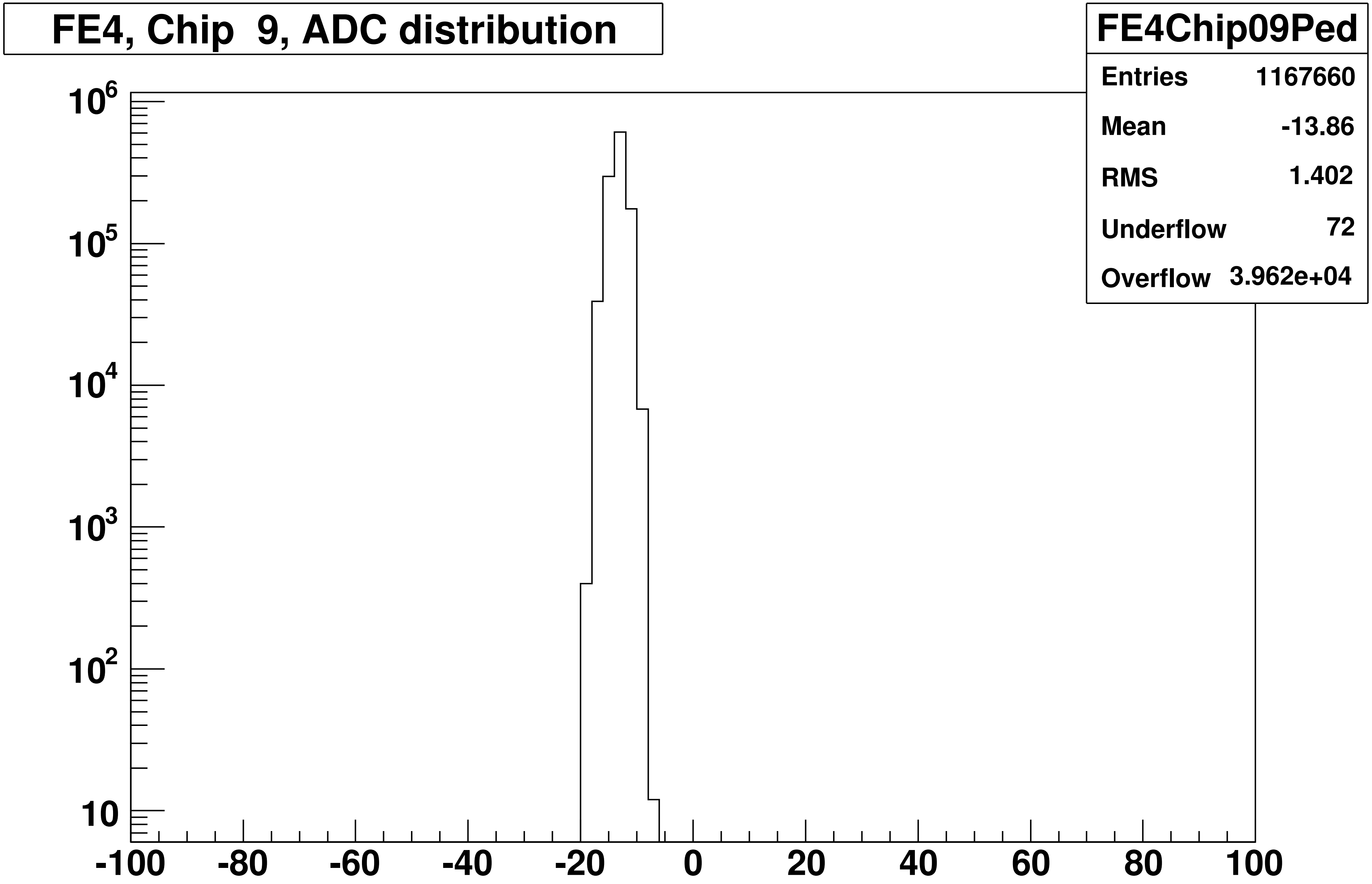
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

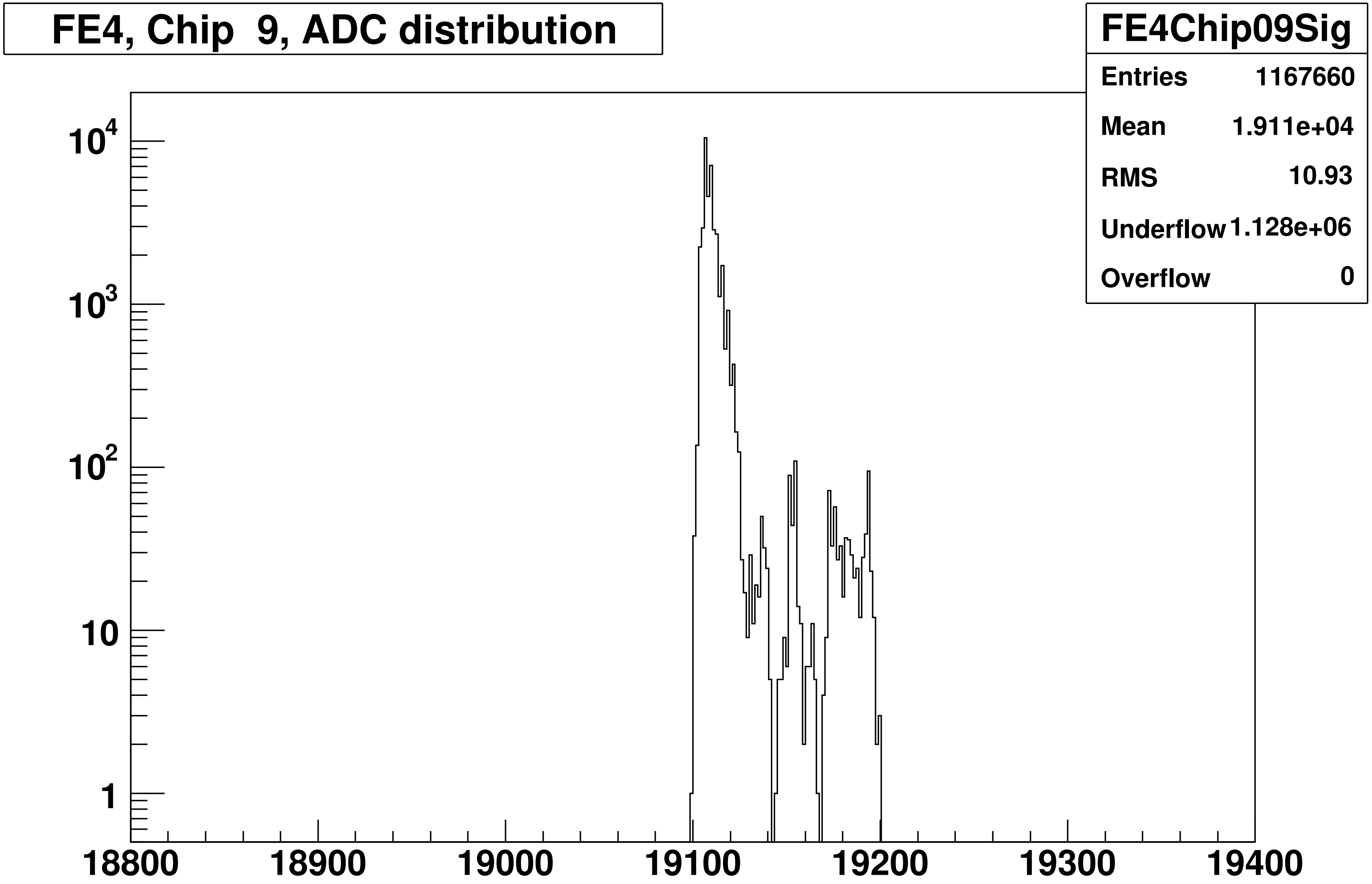

FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 1167660
Mean -0.4744
RMS 1.022
Underflow 0
Overflow 3.915e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 1167660
Mean 1.913e+04
RMS 5.091
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

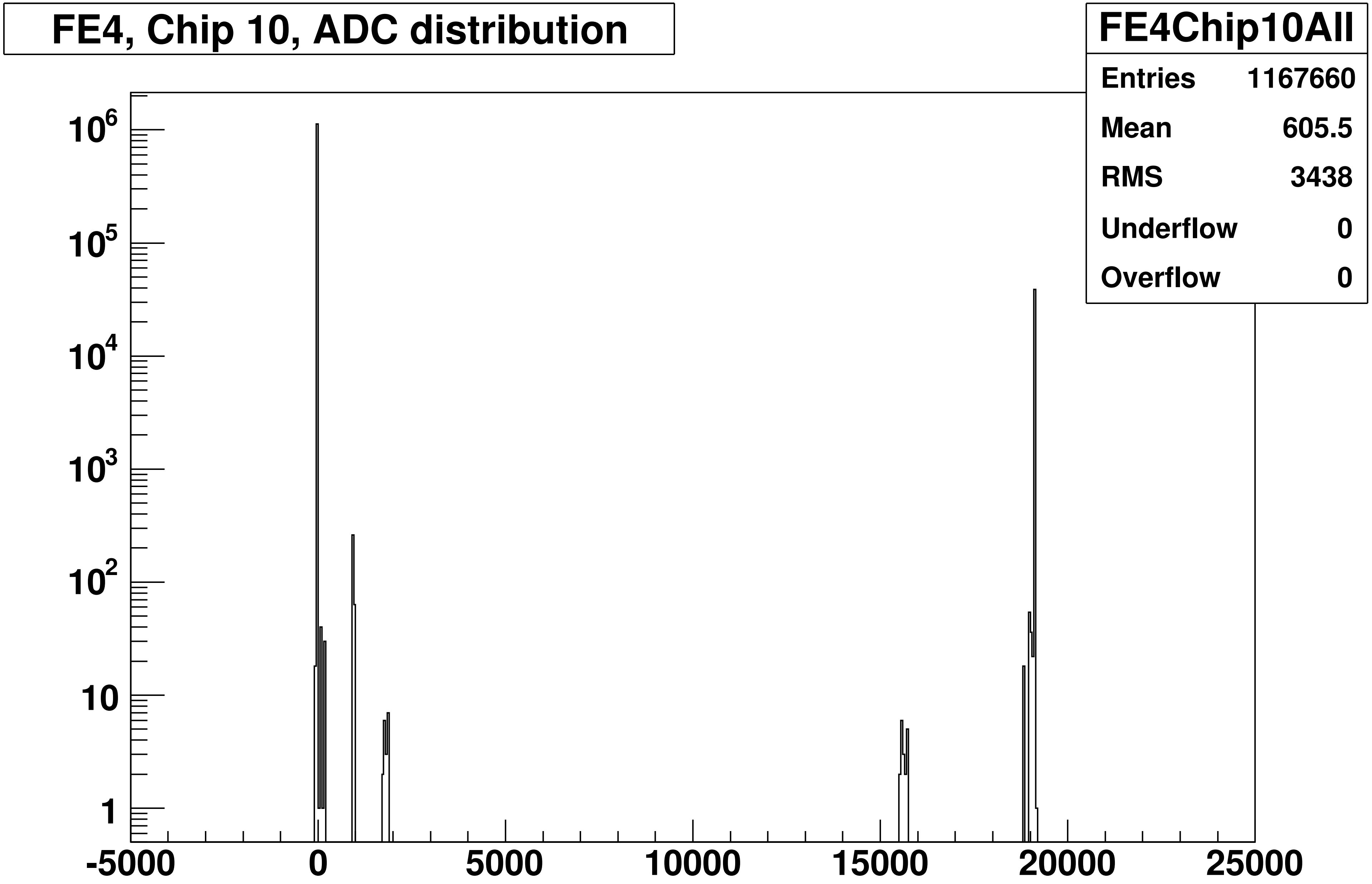

FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 1167660
Mean 635
RMS 3462
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



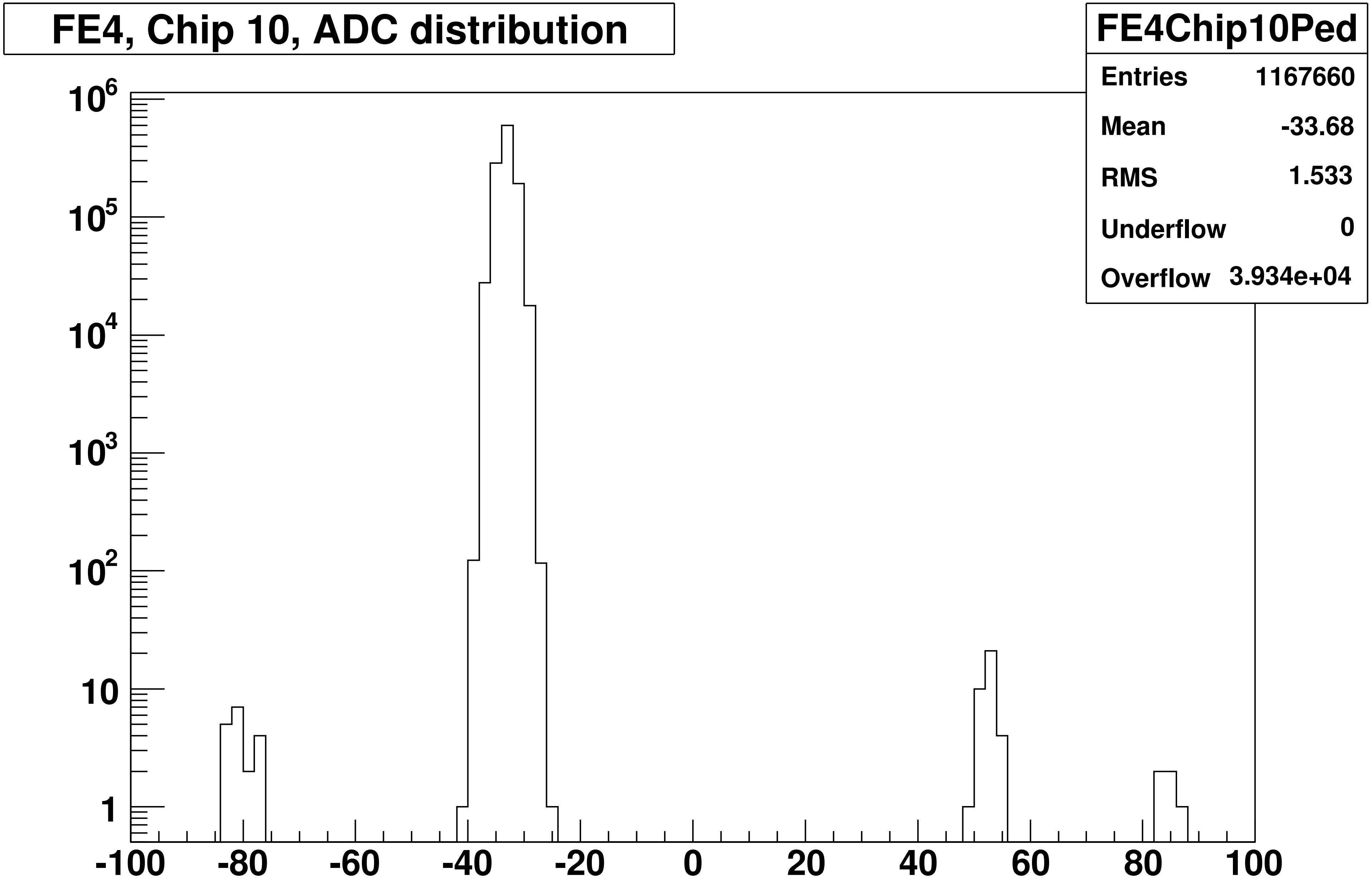
FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 1167660
Mean -13.86
RMS 1.402
Underflow 72
Overflow 3.962e+04
10
10^2
10^3
10^4
10^5
10^6
-100
-80
-60
-40
-20
0
20
40
60
80
100



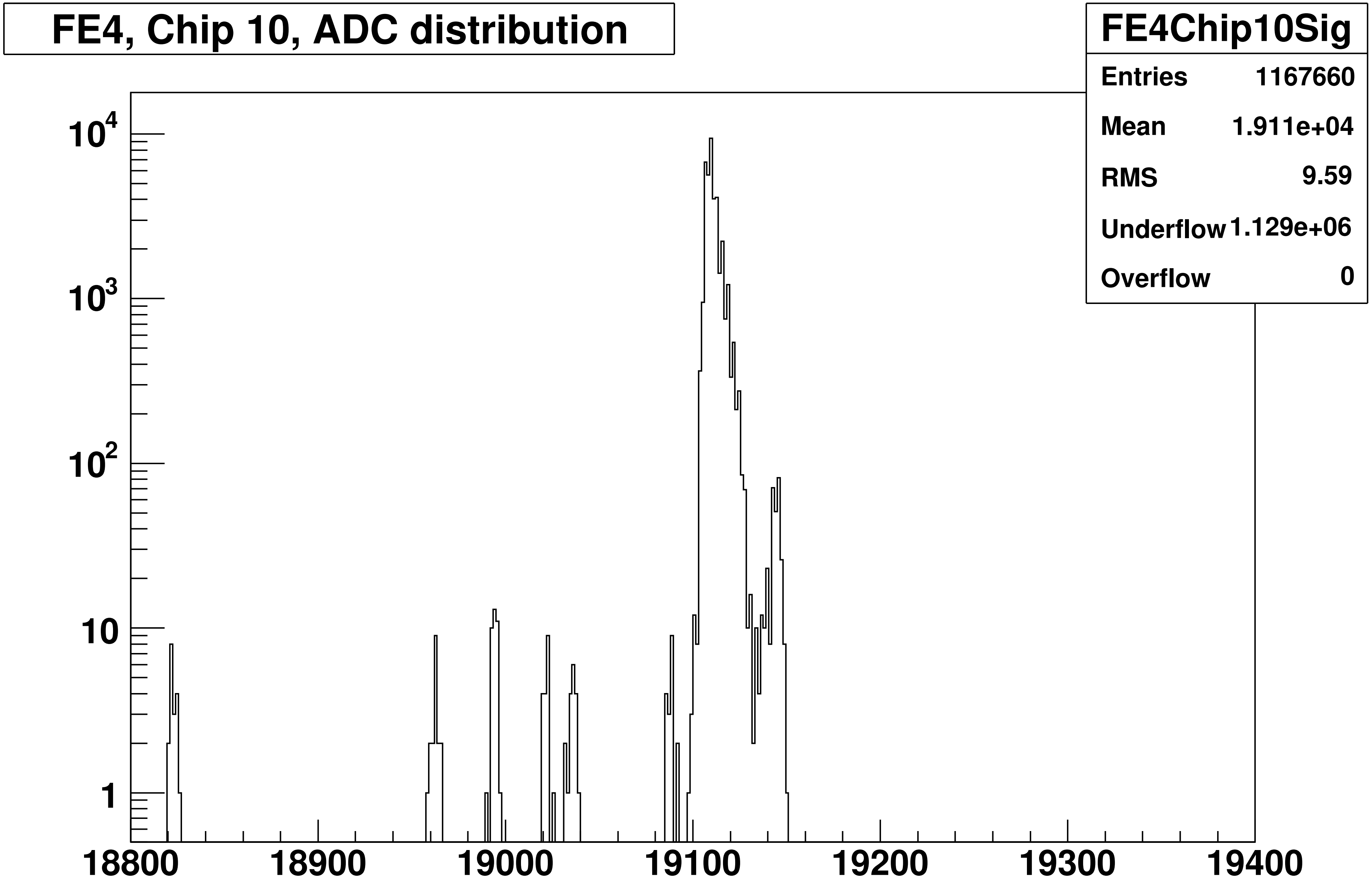
FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 1167660
Mean 1.911e+04
RMS 10.93
Underflow 1.128e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

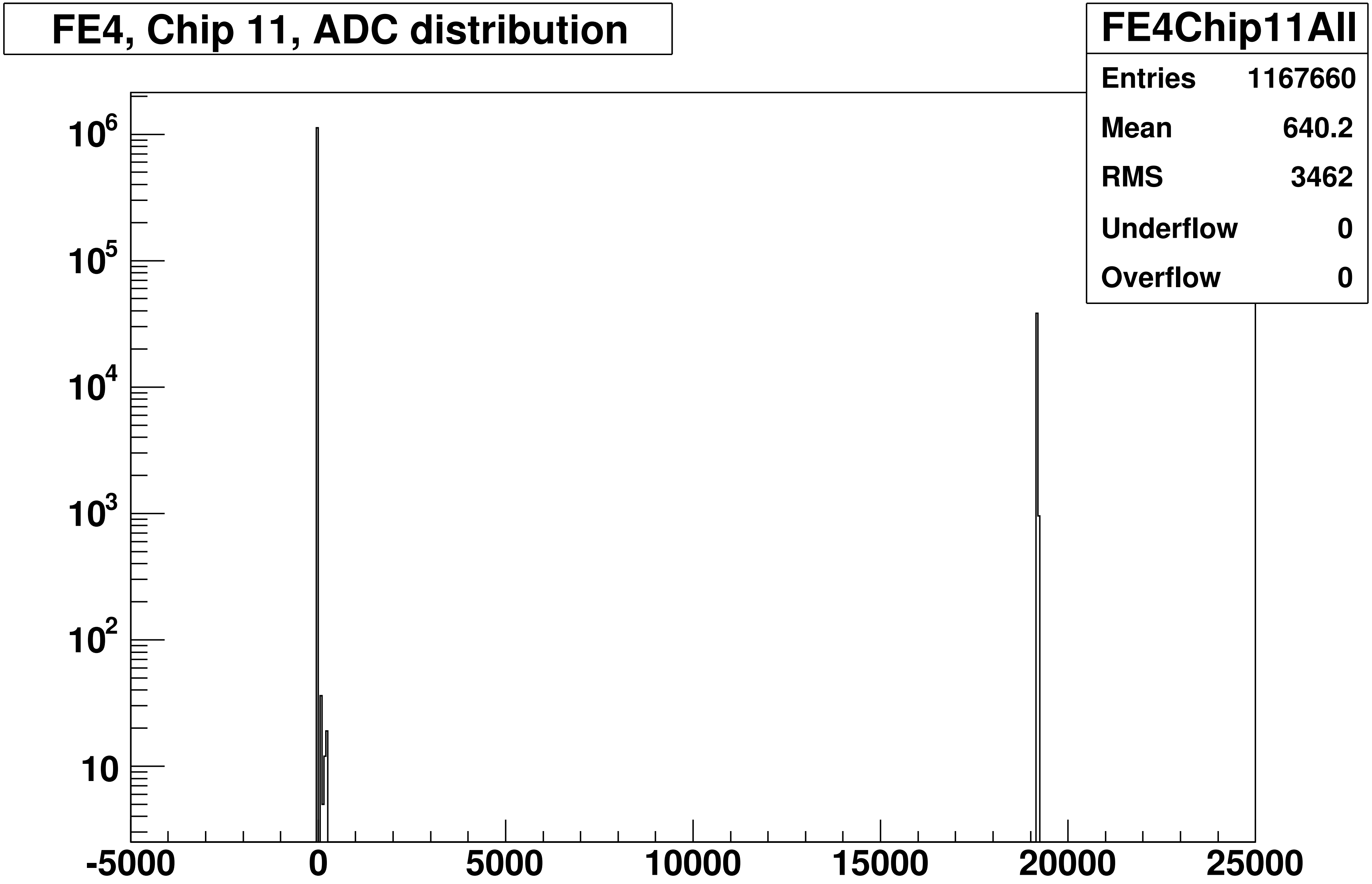


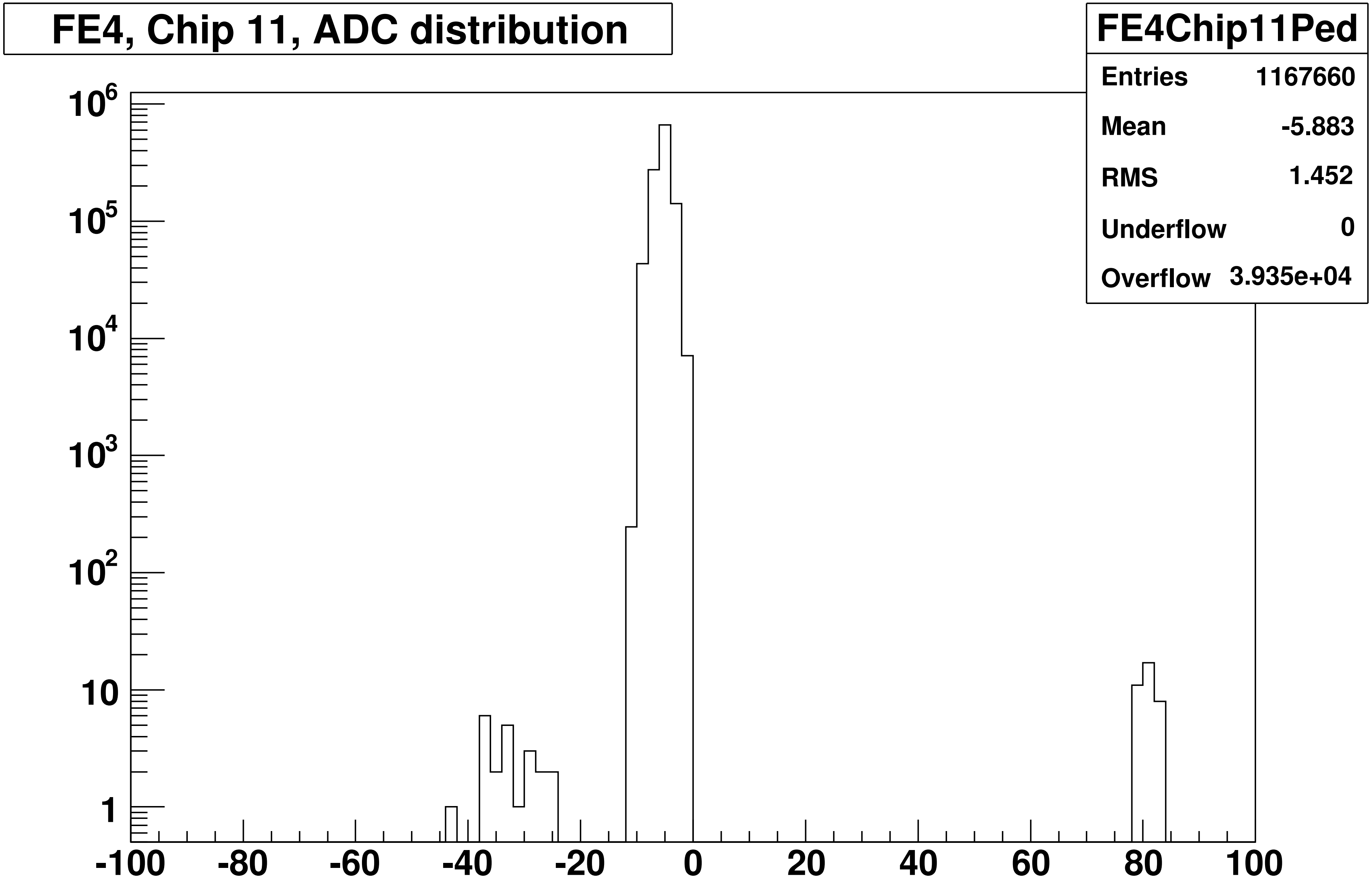
FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 1167660
Mean 605.5
RMS 3438
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

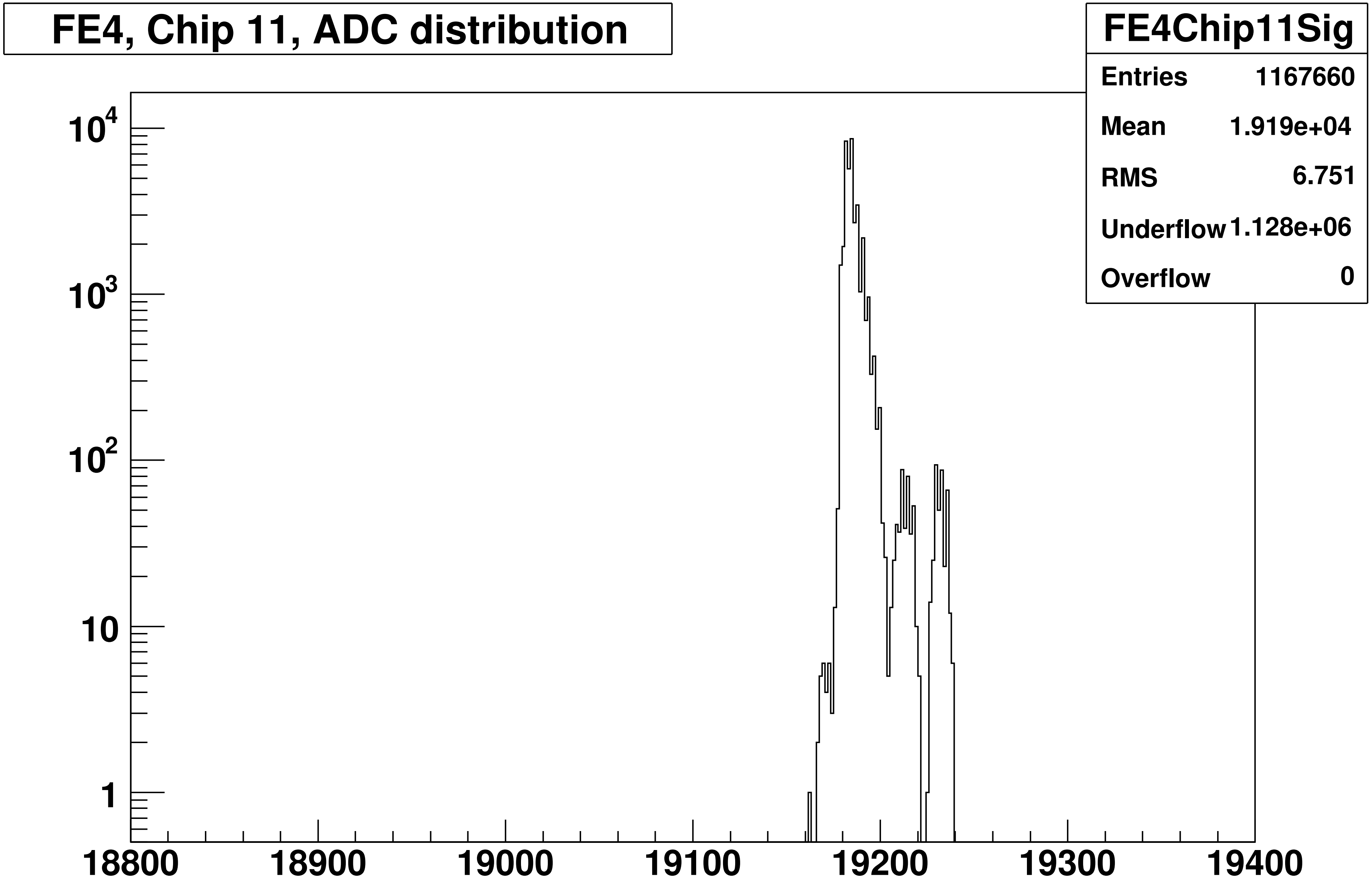


FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 1167660
Mean -33.68
RMS 1.533
Underflow 0
Overflow 3.934e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

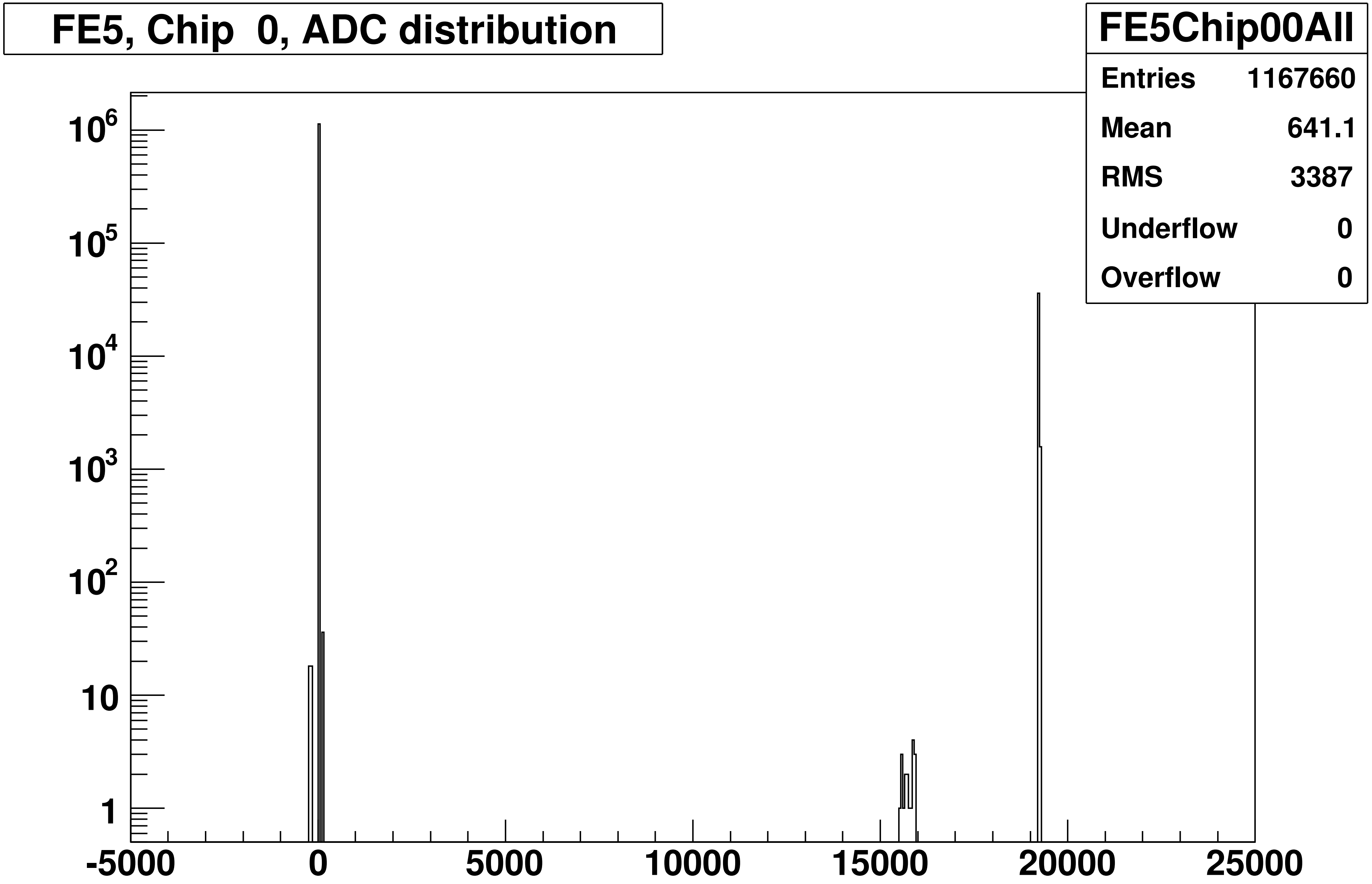

FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 1167660
Mean 1.911e+04
RMS 9.59
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 1167660
Mean 640.2
RMS 3462
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 1167660
Mean -5.883
RMS 1.452
Underflow 0
Overflow 3.935e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 1167660
Mean 1.919e+04
RMS 6.751
Underflow 1.128e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 1167660
Mean 641.1
RMS 3387
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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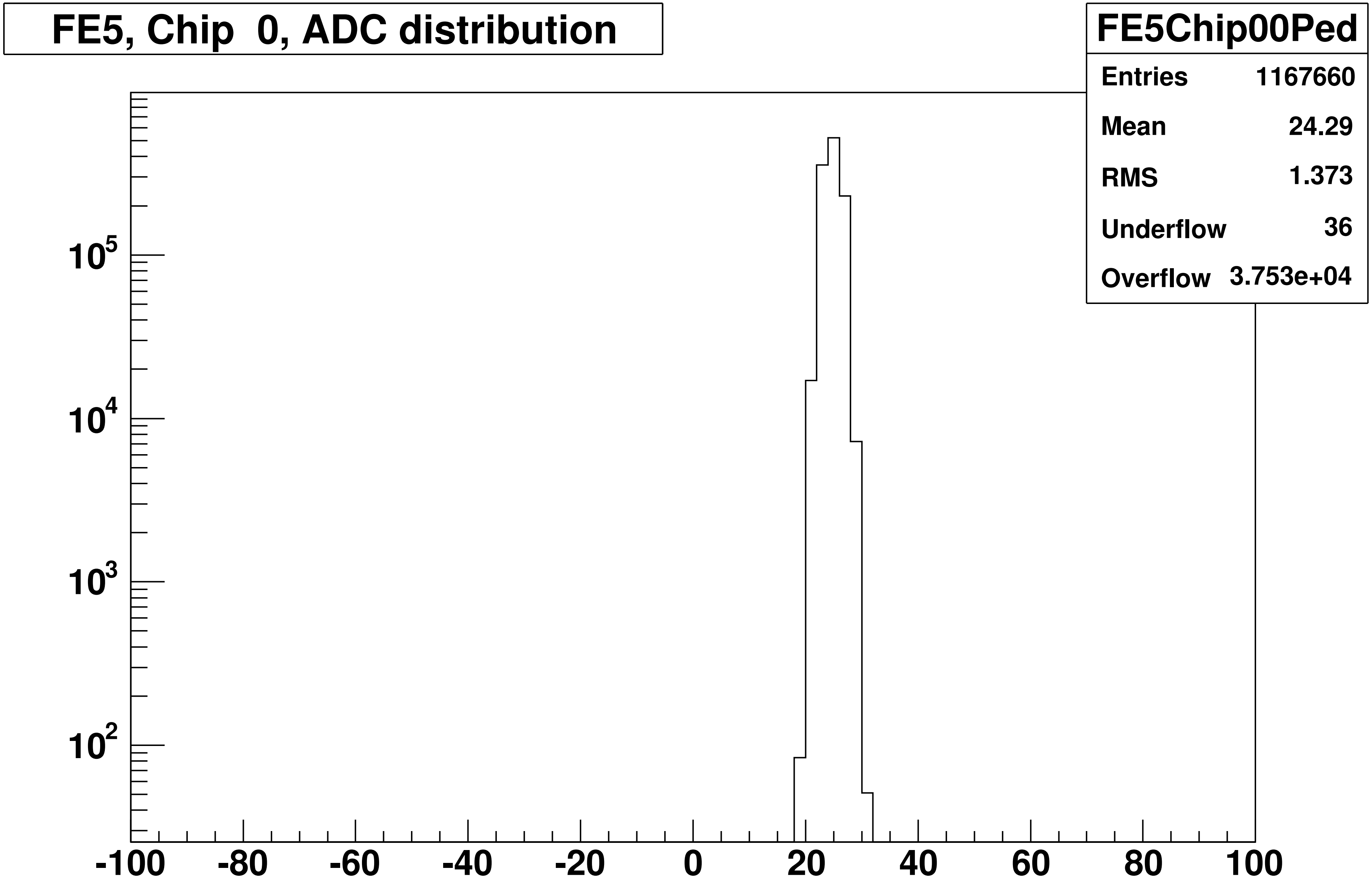
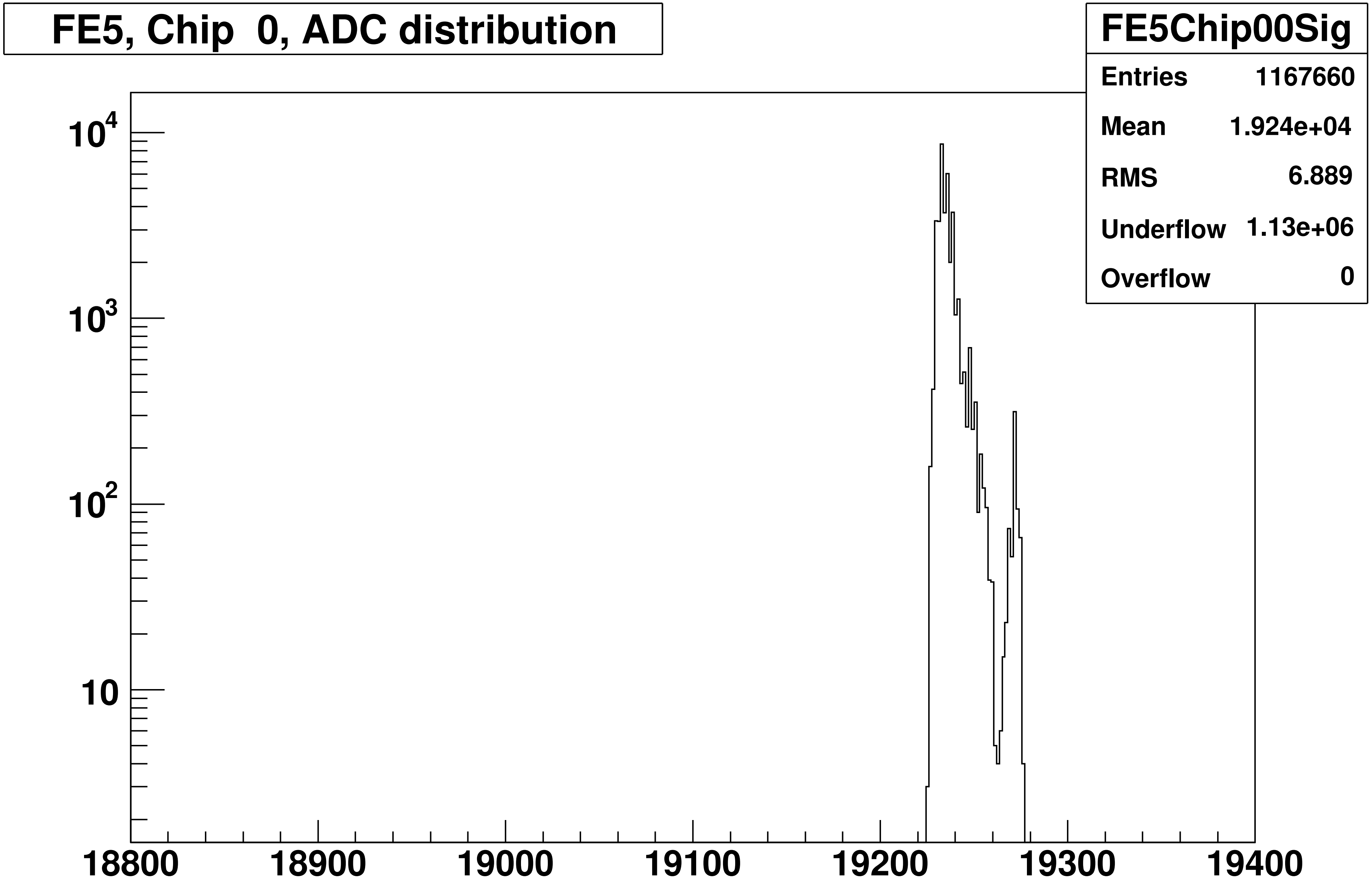
10000
15000
20000
25000


FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 1167660
Mean 24.29
RMS 1.373
Underflow 36
Overflow 3.753e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 1167660
Mean 1.924e+04
RMS 6.889
Underflow 1.13e+06
Overflow 0
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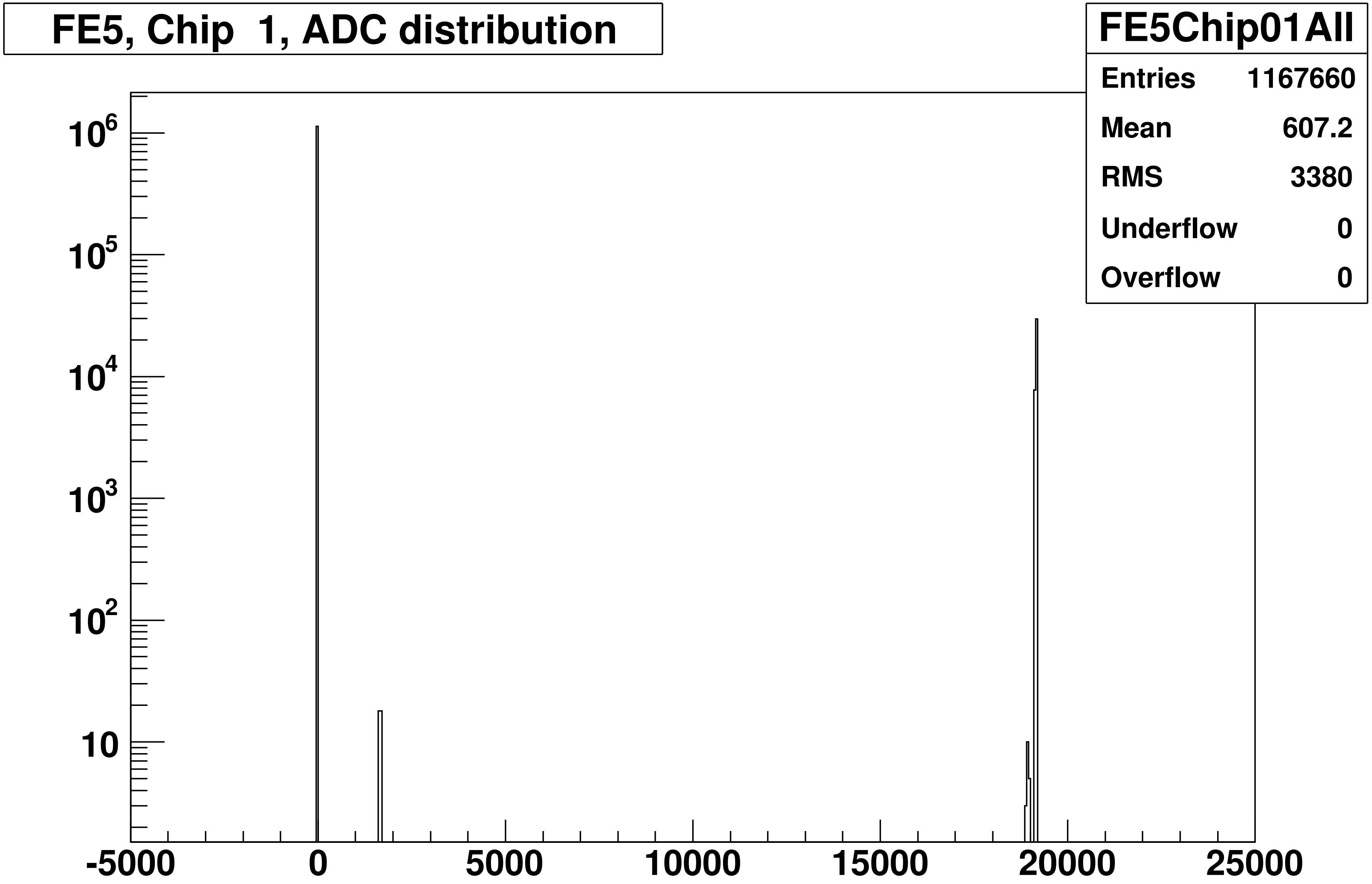
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400



FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 1167660
Mean 607.2
RMS 3380
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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10000
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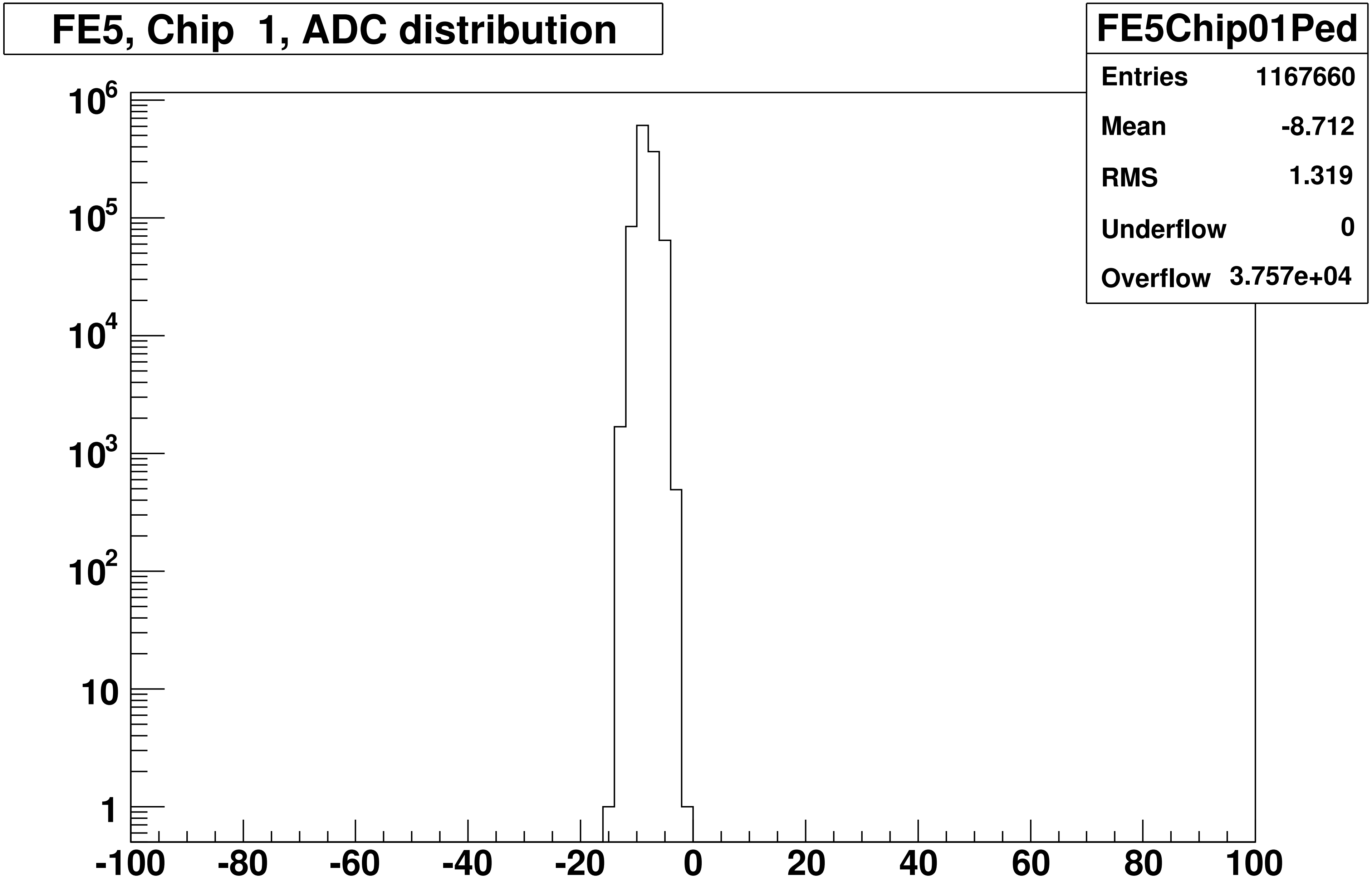
20000
25000



FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 1167660
Mean -8.712
RMS 1.319
Underflow 0
Overflow 3.757e+04
10^6
10^5
10^4
10^3
10^2
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1
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-40
-20
0
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80
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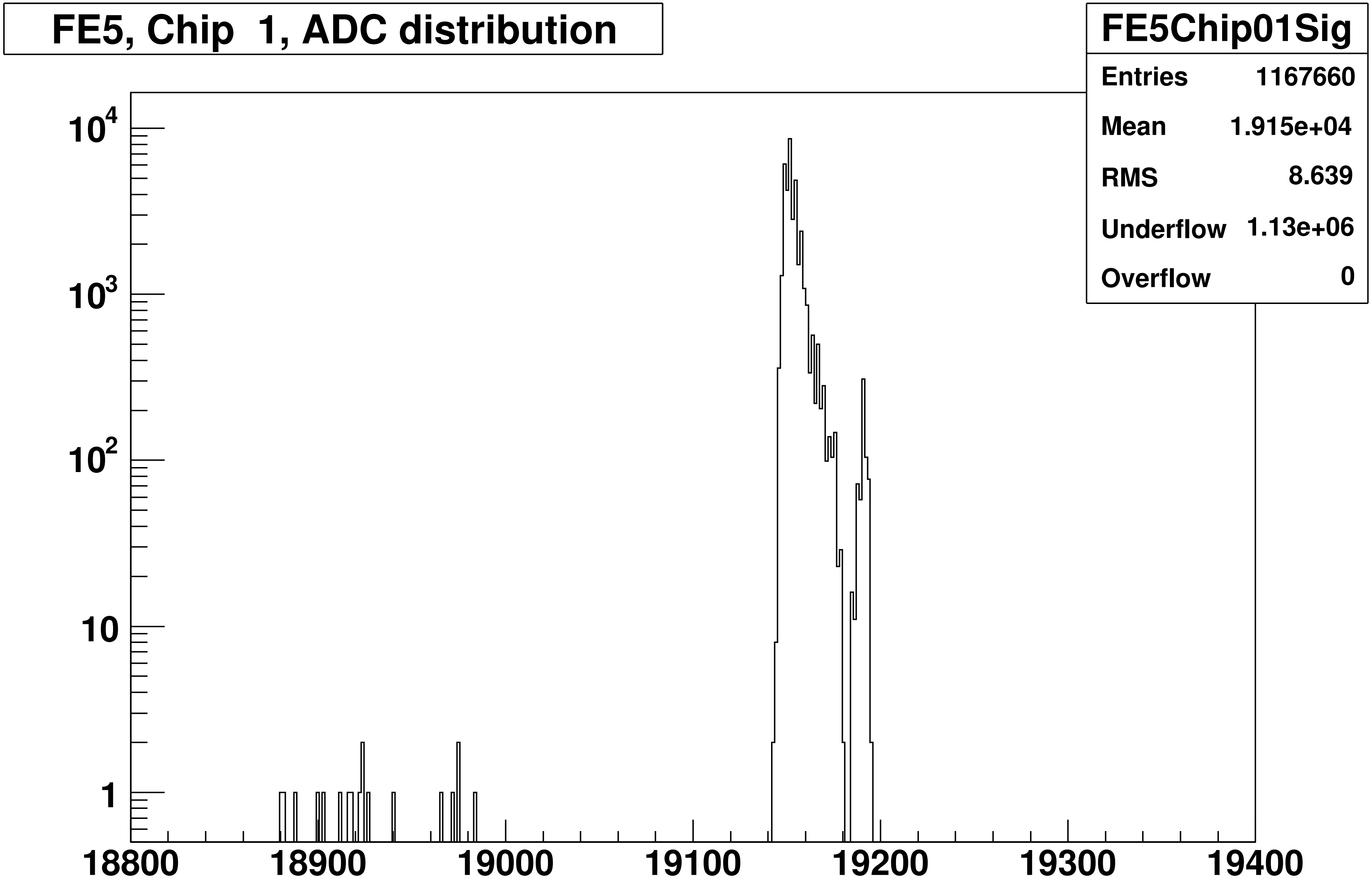
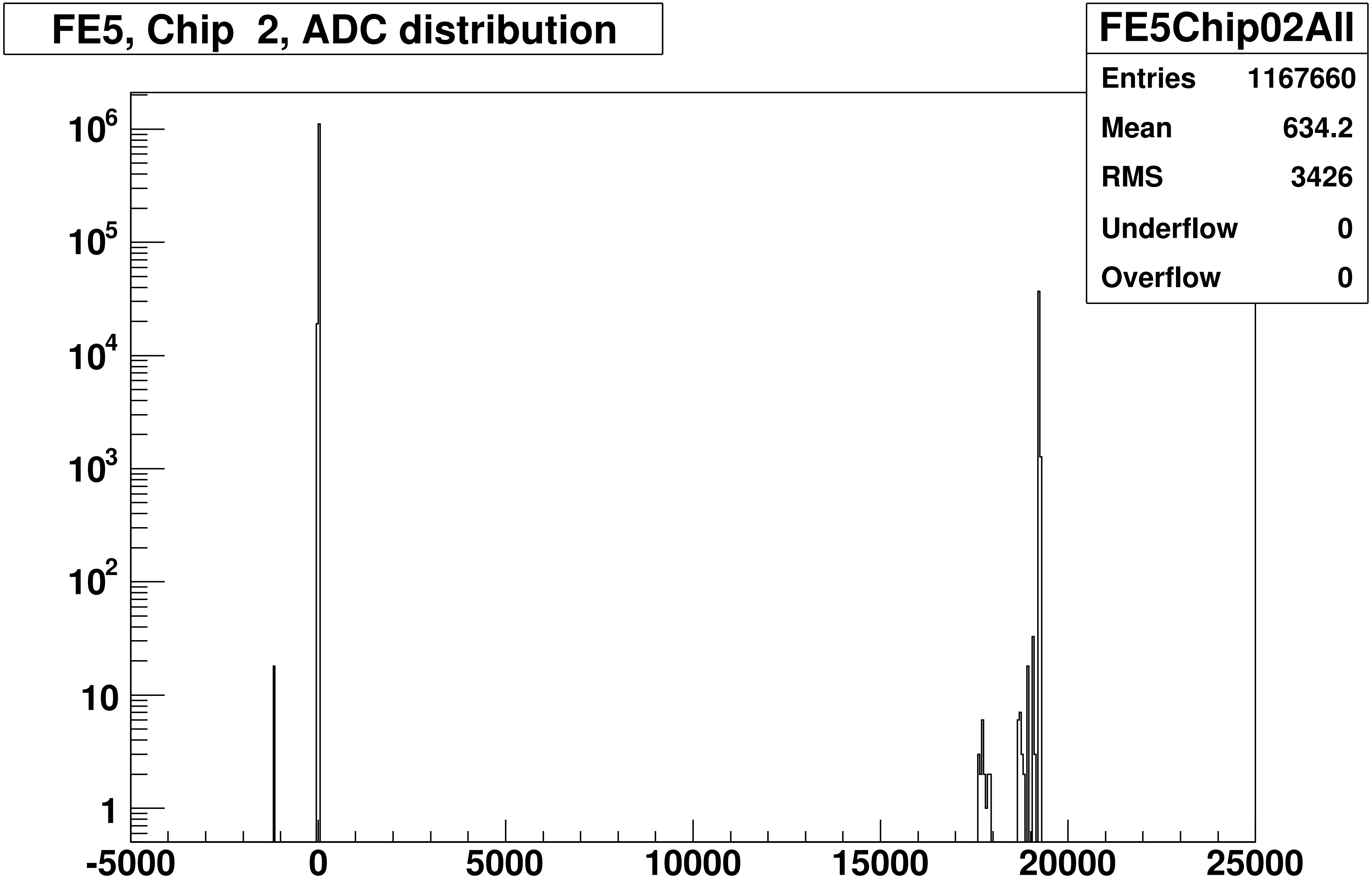


FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 1167660
Mean 1.915e+04
RMS 8.639
Underflow 1.13e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 1167660
Mean 634.2
RMS 3426
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
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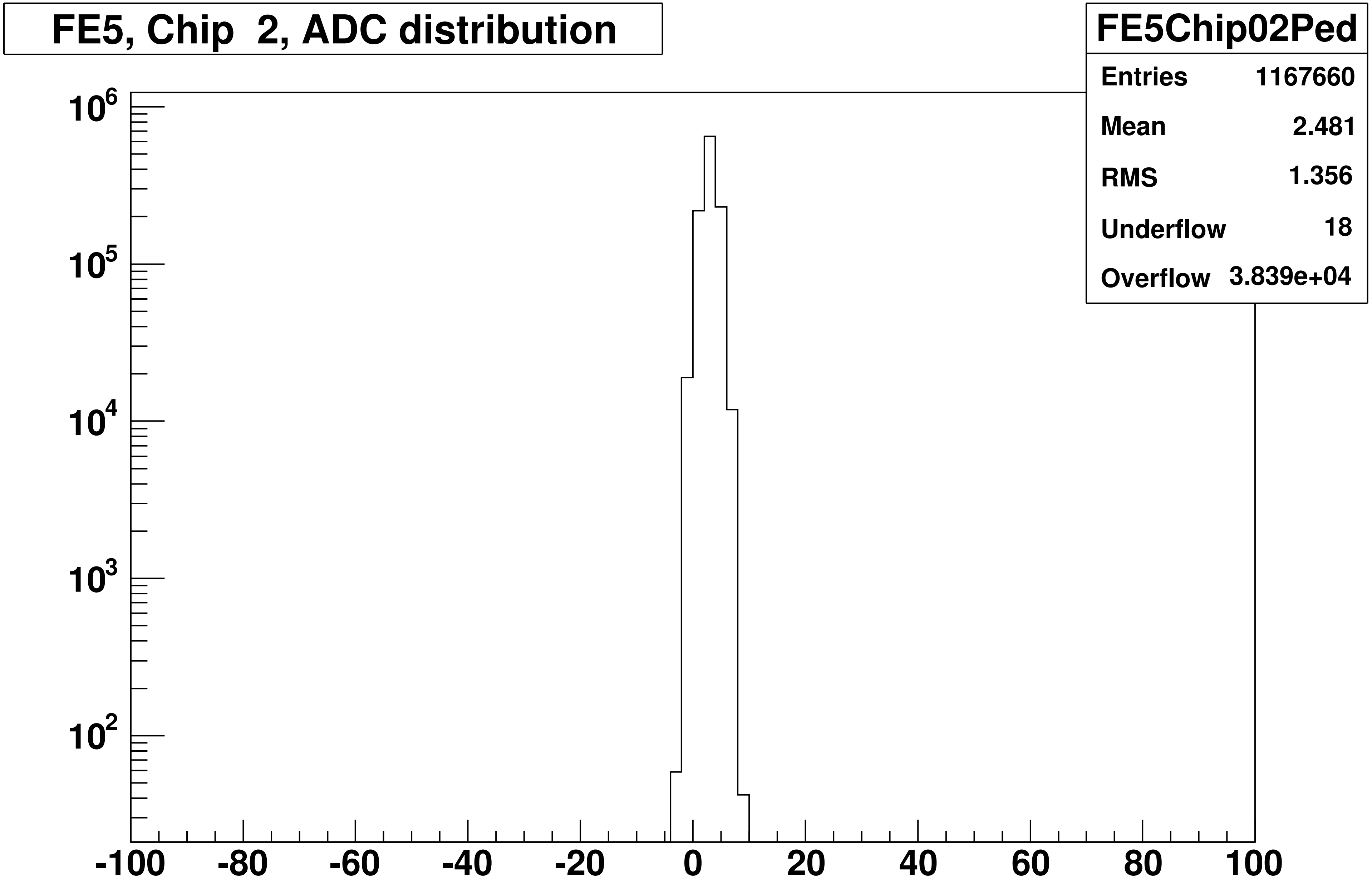
20000
25000


FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 1167660
Mean 2.481
RMS 1.356
Underflow 18
Overflow 3.839e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
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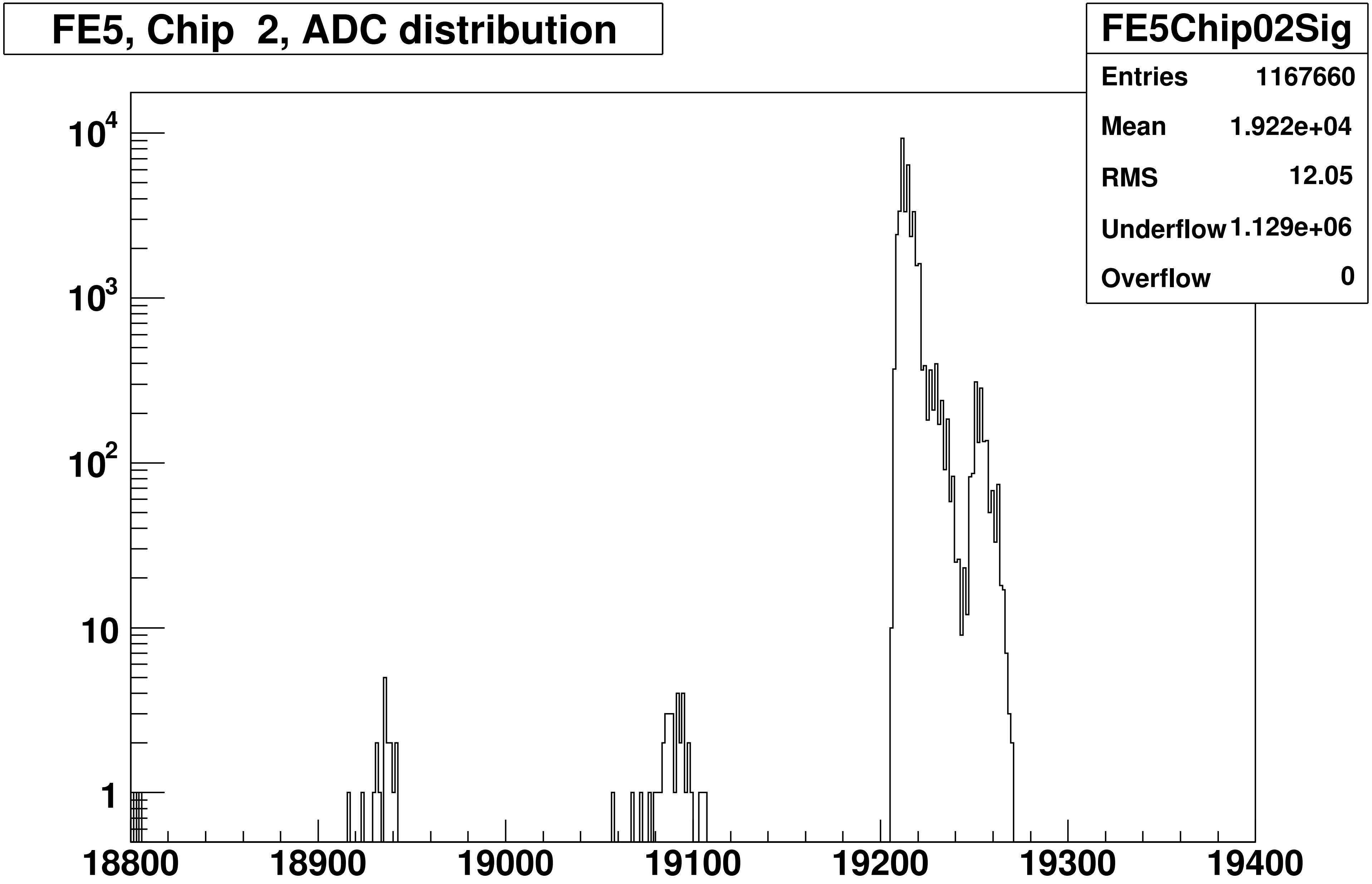
80
100


FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 1167660
Mean 1.922e+04
RMS 12.05
Underflow 1.129e+06
Overflow 0
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10^3
10^2
10
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18800
18900
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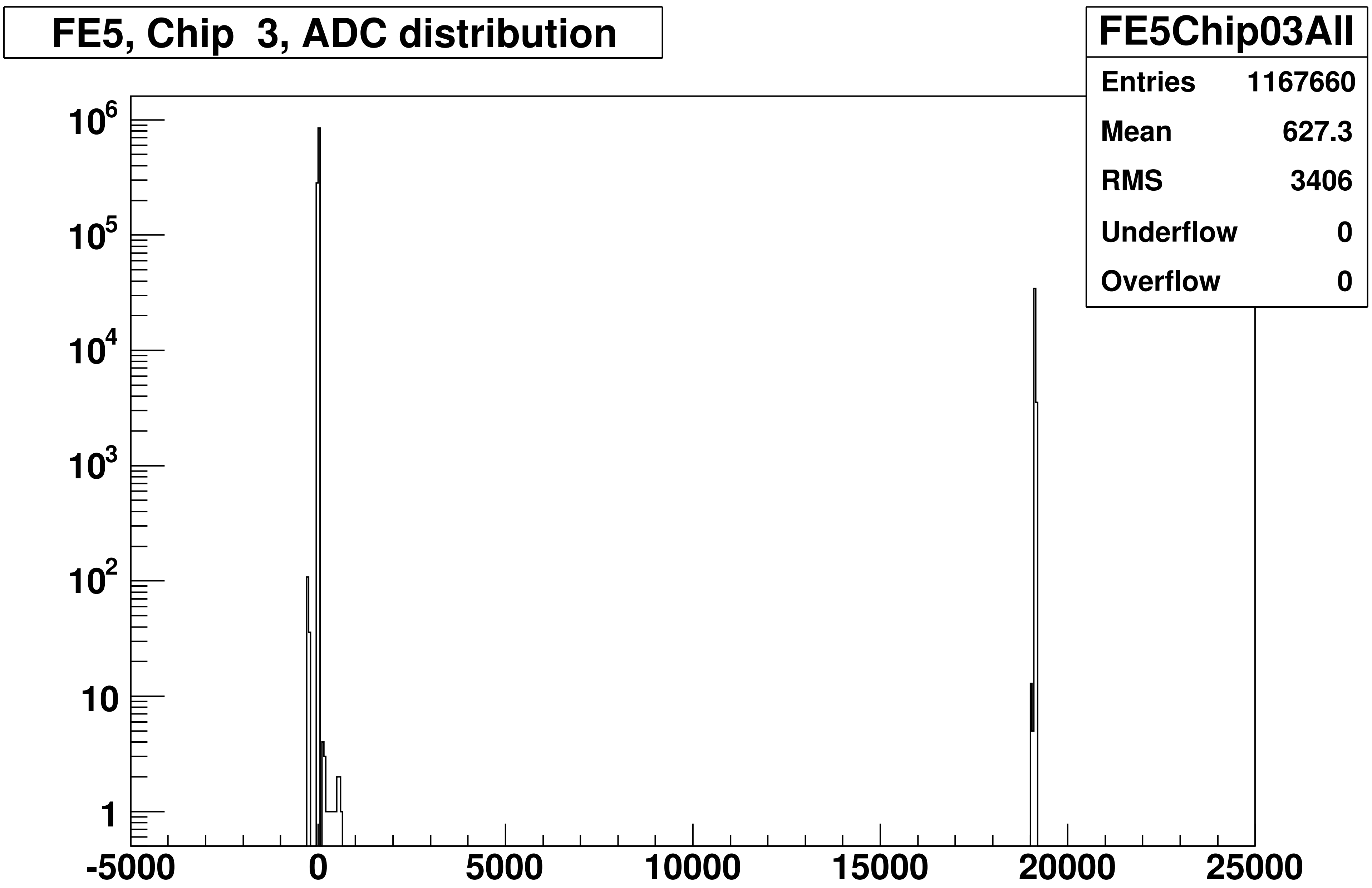

FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 1167660
Mean 627.3
RMS 3406
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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5000
10000
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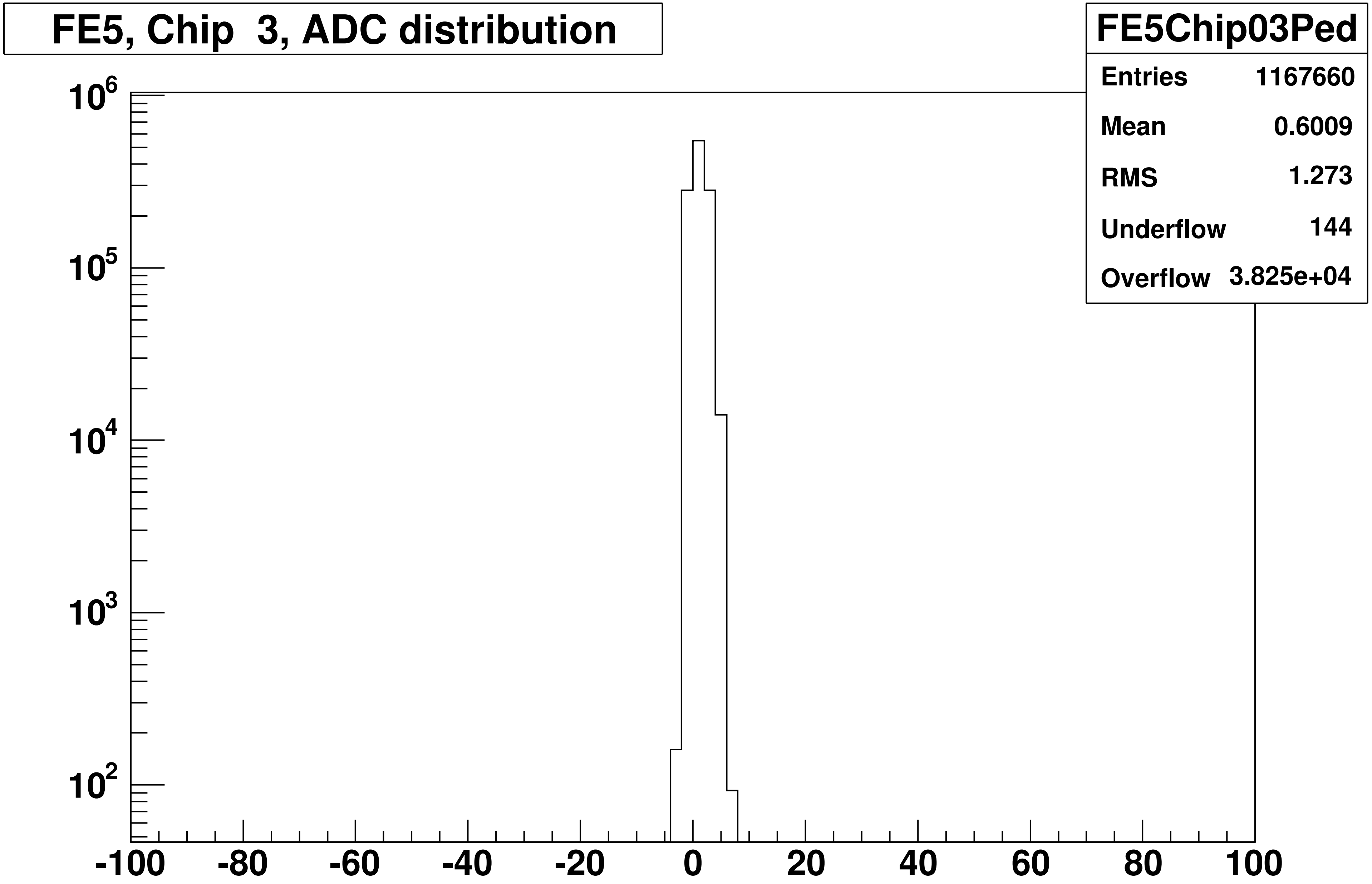
FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 1167660
Mean 0.6009
RMS 1.273
Underflow 144
Overflow 3.825e+04
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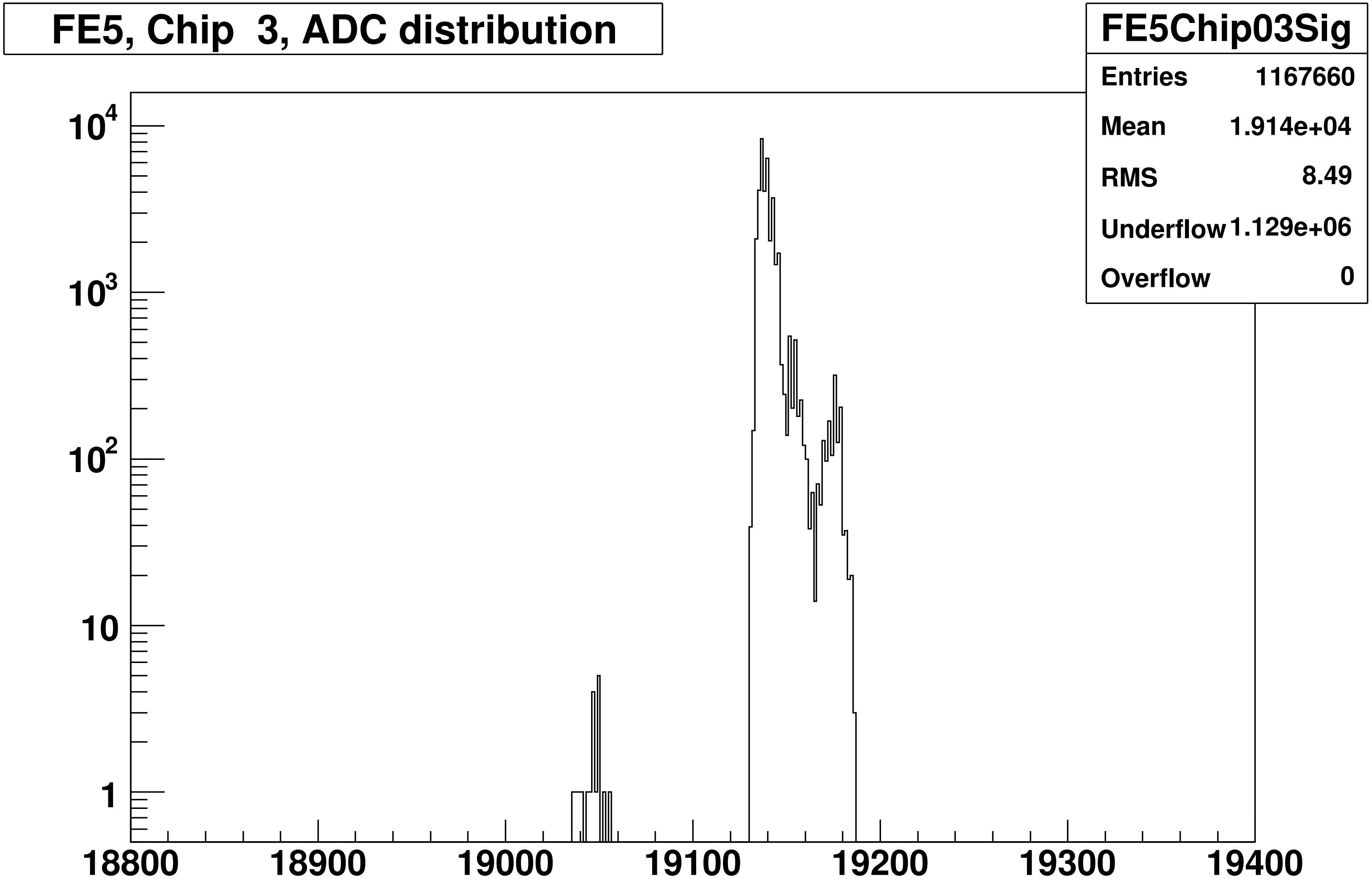
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 1167660
Mean 1.914e+04
RMS 8.49
Underflow 1.129e+06
Overflow 0
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10^3
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10
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18800
18900
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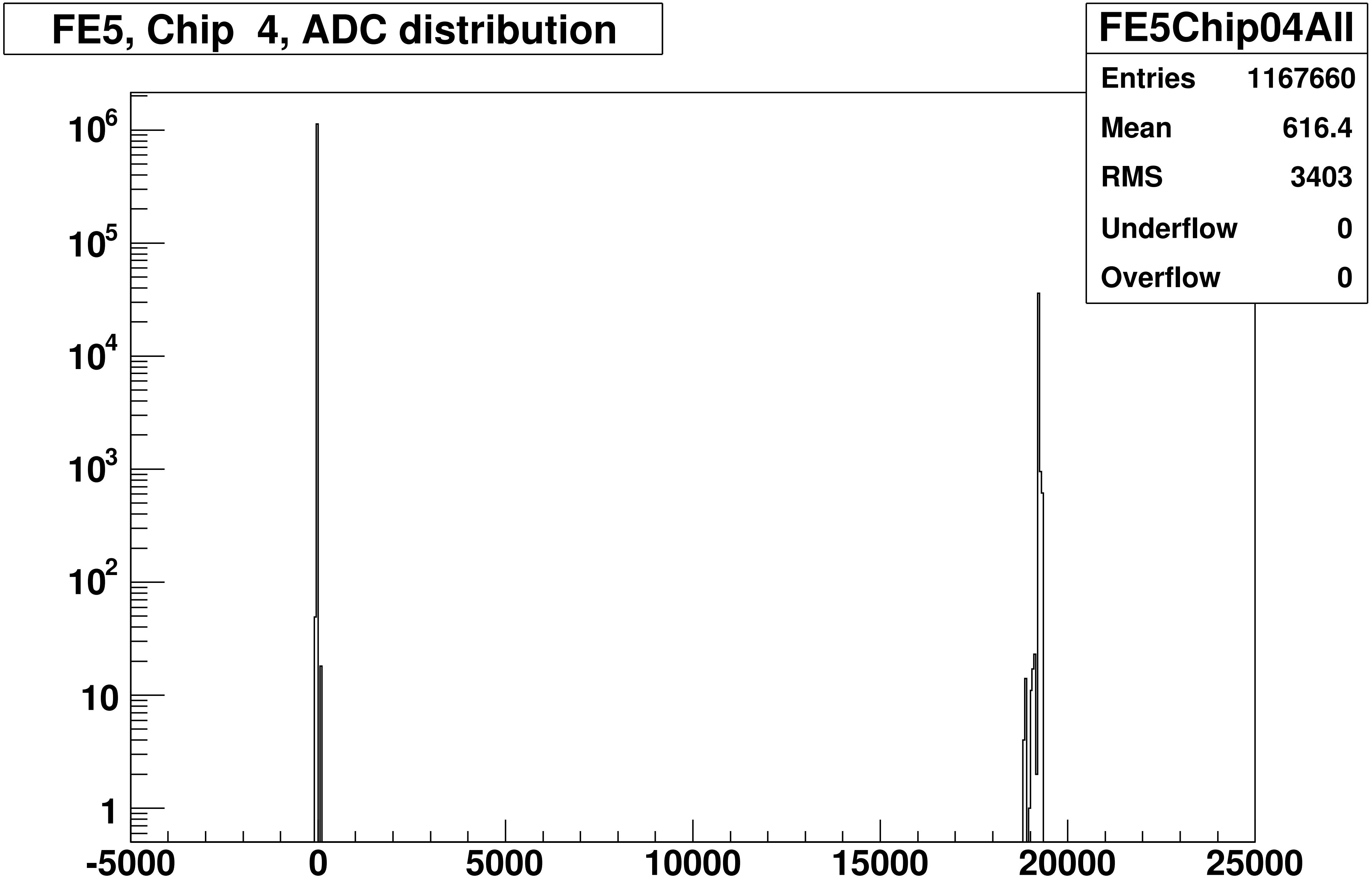

FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 1167660
Mean 616.4
RMS 3403
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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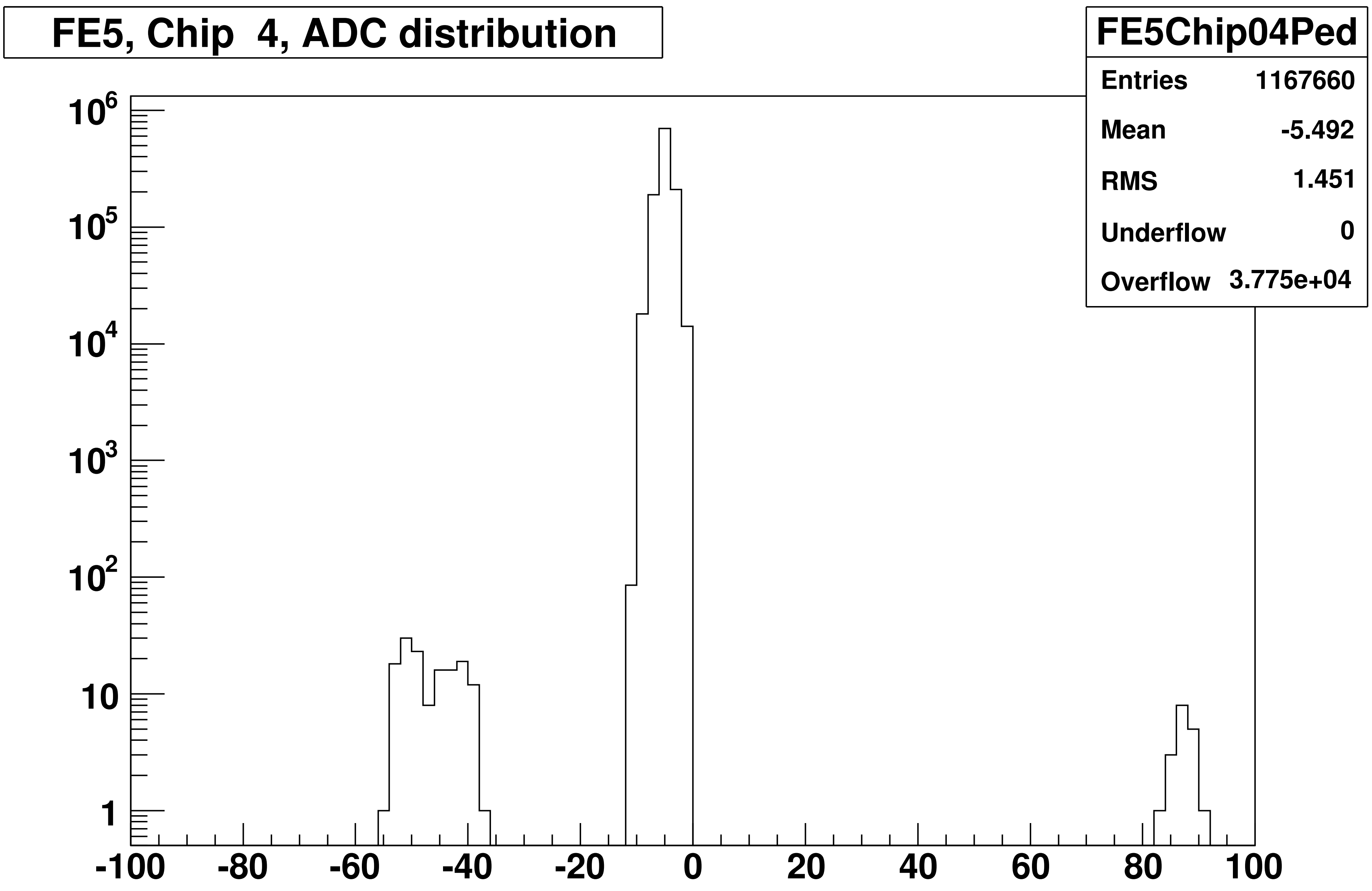
FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 1167660
Mean -5.492
RMS 1.451
Underflow 0
Overflow 3.775e+04
10^6
10^5
10^4
10^3
10^2
10
1
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-20
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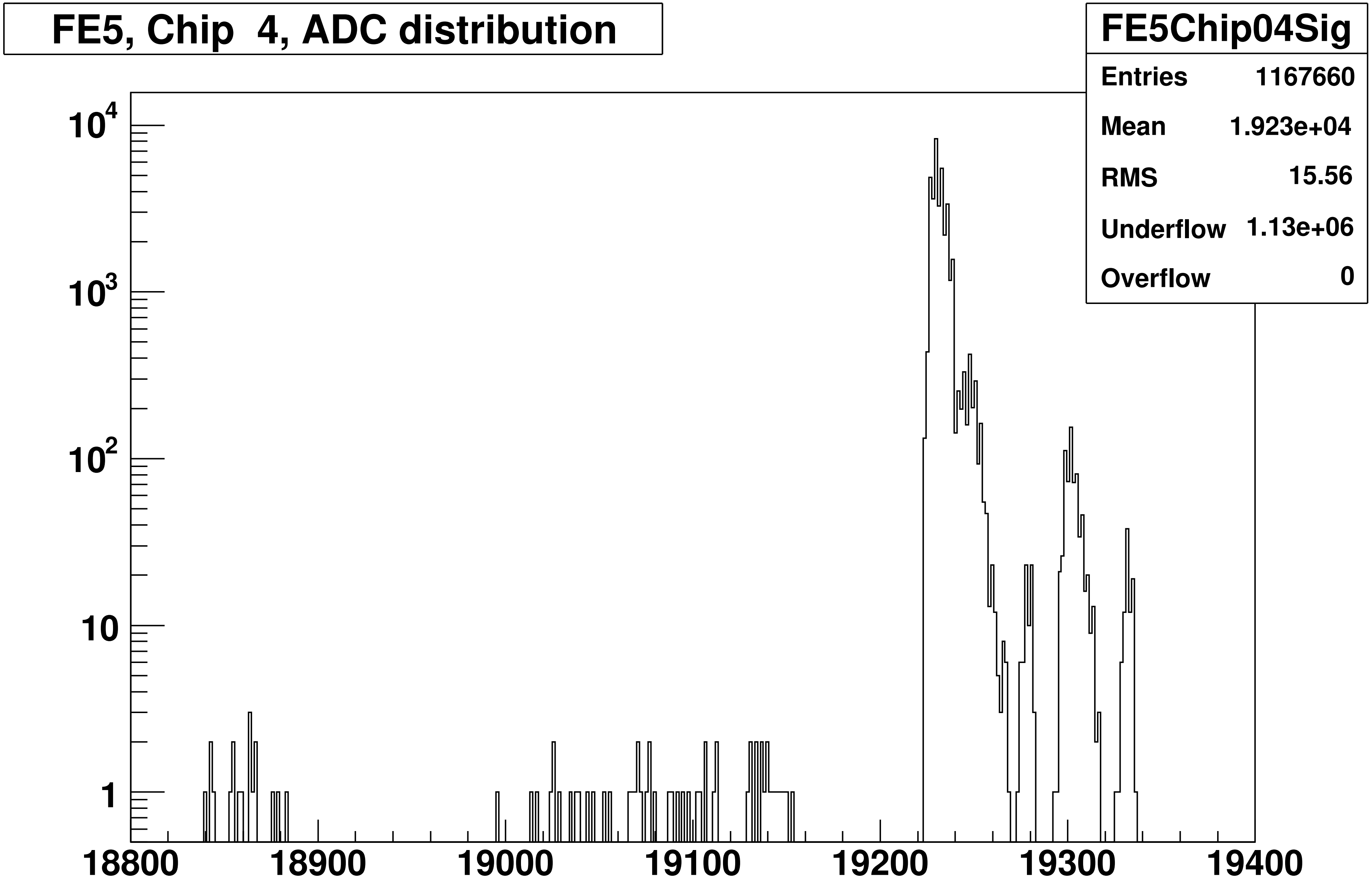
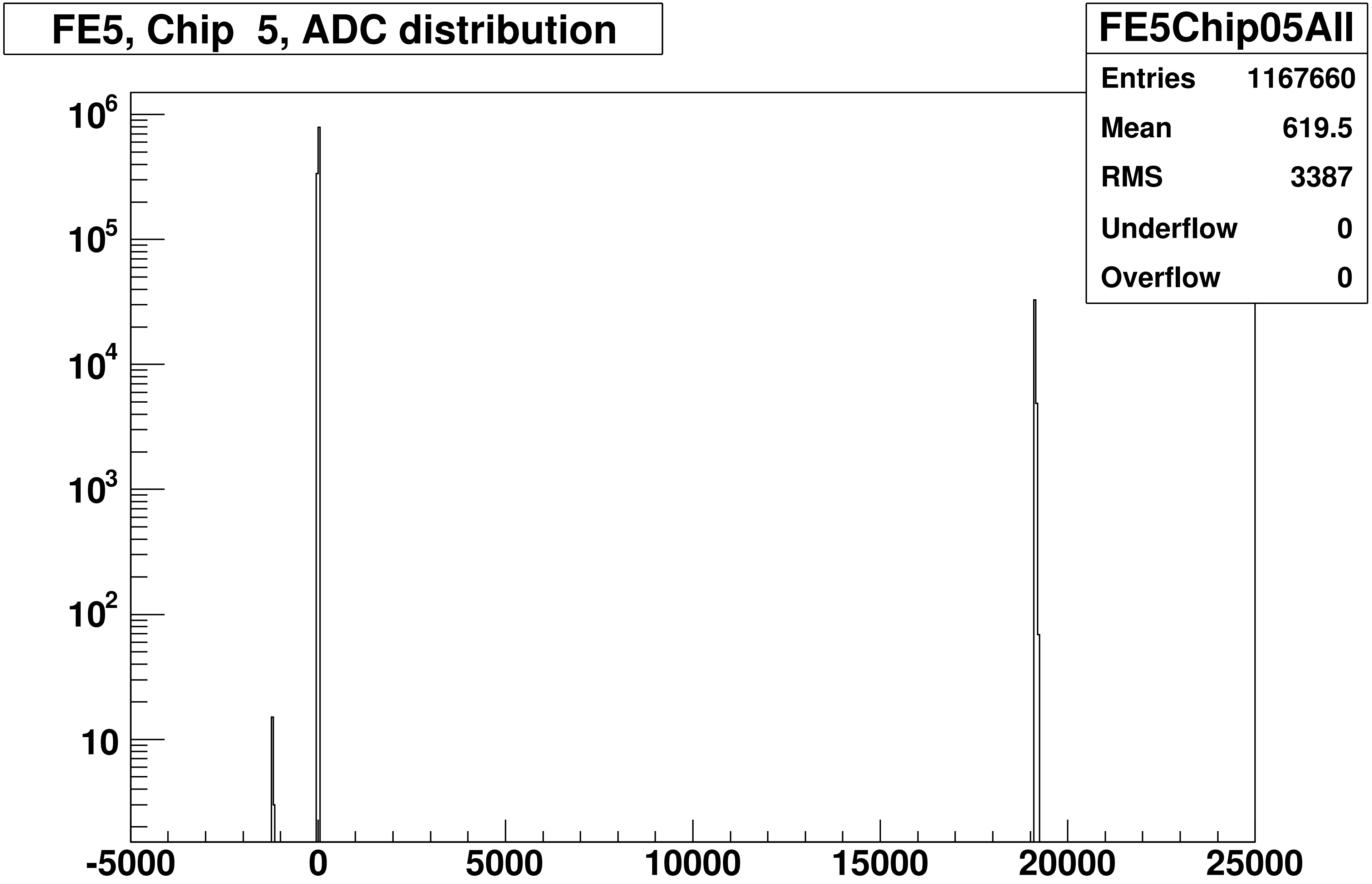
60
80
100


FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 1167660
Mean 1.923e+04
RMS 15.56
Underflow 1.13e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 1167660
Mean 619.5
RMS 3387
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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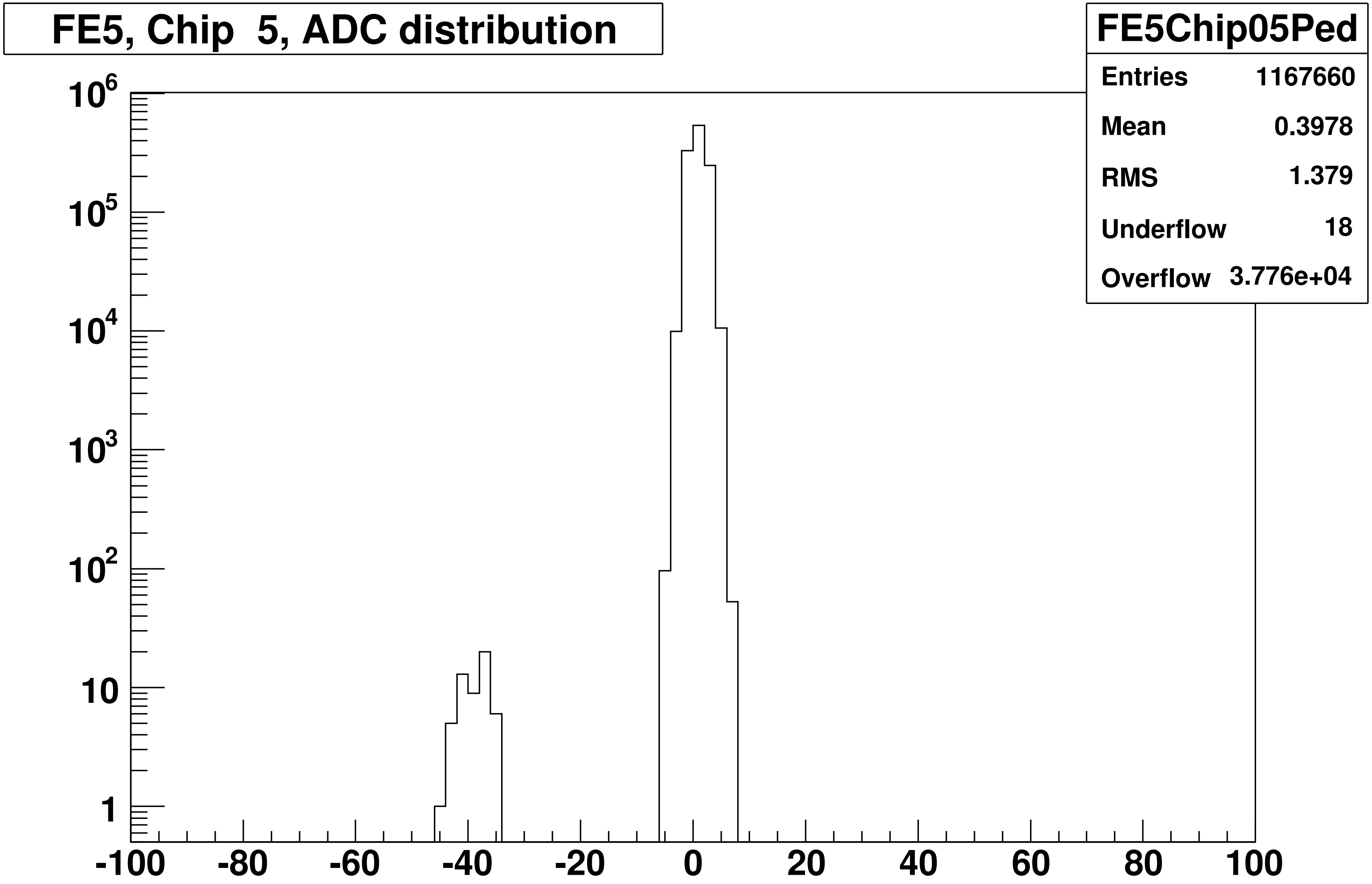
10000
15000
20000
25000



FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 1167660
Mean 0.3978
RMS 1.379
Underflow 18
Overflow 3.776e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
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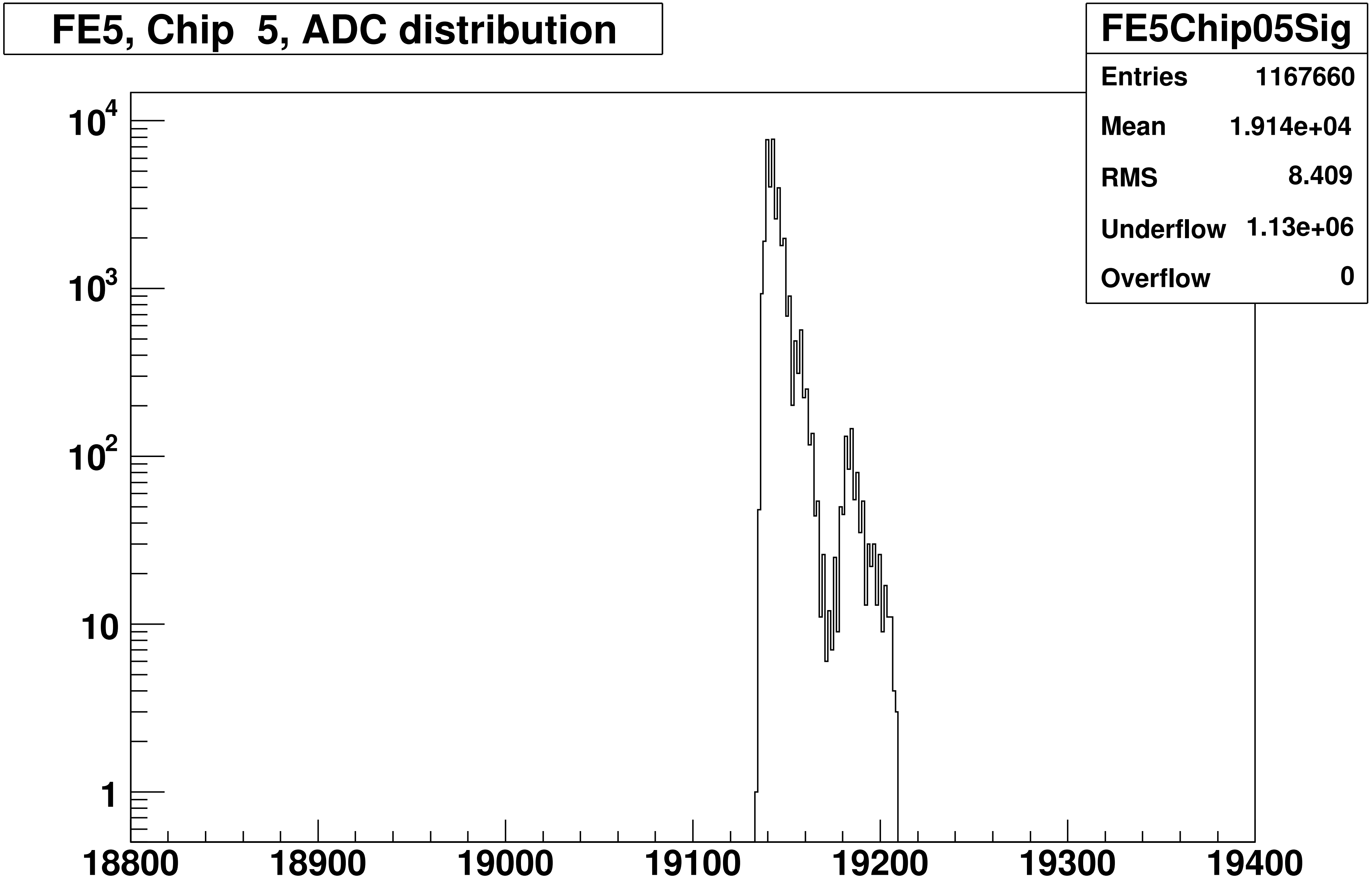
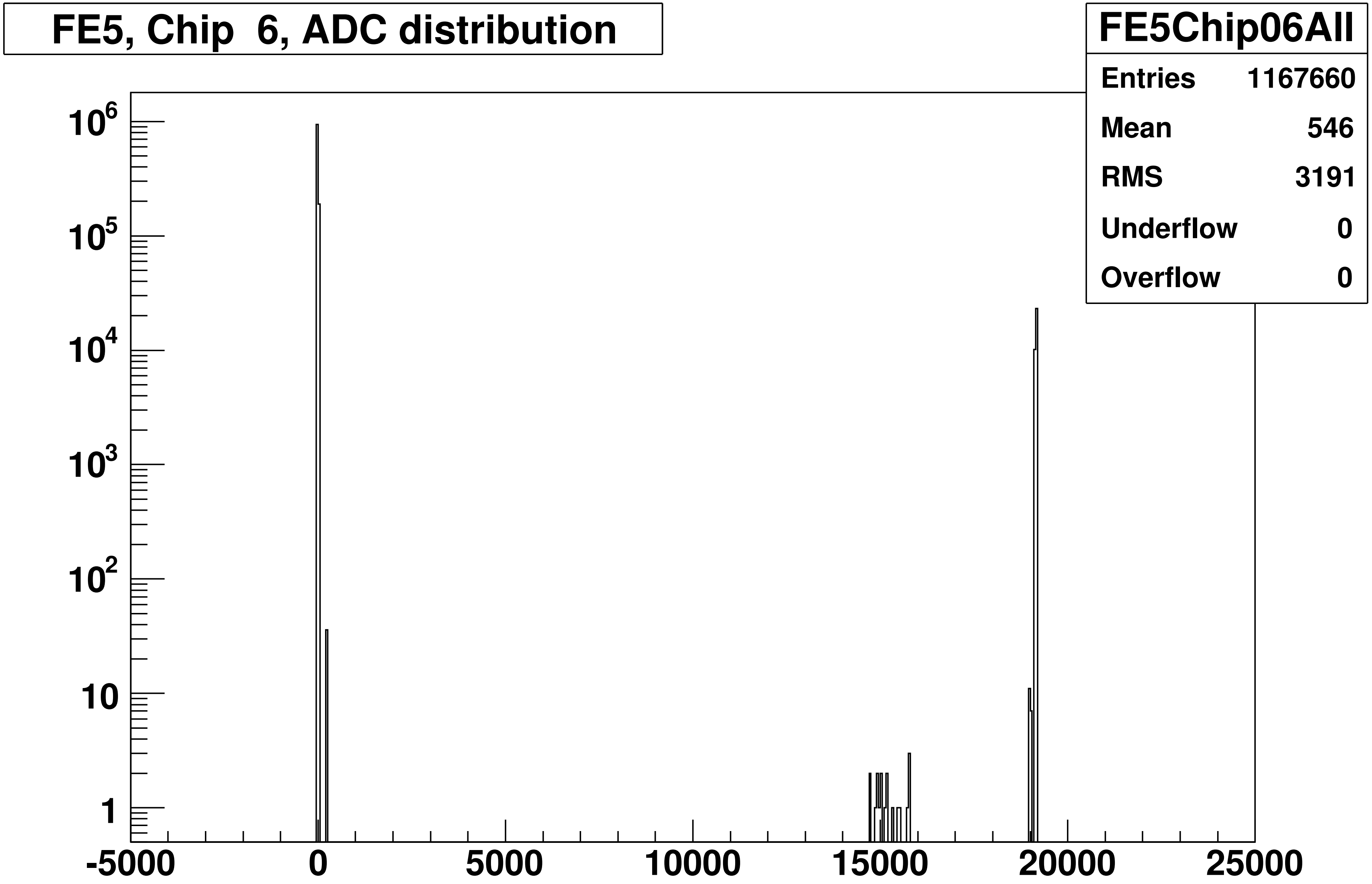
-40
-20
0
20
40
60
80
100



FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 1167660
Mean 1.914e+04
RMS 8.409
Underflow 1.13e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 1167660
Mean 546
RMS 3191
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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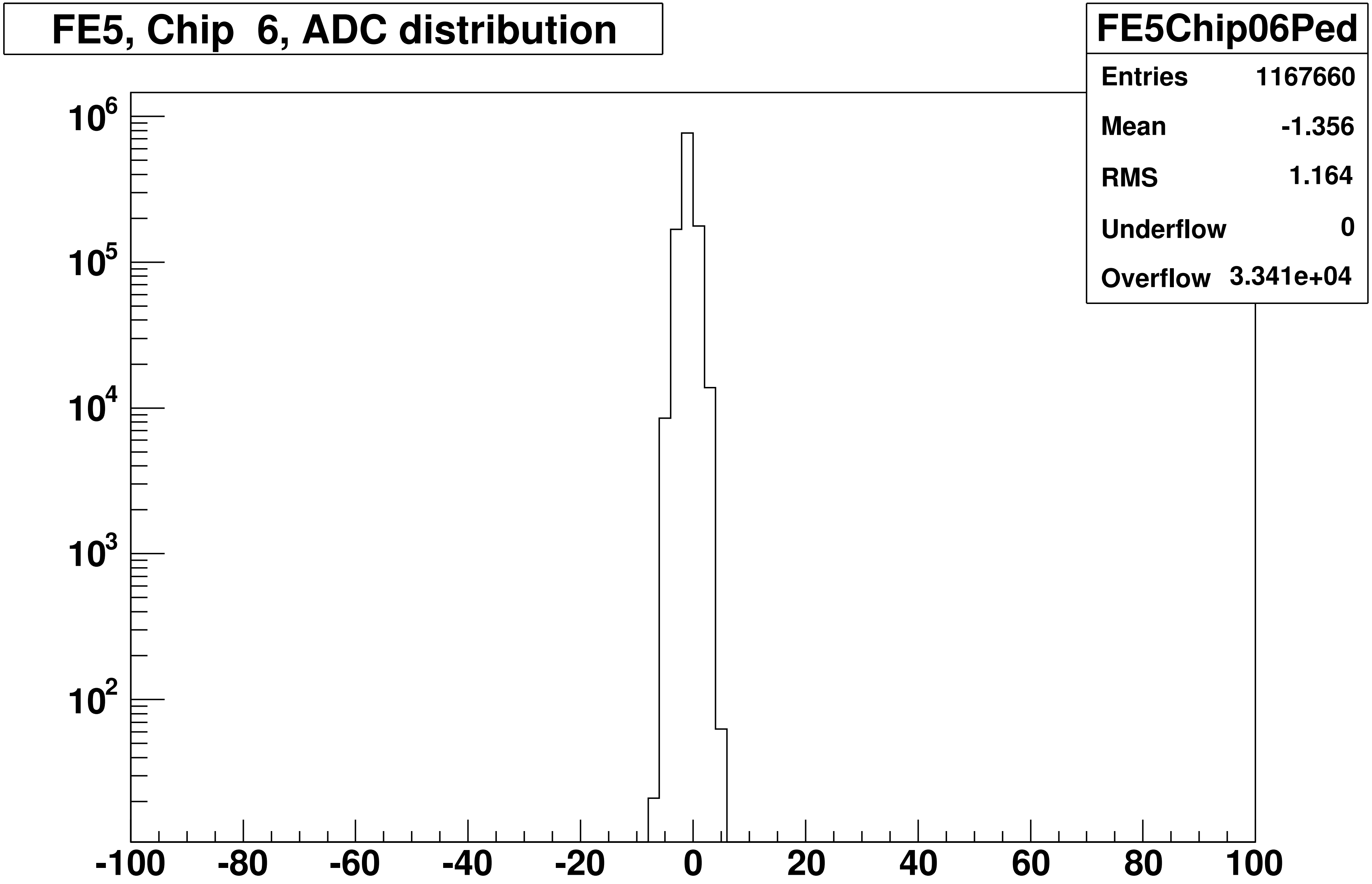
10000
15000
20000
25000


FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 1167660
Mean -1.356
RMS 1.164
Underflow 0
Overflow 3.341e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
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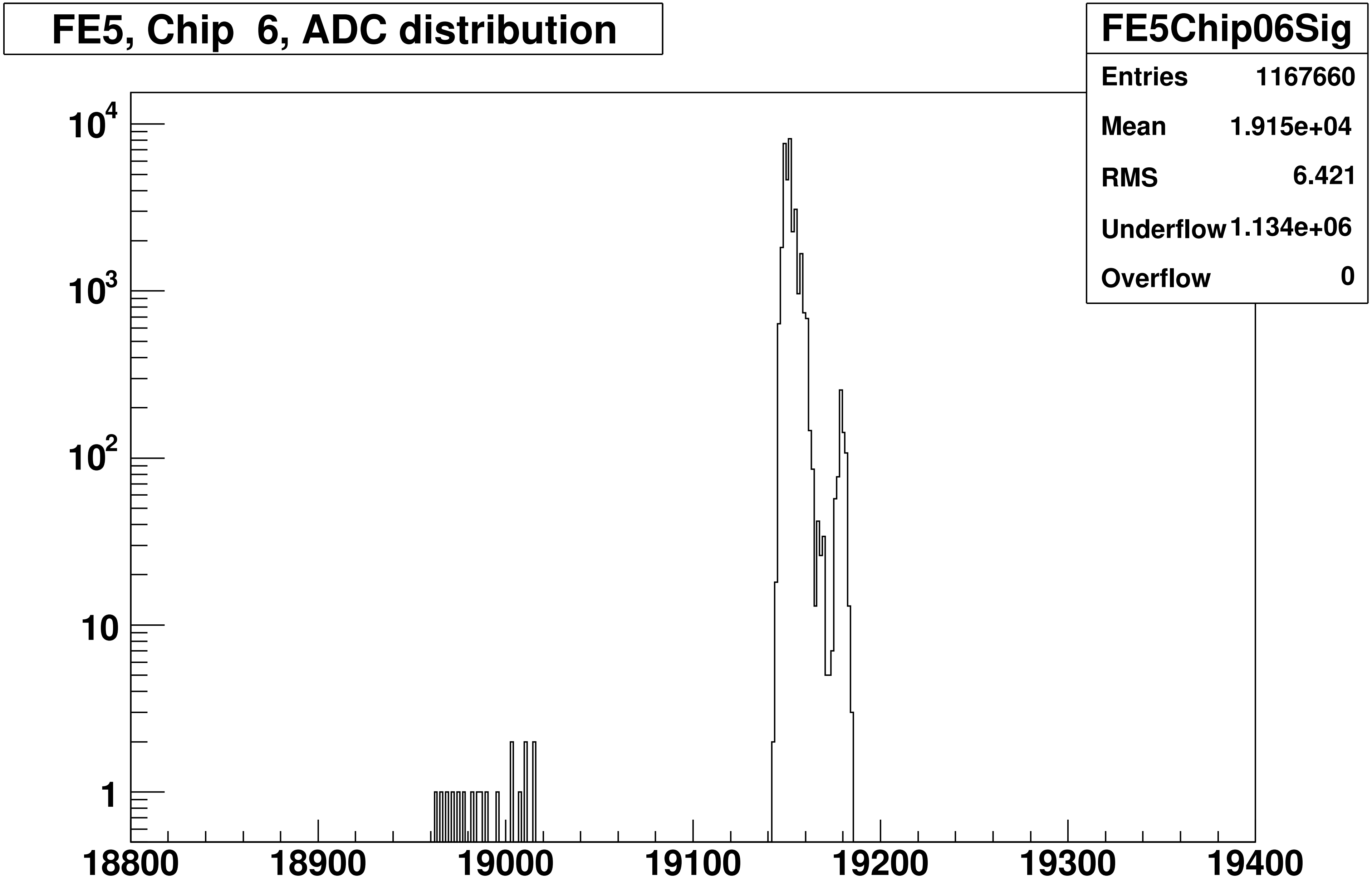
FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 1167660
Mean 1.915e+04
RMS 6.421
Underflow 1.134e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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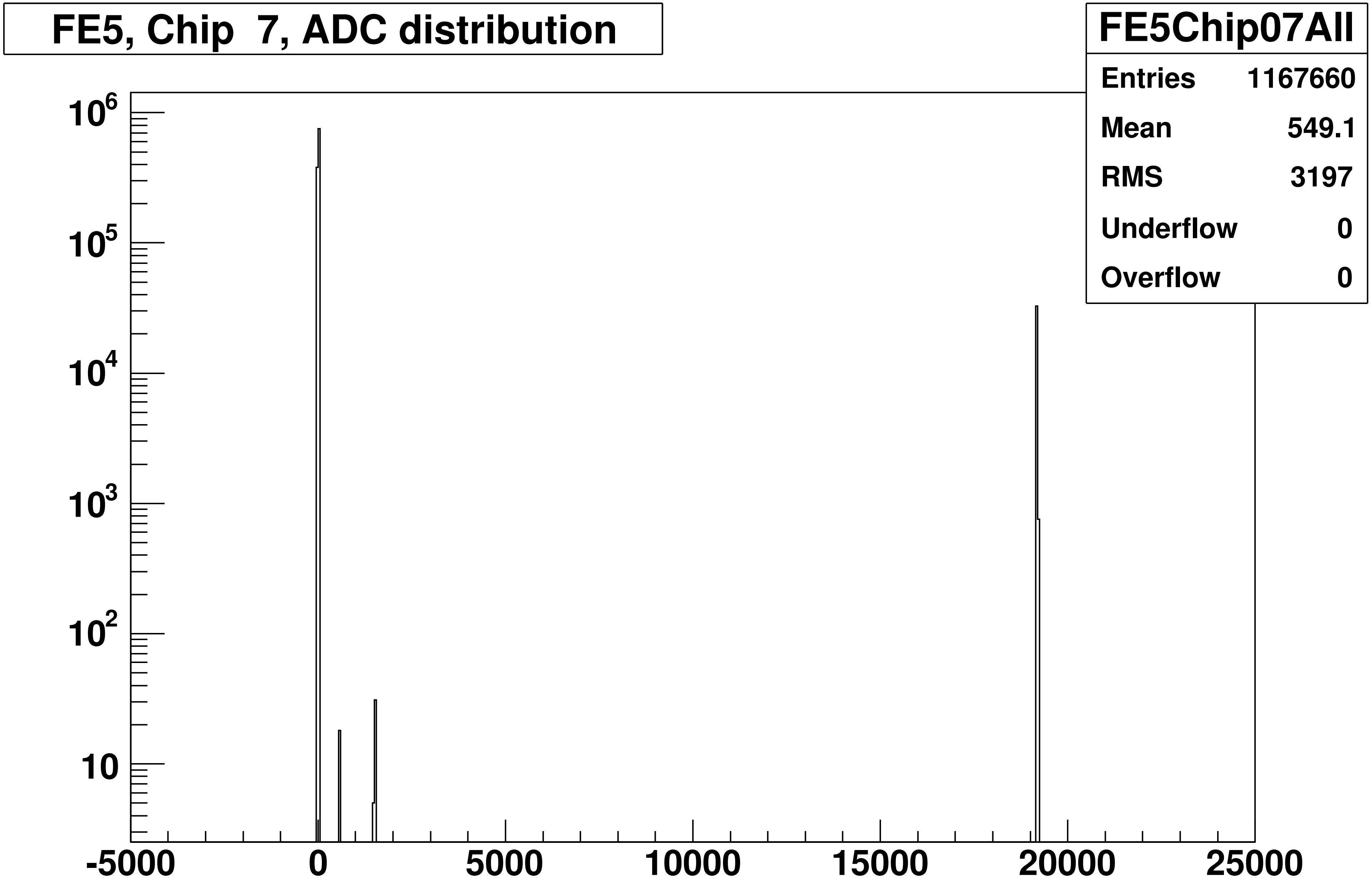
19200
19300
19400


FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 1167660
Mean 549.1
RMS 3197
Underflow 0
Overflow 0
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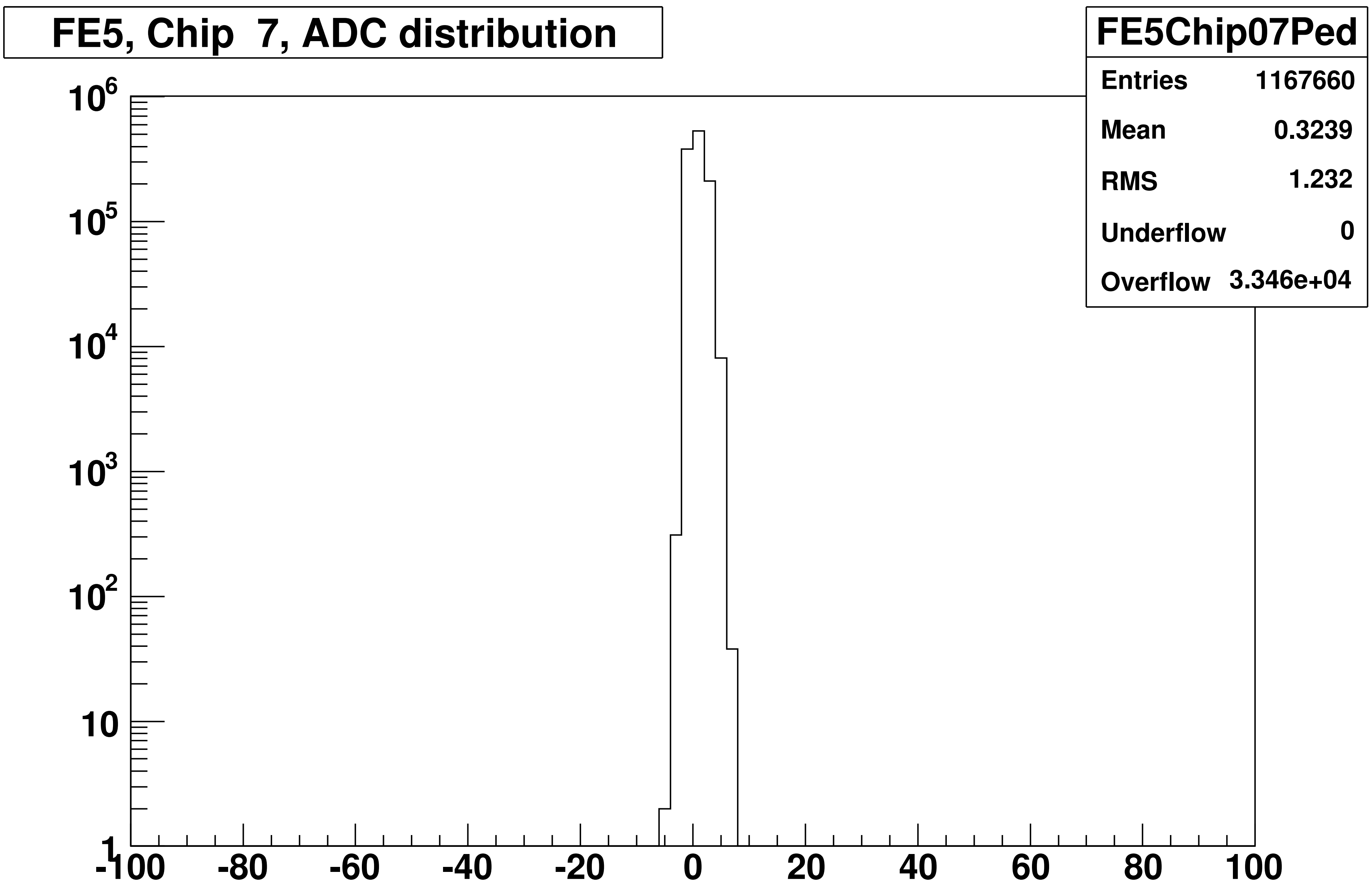
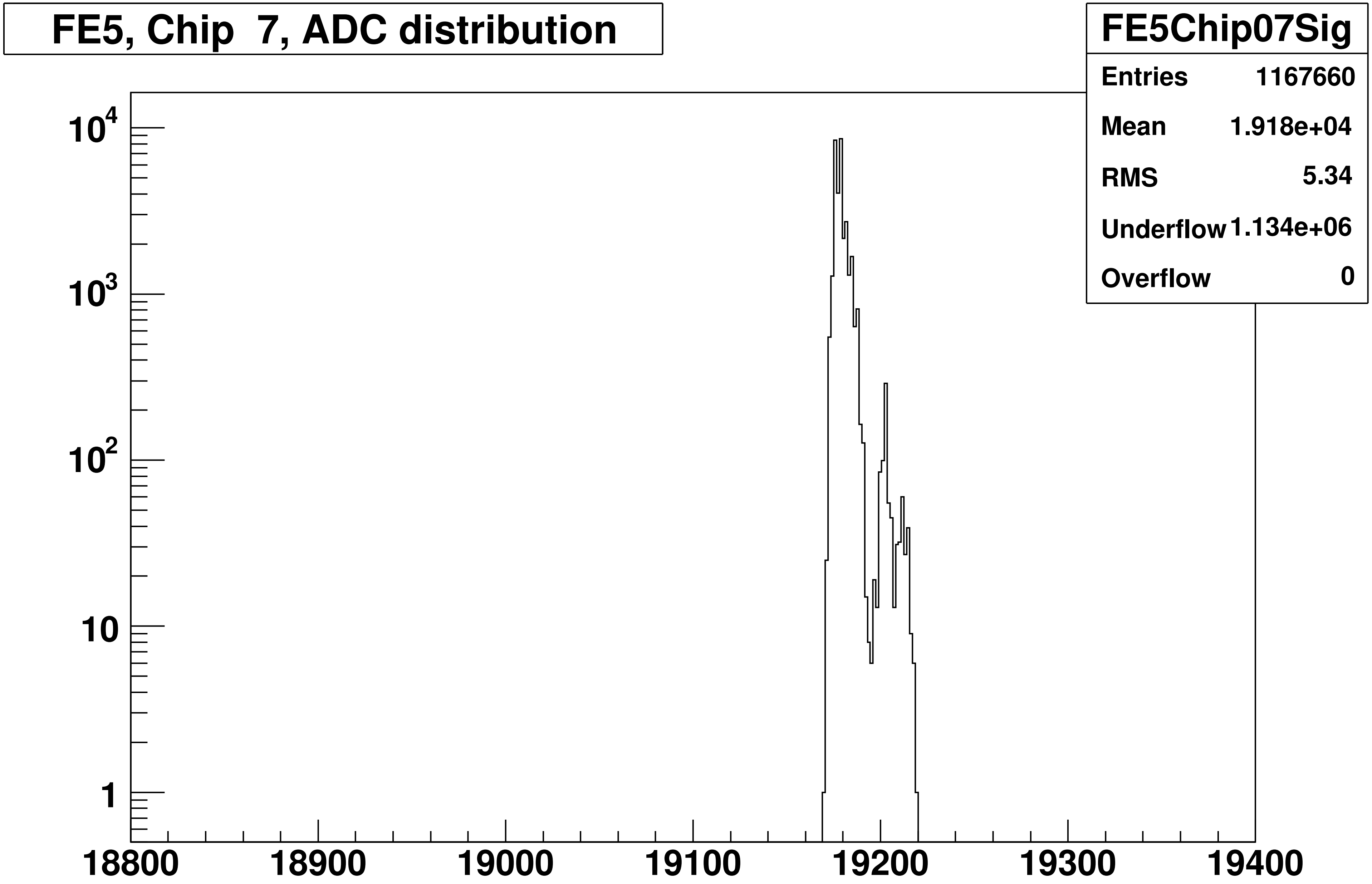
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 1167660
Mean 0.3239
RMS 1.232
Underflow 0
Overflow 3.346e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 1167660
Mean 1.918e+04
RMS 5.34
Underflow 1.134e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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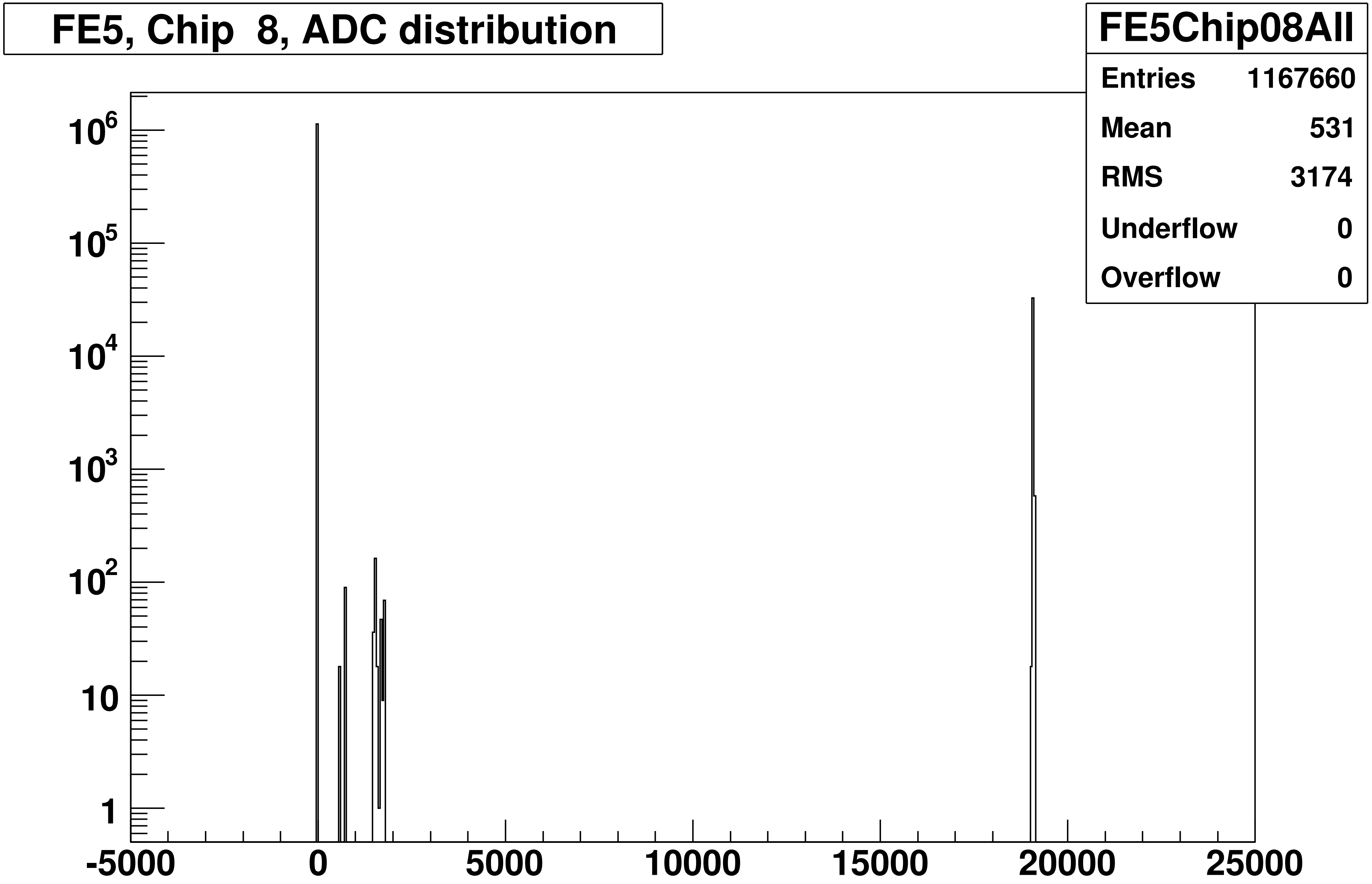
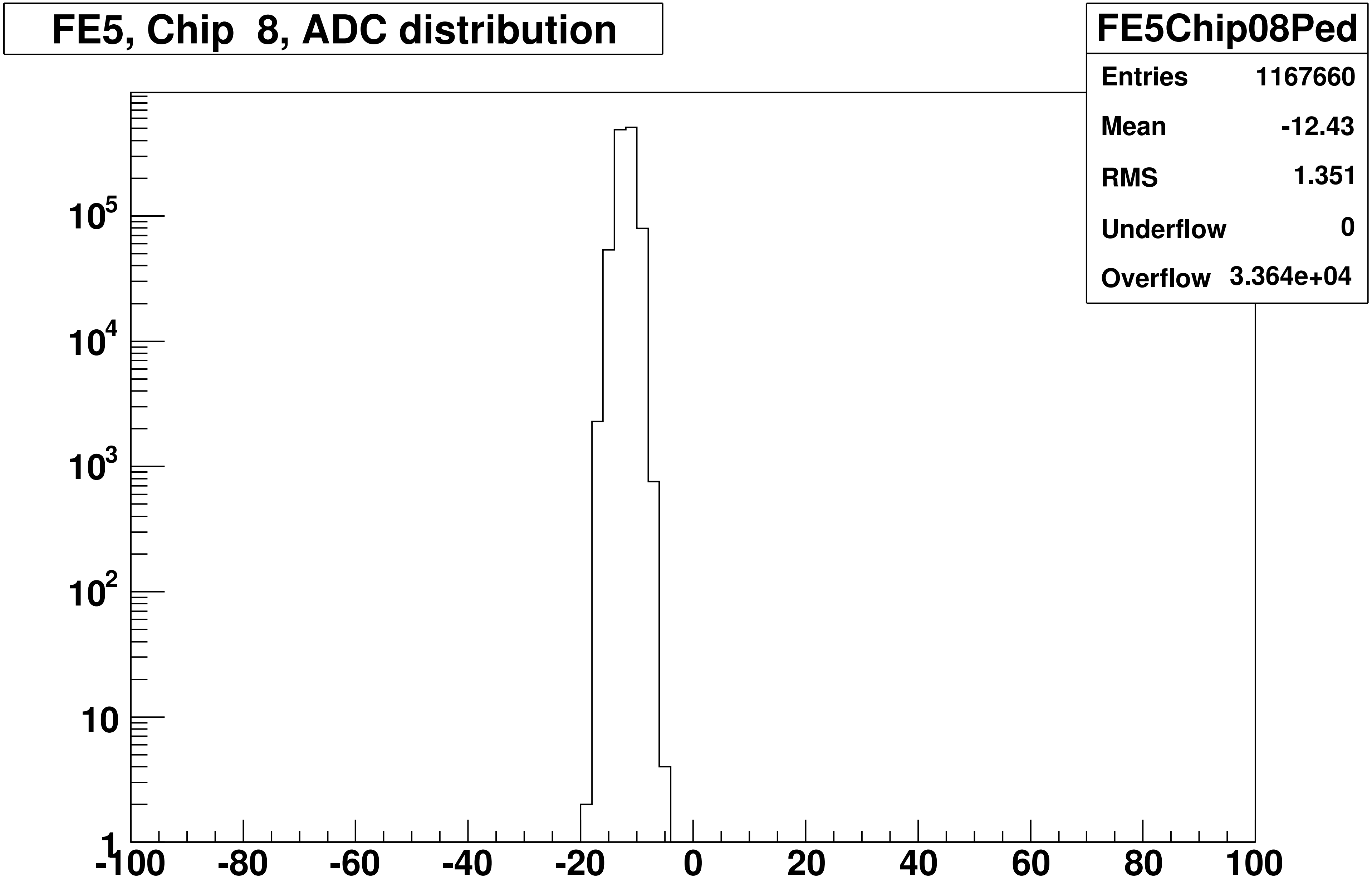
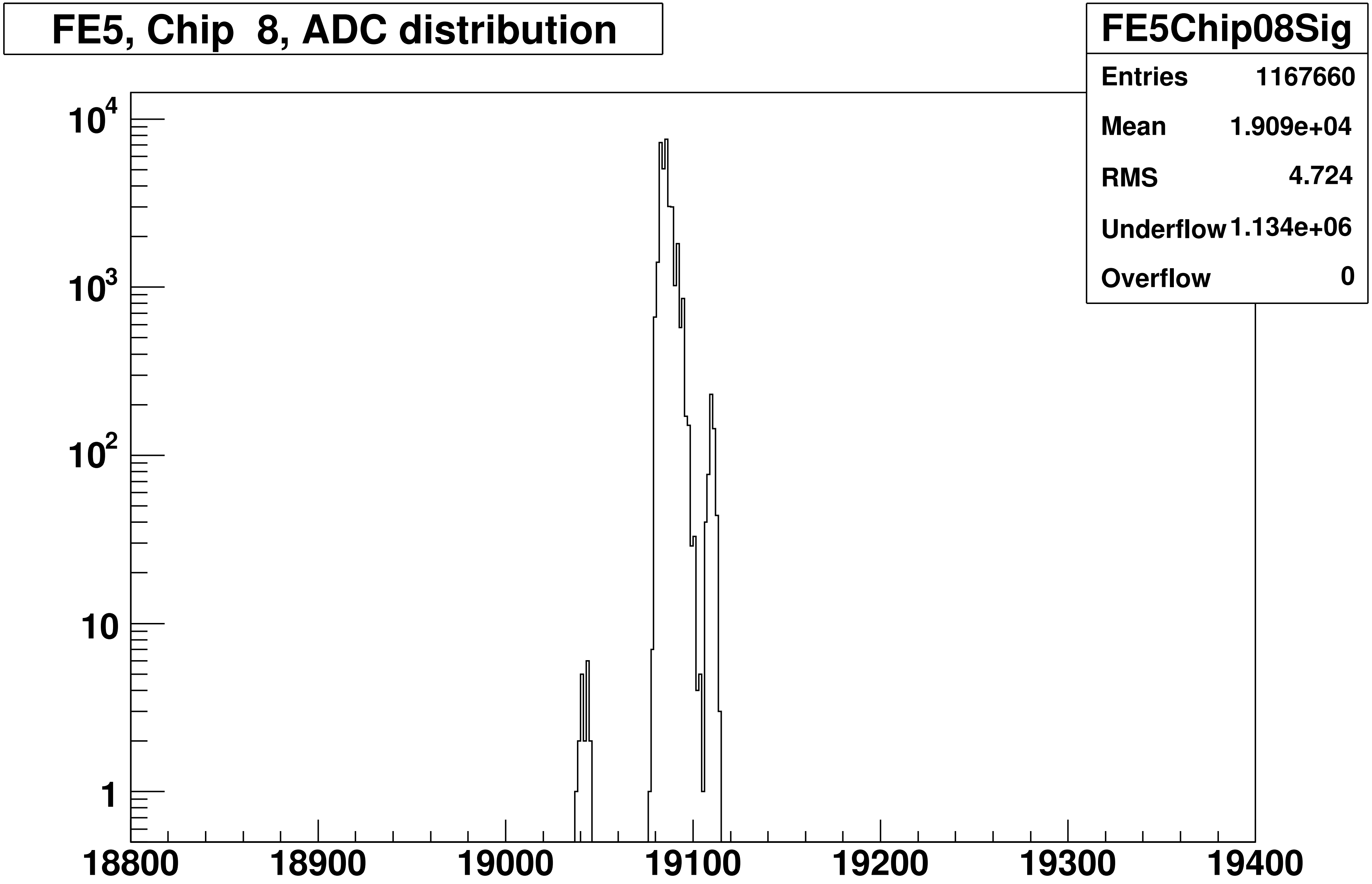
19400


FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 1167660
Mean 531
RMS 3174
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 1167660
Mean -12.43
RMS 1.351
Underflow 0
Overflow 3.364e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 1167660
Mean 1.909e+04
RMS 4.724
Underflow 1.134e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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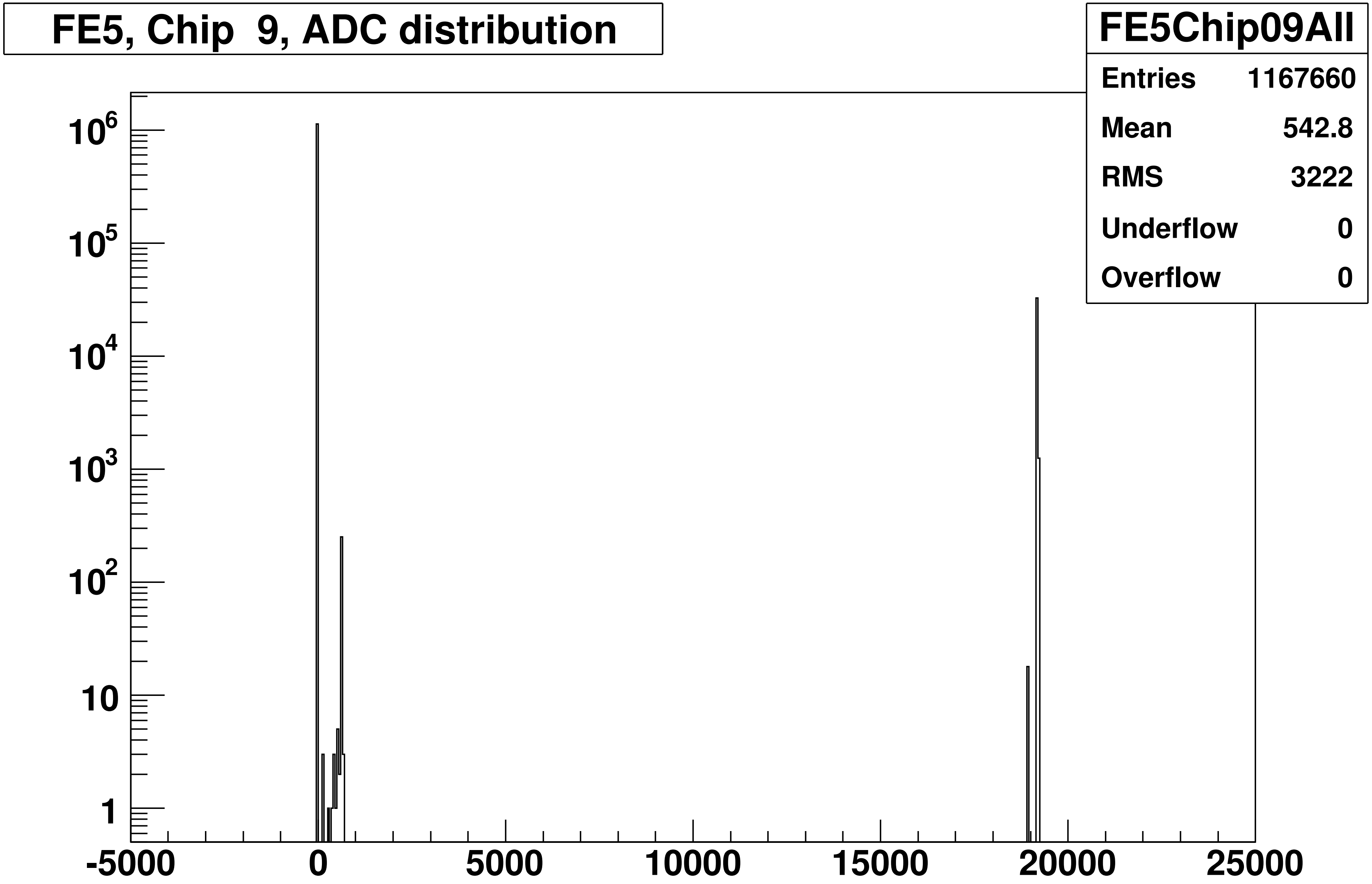
19100
19200
19300
19400


FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 1167660
Mean 542.8
RMS 3222
Underflow 0
Overflow 0
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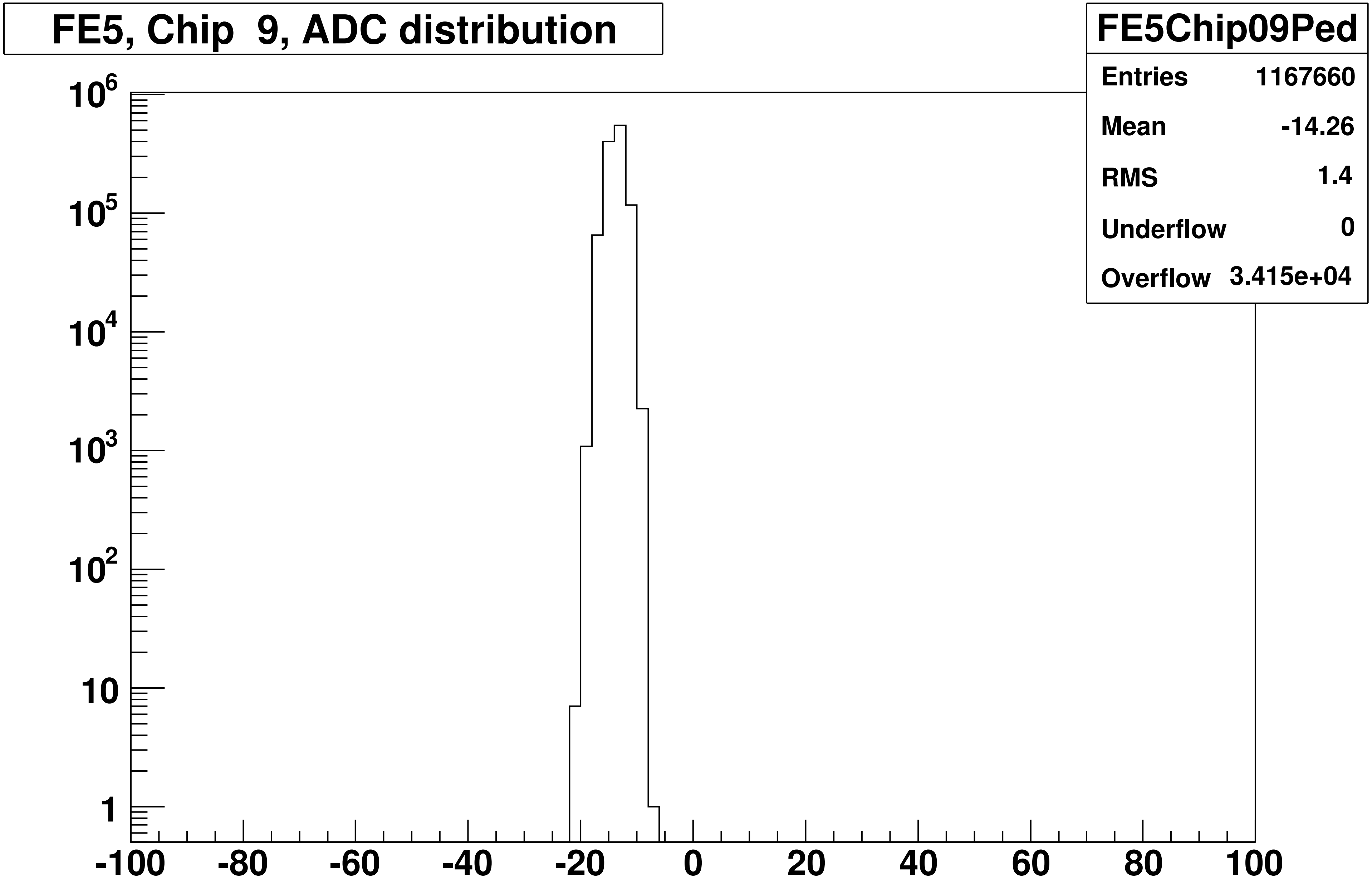
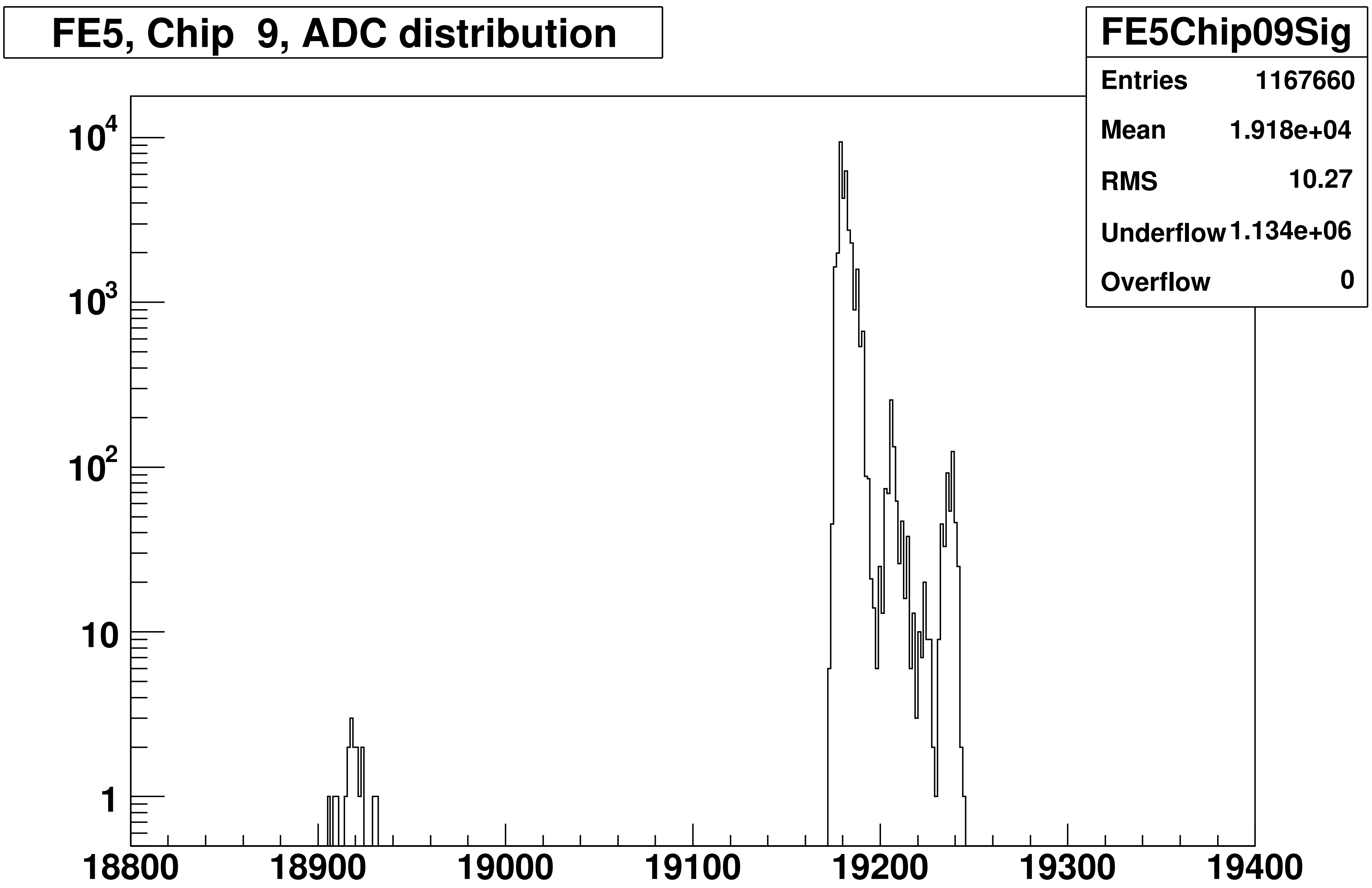
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 1167660
Mean -14.26
RMS 1.4
Underflow 0
Overflow 3.415e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 1167660
Mean 1.918e+04
RMS 10.27
Underflow 1.134e+06
Overflow 0
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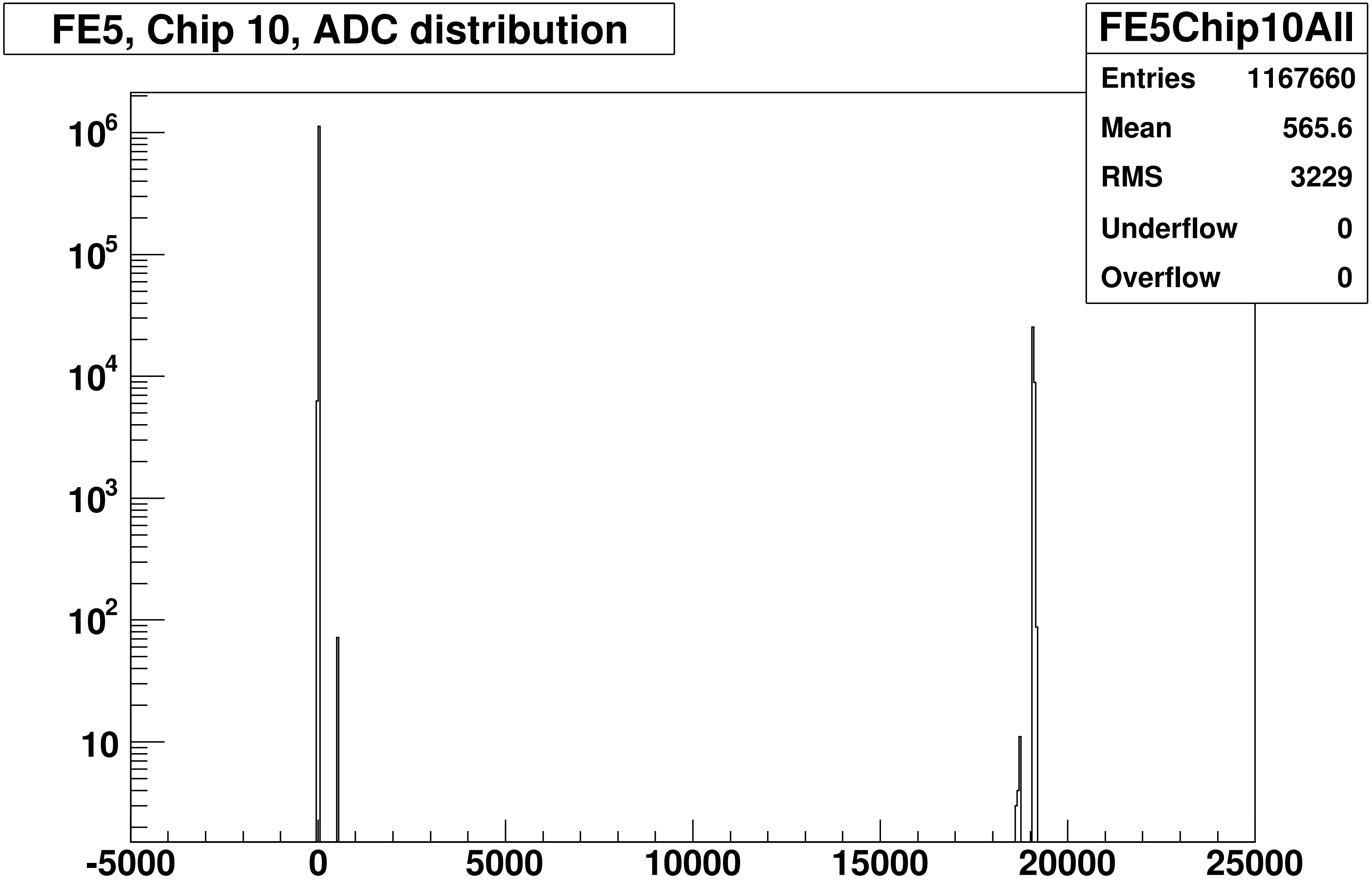
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 1167660
Mean 565.6
RMS 3229
Underflow 0
Overflow 0
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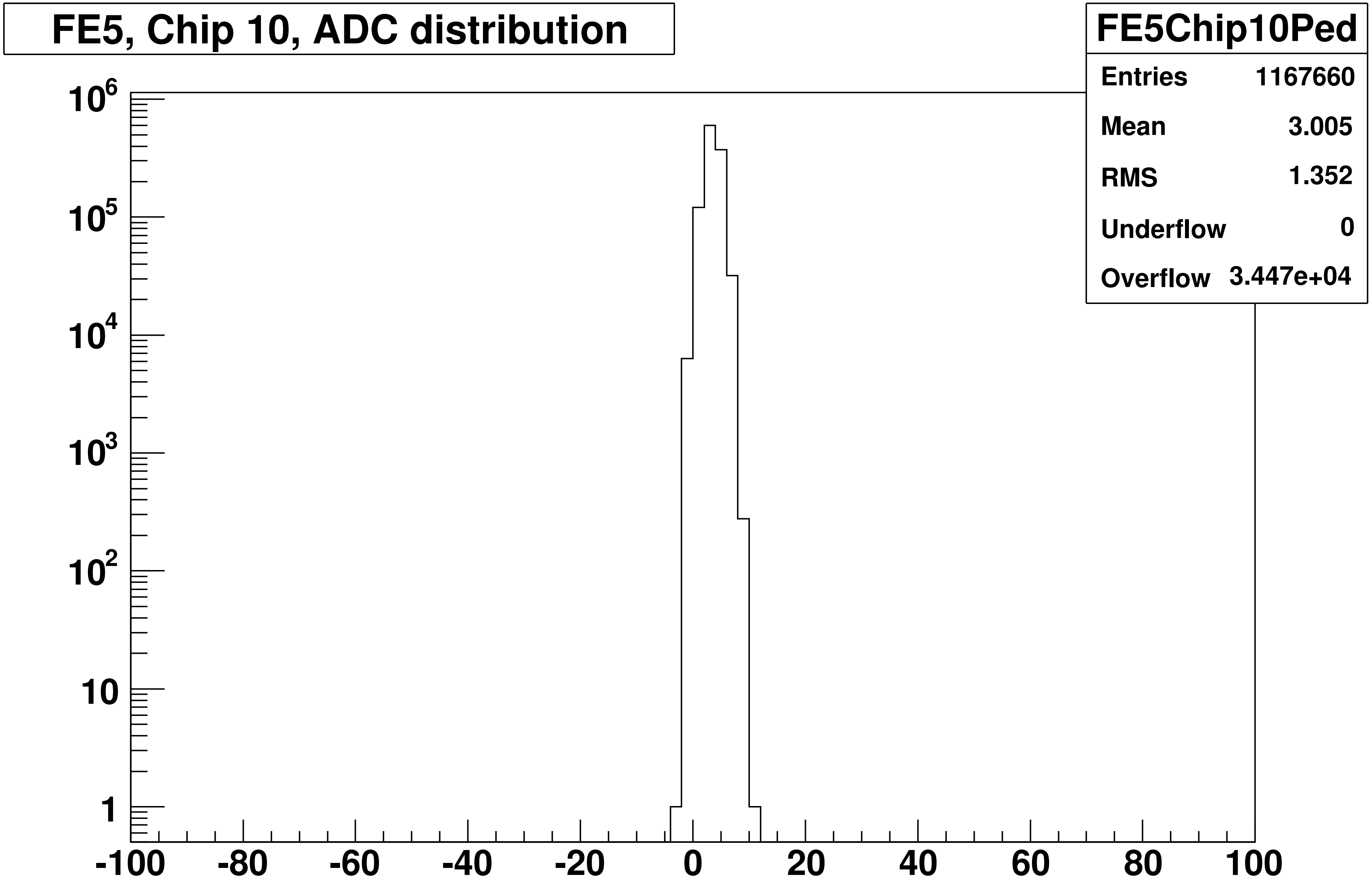
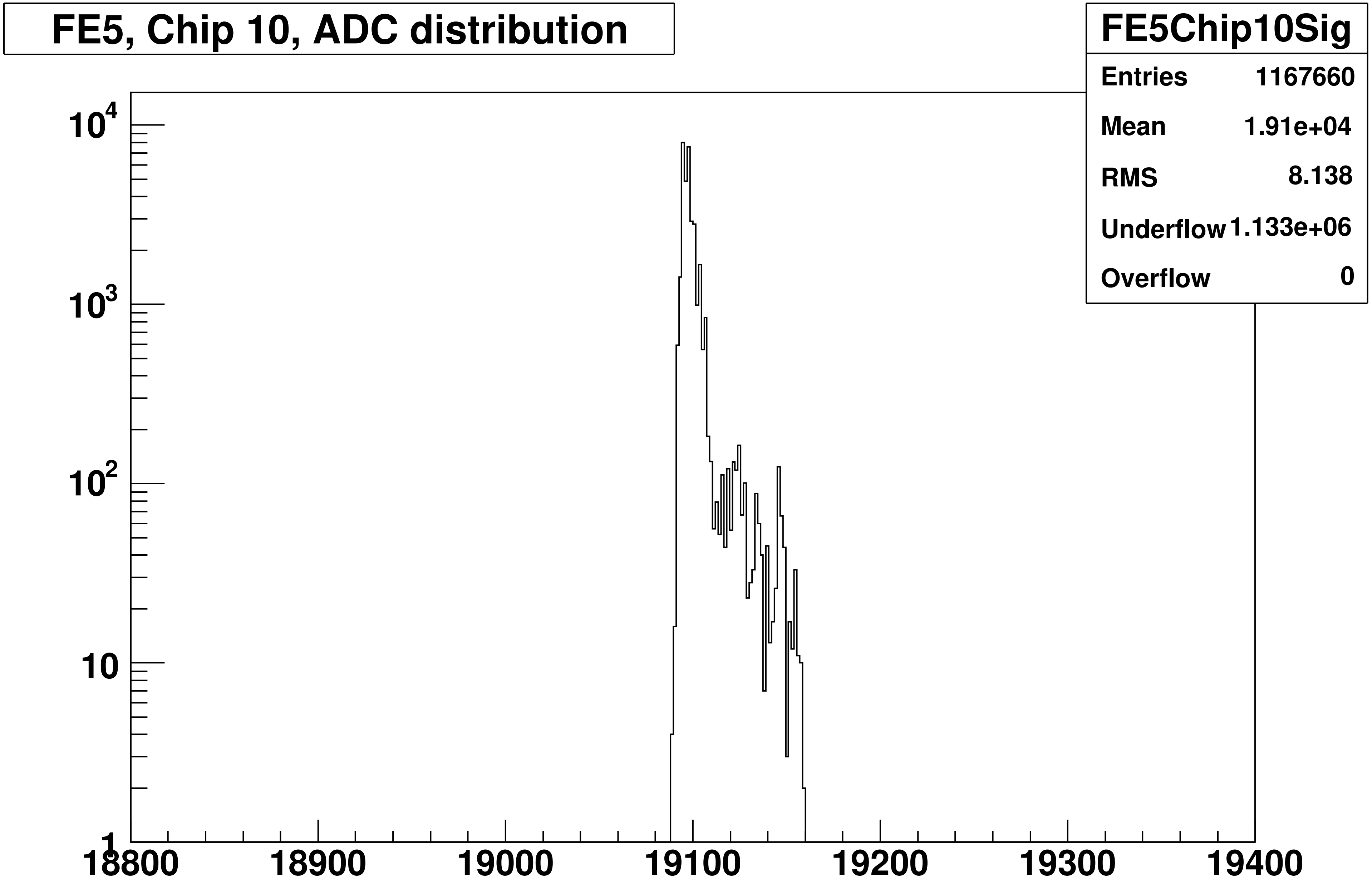
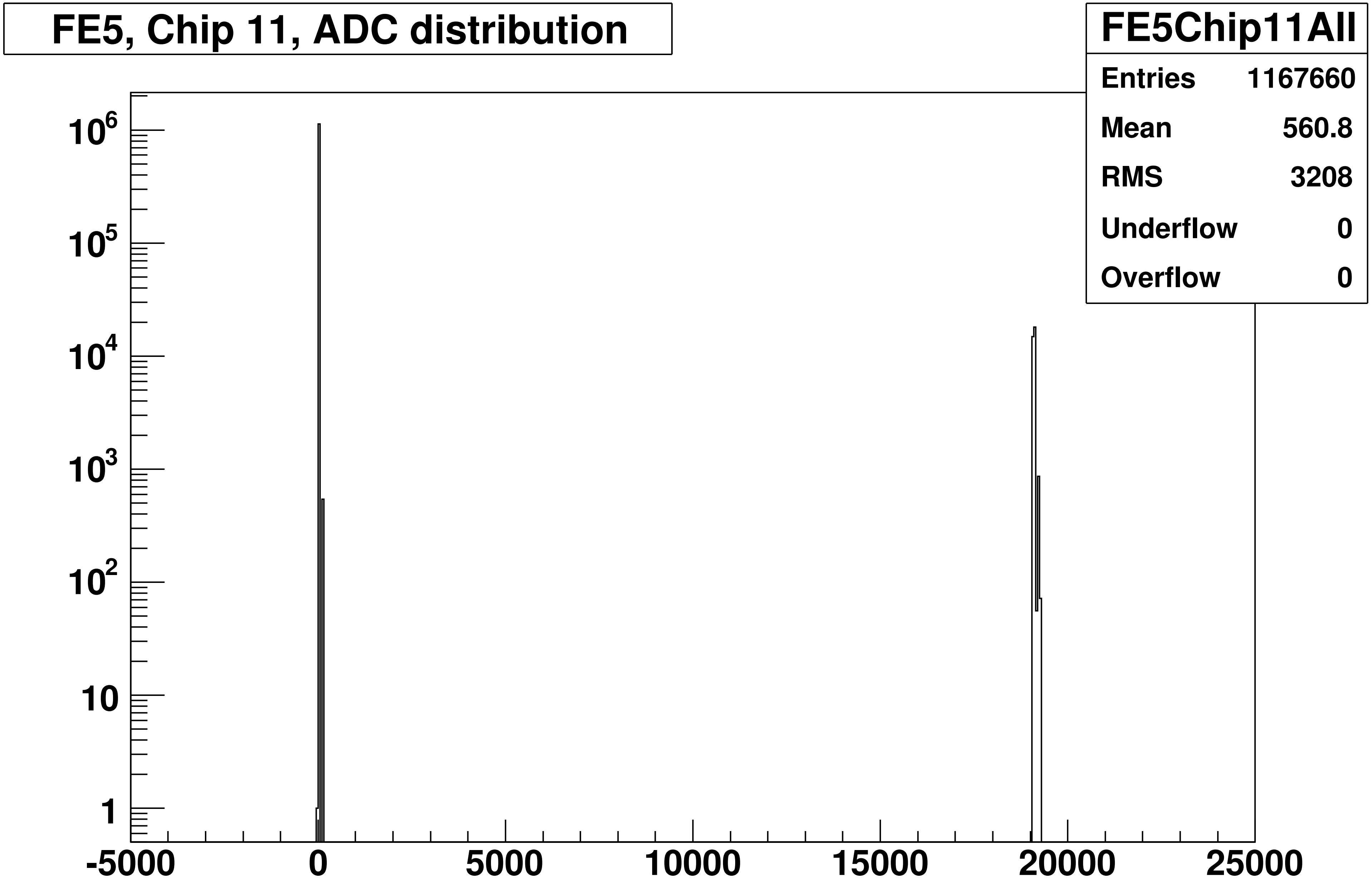
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 1167660
Mean 3.005
RMS 1.352
Underflow 0
Overflow 3.447e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 1167660
Mean 1.91e+04
RMS 8.138
Underflow 1.133e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 1167660
Mean 560.8
RMS 3208
Underflow 0
Overflow 0
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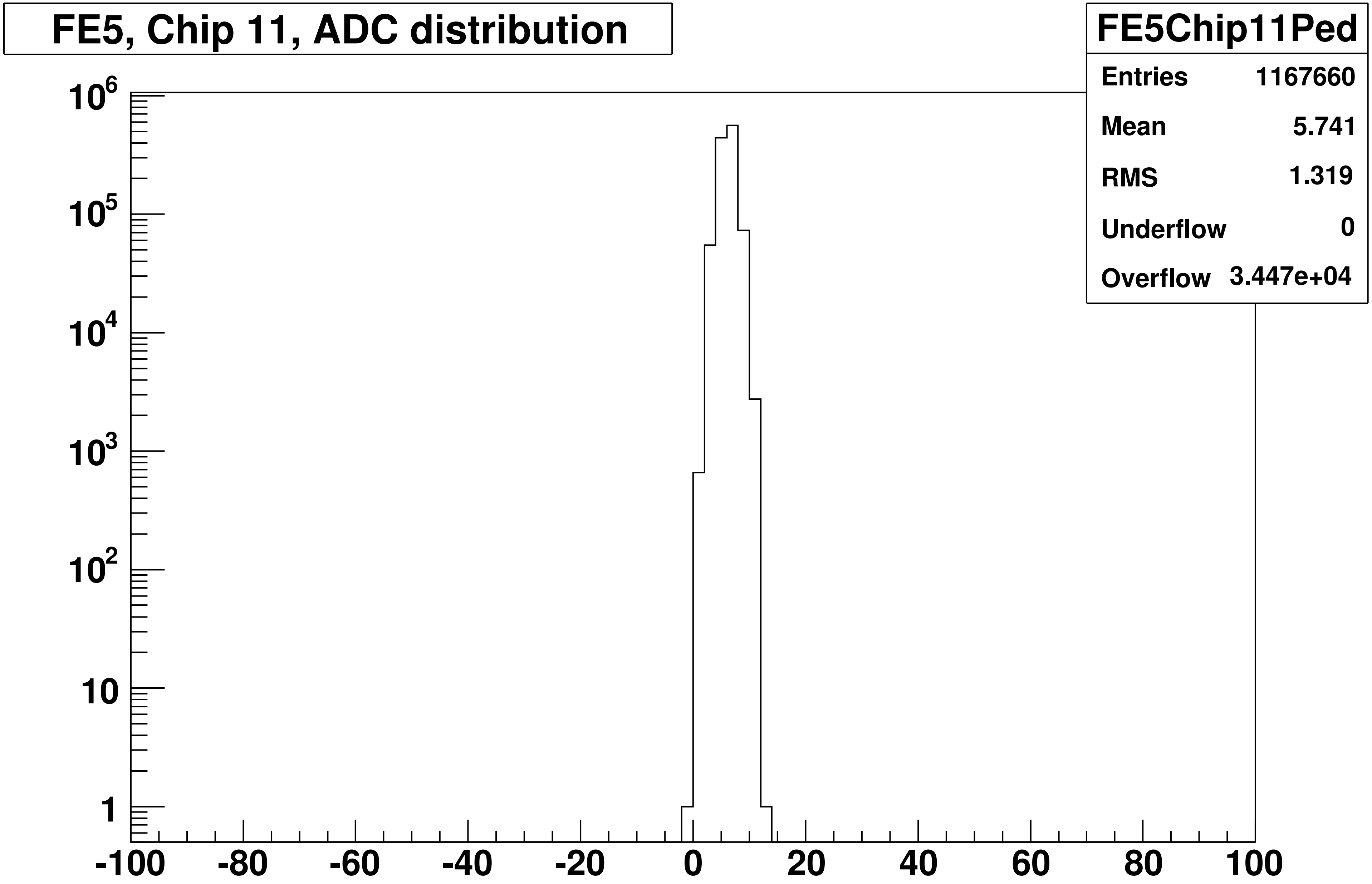
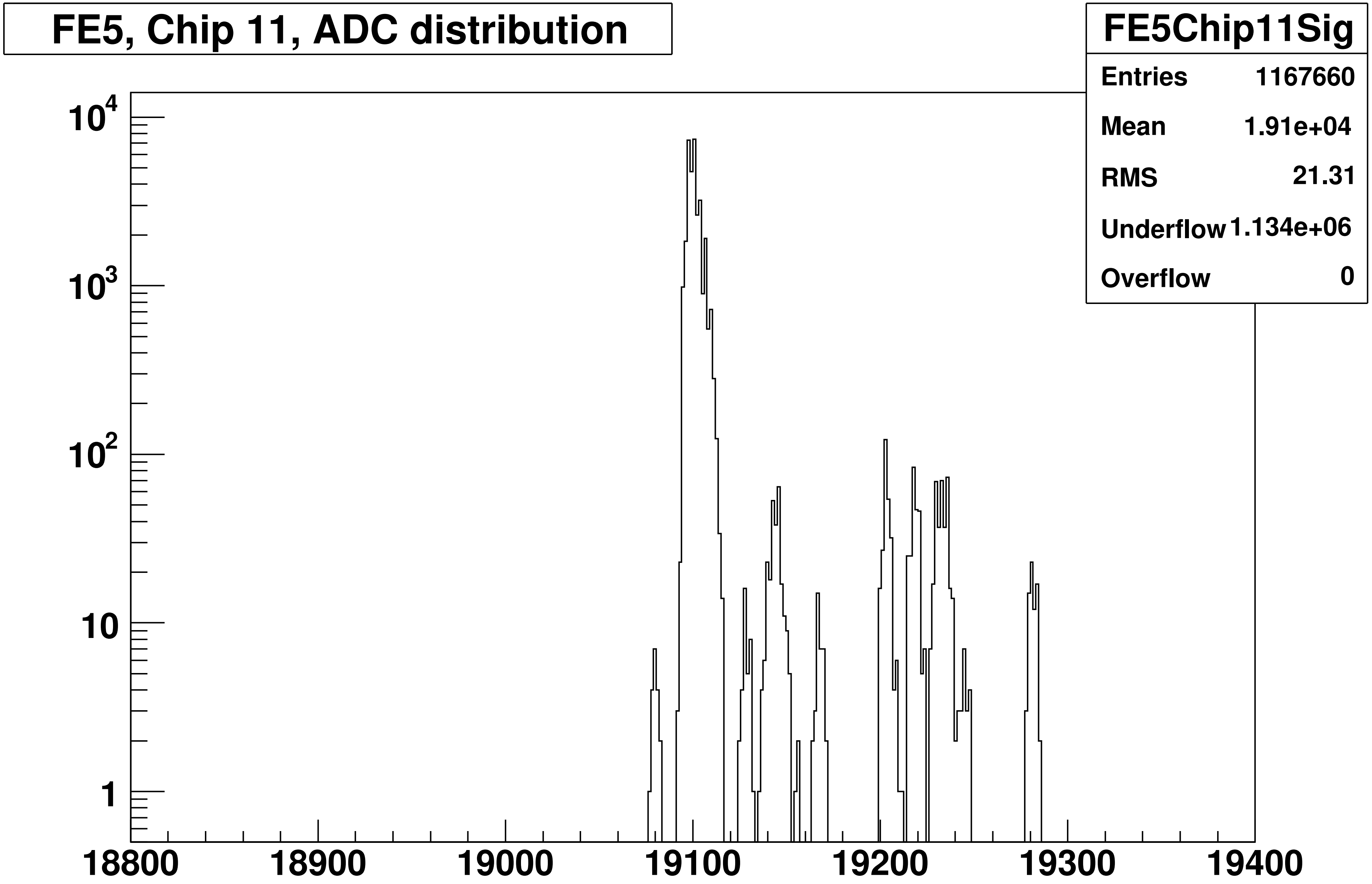
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 1167660
Mean 5.741
RMS 1.319
Underflow 0
Overflow 3.447e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 1167660
Mean 1.91e+04
RMS 21.31
Underflow 1.134e+06
Overflow 0
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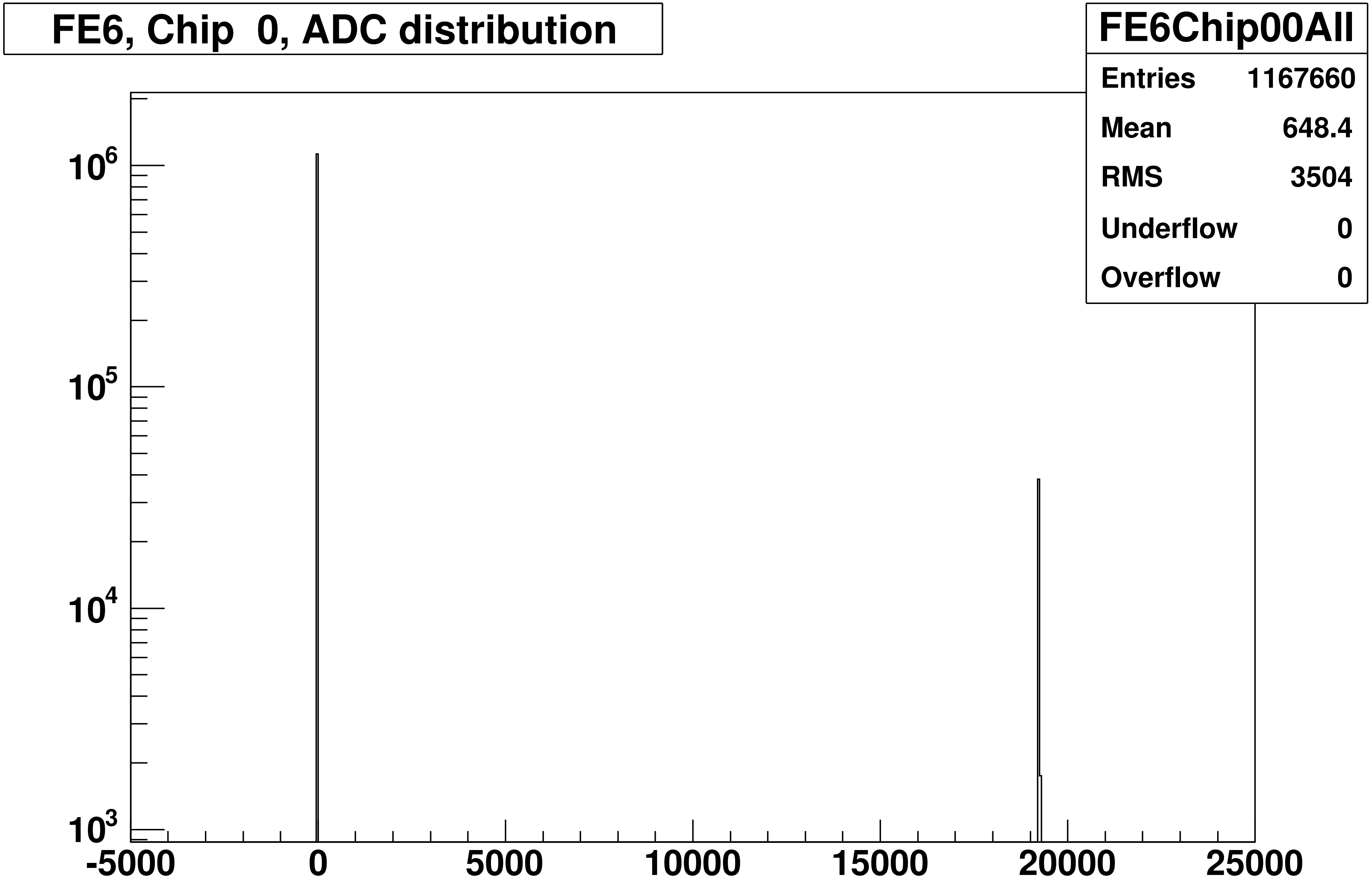
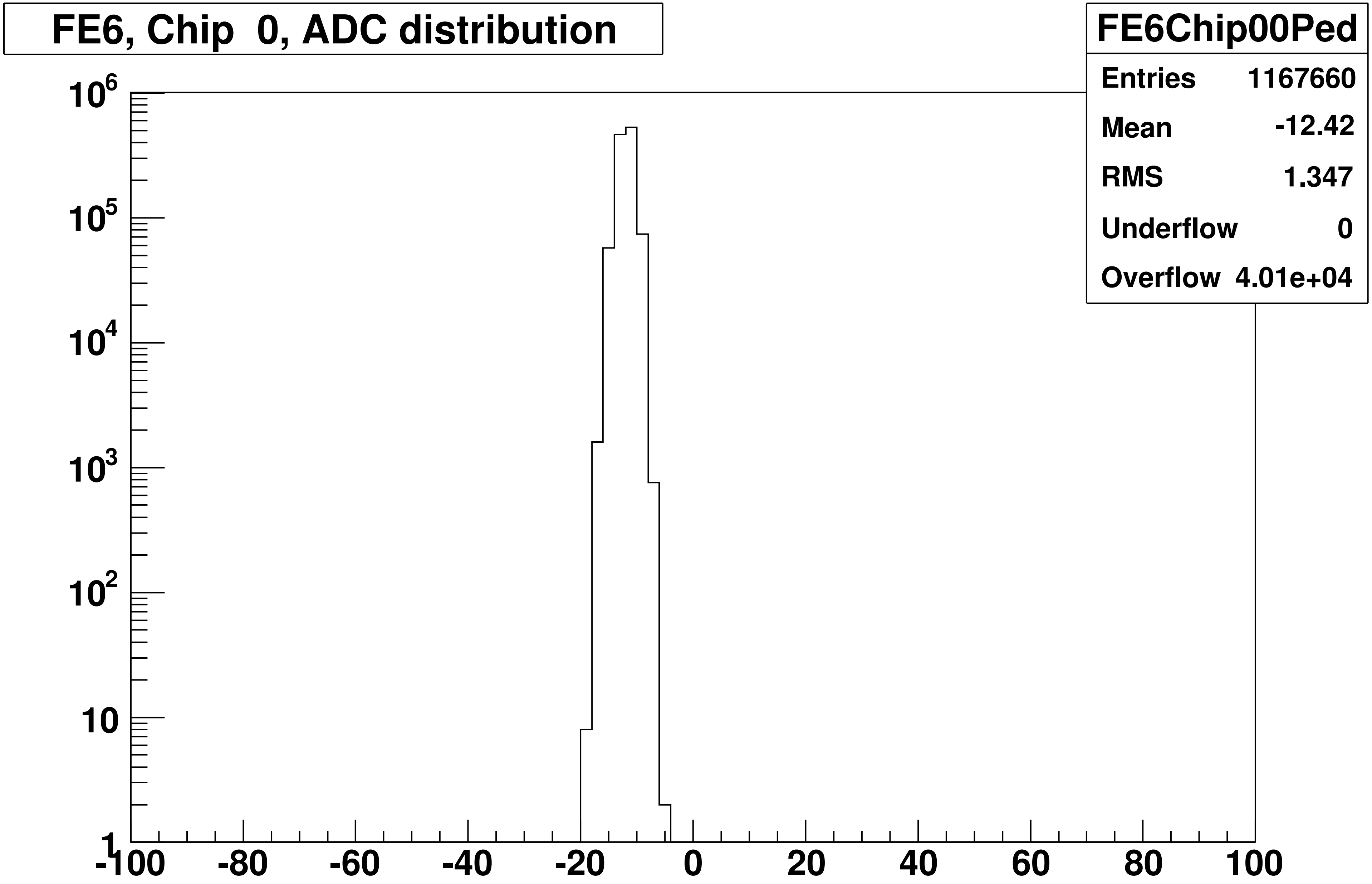
18900
19000
19100
19200
19300
19400
1
10
10^2
10^3
10^4



FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 1167660
Mean 648.4
RMS 3504
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 1167660
Mean -12.42
RMS 1.347
Underflow 0
Overflow 4.01e+04
1
10
10^2
10^3
10^4
10^5
10^6
-100
-80
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-40
-20
0
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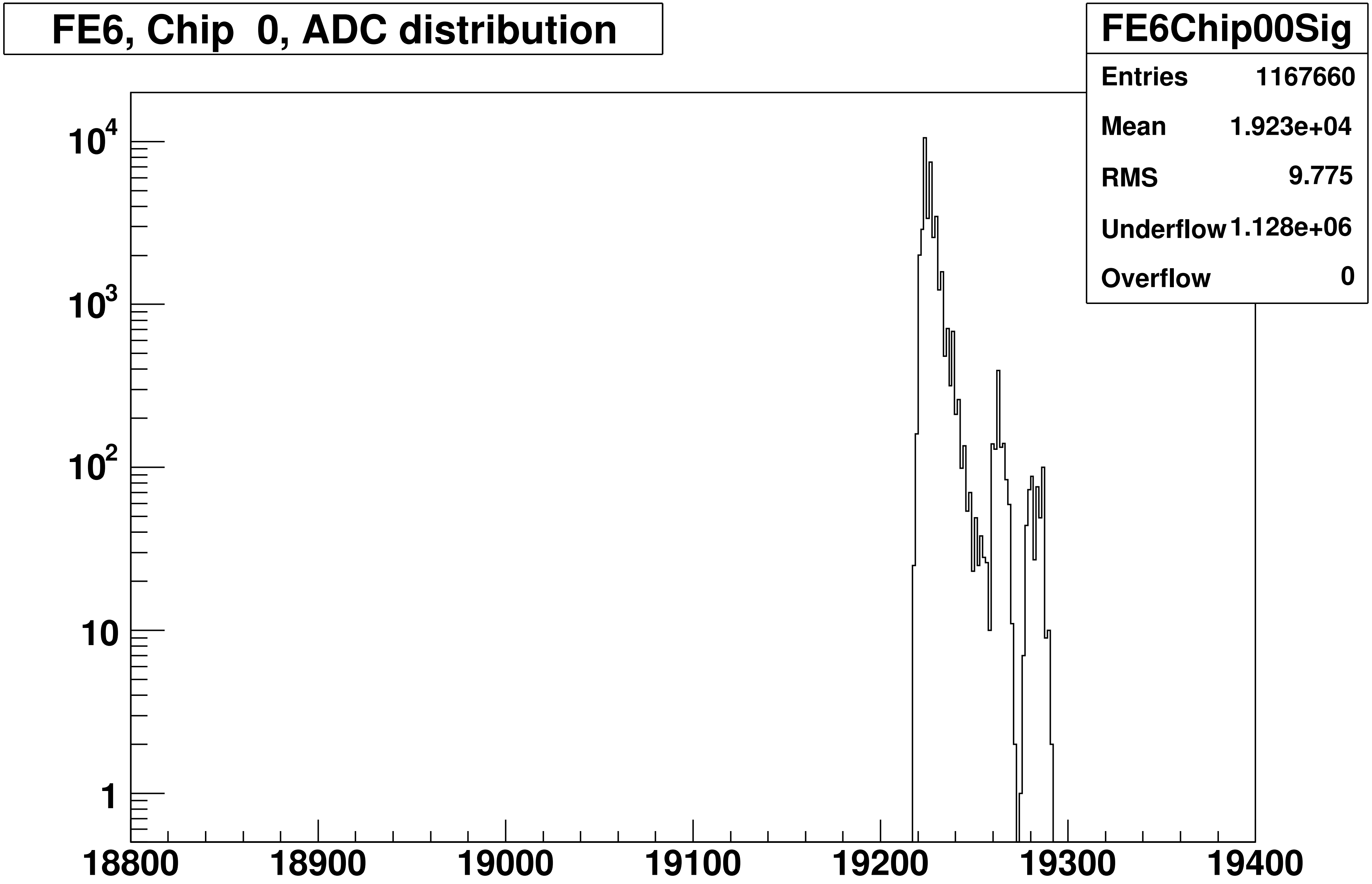
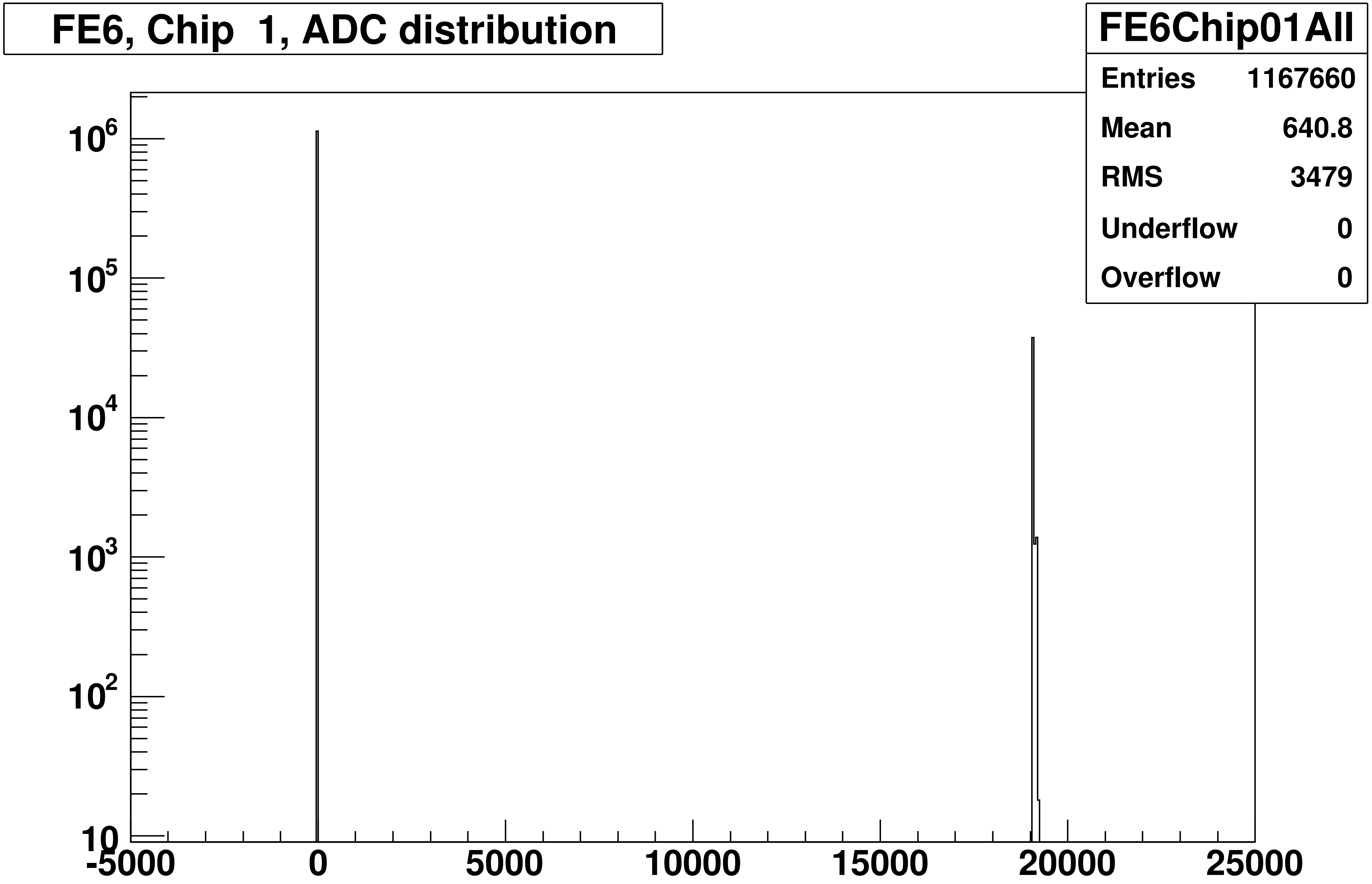
40
60
80
100


FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 1167660
Mean 1.923e+04
RMS 9.775
Underflow 1.128e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 1167660
Mean 640.8
RMS 3479
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
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10000
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20000
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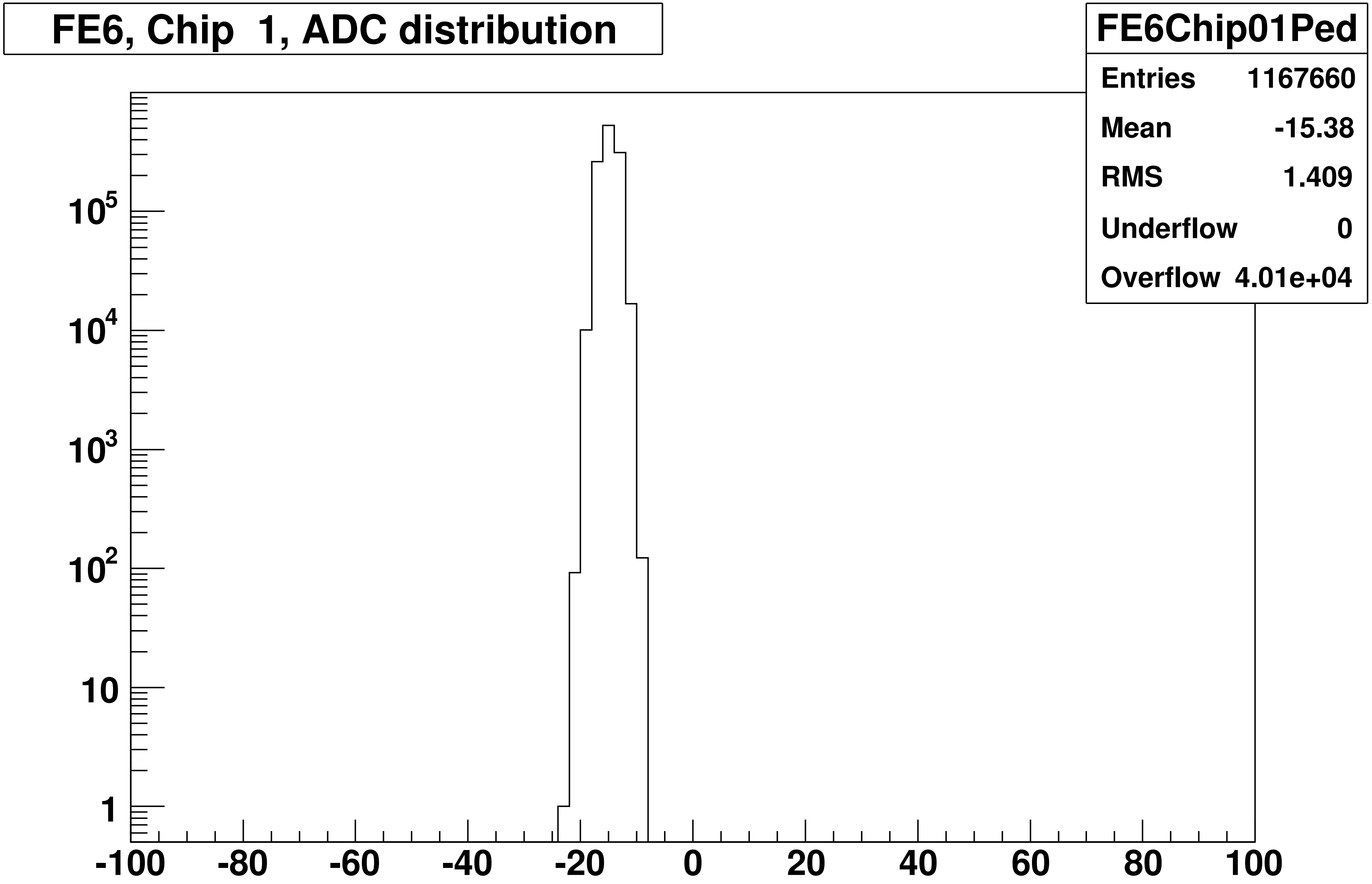

FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 1167660
Mean -15.38
RMS 1.409
Underflow 0
Overflow 4.01e+04
10^5
10^4
10^3
10^2
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1
-100
-80
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-40
-20
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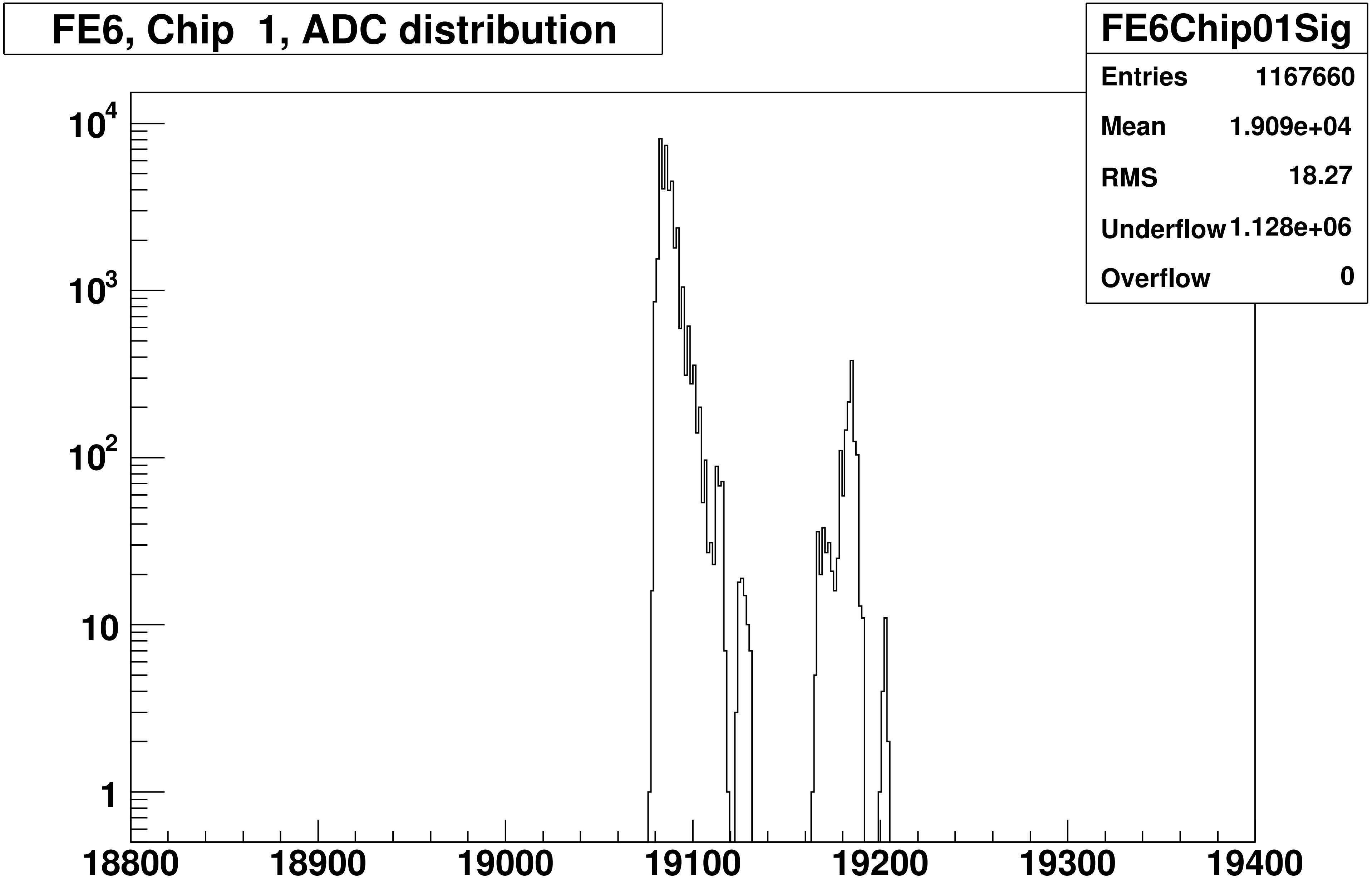

FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 1167660
Mean 1.909e+04
RMS 18.27
Underflow 1.128e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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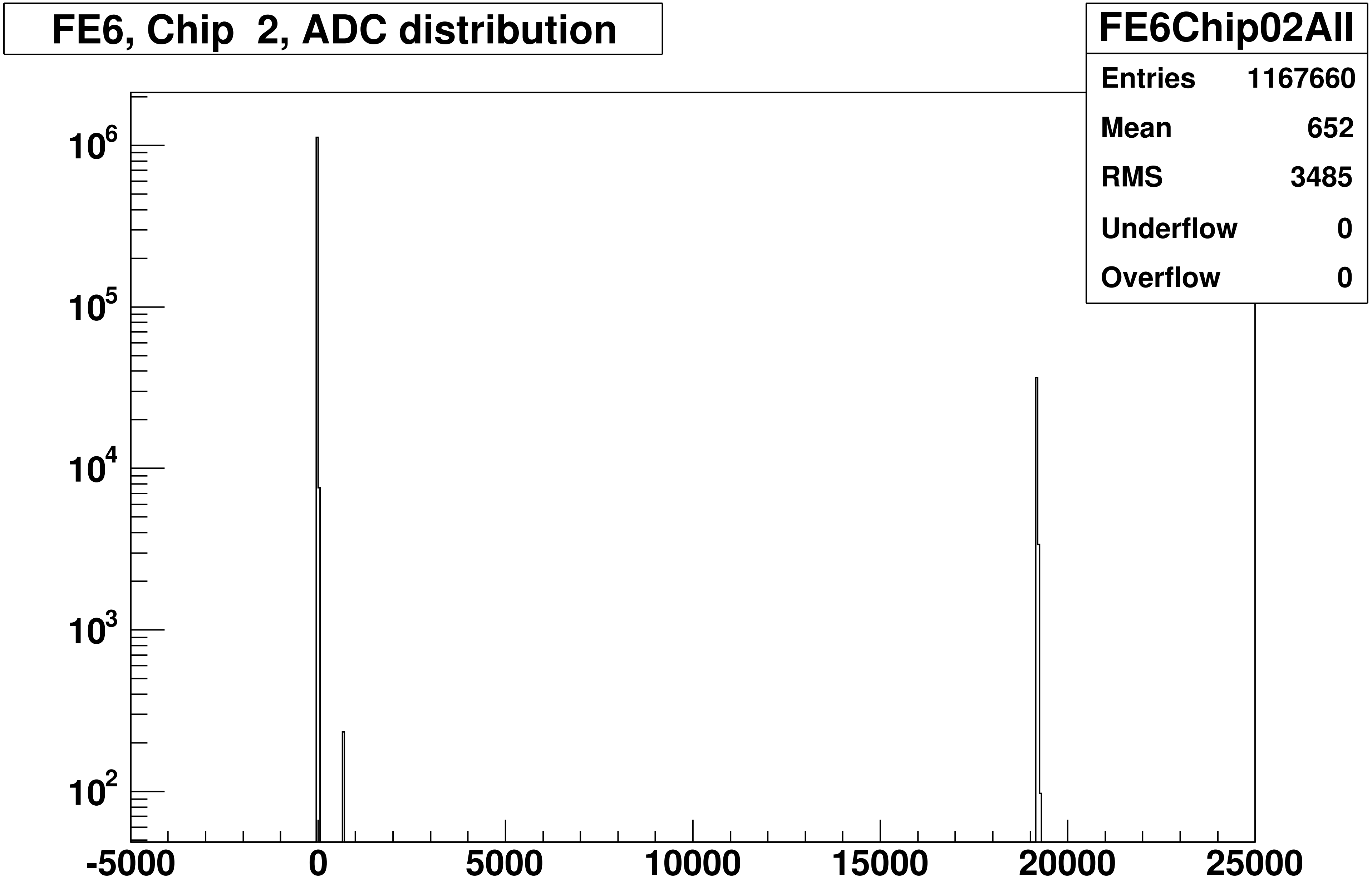

FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 1167660
Mean 652
RMS 3485
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
-5000
0
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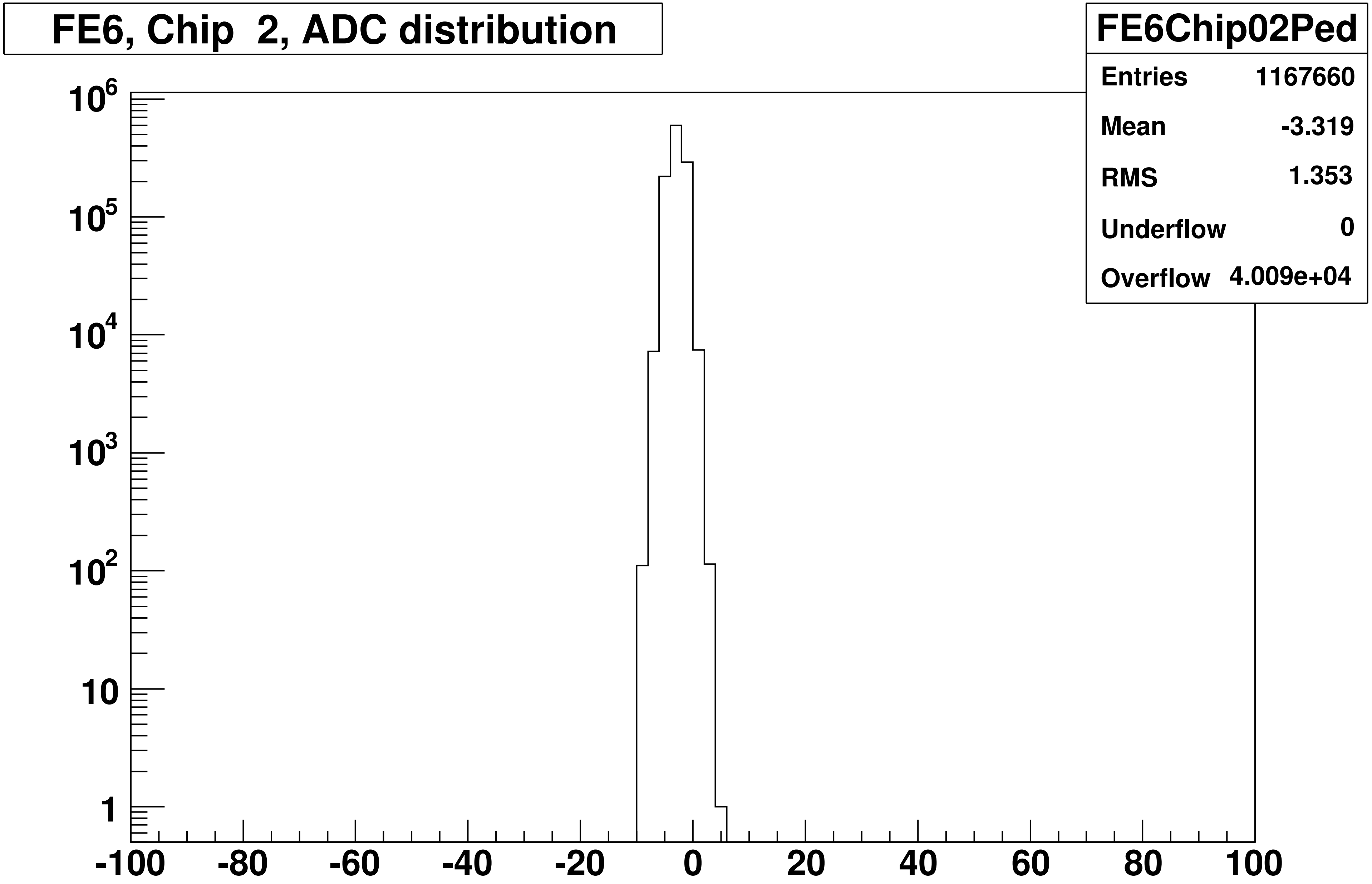
10000
15000
20000
25000


FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 1167660
Mean -3.319
RMS 1.353
Underflow 0
Overflow 4.009e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
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-40
-20
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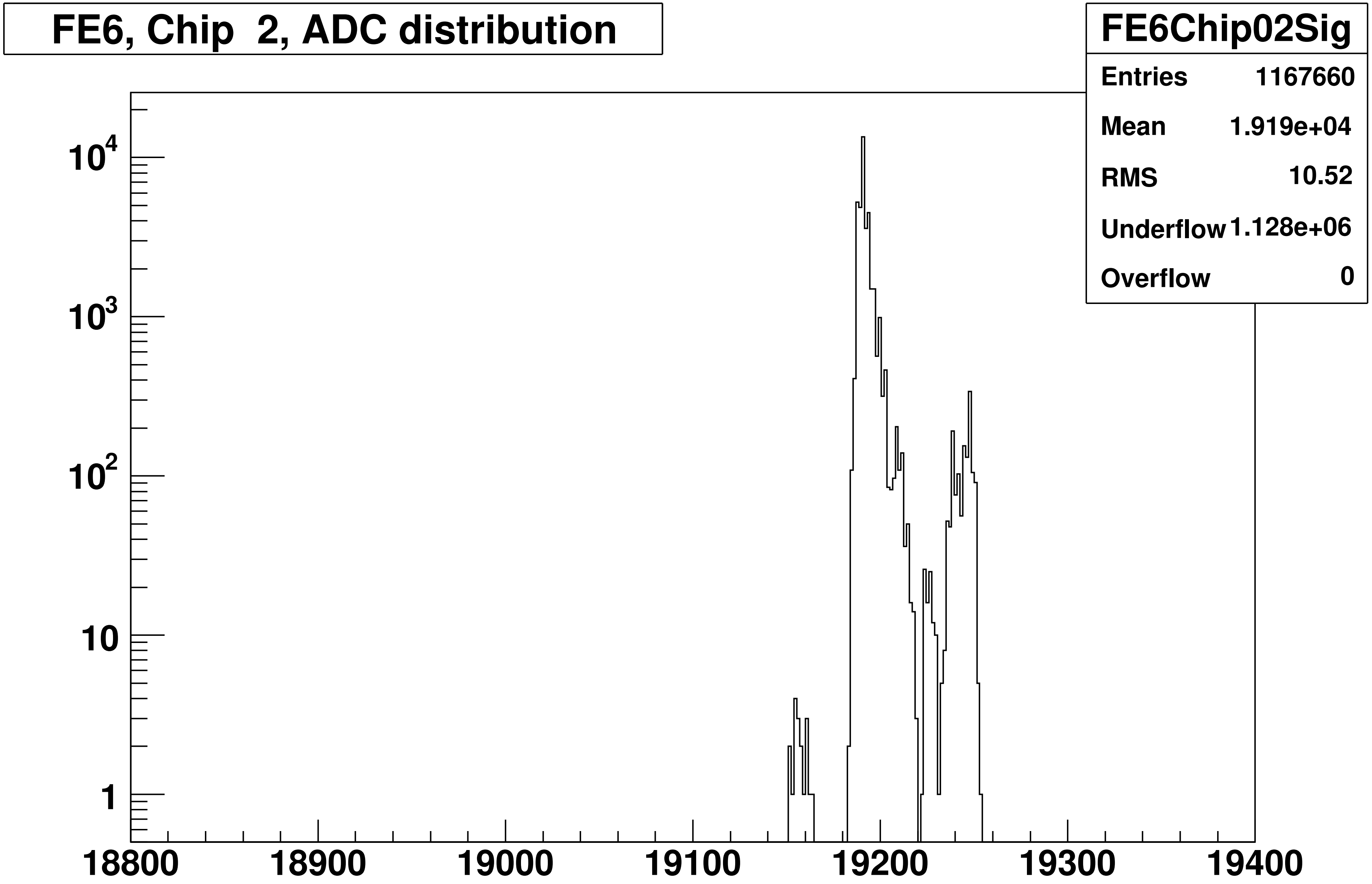
FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 1167660
Mean 1.919e+04
RMS 10.52
Underflow 1.128e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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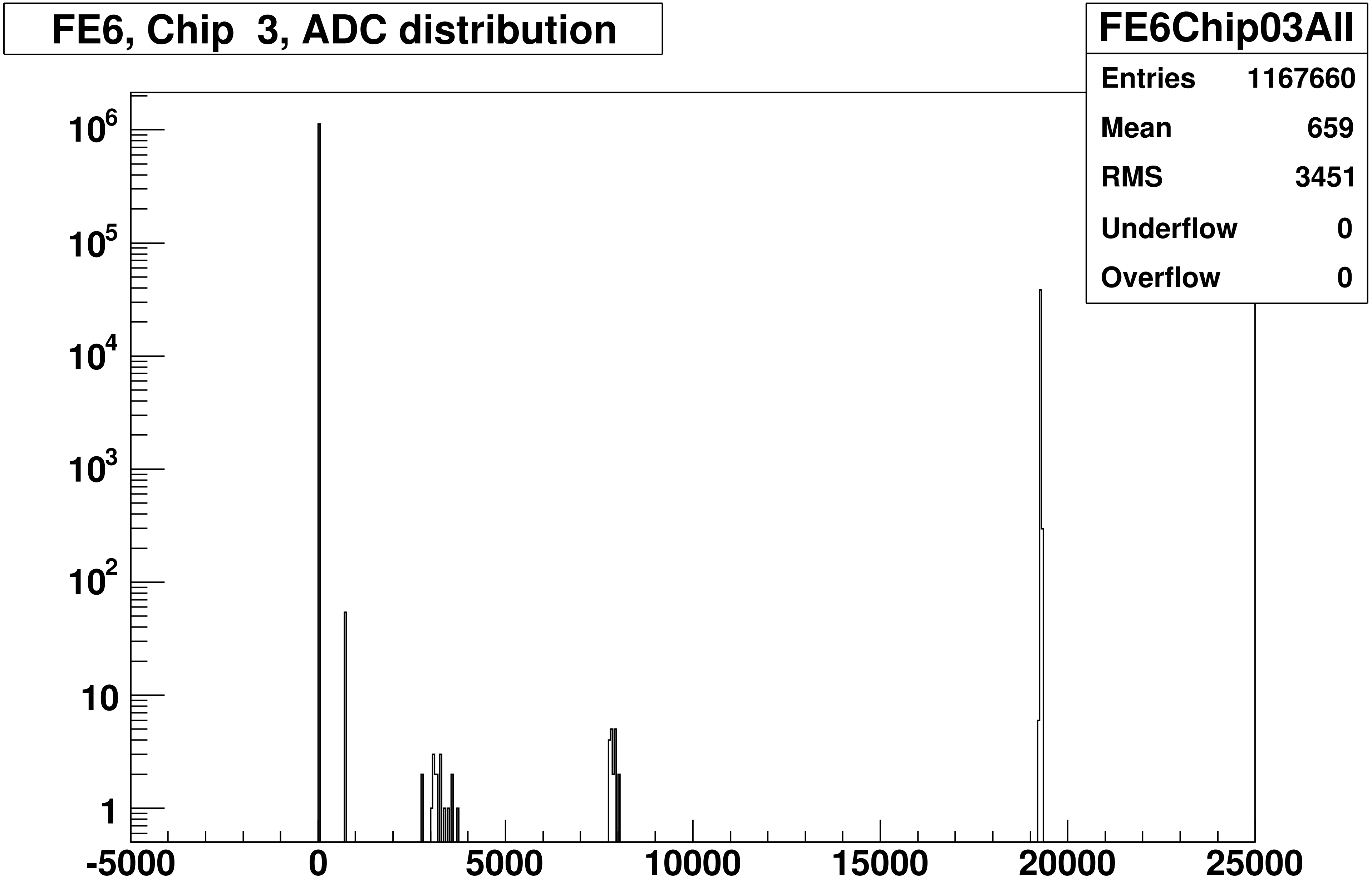
19100
19200
19300
19400



FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 1167660
Mean 659
RMS 3451
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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-5000
0
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10000
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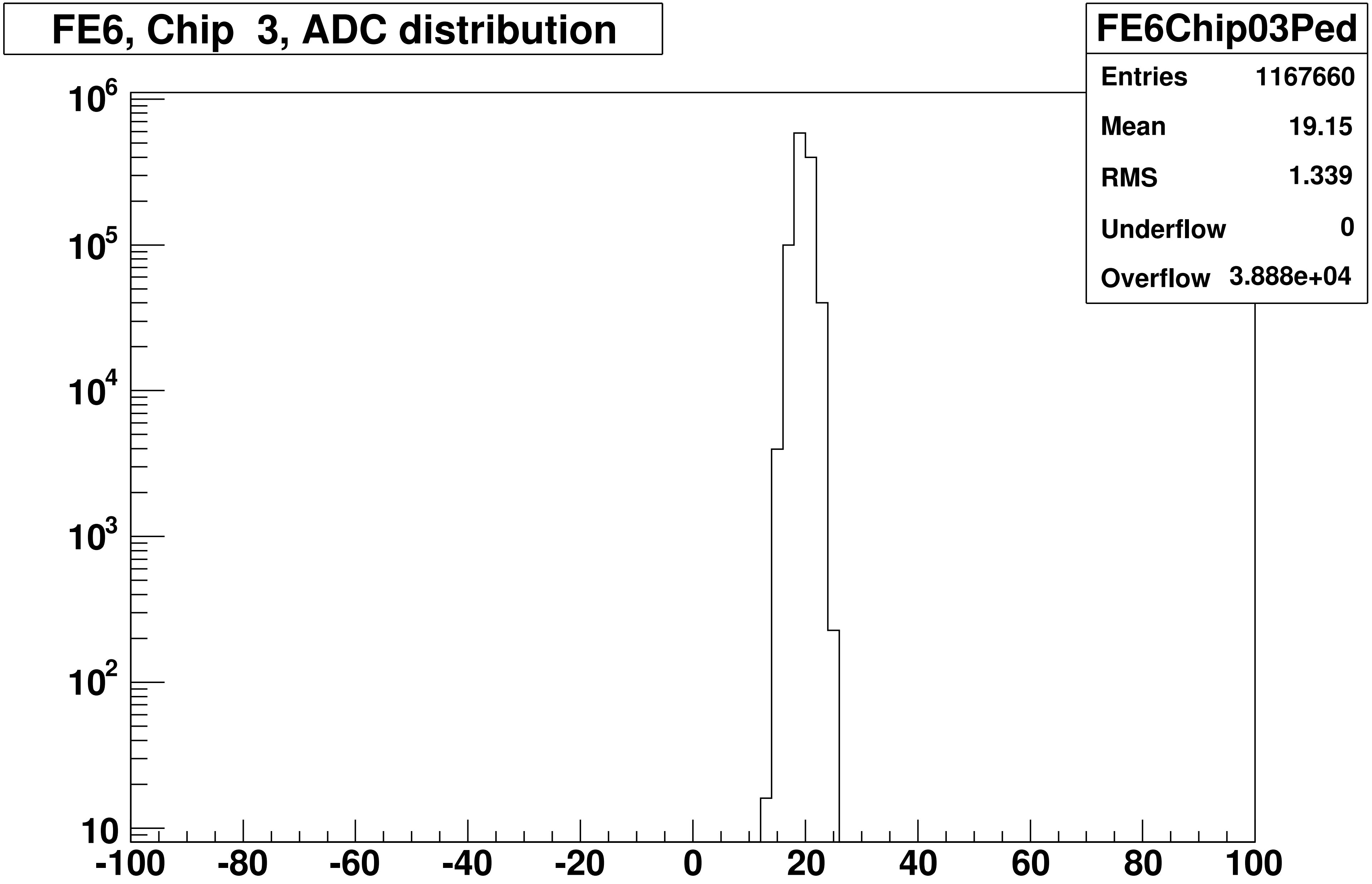
20000
25000


FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 1167660
Mean 19.15
RMS 1.339
Underflow 0
Overflow 3.888e+04
10^6
10^5
10^4
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10^2
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-20
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80
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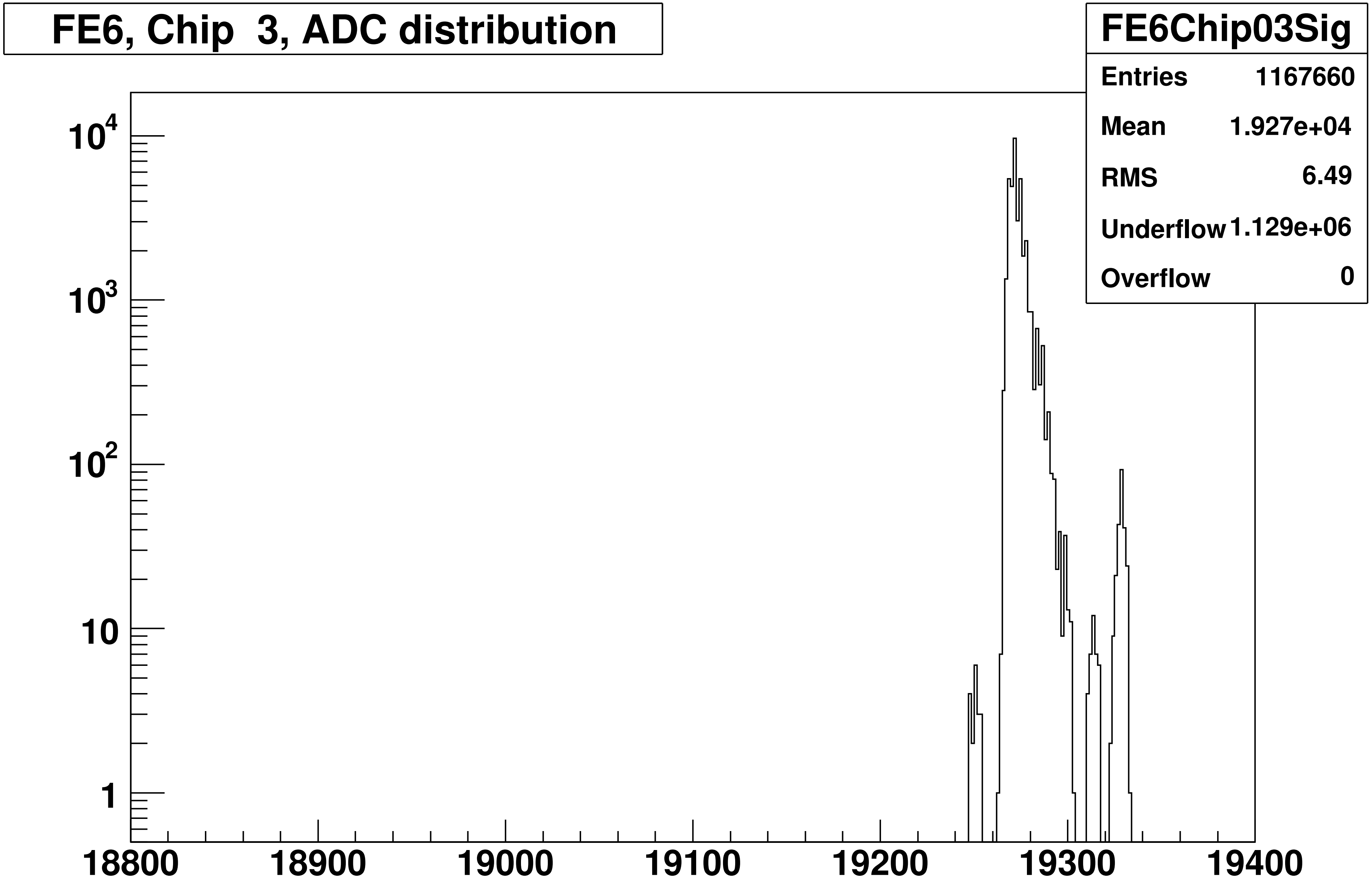

FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 1167660
Mean 1.927e+04
RMS 6.49
Underflow 1.129e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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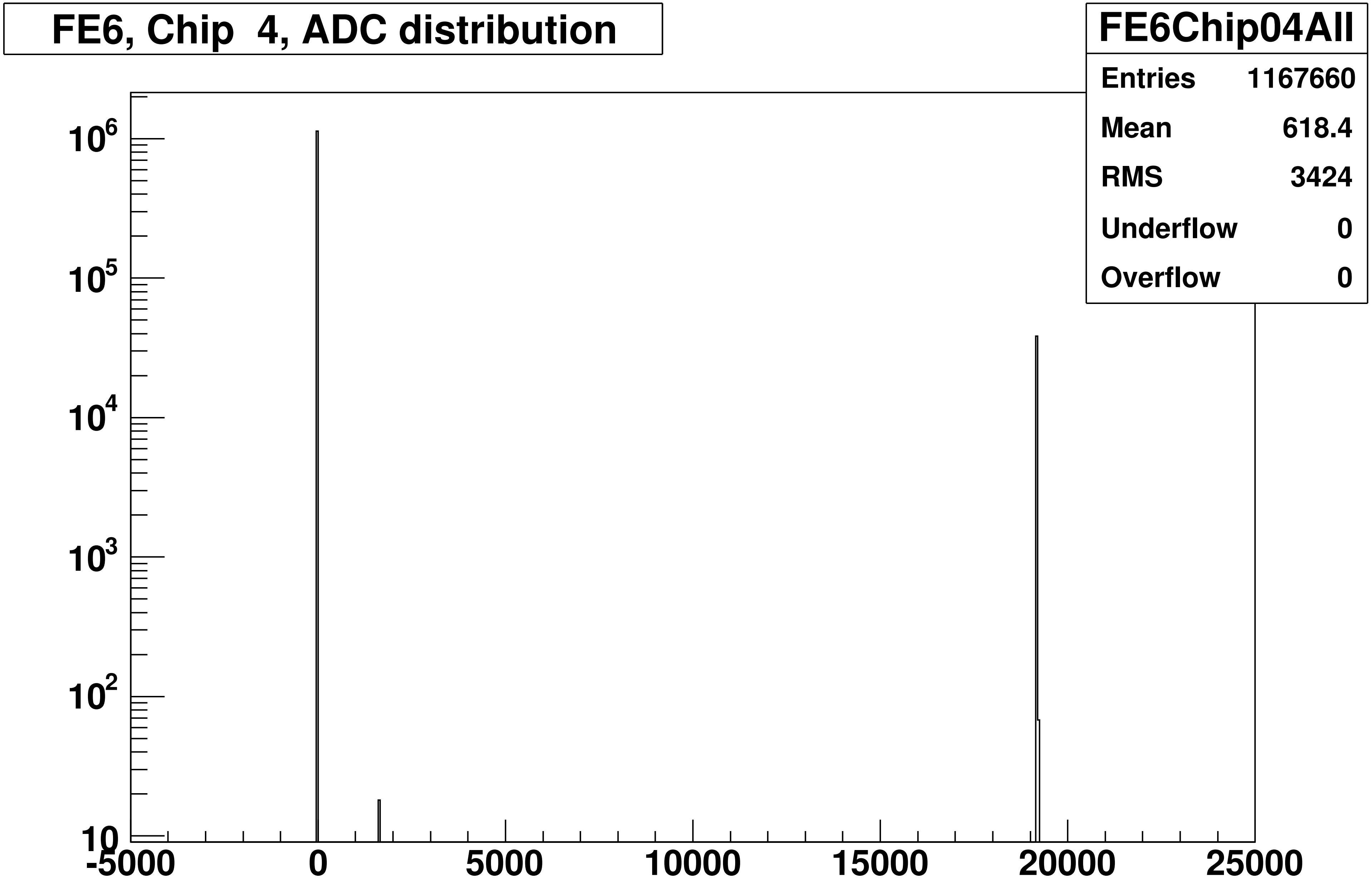
19400


FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 1167660
Mean 618.4
RMS 3424
Underflow 0
Overflow 0
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10^3
10^4
10^5
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0
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10000
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20000
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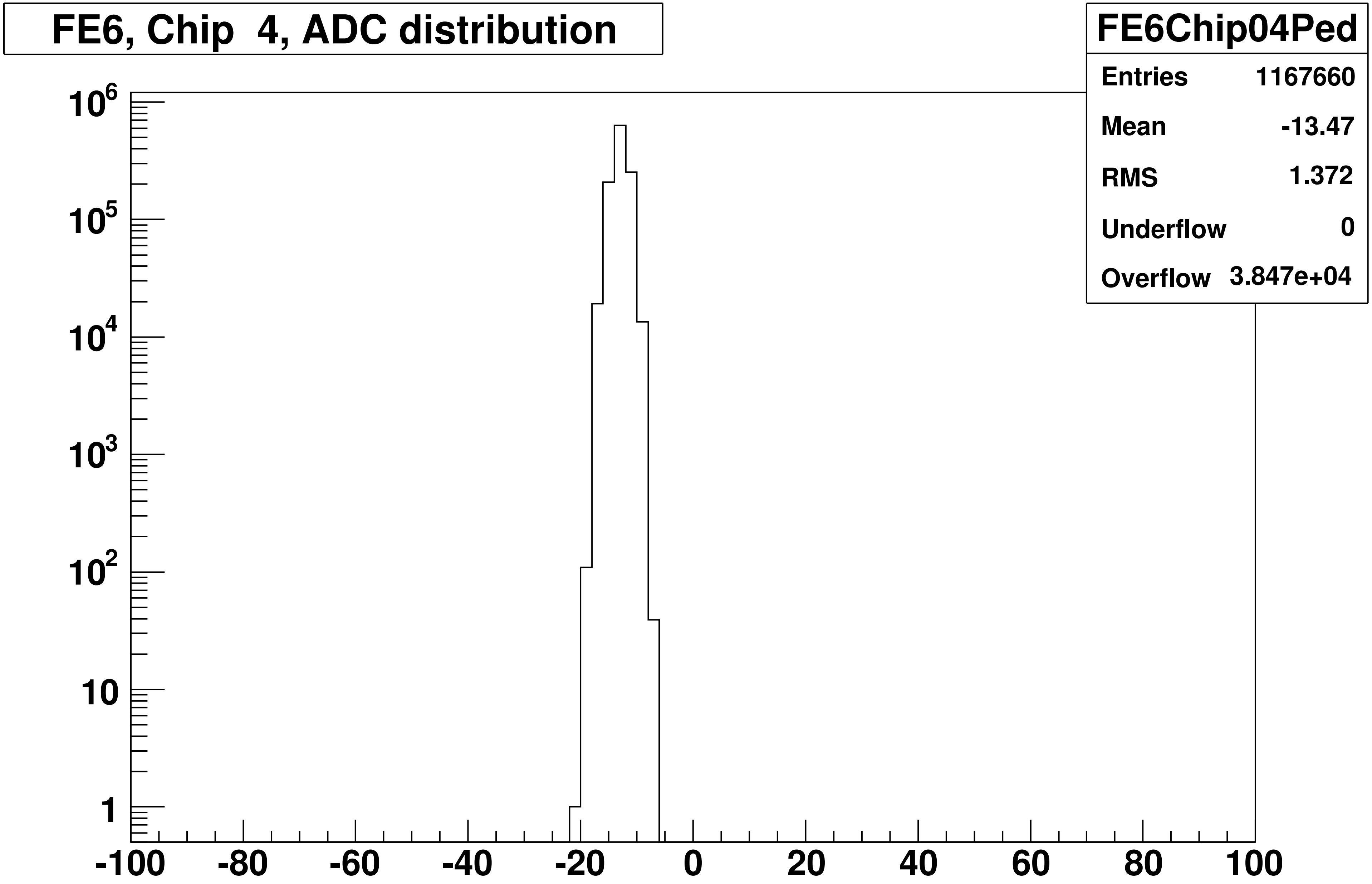
FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 1167660
Mean -13.47
RMS 1.372
Underflow 0
Overflow 3.847e+04
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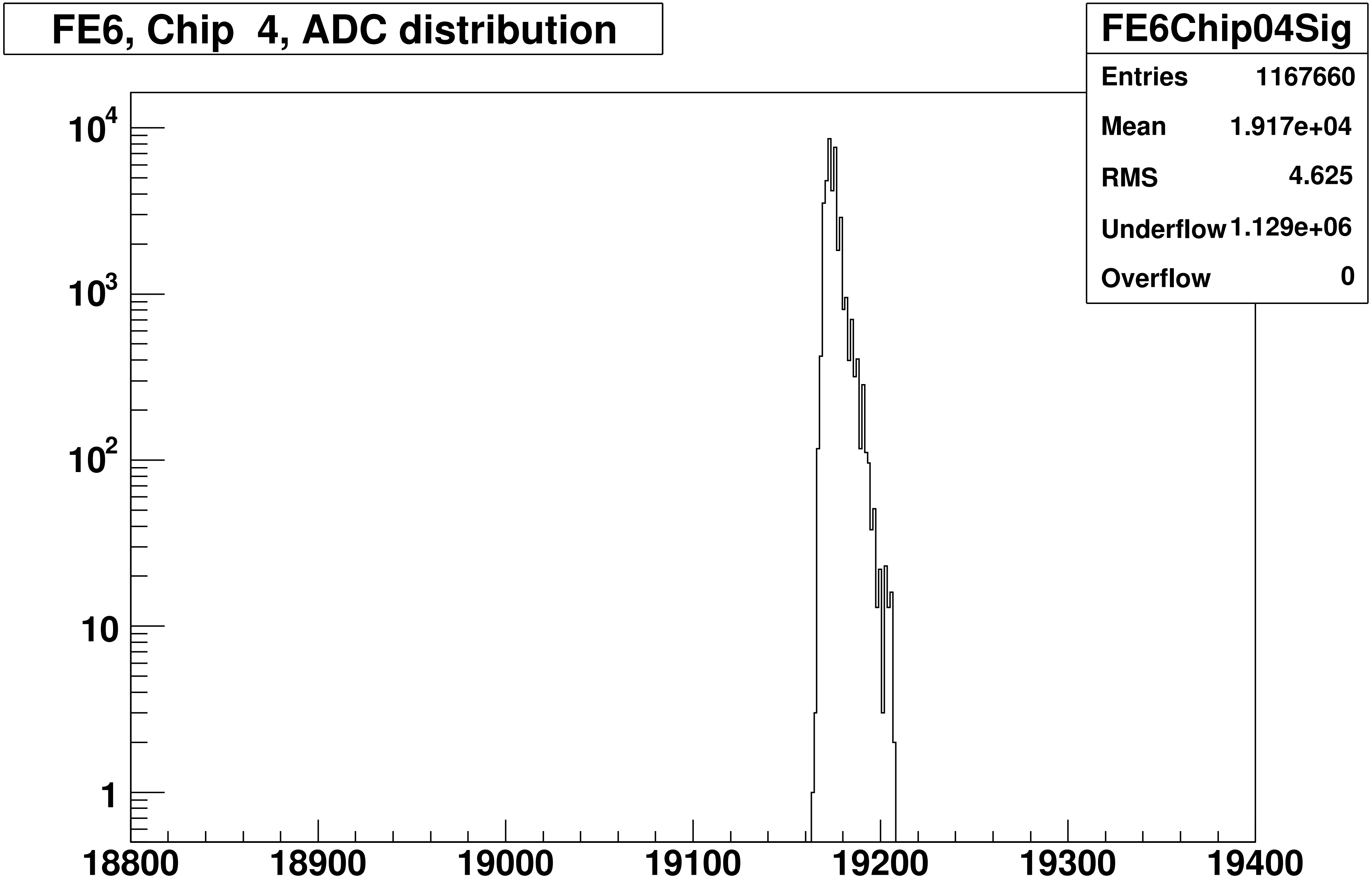
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 1167660
Mean 1.917e+04
RMS 4.625
Underflow 1.129e+06
Overflow 0
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10^3
10^2
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1
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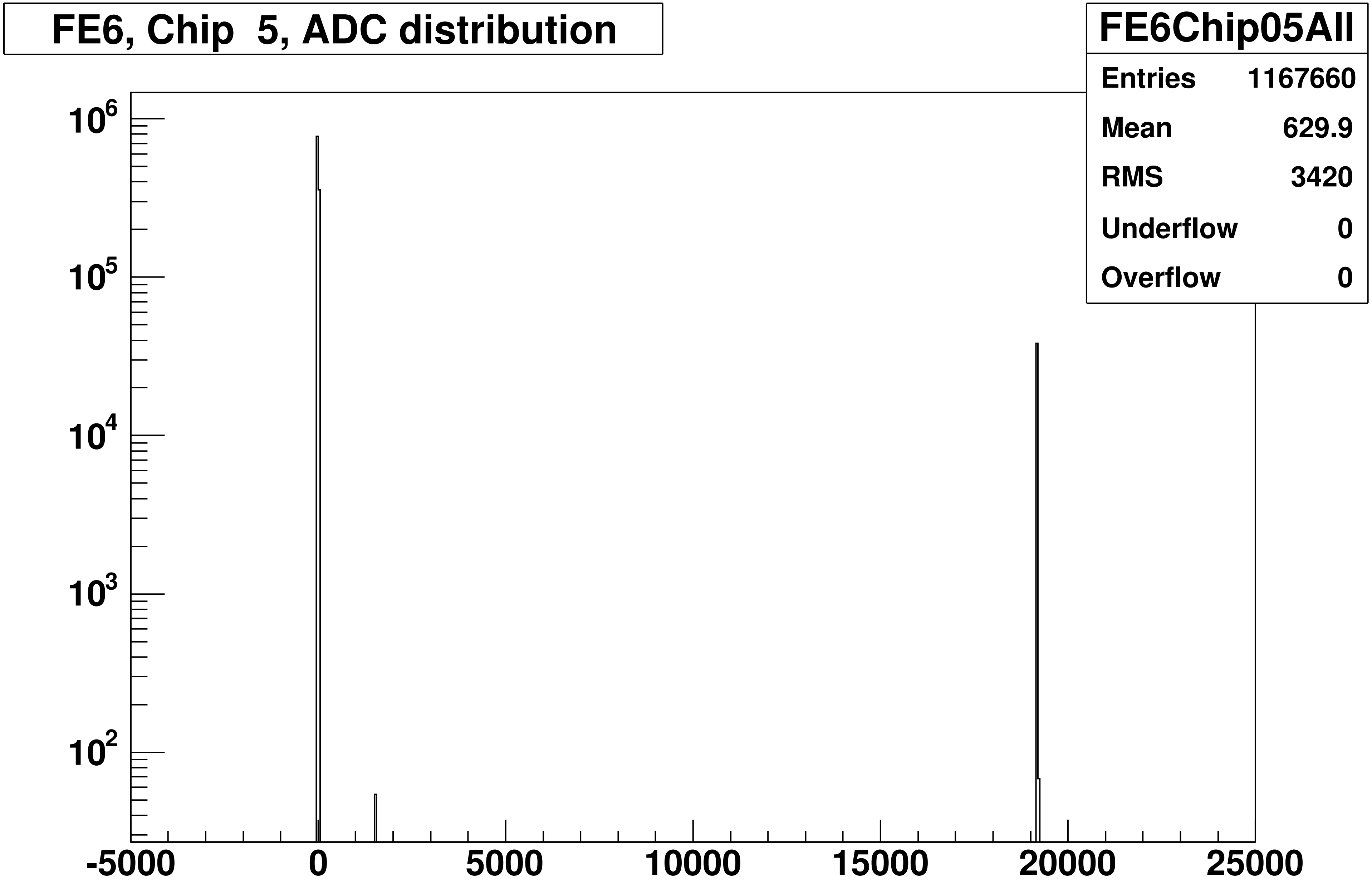

FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 1167660
Mean 629.9
RMS 3420
Underflow 0
Overflow 0
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10^5
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10^3
10^2
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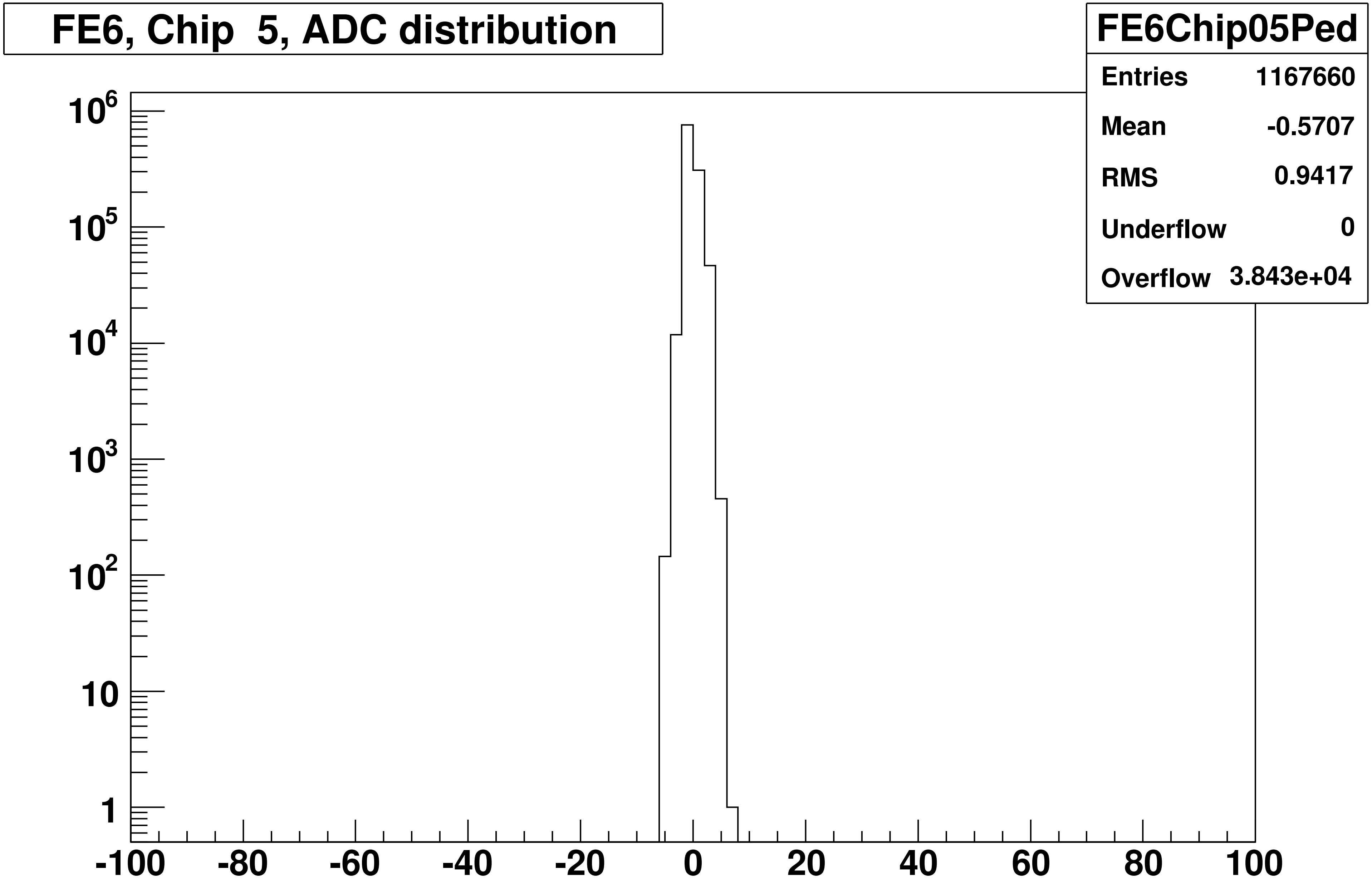
FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 1167660
Mean -0.5707
RMS 0.9417
Underflow 0
Overflow 3.843e+04
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10^5
10^4
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-20
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80
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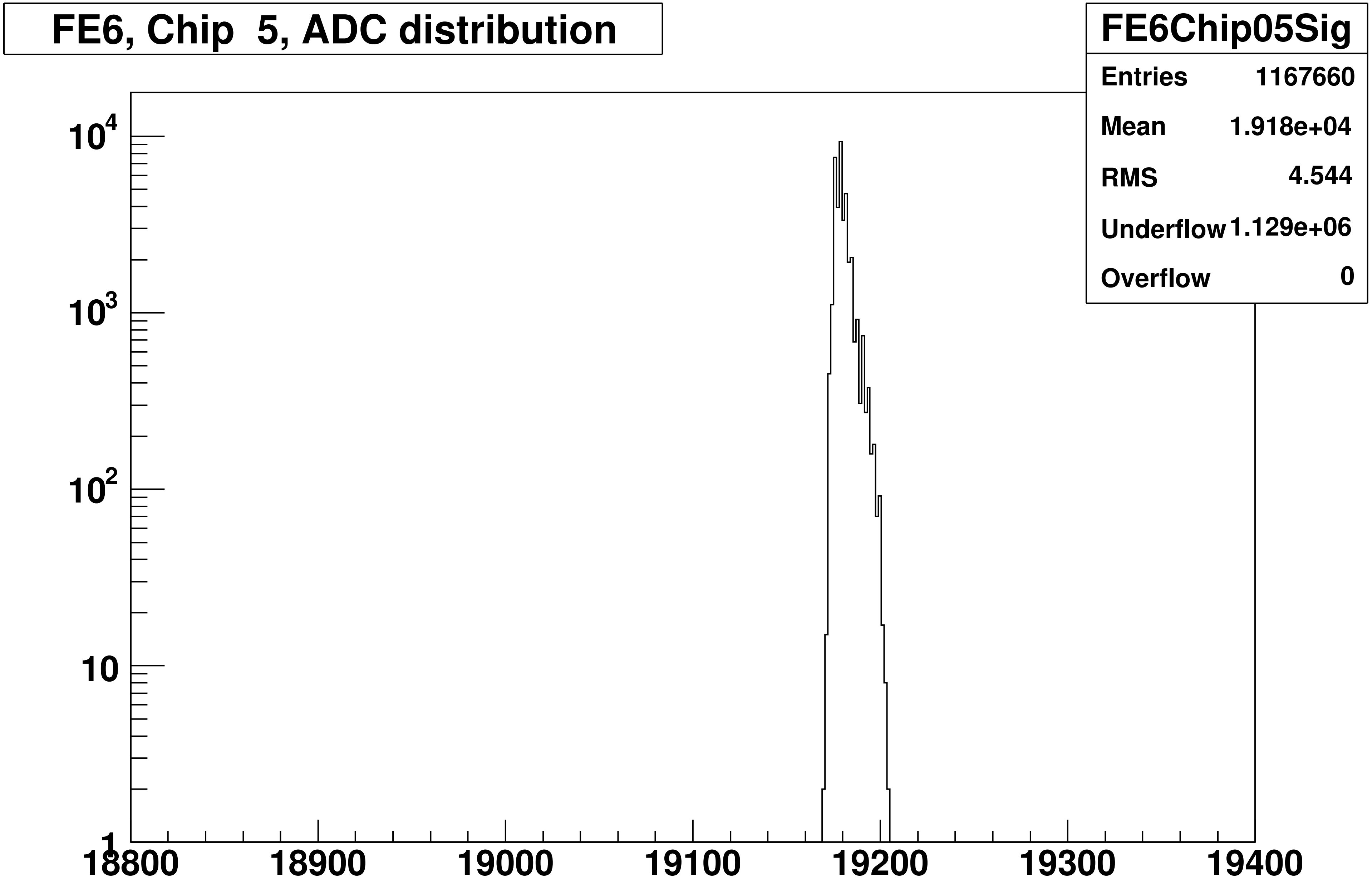
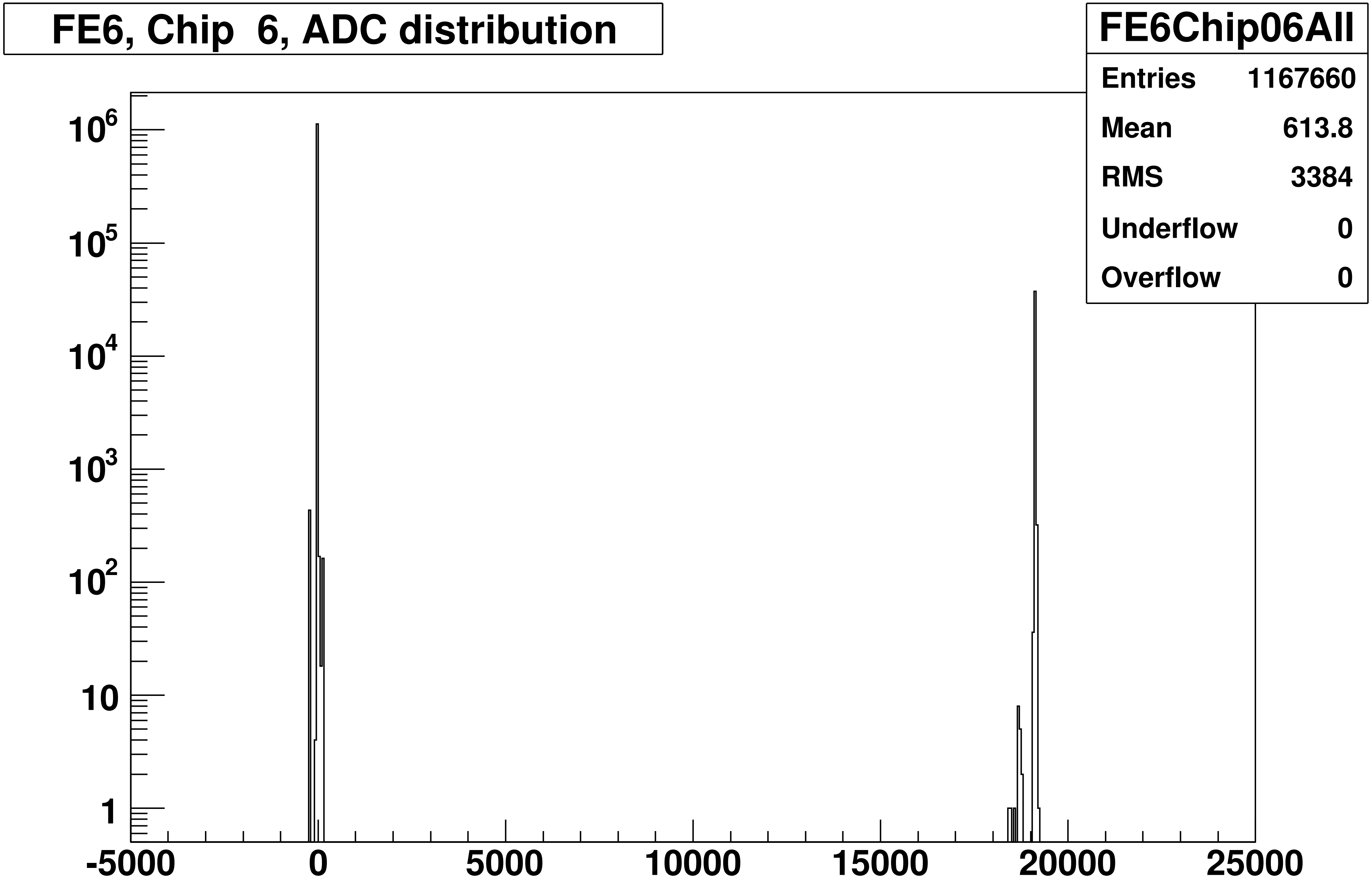

FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 1167660
Mean 1.918e+04
RMS 4.544
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 1167660
Mean 613.8
RMS 3384
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
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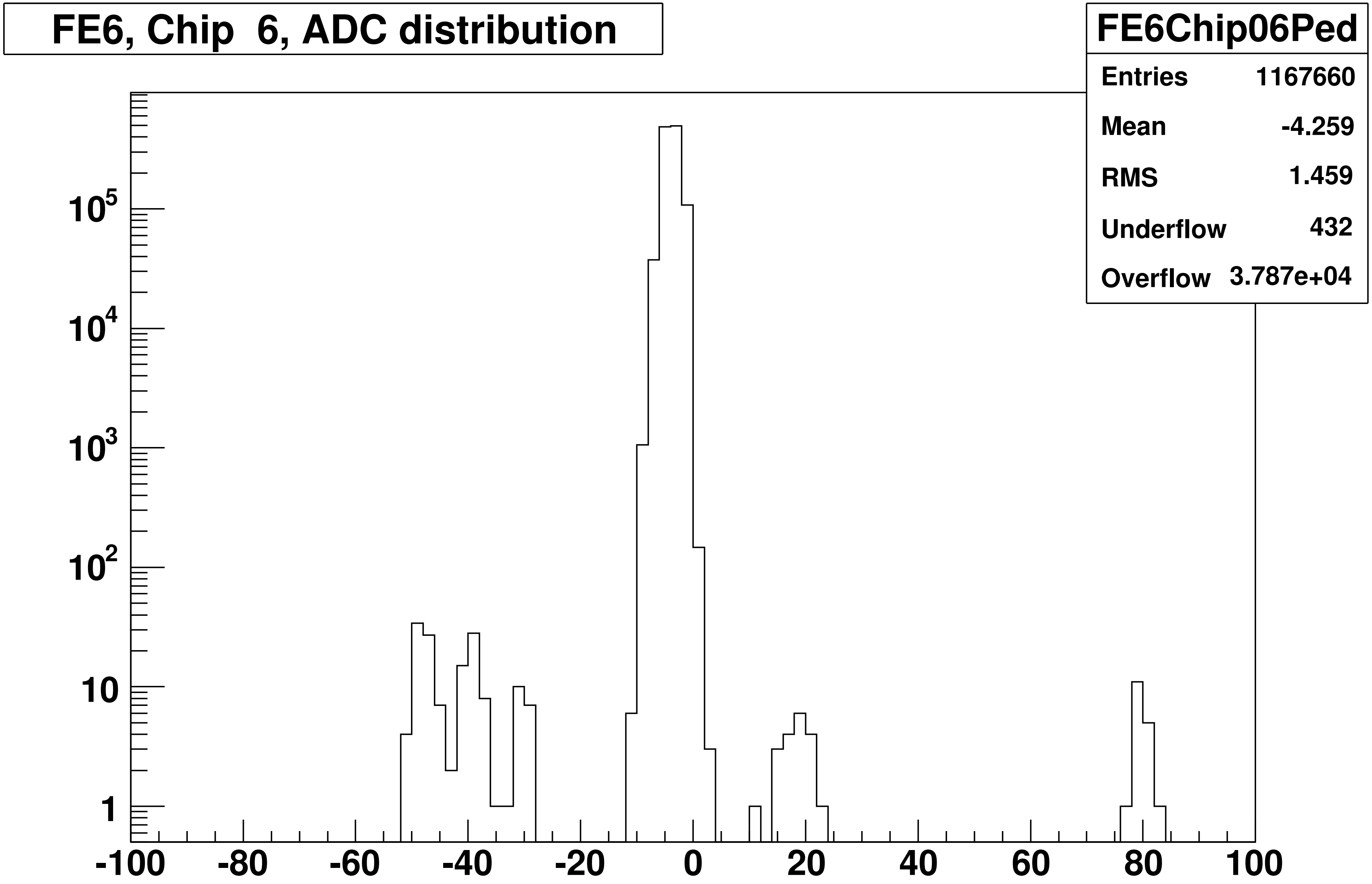

FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 1167660
Mean -4.259
RMS 1.459
Underflow 432
Overflow 3.787e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
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-20
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60
80
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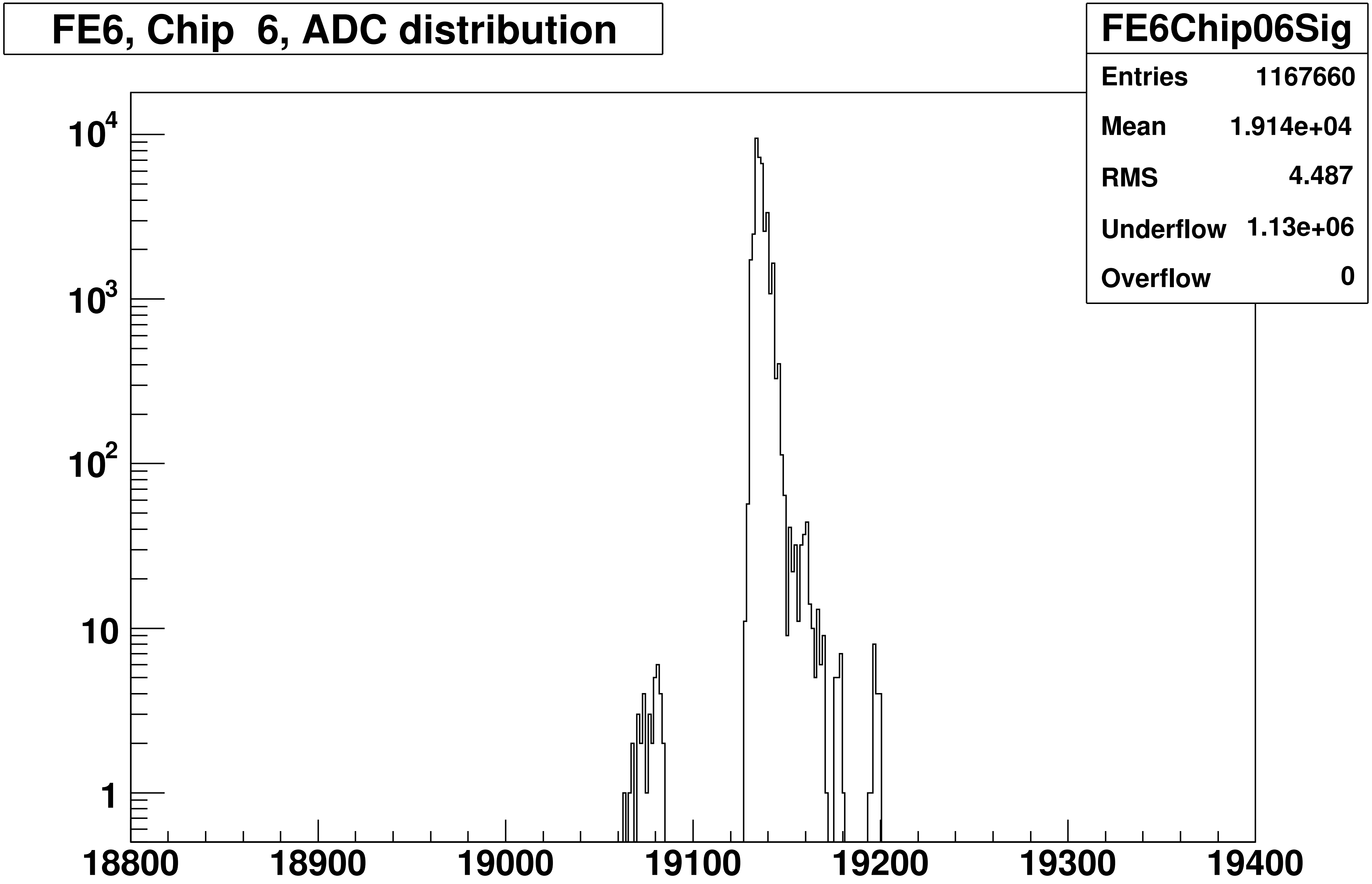

FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 1167660
Mean 1.914e+04
RMS 4.487
Underflow 1.13e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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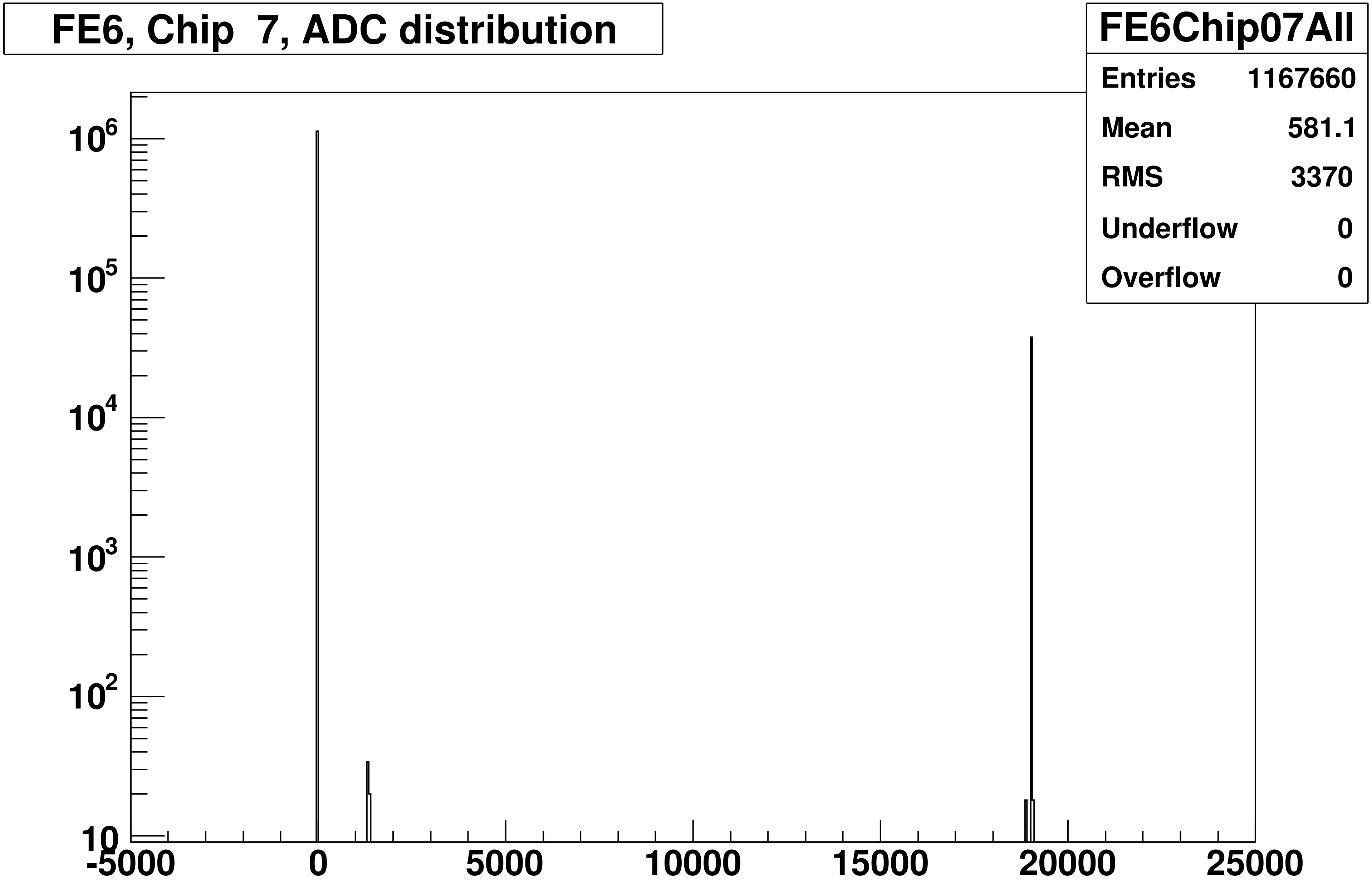
19100
19200
19300
19400



FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 1167660
Mean 581.1
RMS 3370
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
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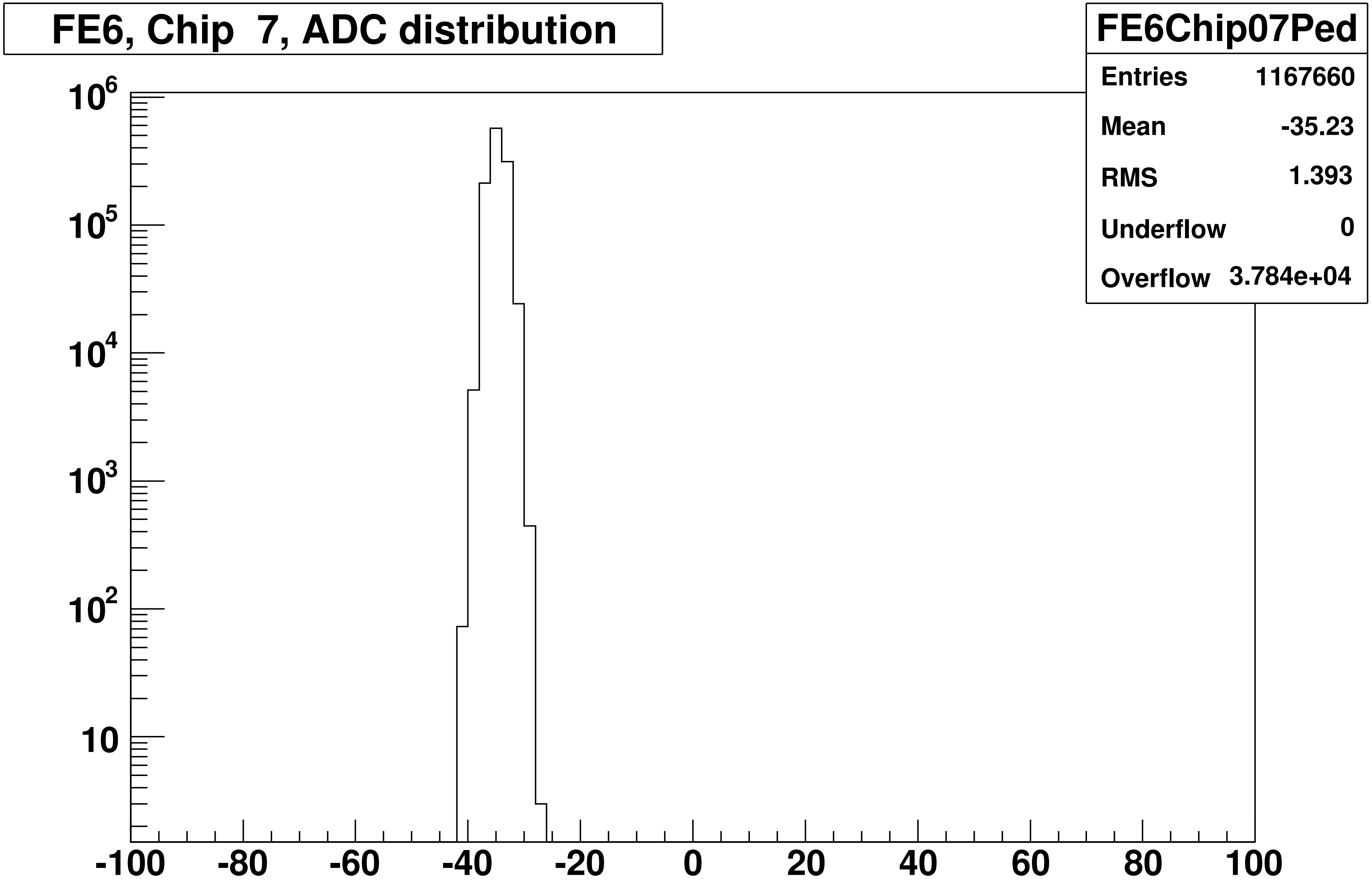
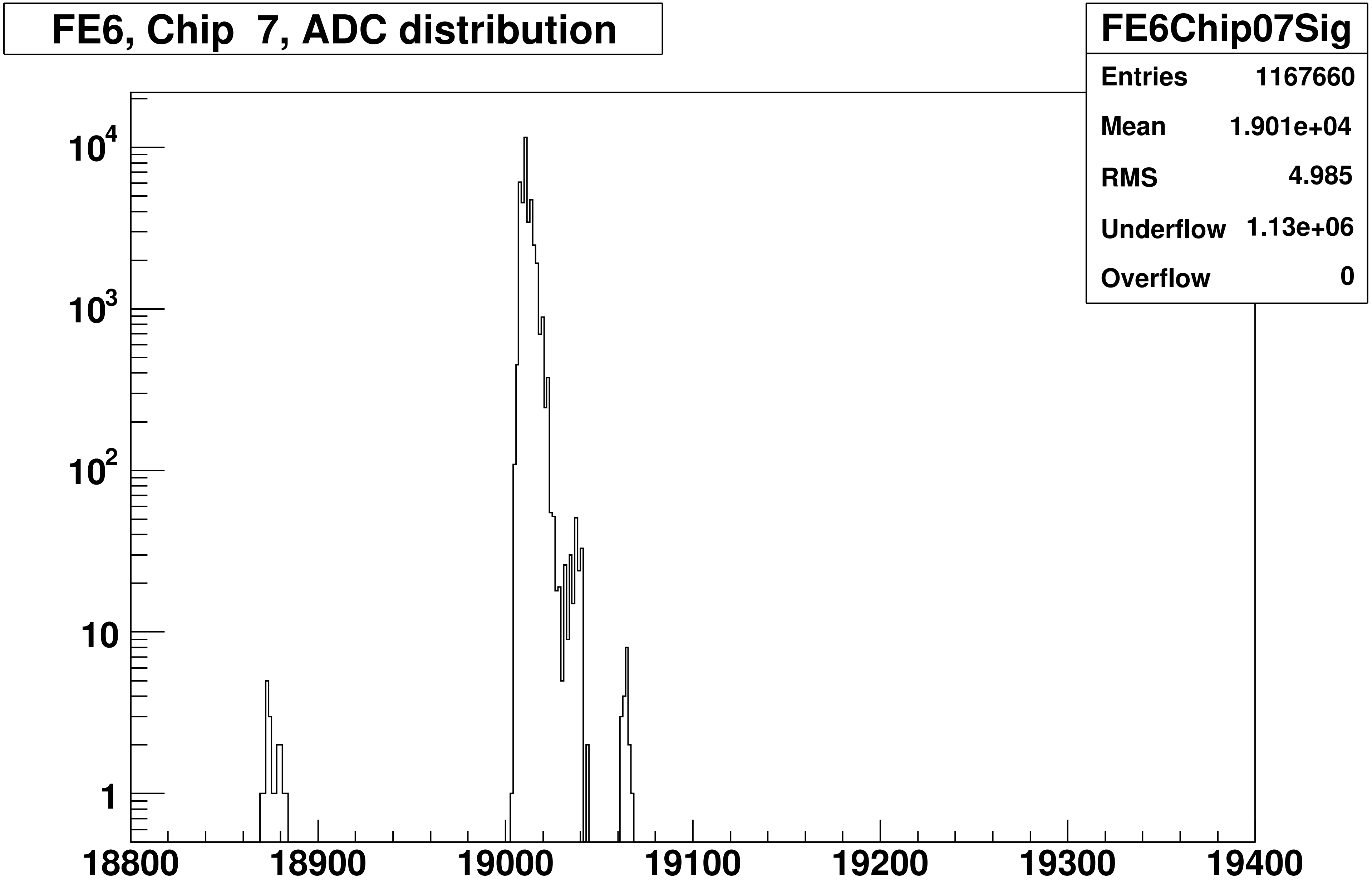
20000
25000



FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 1167660
Mean -35.23
RMS 1.393
Underflow 0
Overflow 3.784e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 1167660
Mean 1.901e+04
RMS 4.985
Underflow 1.13e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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19100
19200
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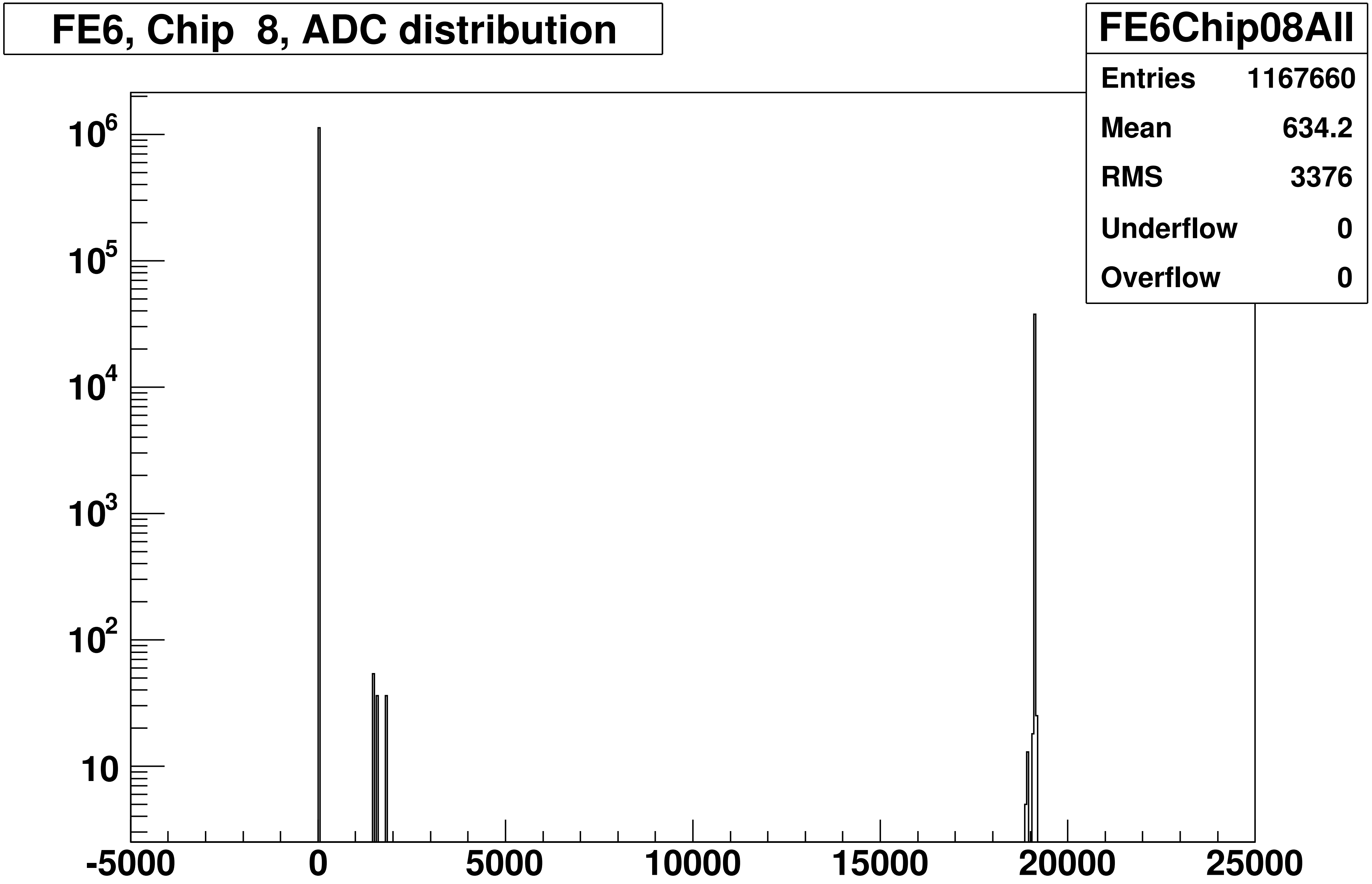
19400


FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 1167660
Mean 634.2
RMS 3376
Underflow 0
Overflow 0
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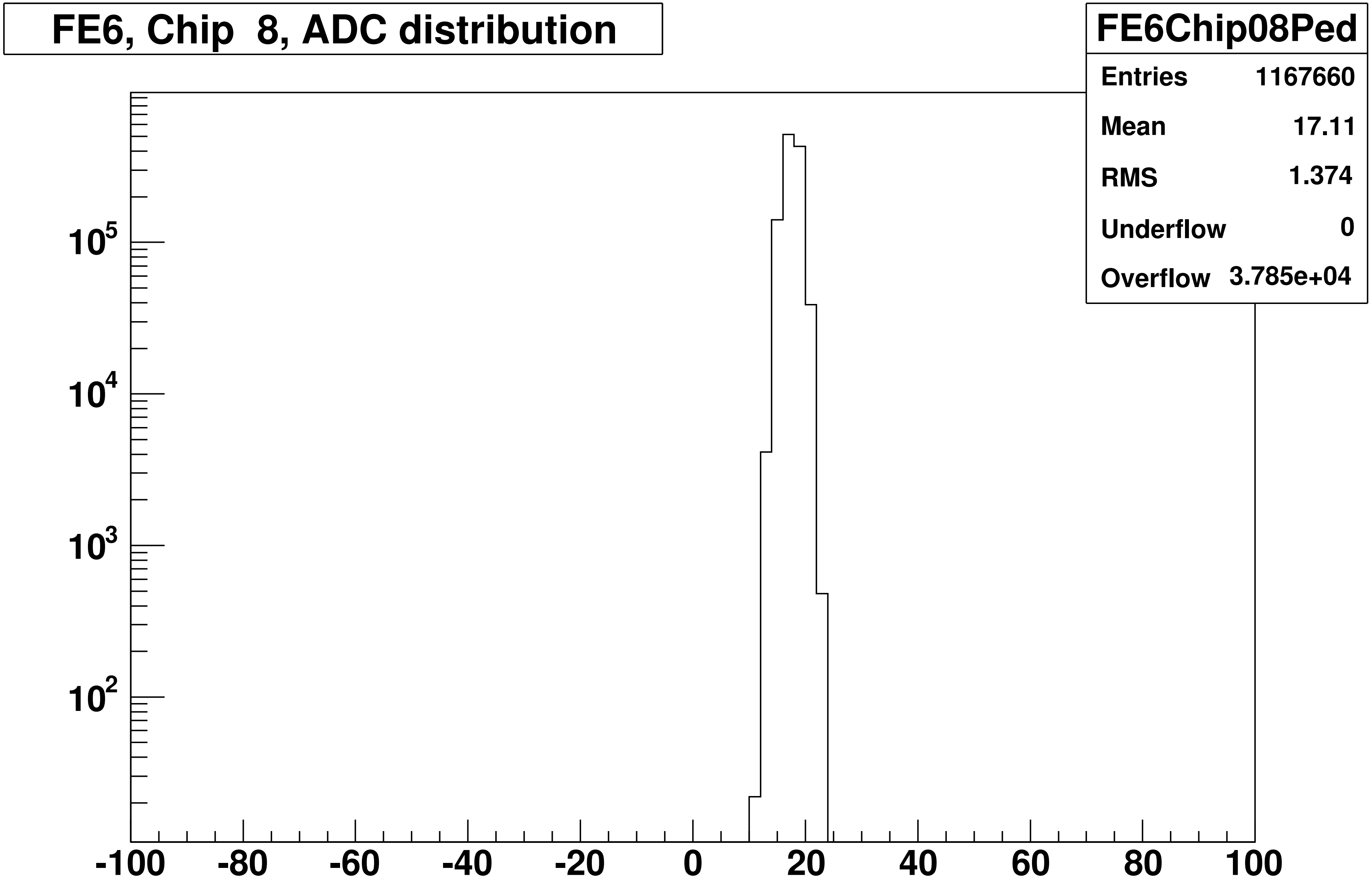
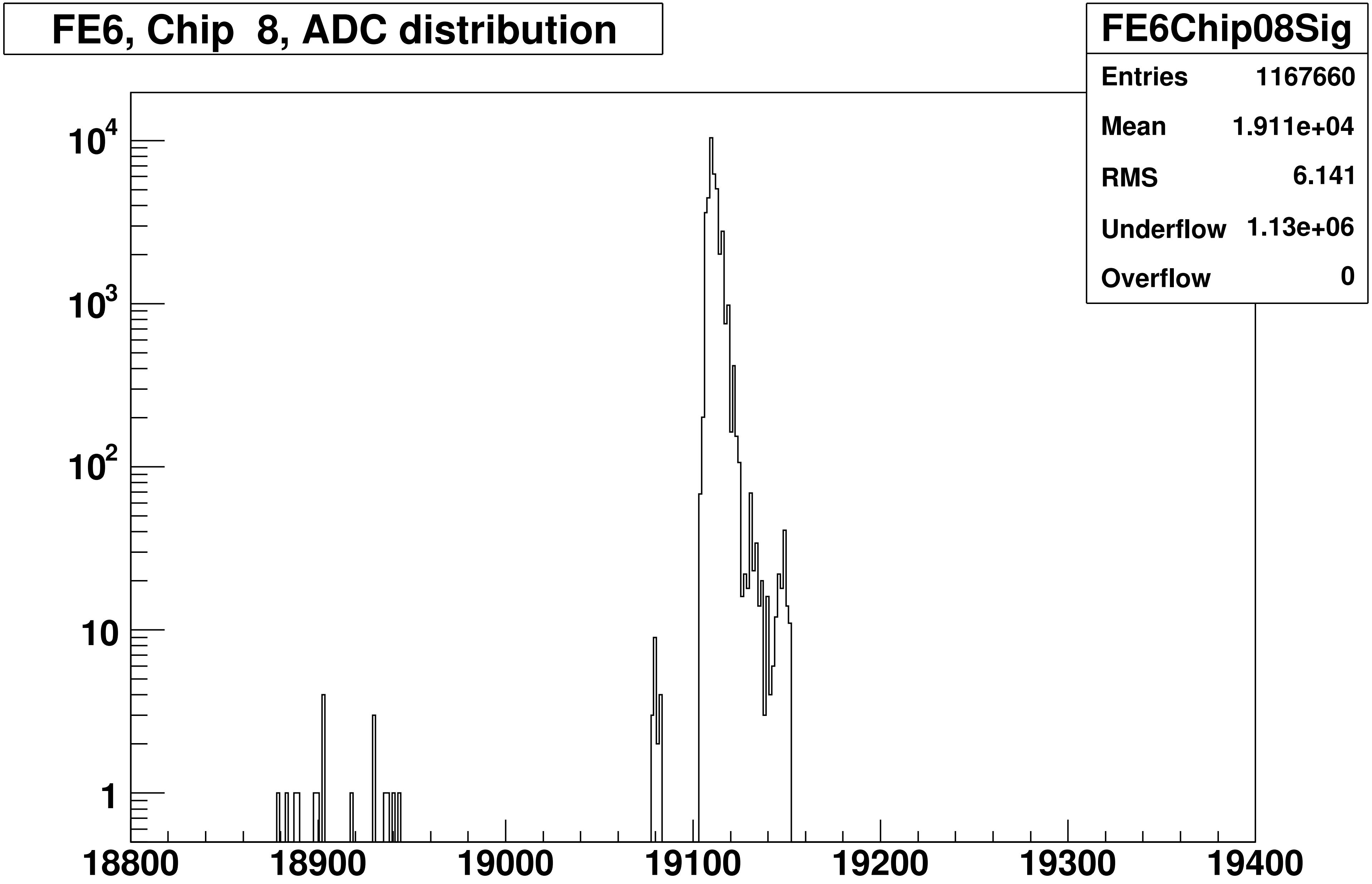
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 1167660
Mean 17.11
RMS 1.374
Underflow 0
Overflow 3.785e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 1167660
Mean 1.911e+04
RMS 6.141
Underflow 1.13e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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19100
19200
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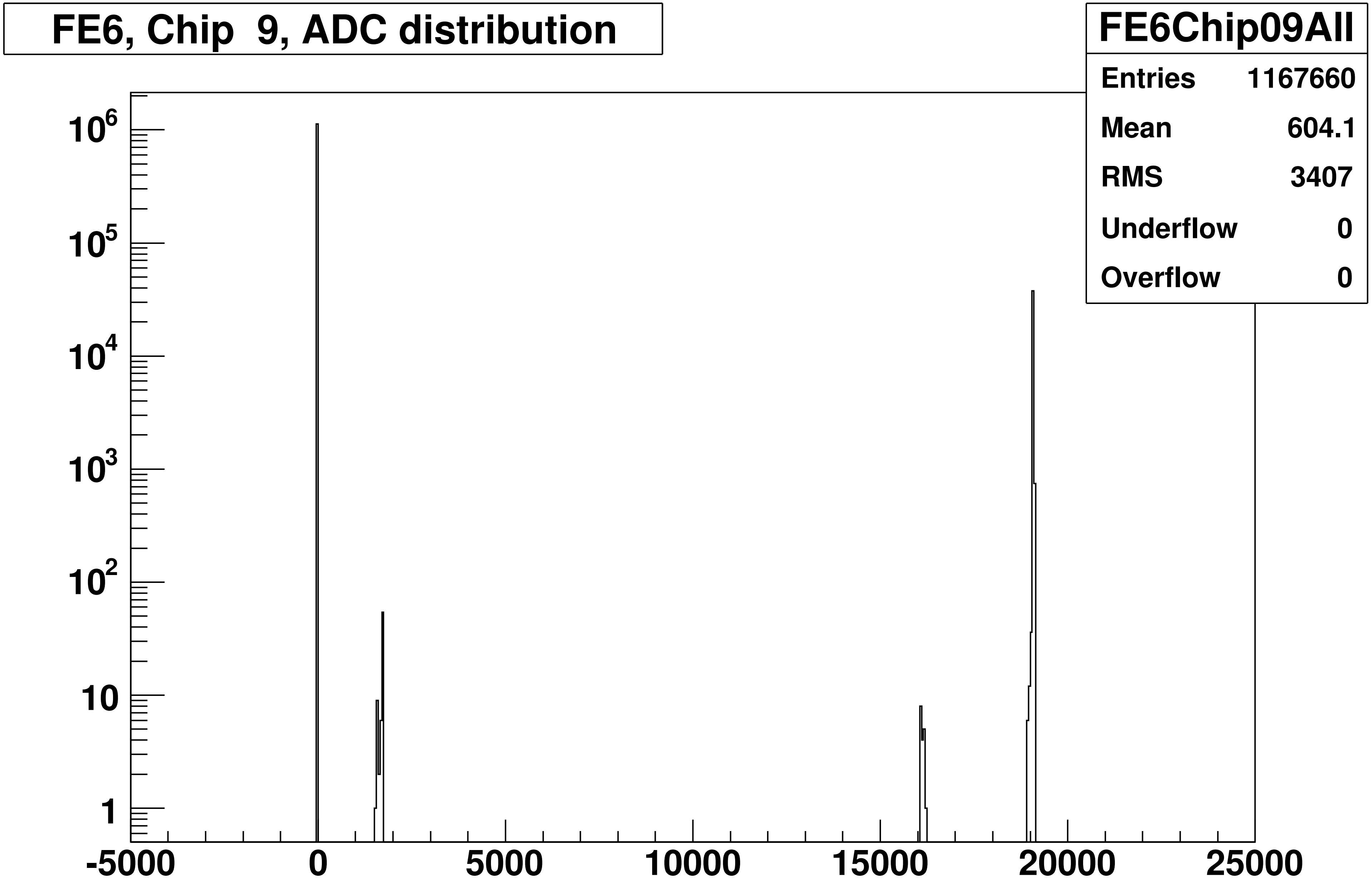
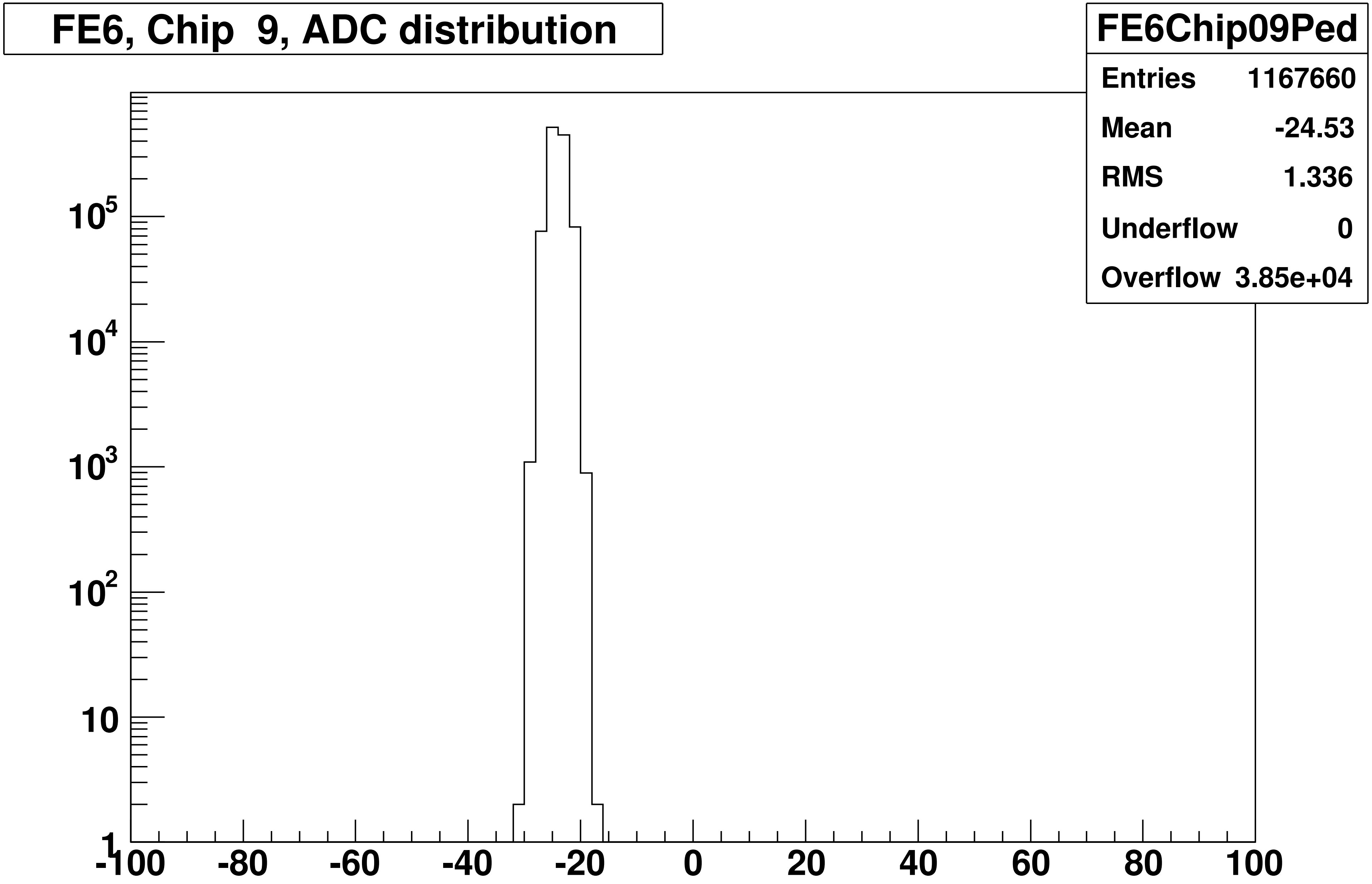
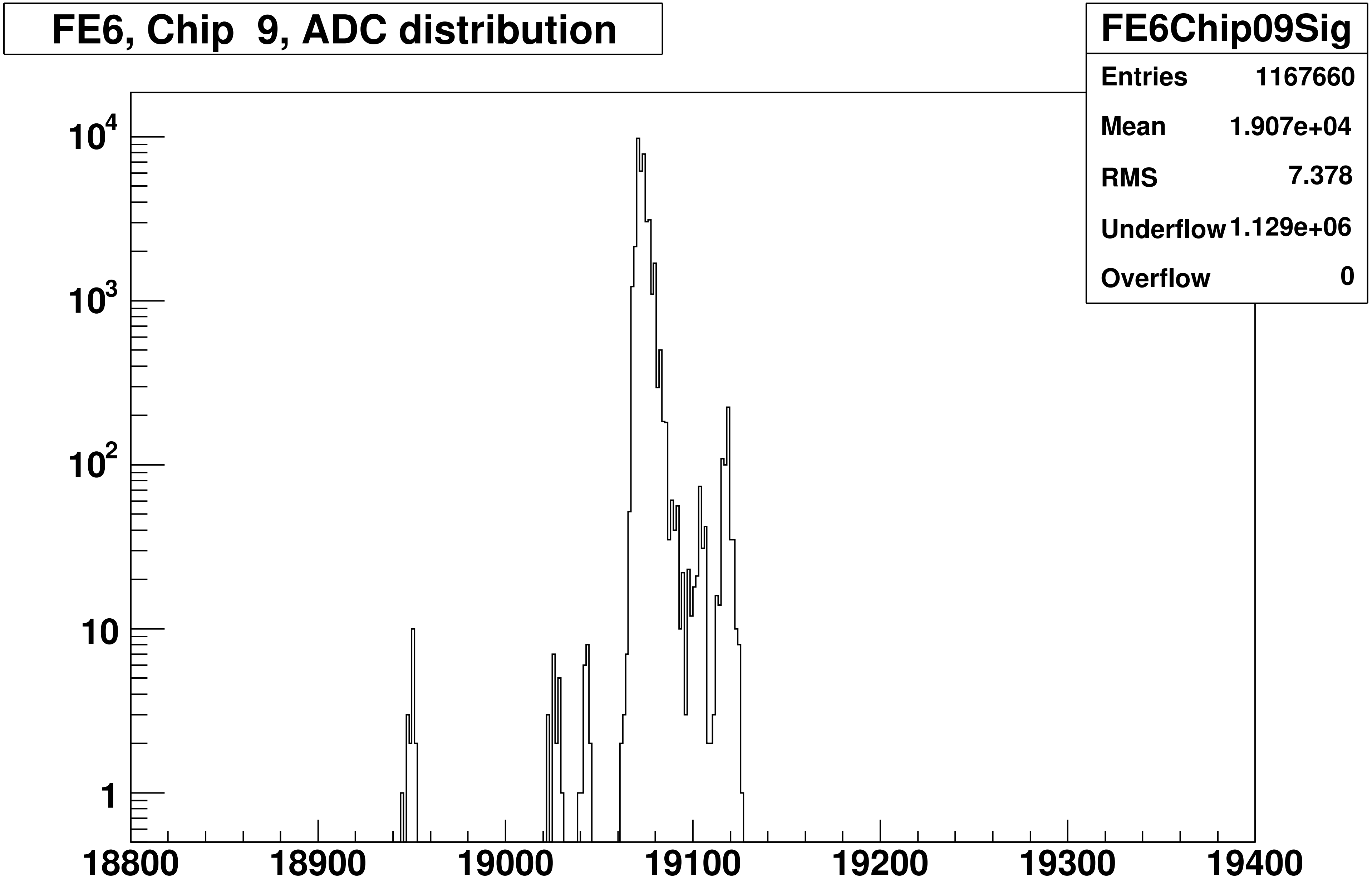
19400

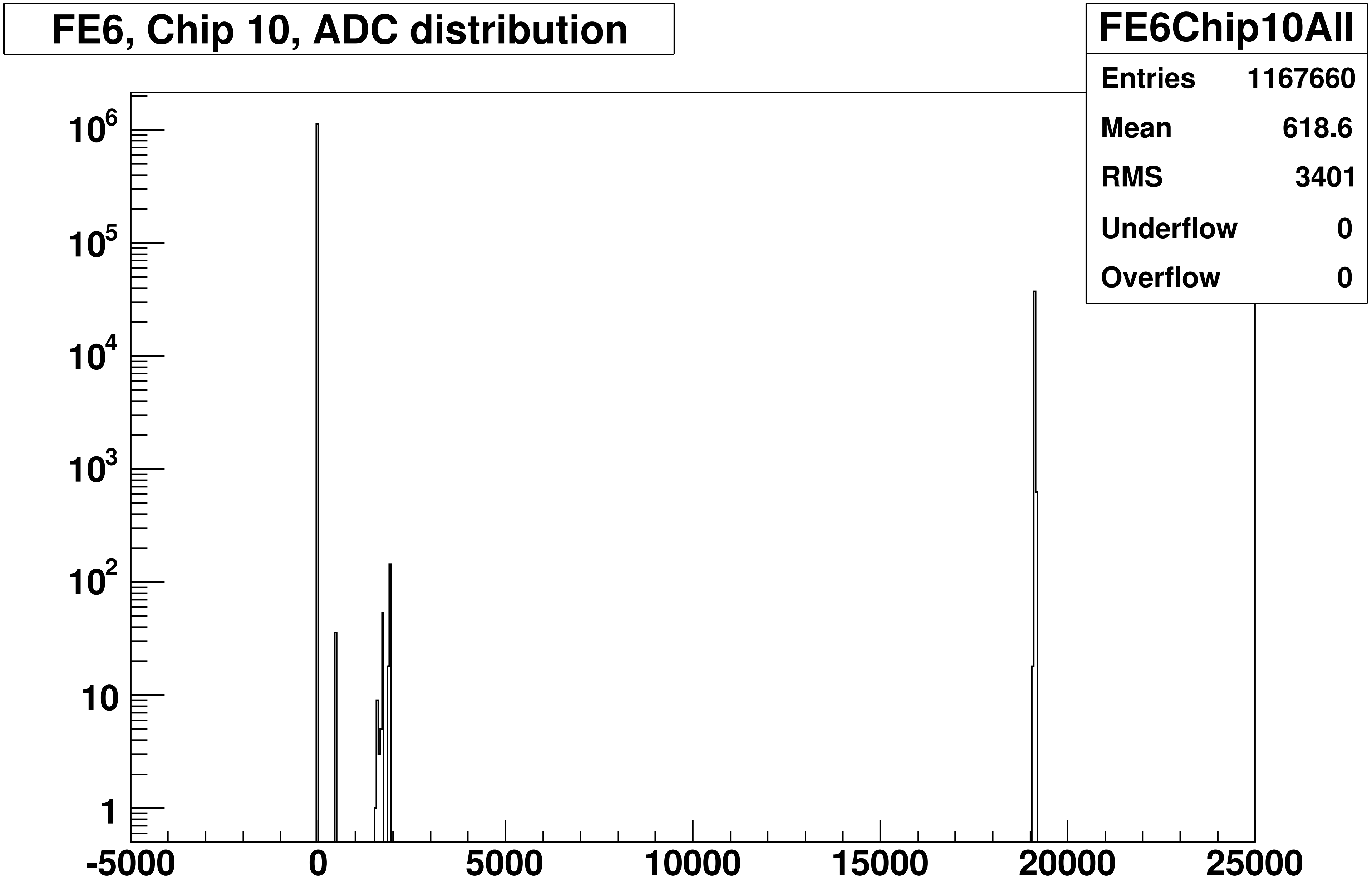


FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 1167660
Mean 604.1
RMS 3407
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

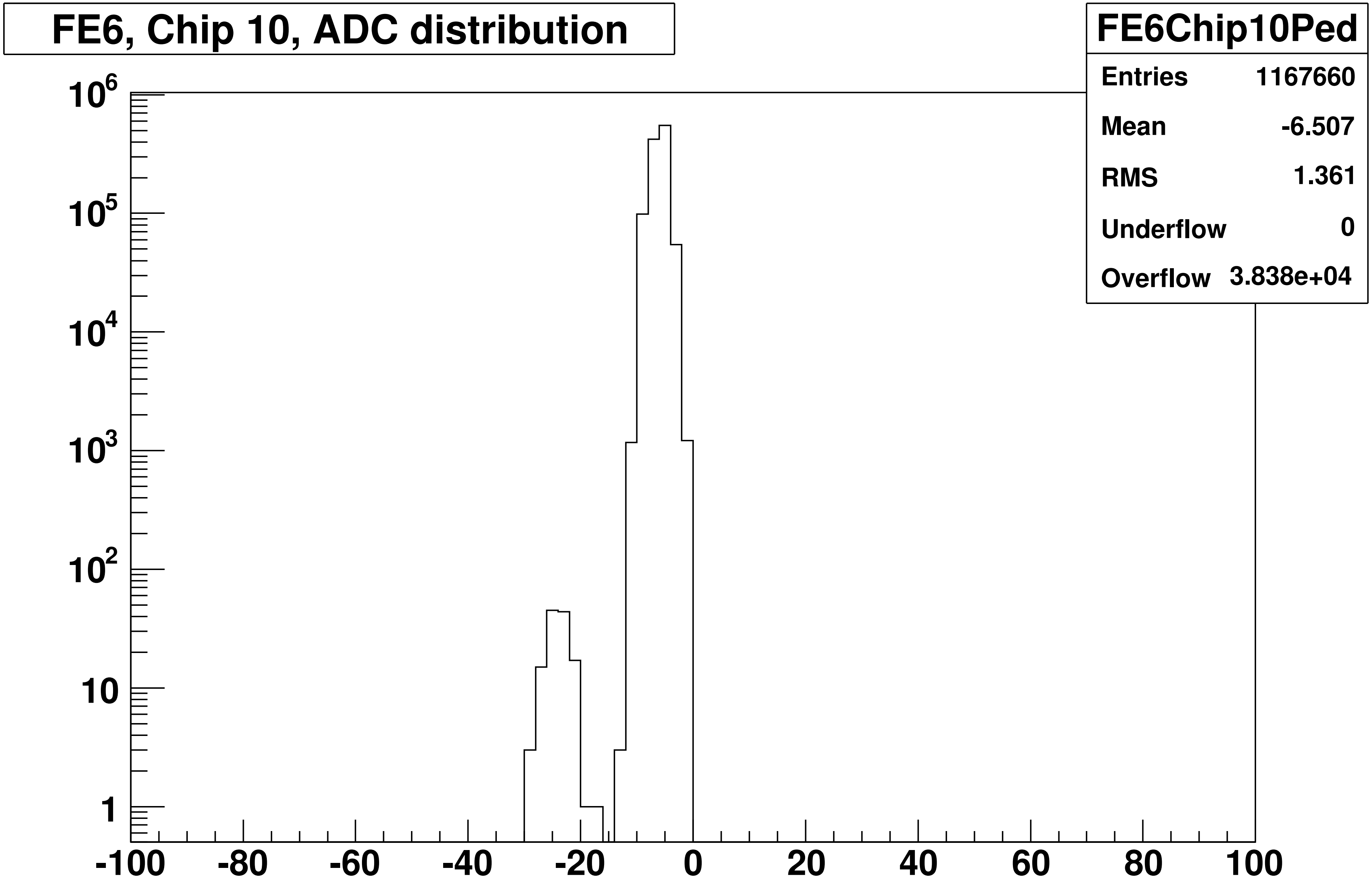

FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 1167660
Mean -24.53
RMS 1.336
Underflow 0
Overflow 3.85e+04
1
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

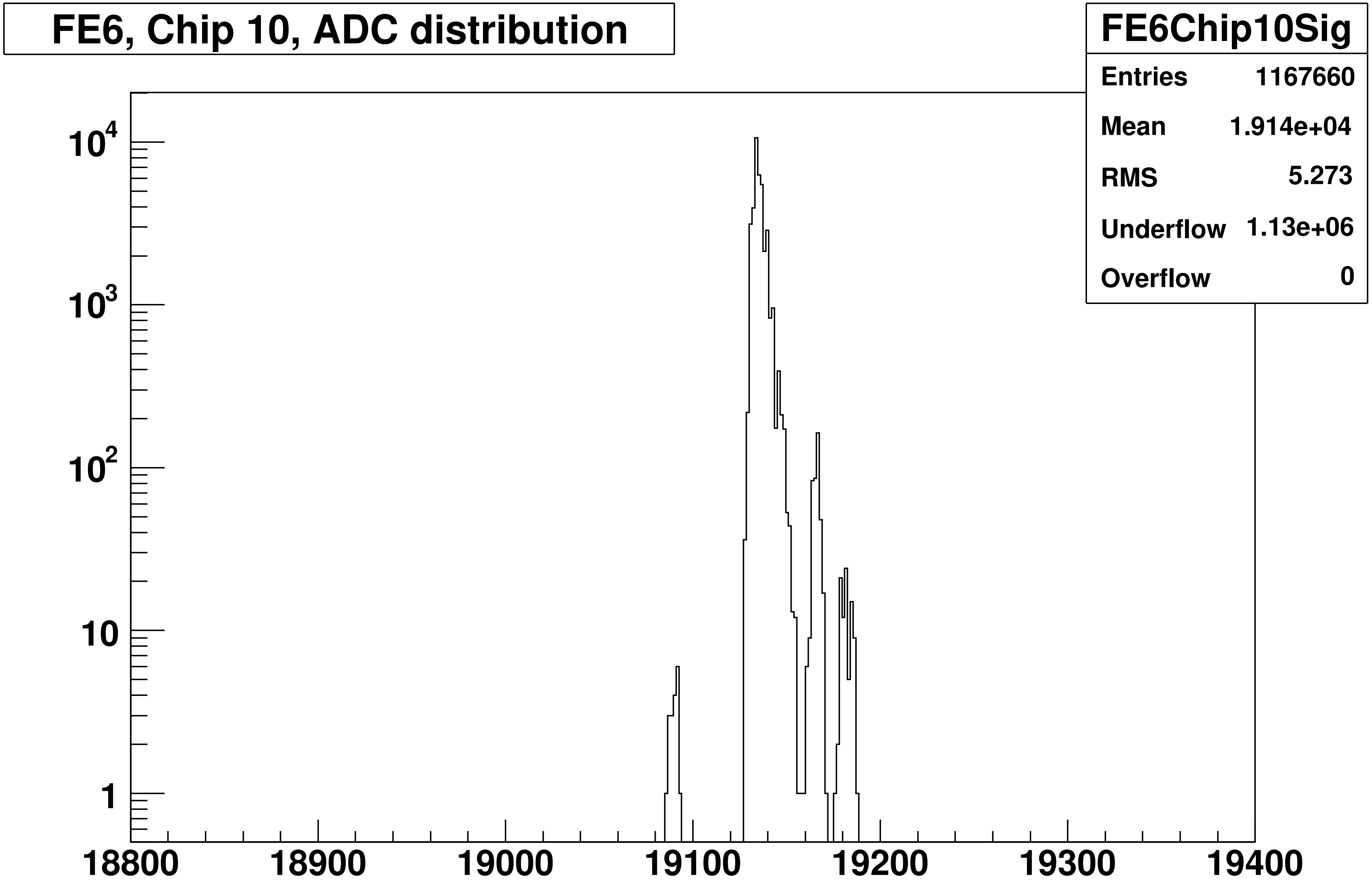


FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 1167660
Mean 1.907e+04
RMS 7.378
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

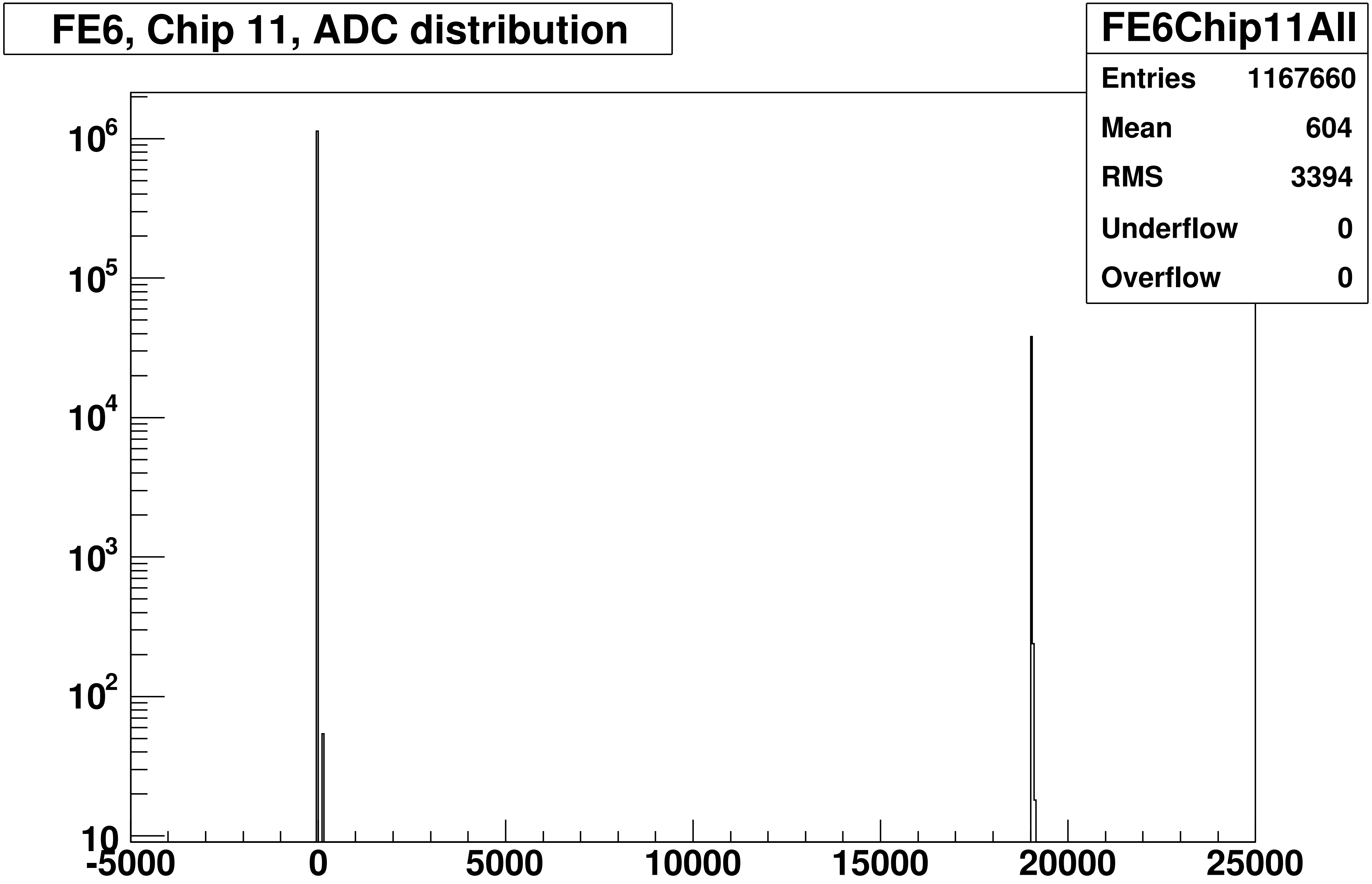


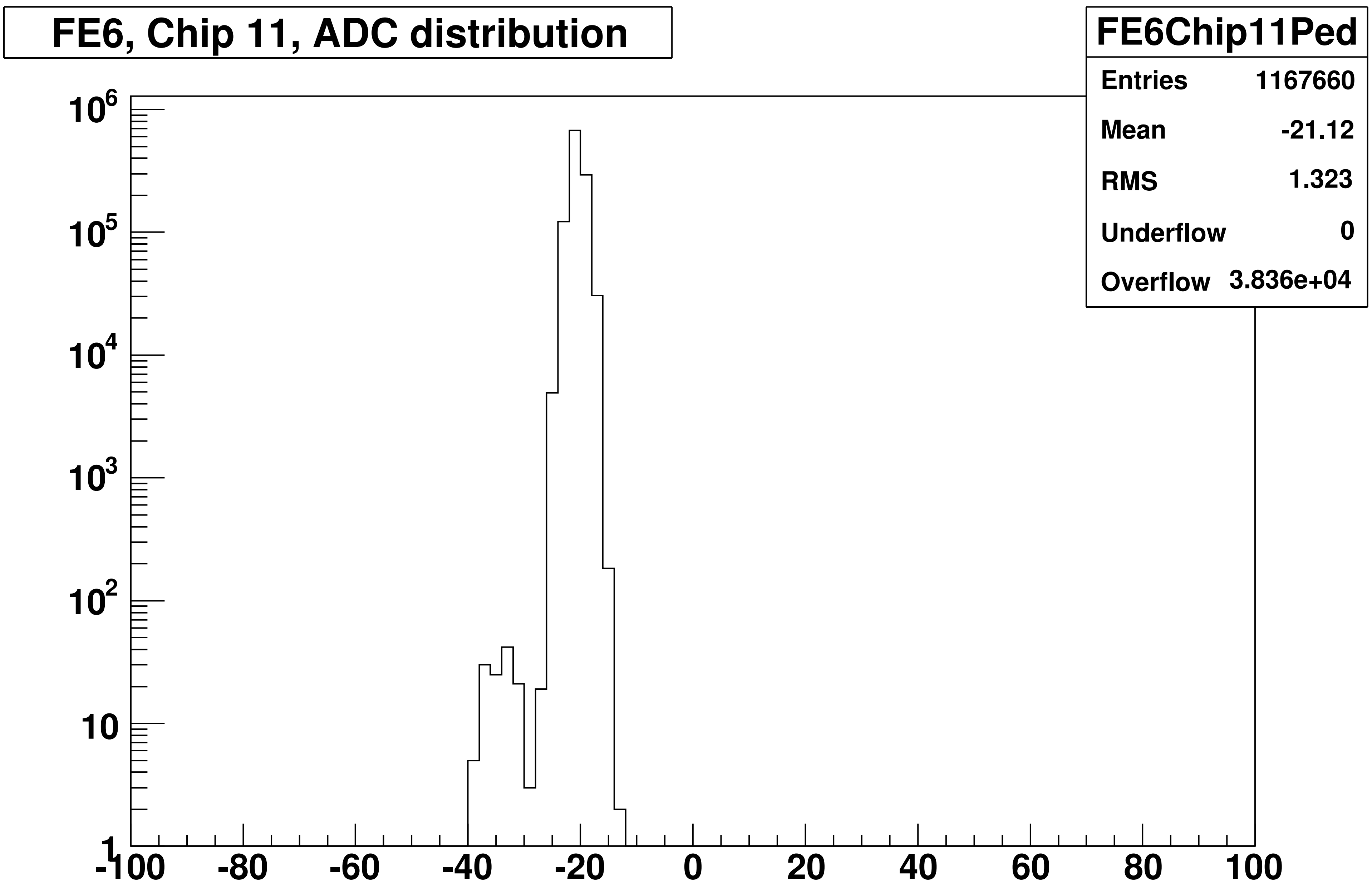
FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 1167660
Mean 618.6
RMS 3401
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 1167660
Mean -6.507
RMS 1.361
Underflow 0
Overflow 3.838e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

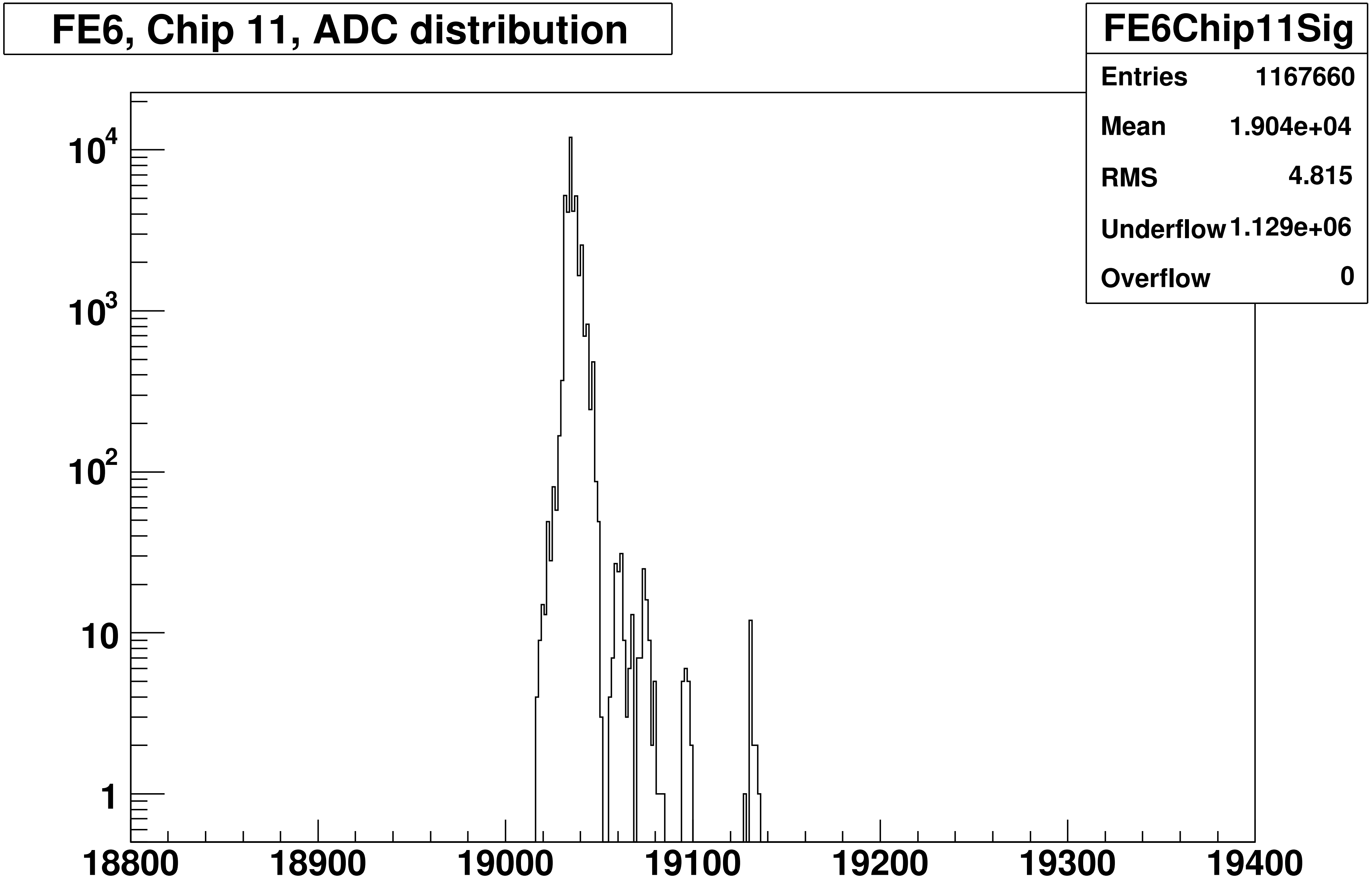


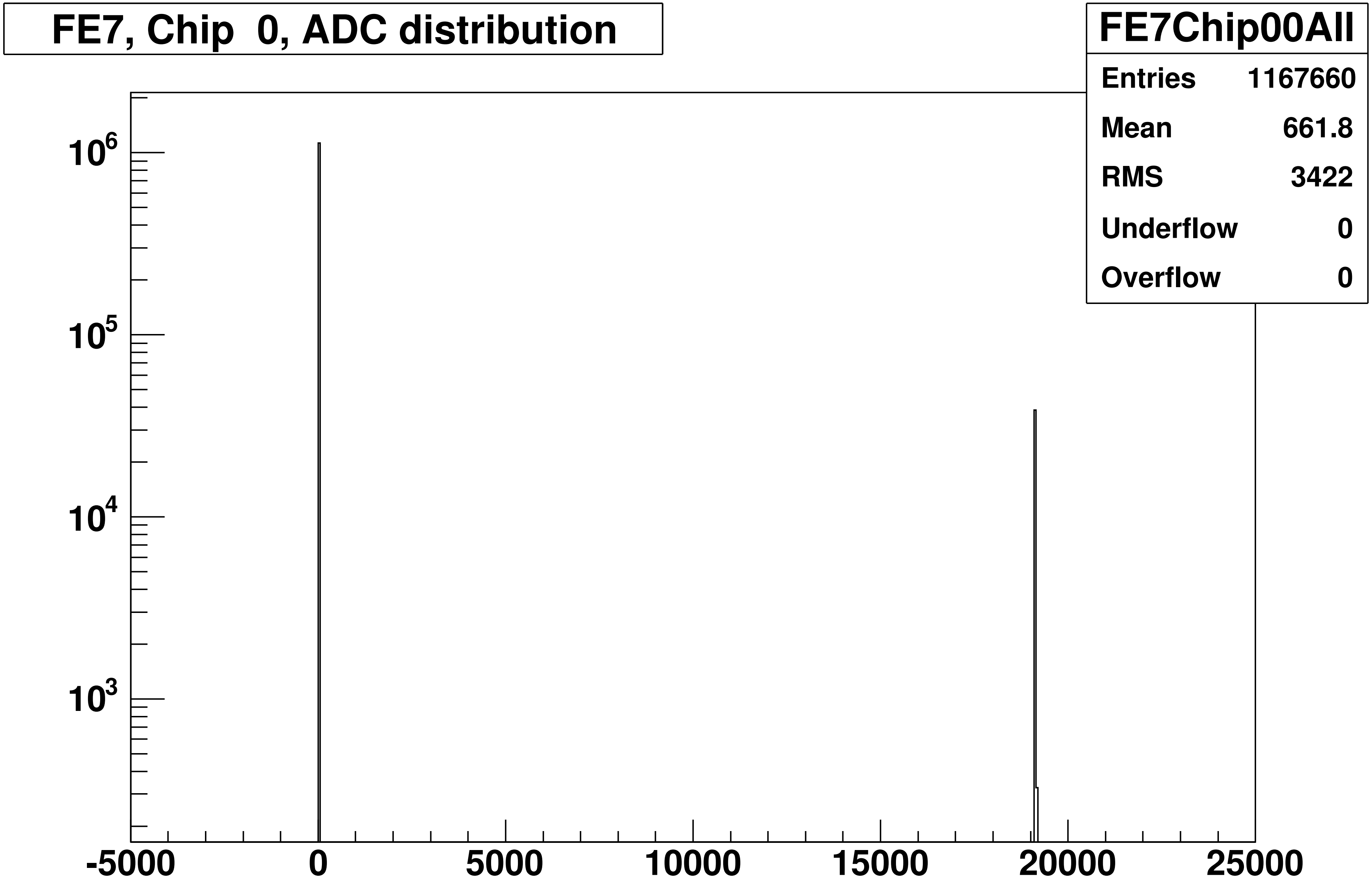
FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 1167660
Mean 1.914e+04
RMS 5.273
Underflow 1.13e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

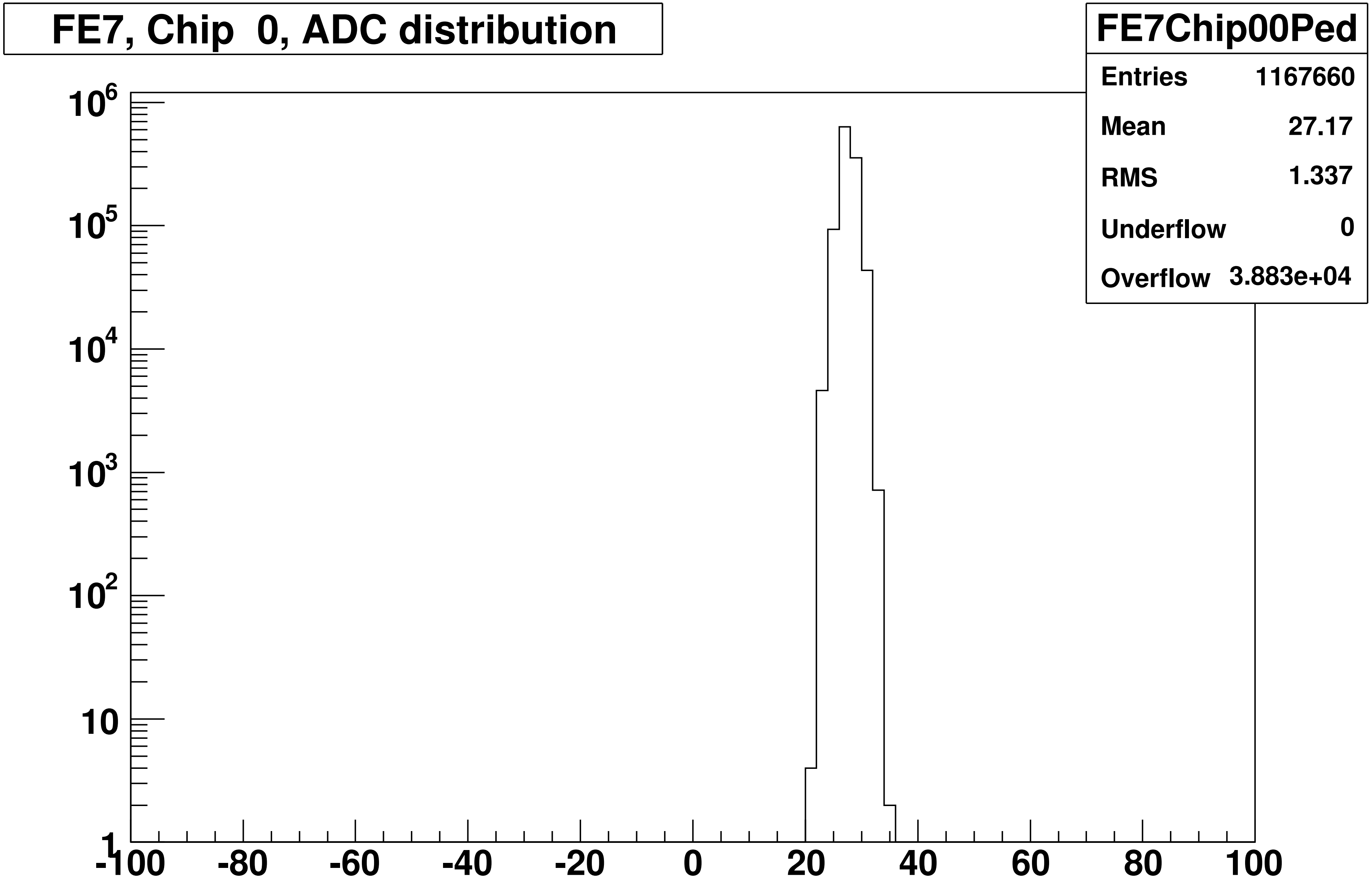

FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 1167660
Mean 604
RMS 3394
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

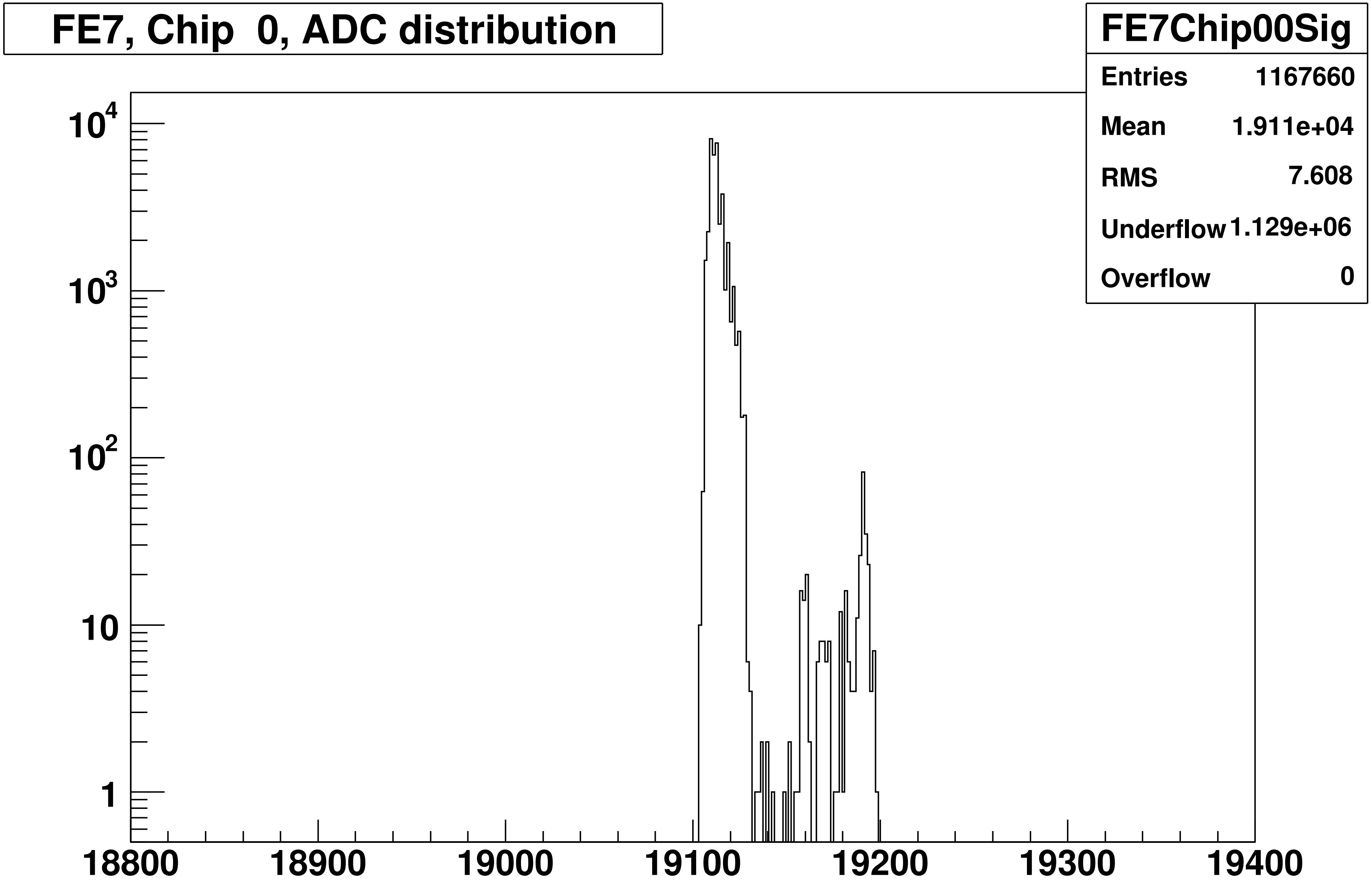


FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 1167660
Mean -21.12
RMS 1.323
Underflow 0
Overflow 3.836e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 1167660
Mean 1.904e+04
RMS 4.815
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 1167660
Mean 661.8
RMS 3422
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 1167660
Mean 27.17
RMS 1.337
Underflow 0
Overflow 3.883e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 1167660
Mean 1.911e+04
RMS 7.608
Underflow 1.129e+06
Overflow 0
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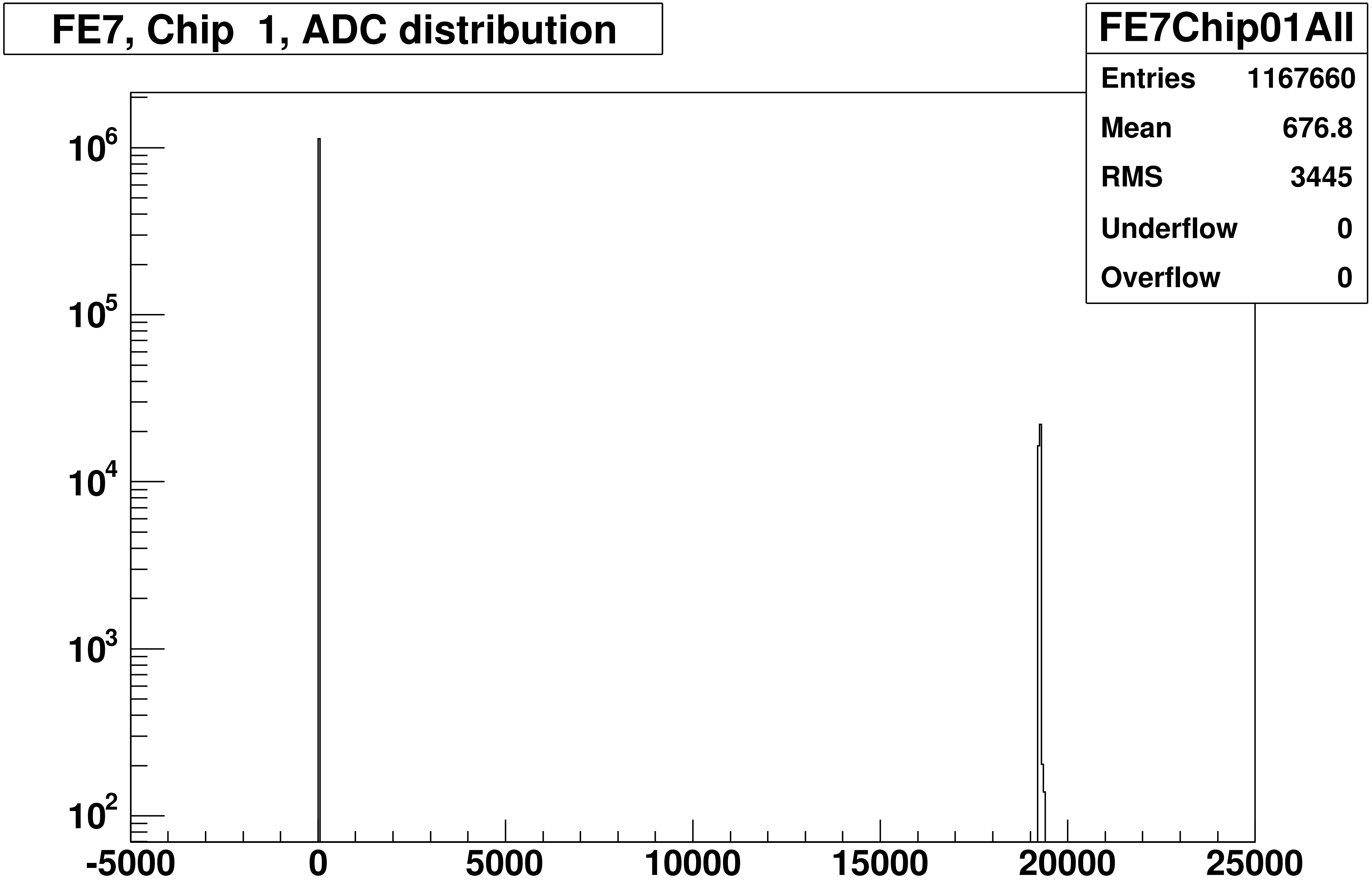
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 1167660
Mean 676.8
RMS 3445
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
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0
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10000
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20000
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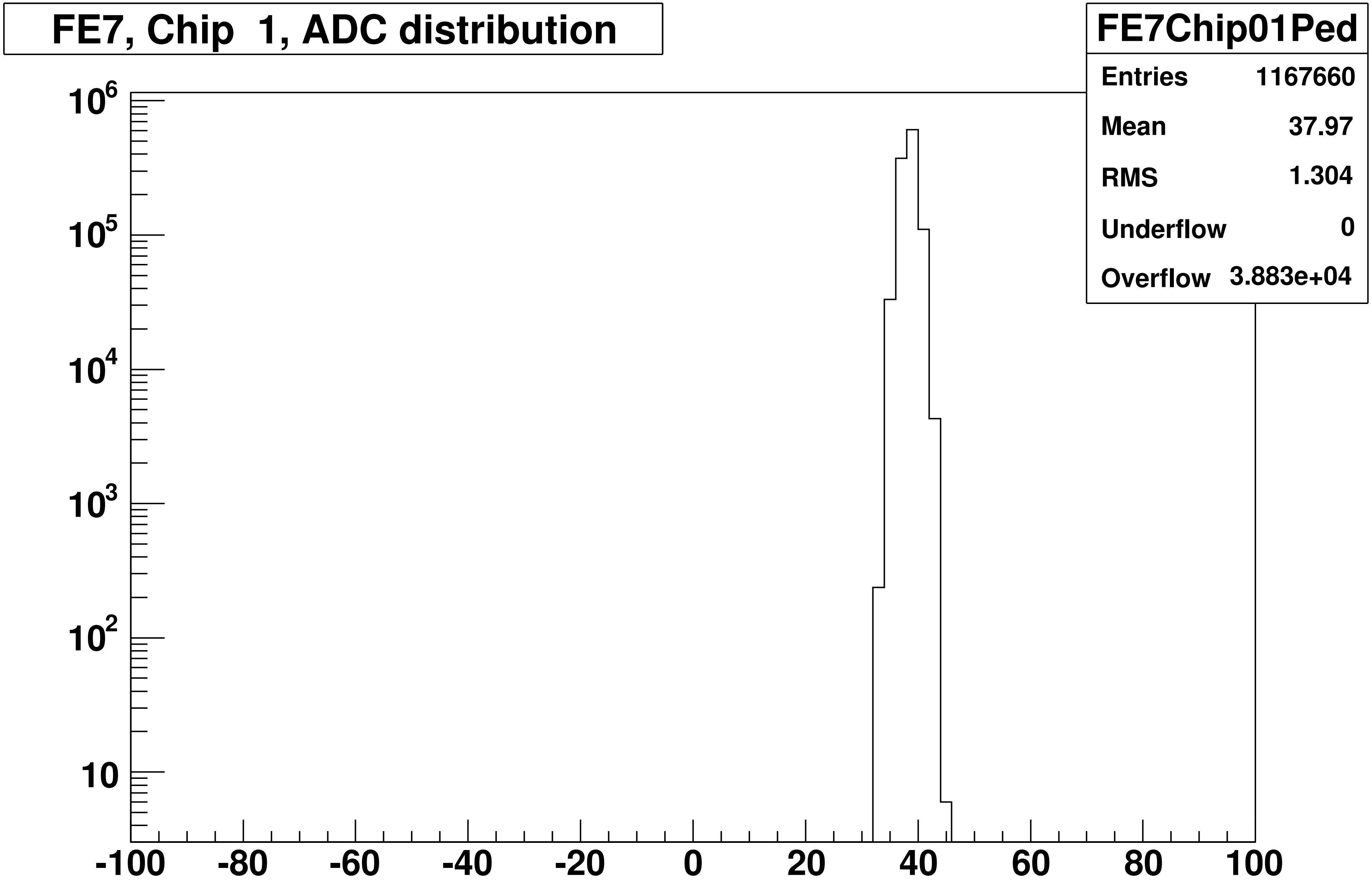
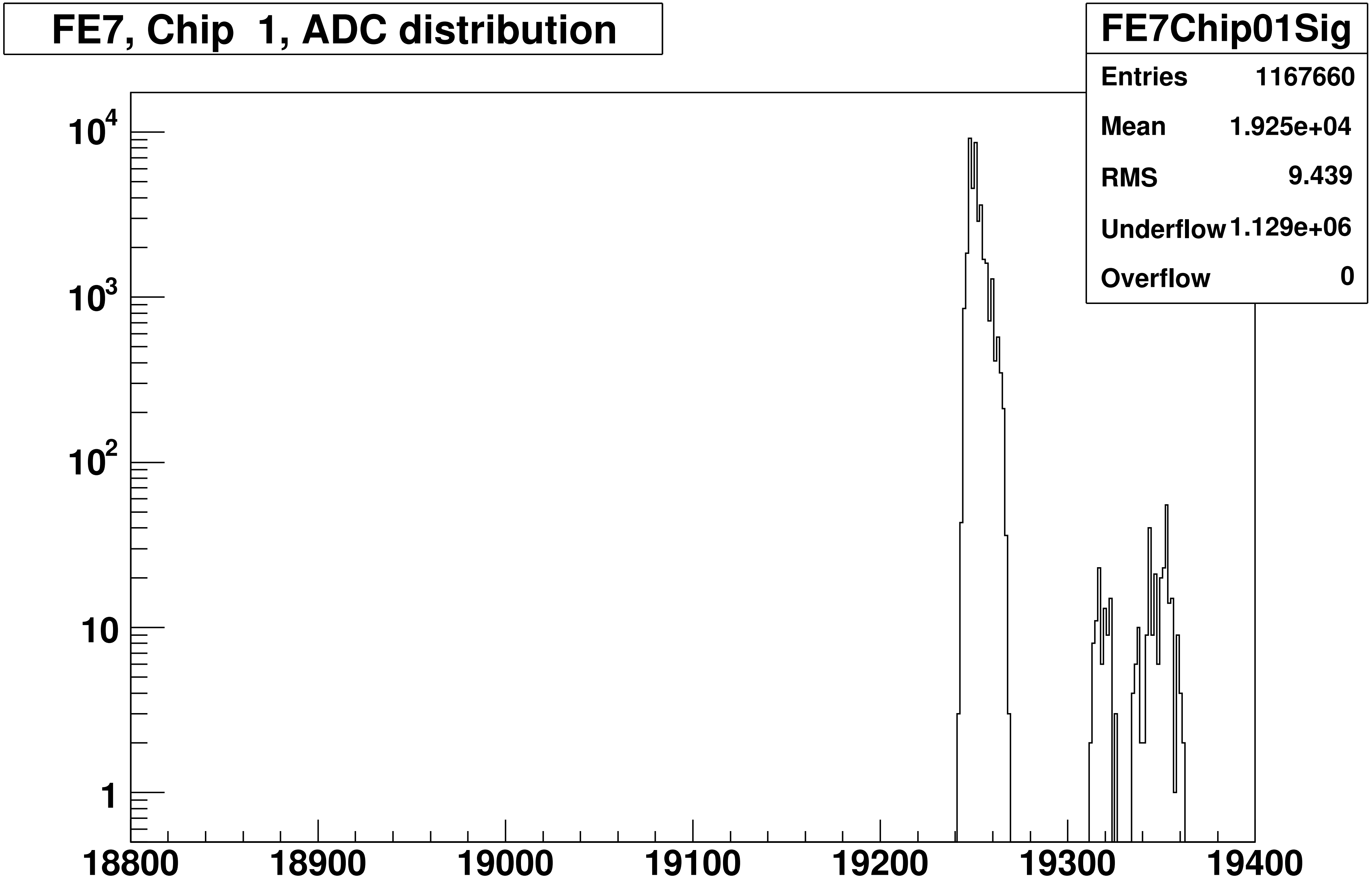


FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 1167660
Mean 37.97
RMS 1.304
Underflow 0
Overflow 3.883e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 1167660
Mean 1.925e+04
RMS 9.439
Underflow 1.129e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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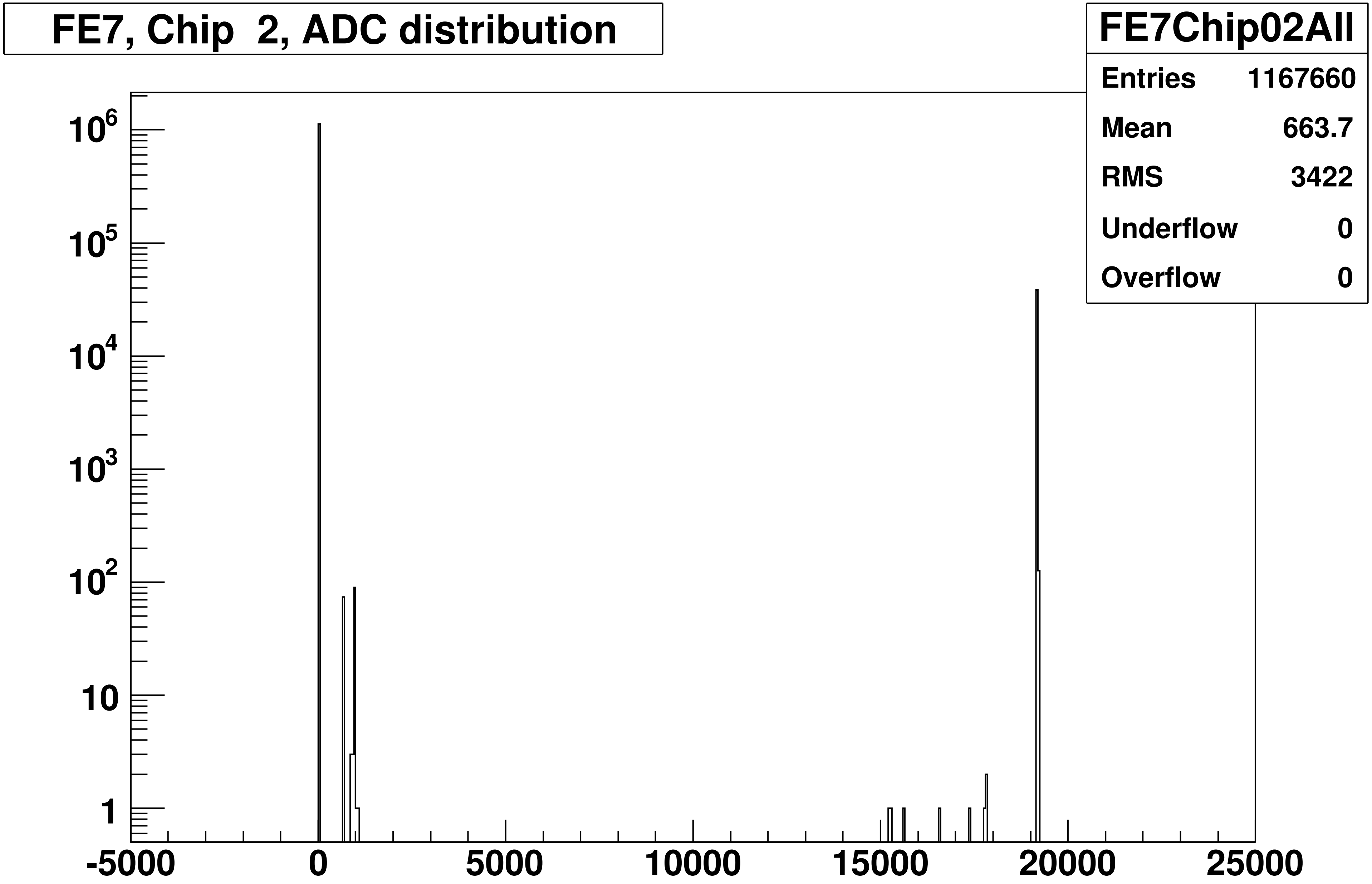

FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 1167660
Mean 663.7
RMS 3422
Underflow 0
Overflow 0
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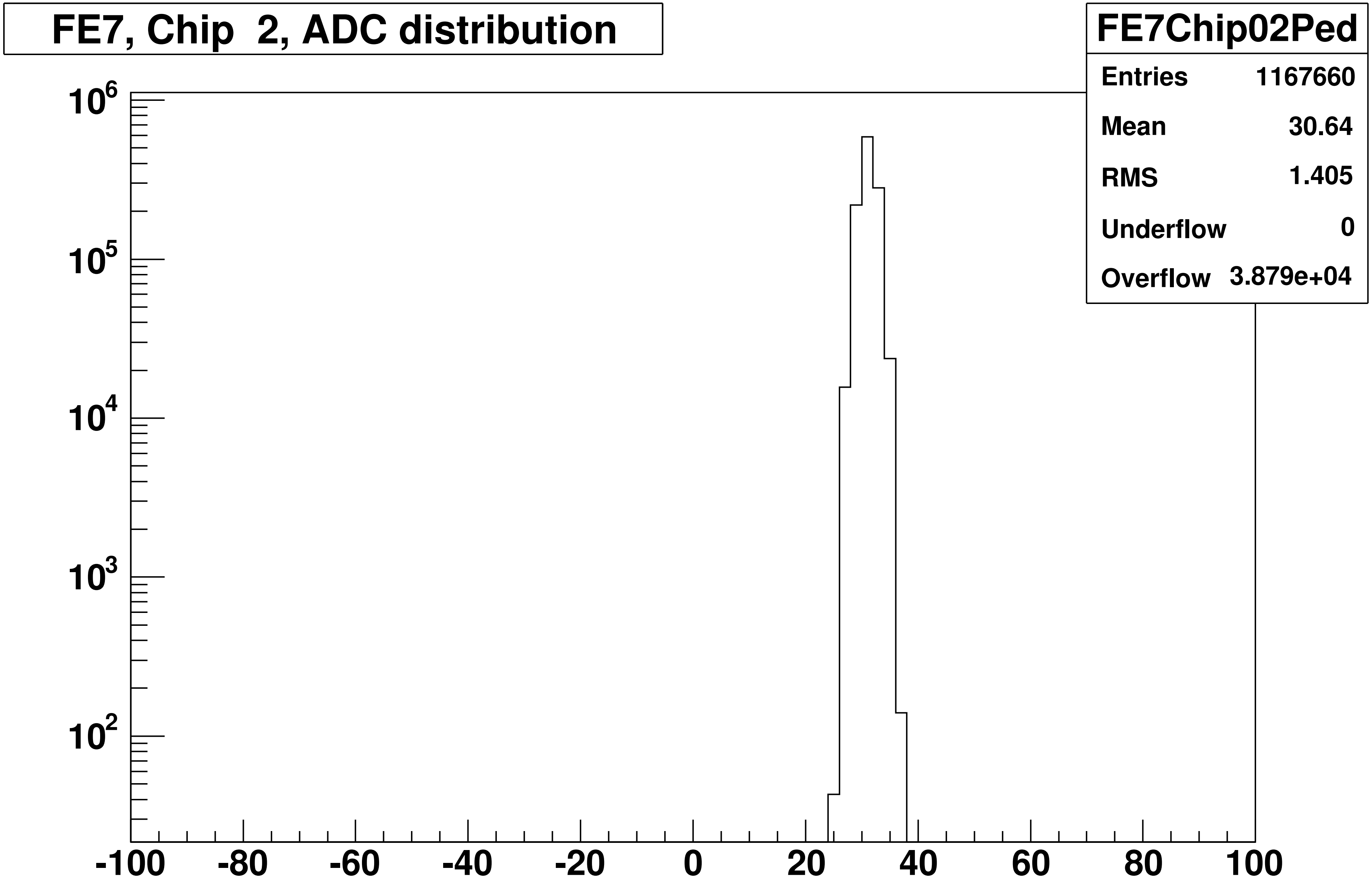
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 1167660
Mean 30.64
RMS 1.405
Underflow 0
Overflow 3.879e+04
10^6
10^5
10^4
10^3
10^2
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-80
-60
-40
-20
0
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80
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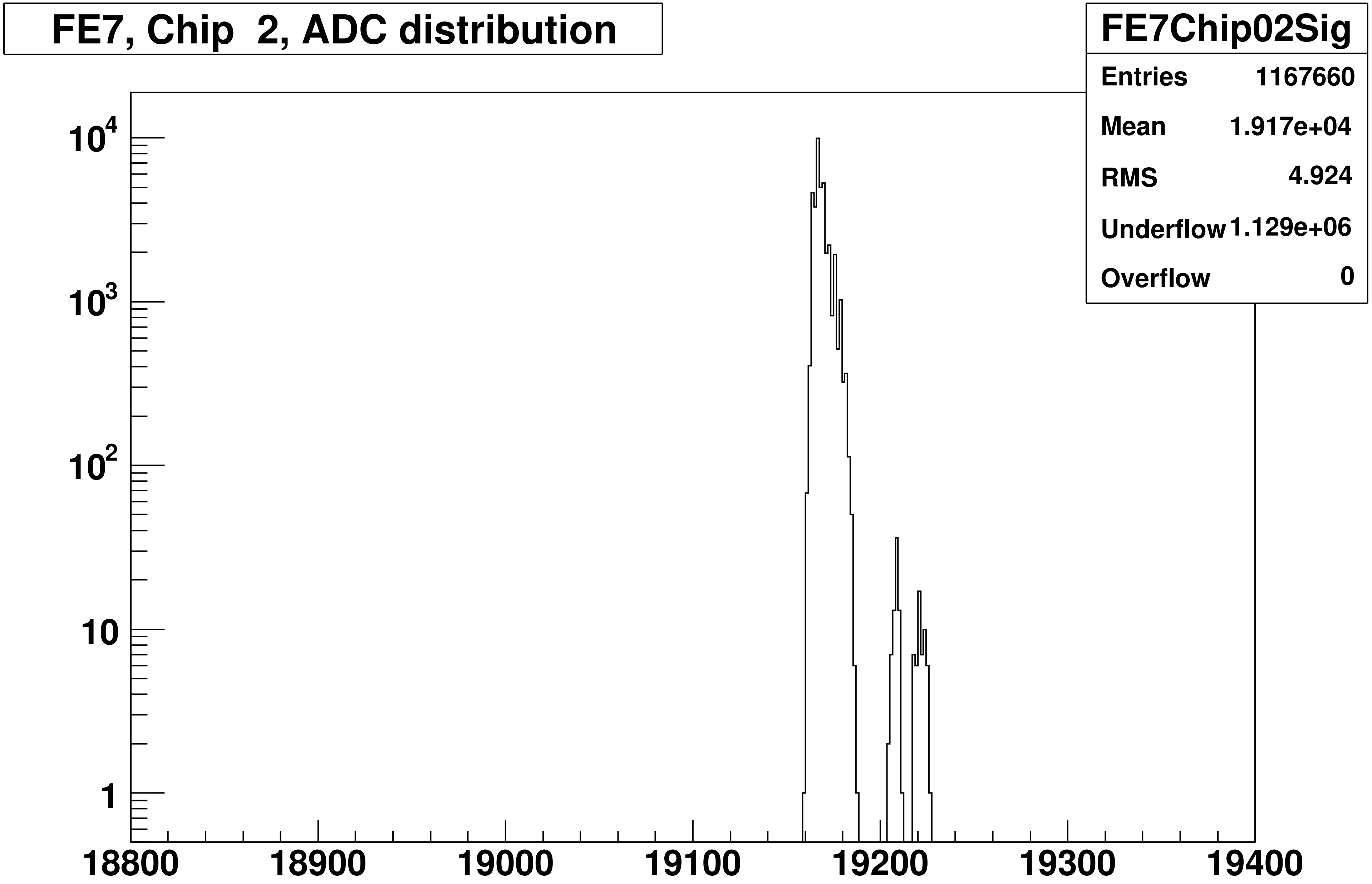
FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 1167660
Mean 1.917e+04
RMS 4.924
Underflow 1.129e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
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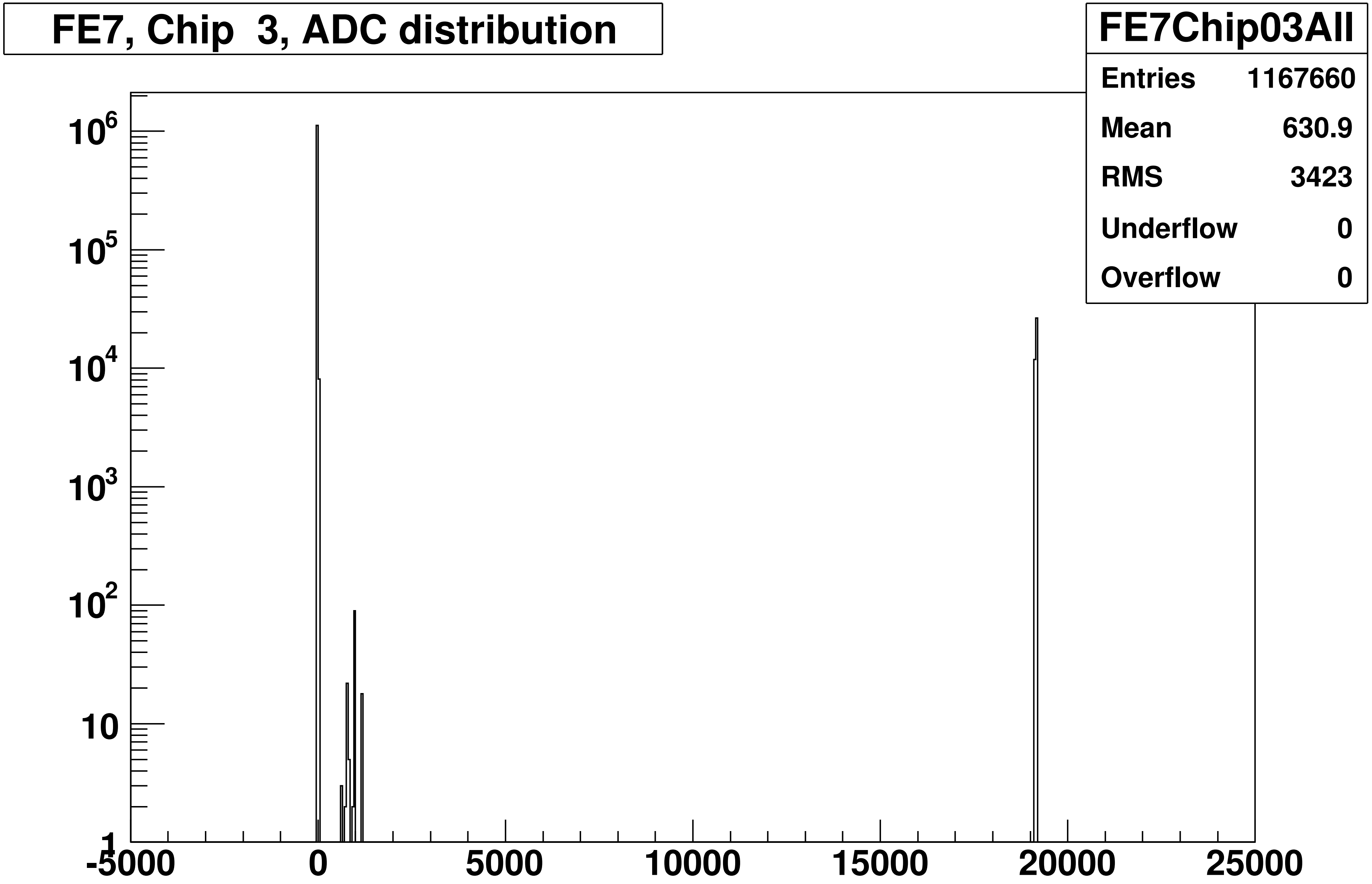
19400



FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 1167660
Mean 630.9
RMS 3423
Underflow 0
Overflow 0
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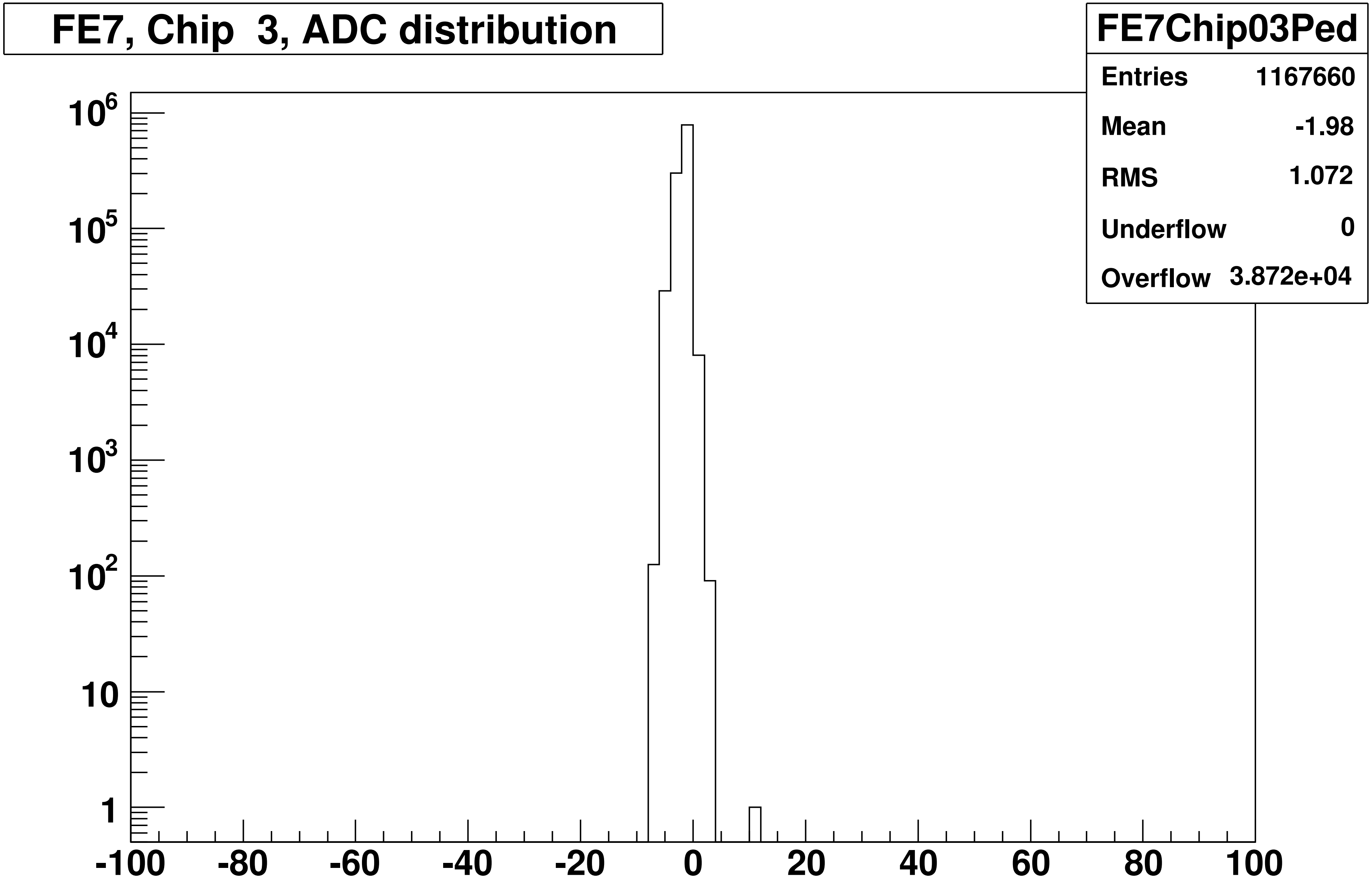
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 1167660
Mean -1.98
RMS 1.072
Underflow 0
Overflow 3.872e+04
10^6
10^5
10^4
10^3
10^2
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1
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-40
-20
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80
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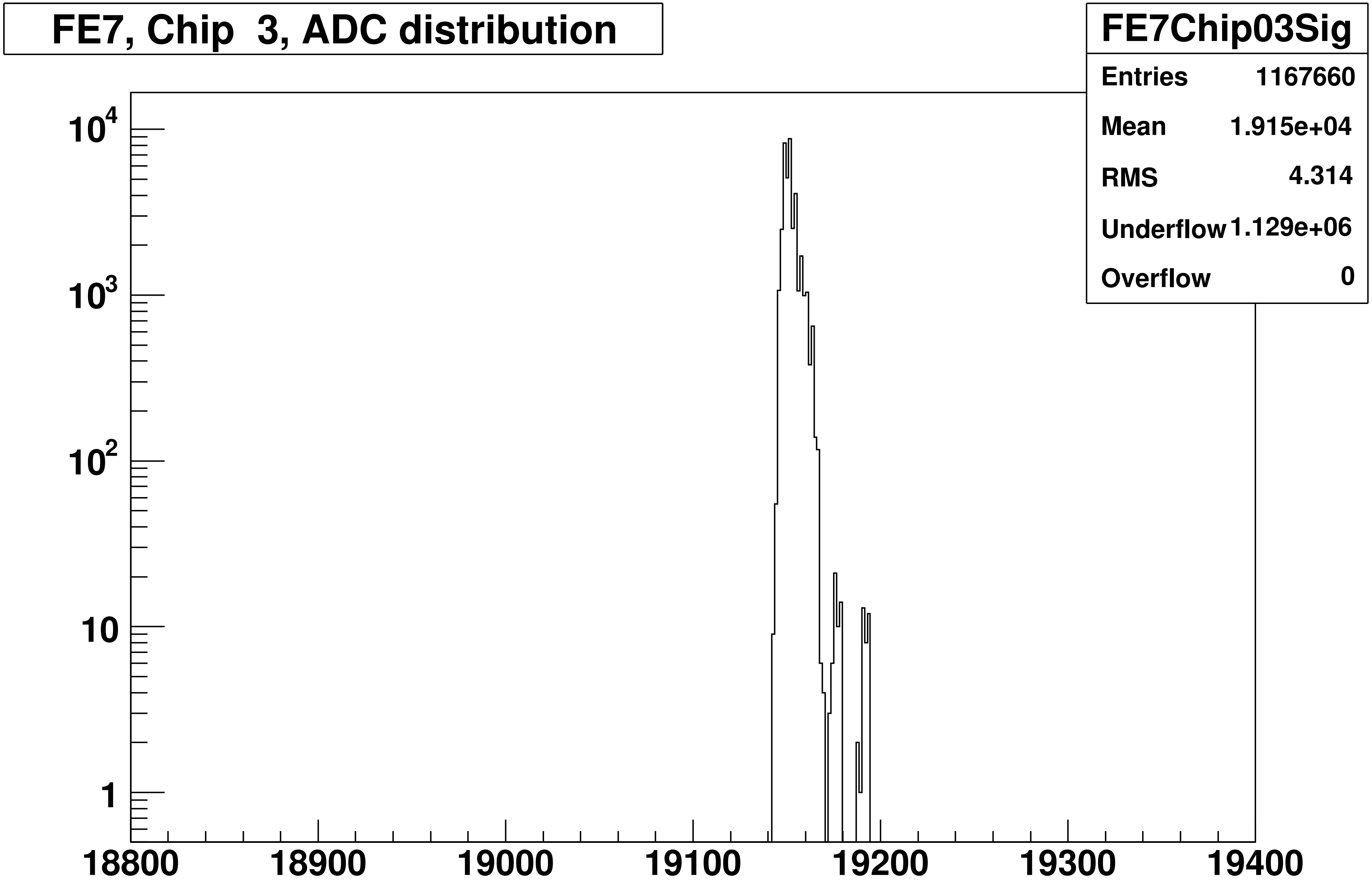

FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 1167660
Mean 1.915e+04
RMS 4.314
Underflow 1.129e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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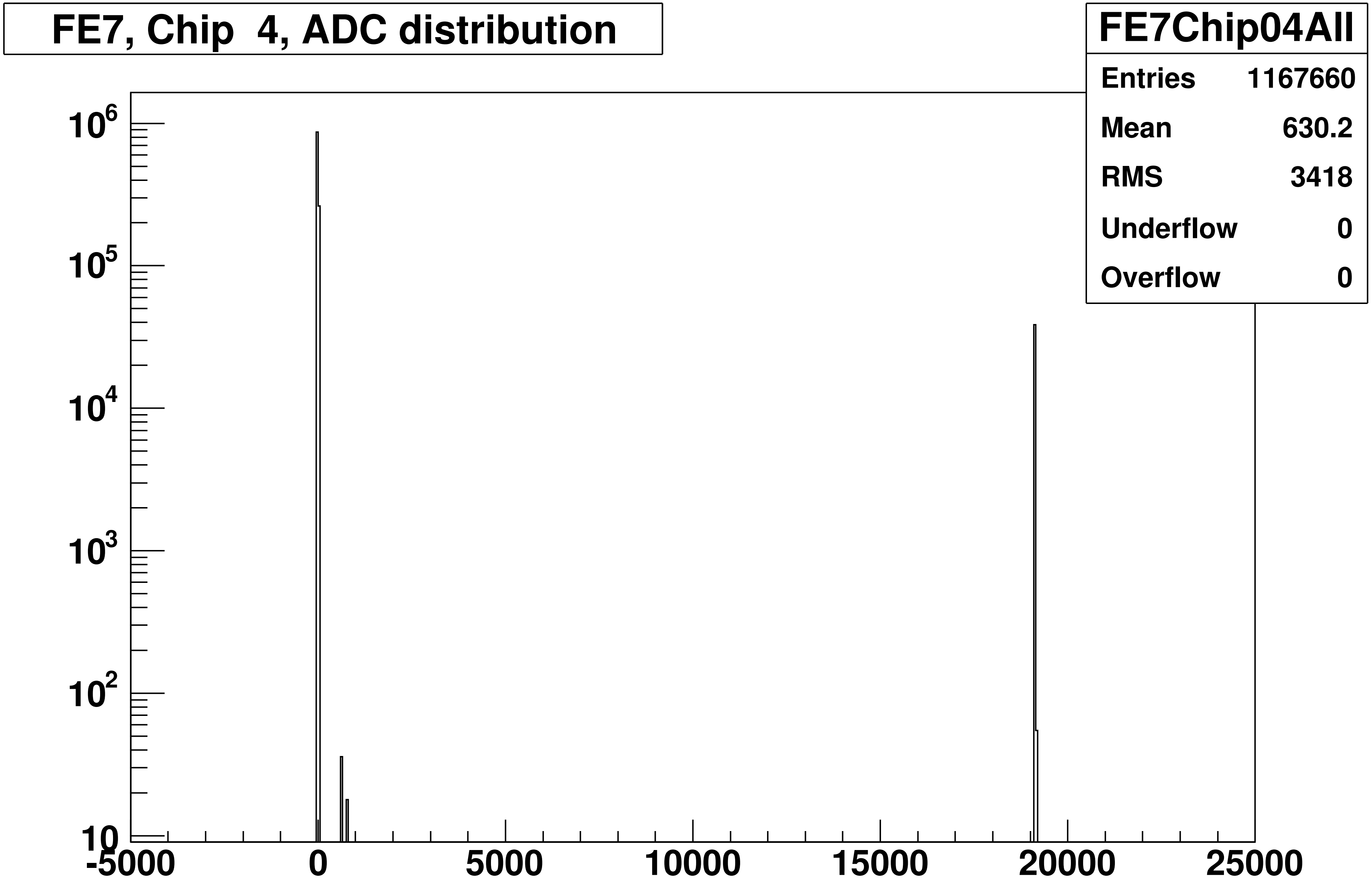
19400



FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 1167660
Mean 630.2
RMS 3418
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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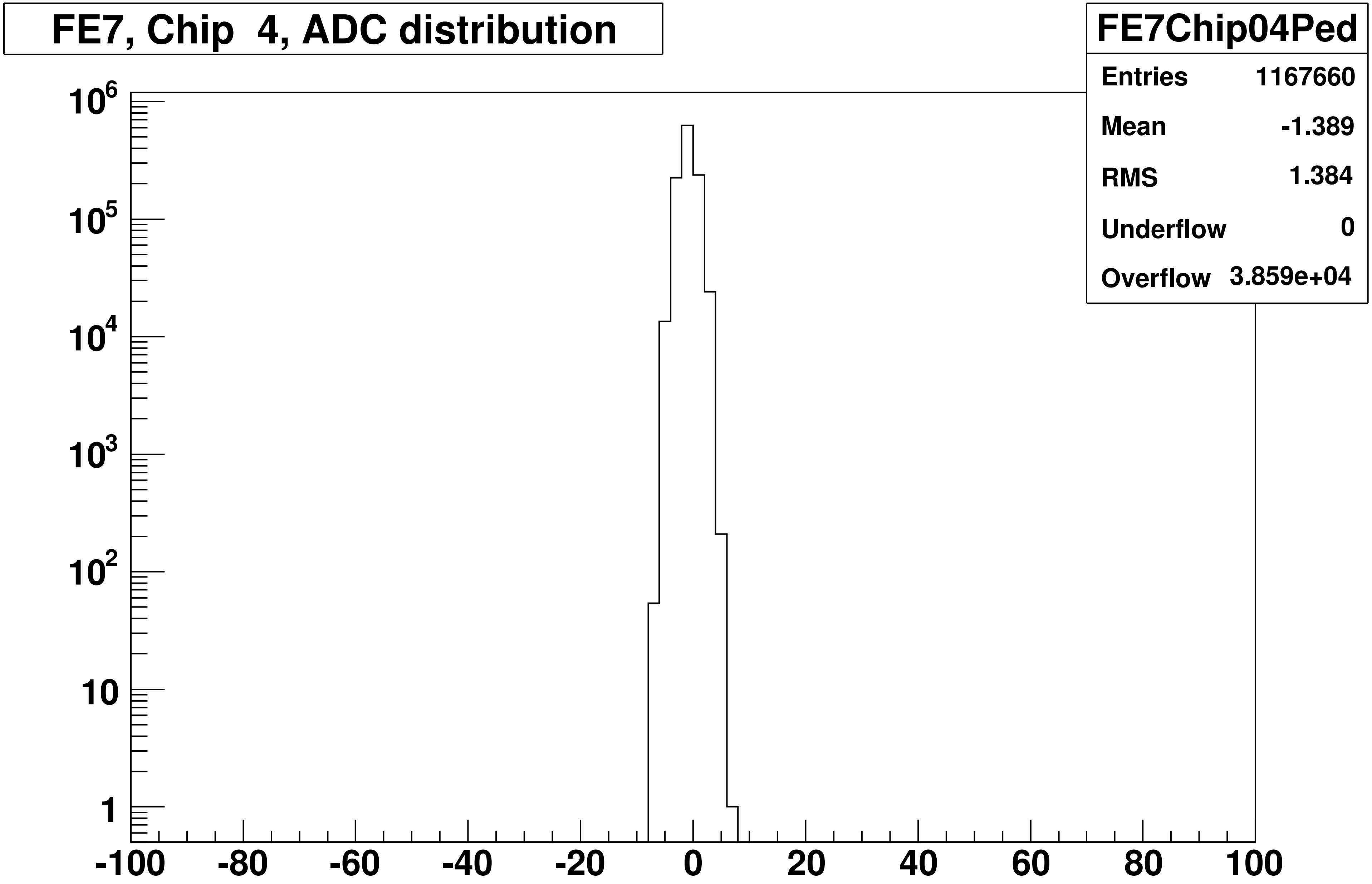
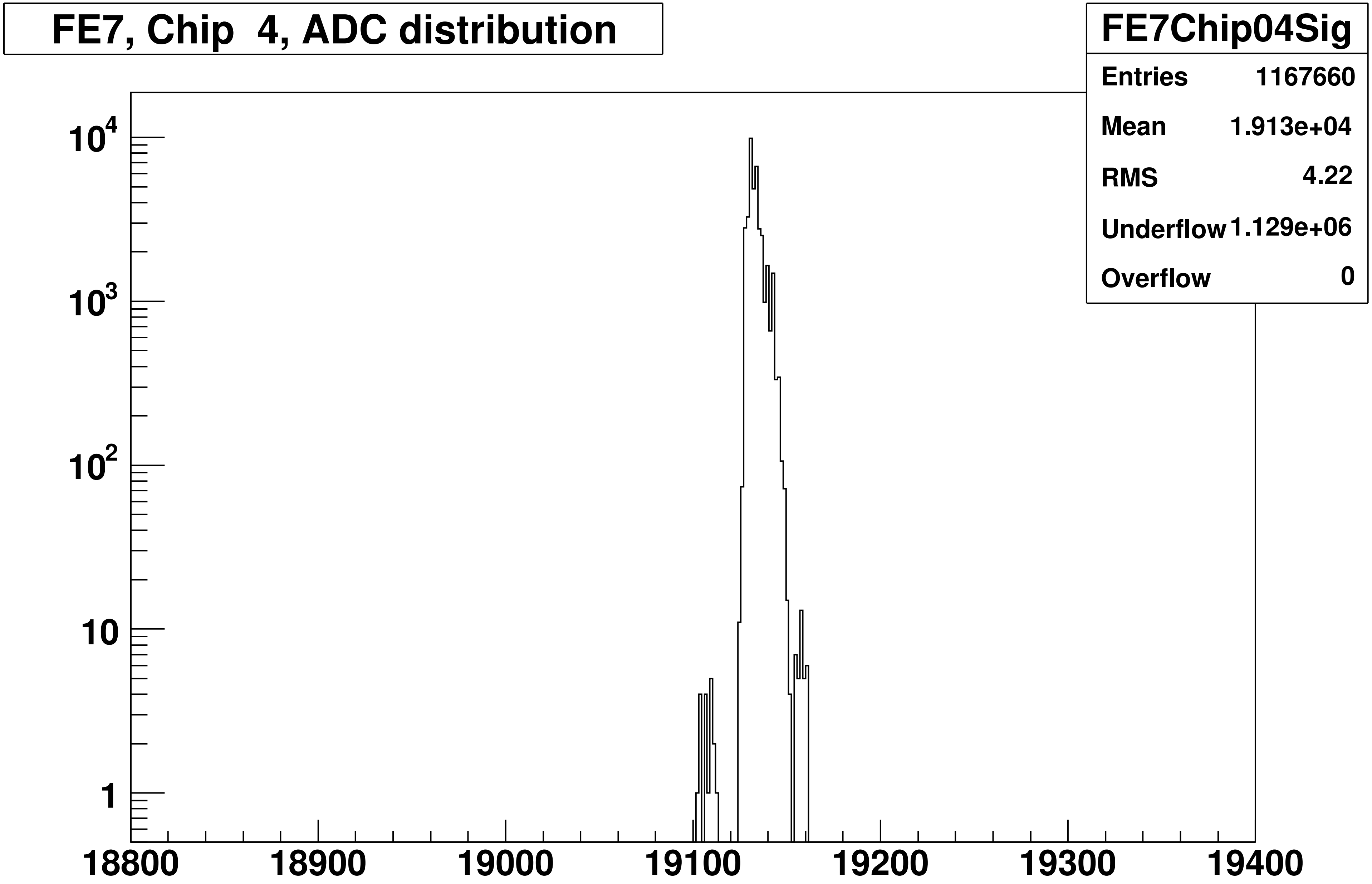
0
5000
10000
15000
20000
25000



FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 1167660
Mean -1.389
RMS 1.384
Underflow 0
Overflow 3.859e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 1167660
Mean 1.913e+04
RMS 4.22
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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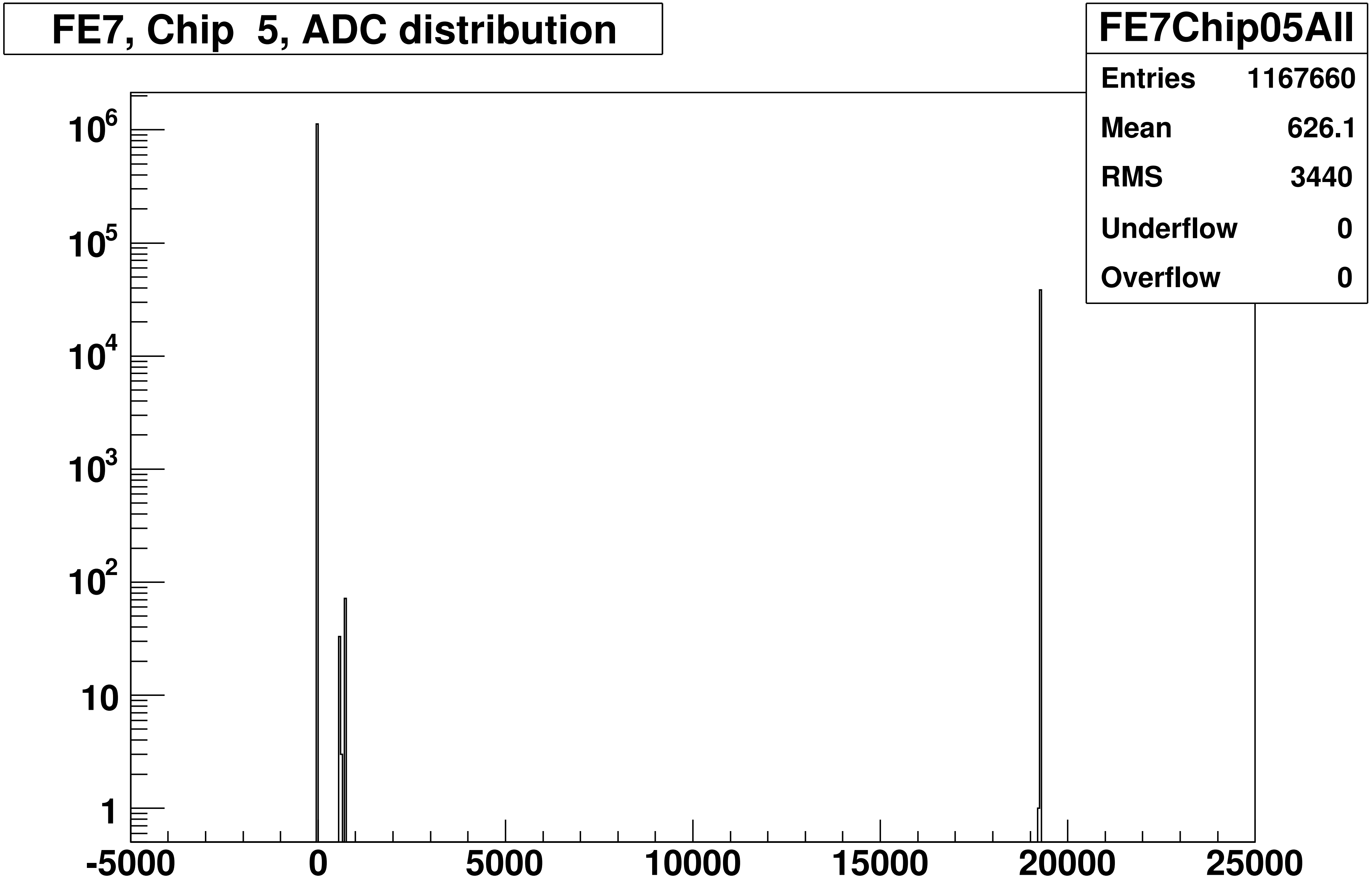
19300
19400


FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 1167660
Mean 626.1
RMS 3440
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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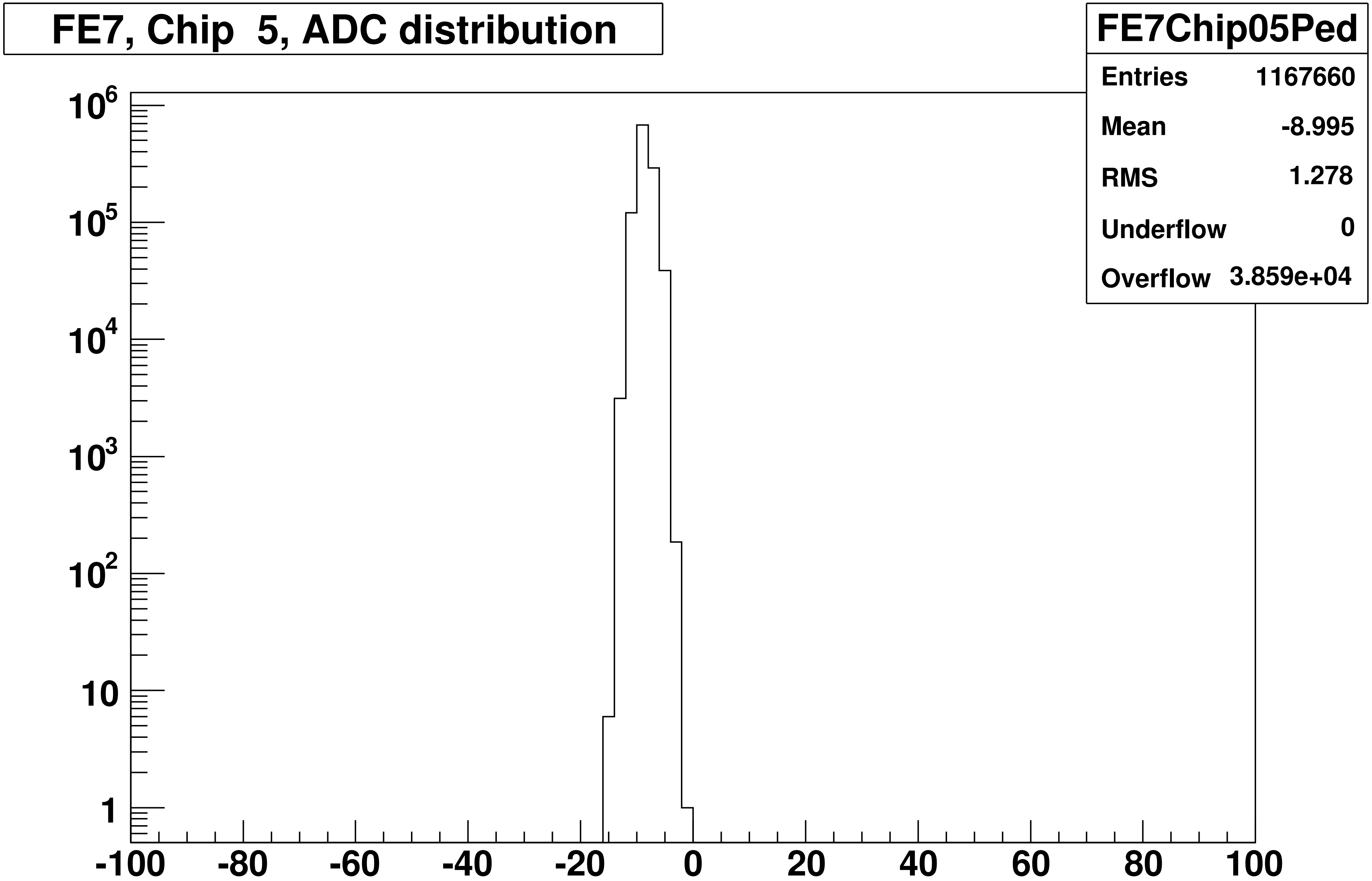
10000
15000
20000
25000


FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 1167660
Mean -8.995
RMS 1.278
Underflow 0
Overflow 3.859e+04
10^6
10^5
10^4
10^3
10^2
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1
-100
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-40
-20
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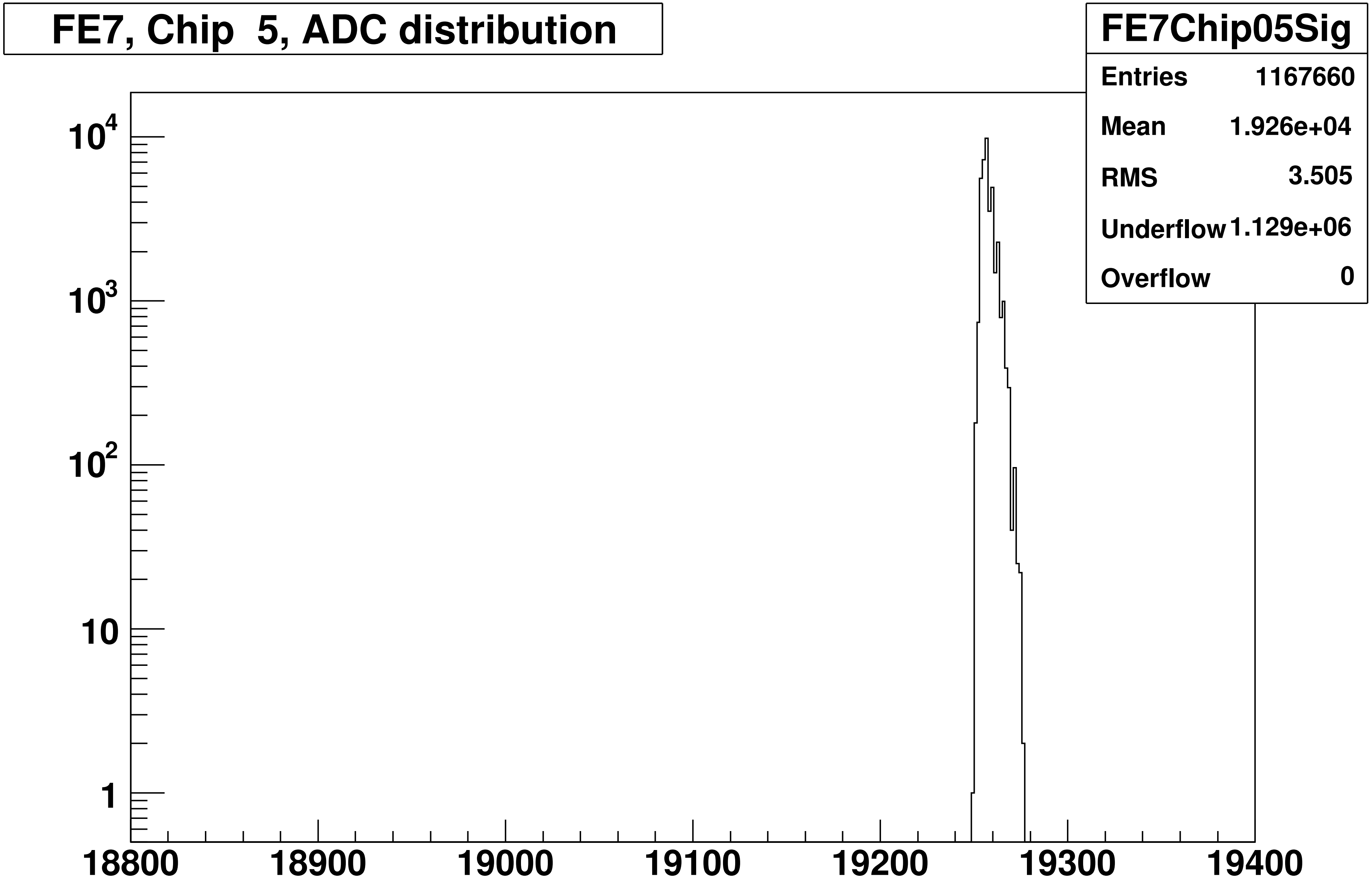
40
60
80
100


FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 1167660
Mean 1.926e+04
RMS 3.505
Underflow 1.129e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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19200
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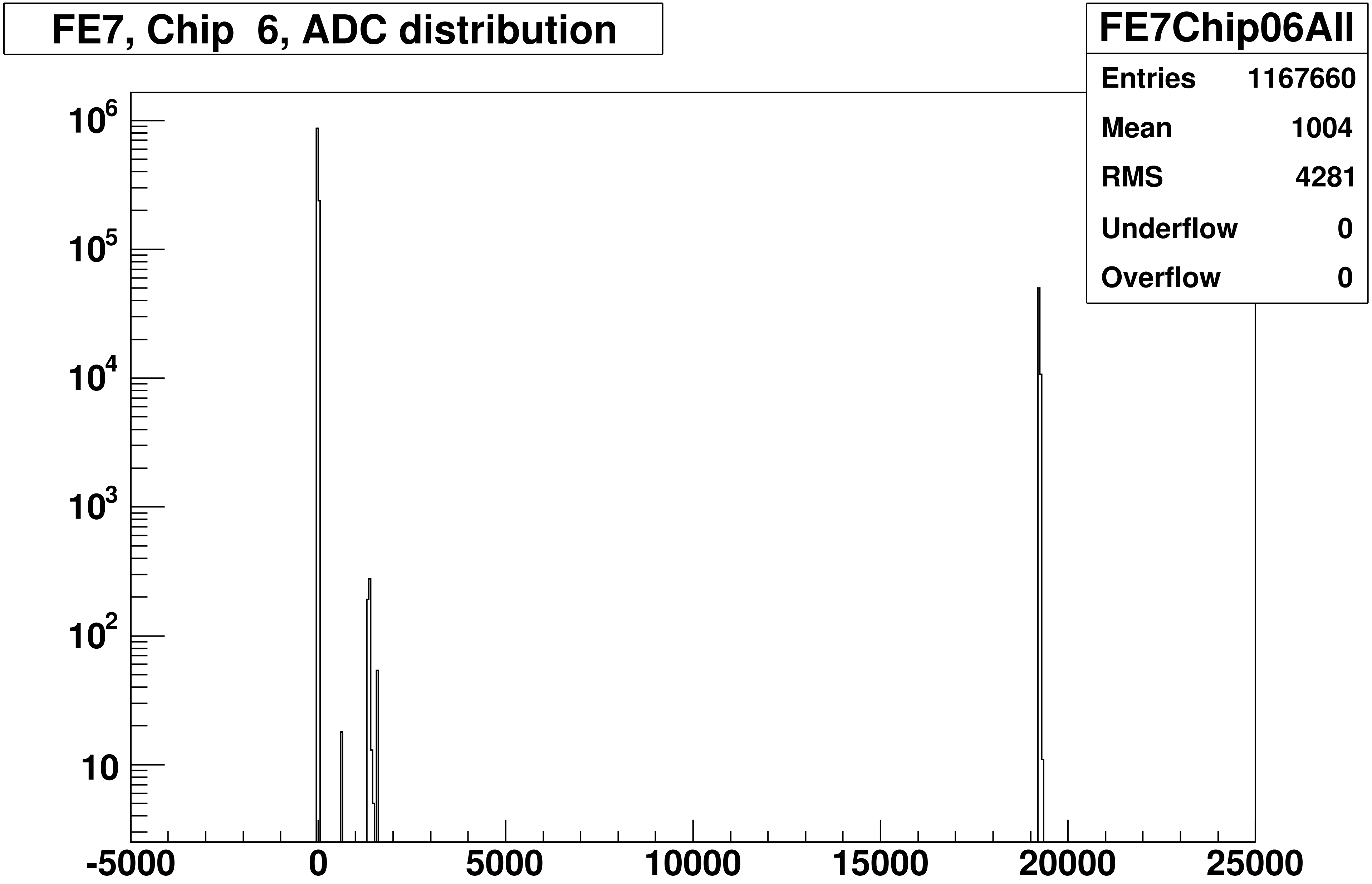
19400



FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 1167660
Mean 1004
RMS 4281
Underflow 0
Overflow 0
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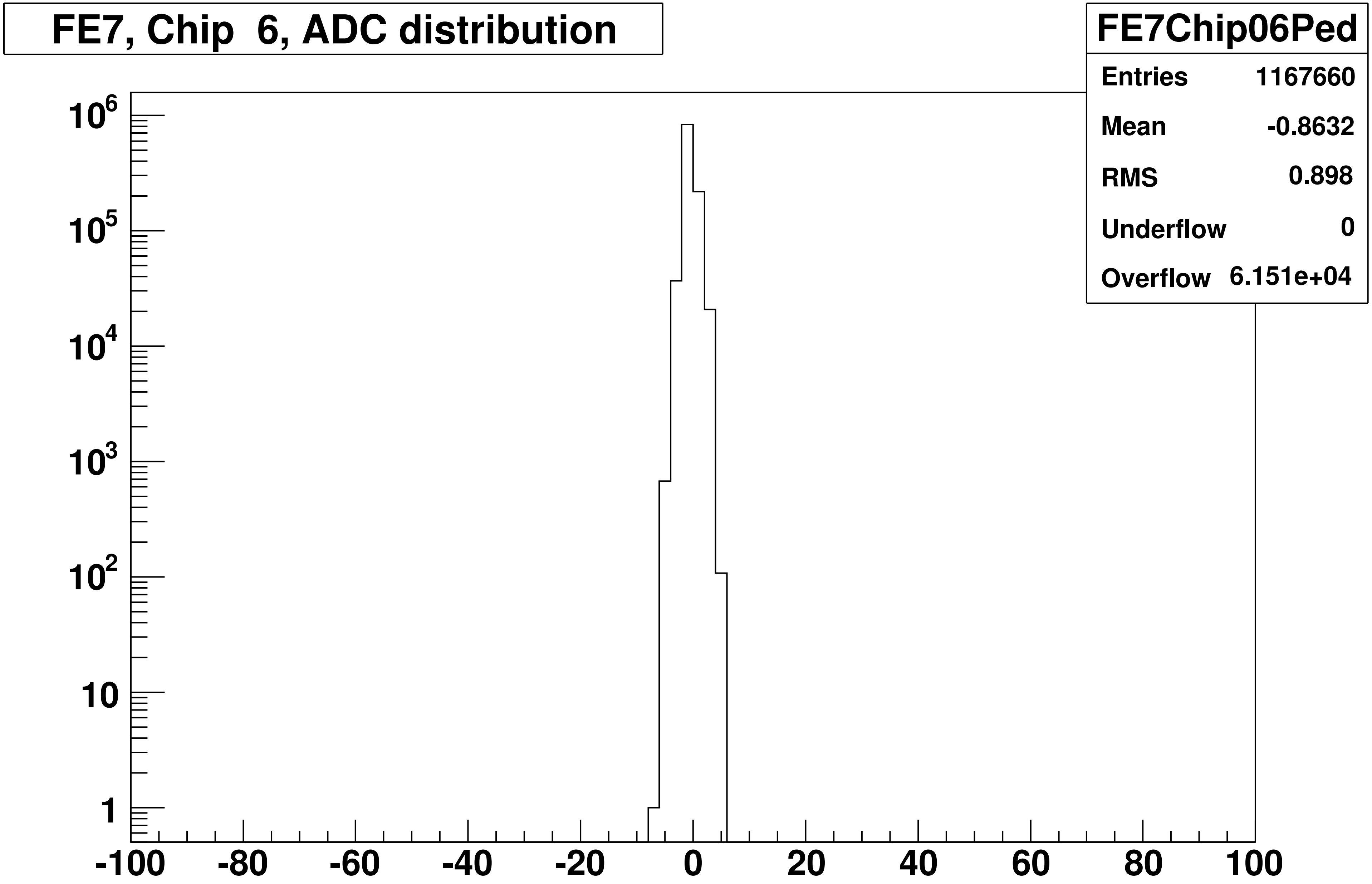
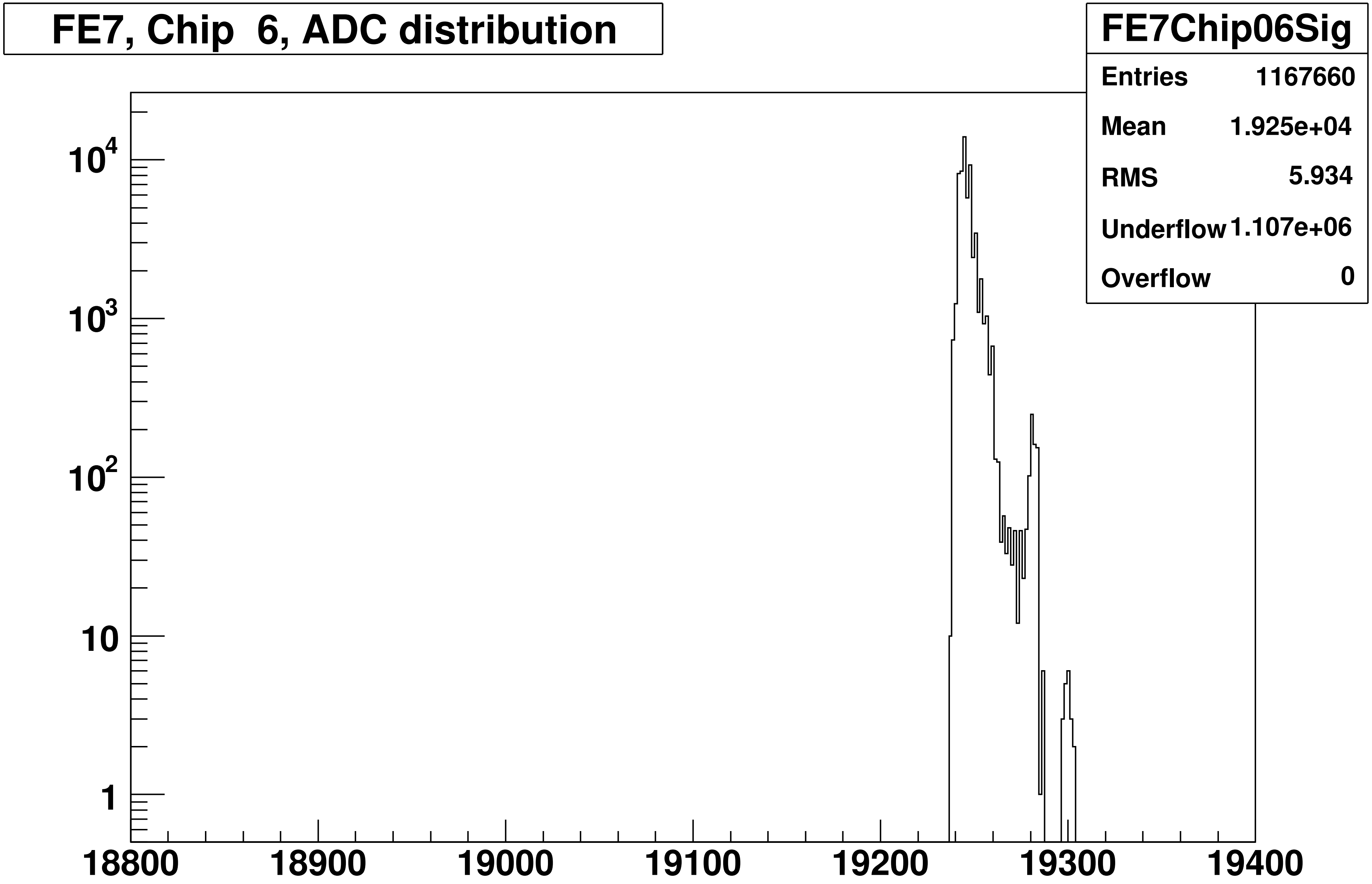
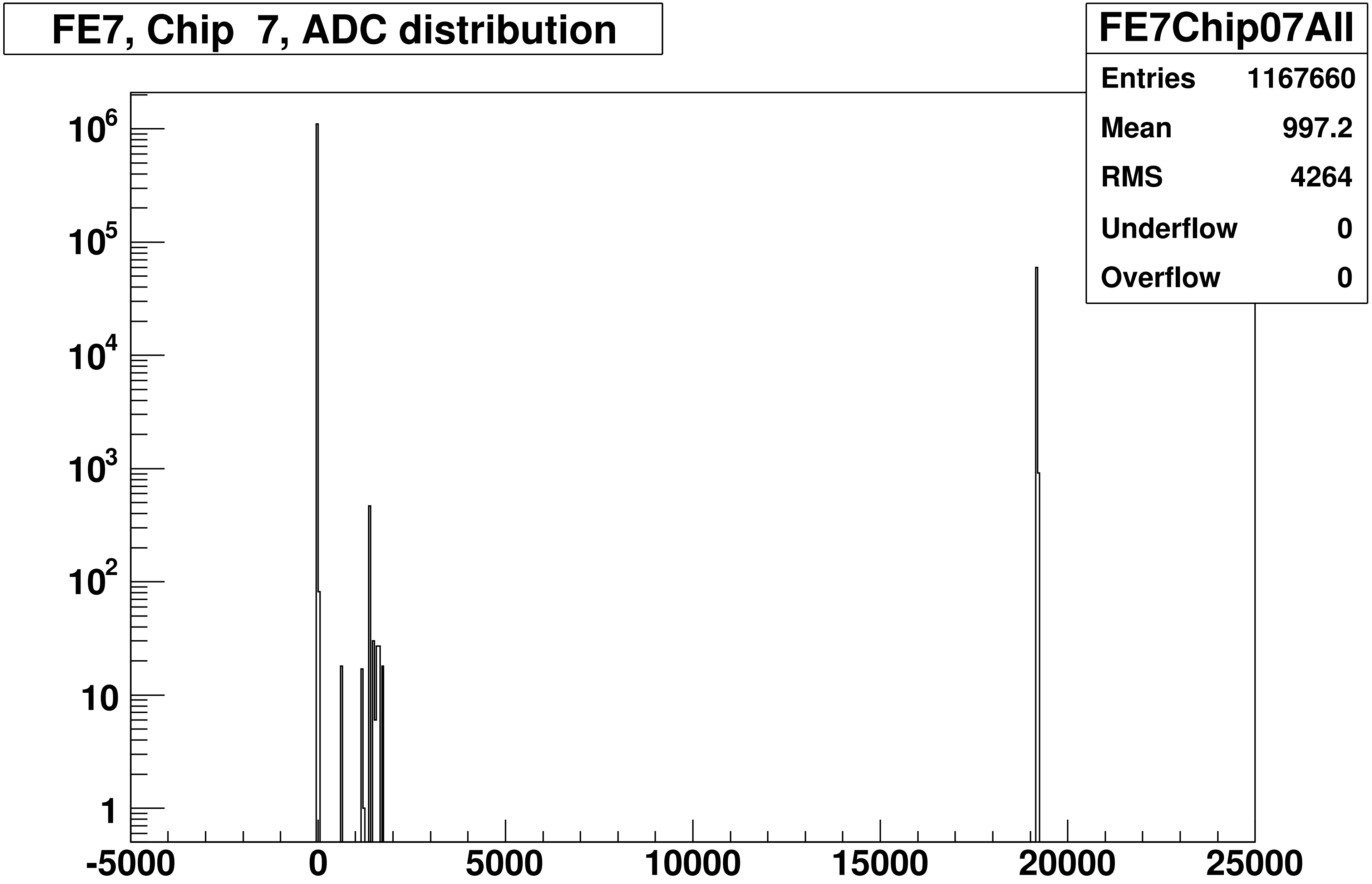
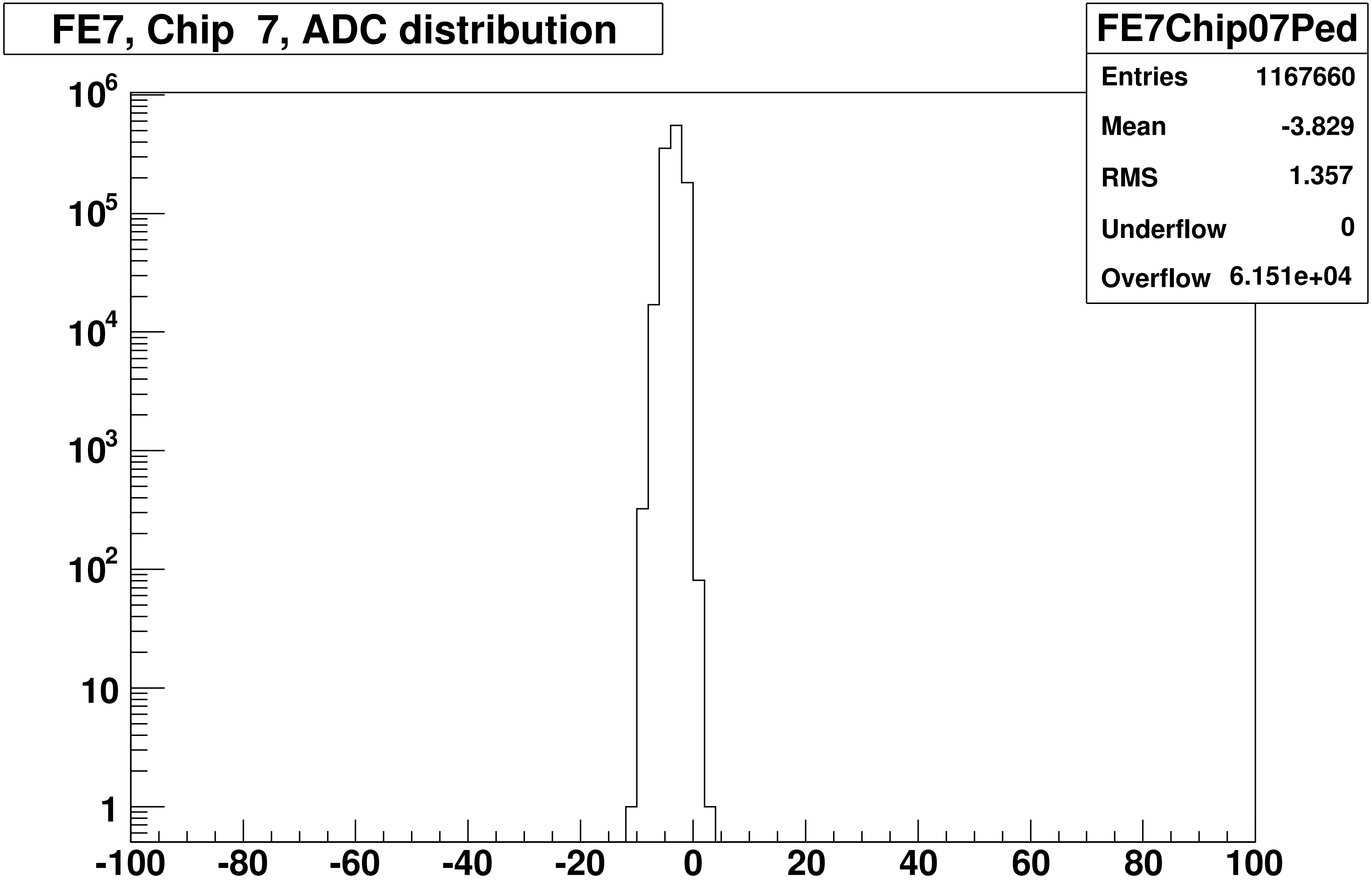
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

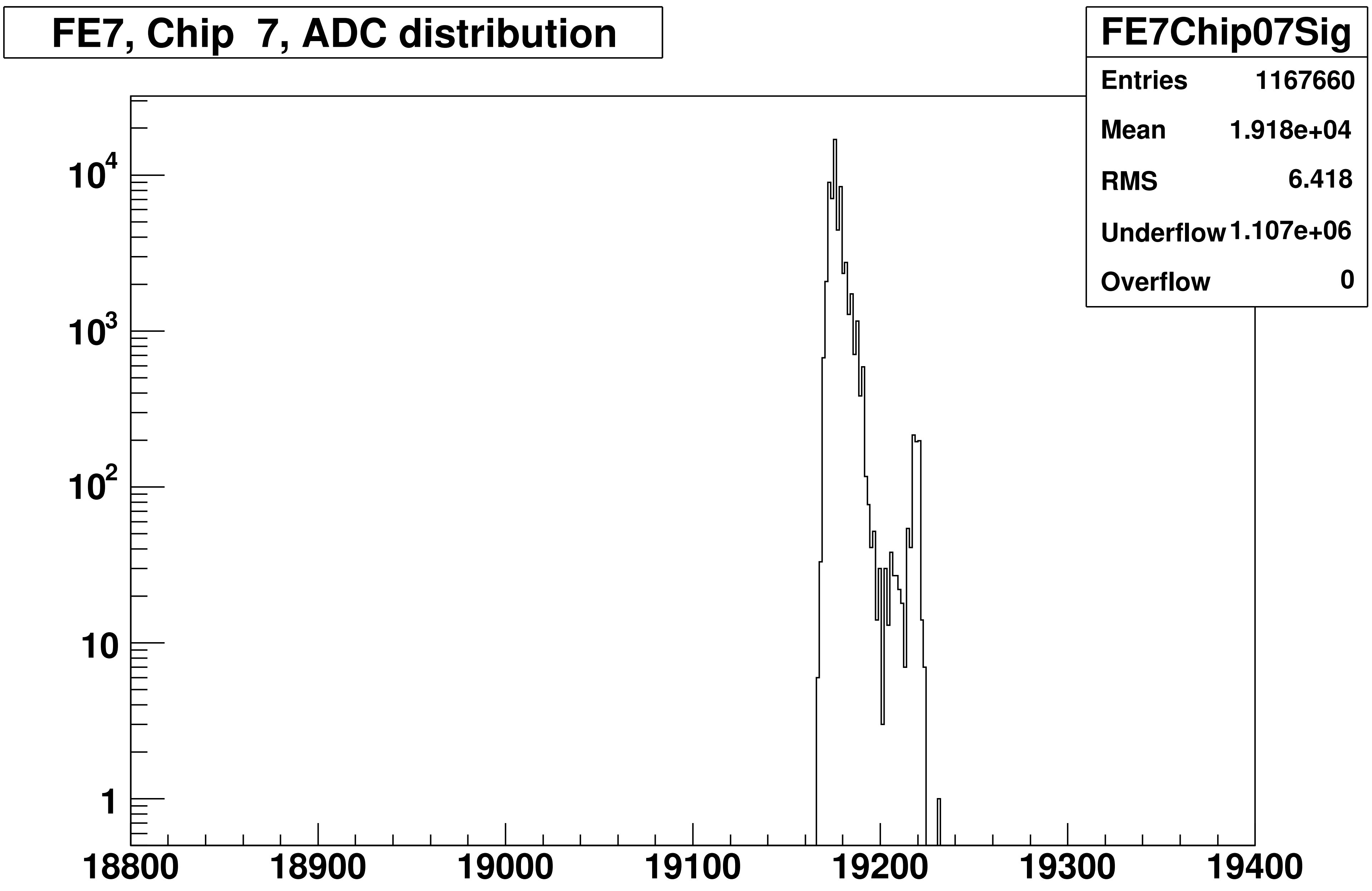

FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 1167660
Mean -0.8632
RMS 0.898
Underflow 0
Overflow 6.151e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

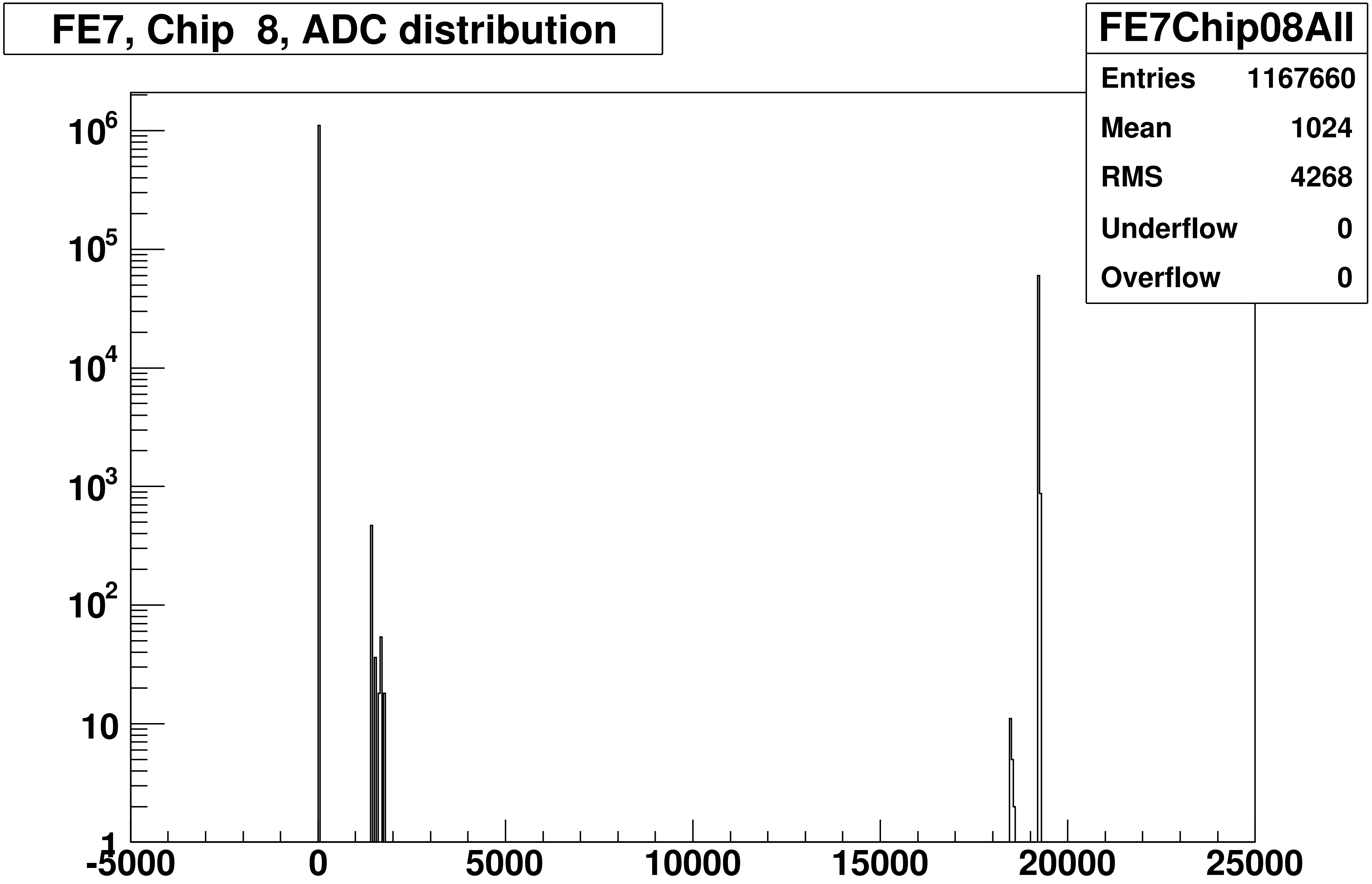


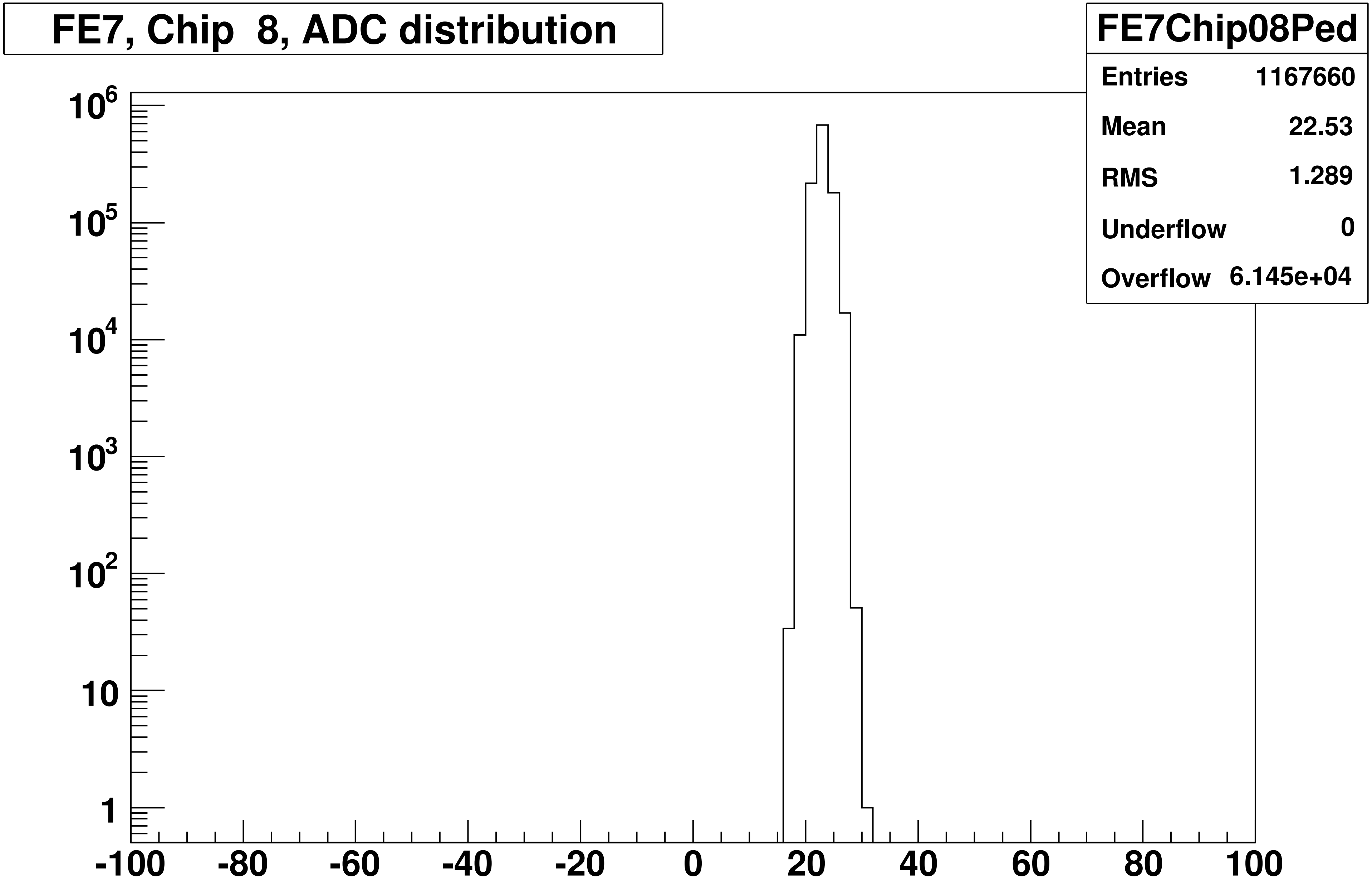
FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 1167660
Mean 1.925e+04
RMS 5.934
Underflow 1.107e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 1167660
Mean 997.2
RMS 4264
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

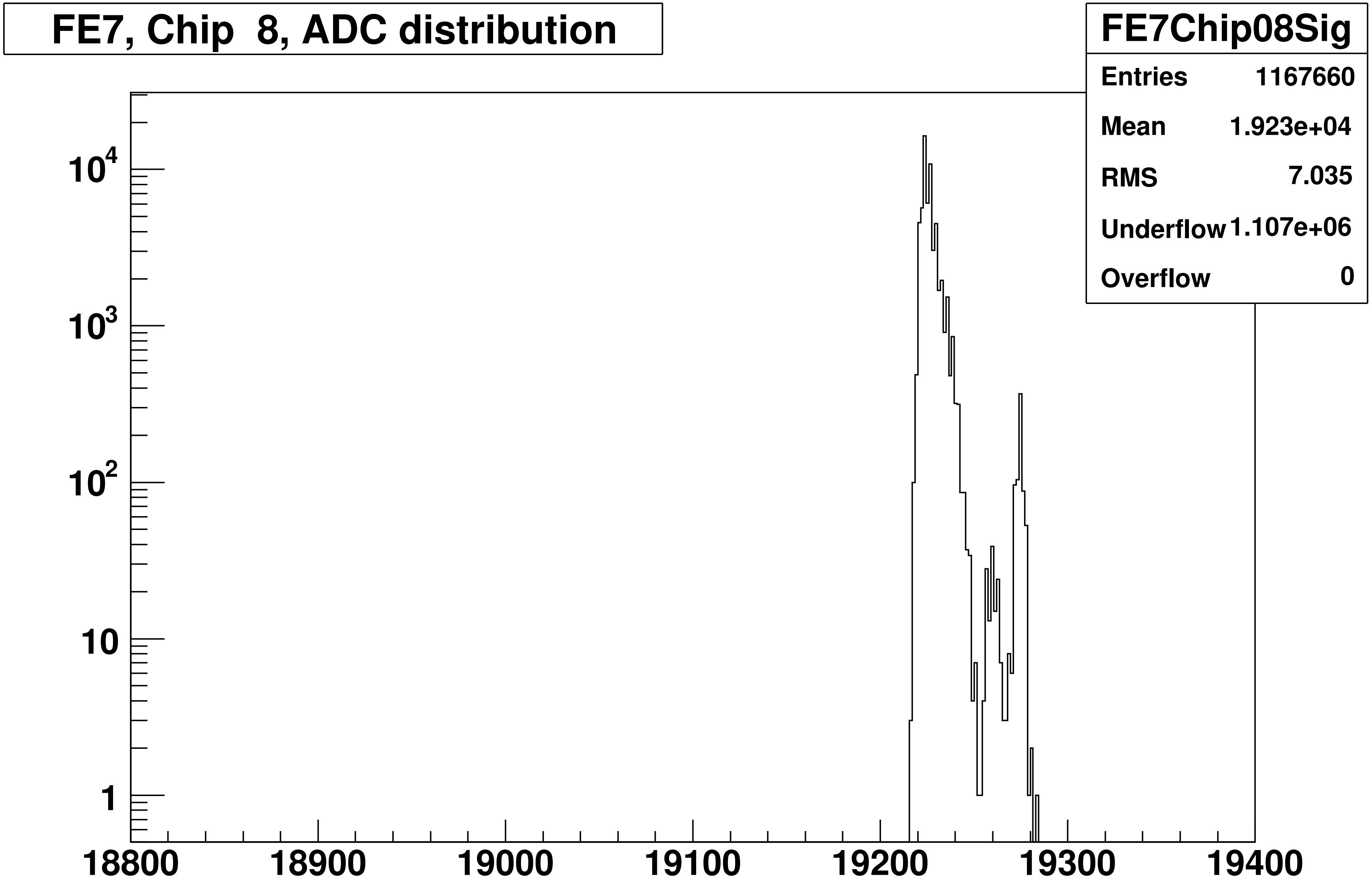

FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 1167660
Mean -3.829
RMS 1.357
Underflow 0
Overflow 6.151e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

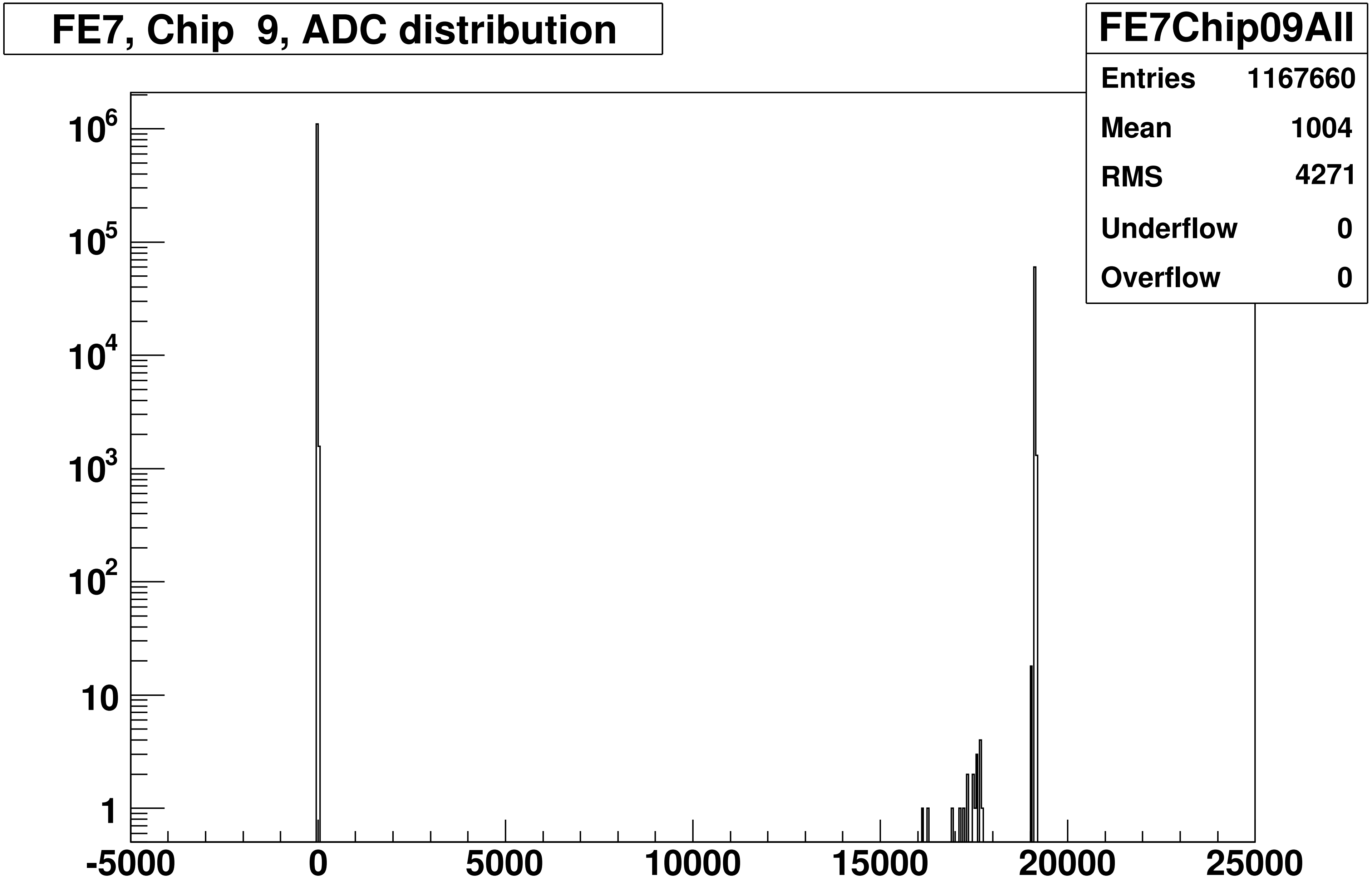

FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 1167660
Mean 1.918e+04
RMS 6.418
Underflow 1.107e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

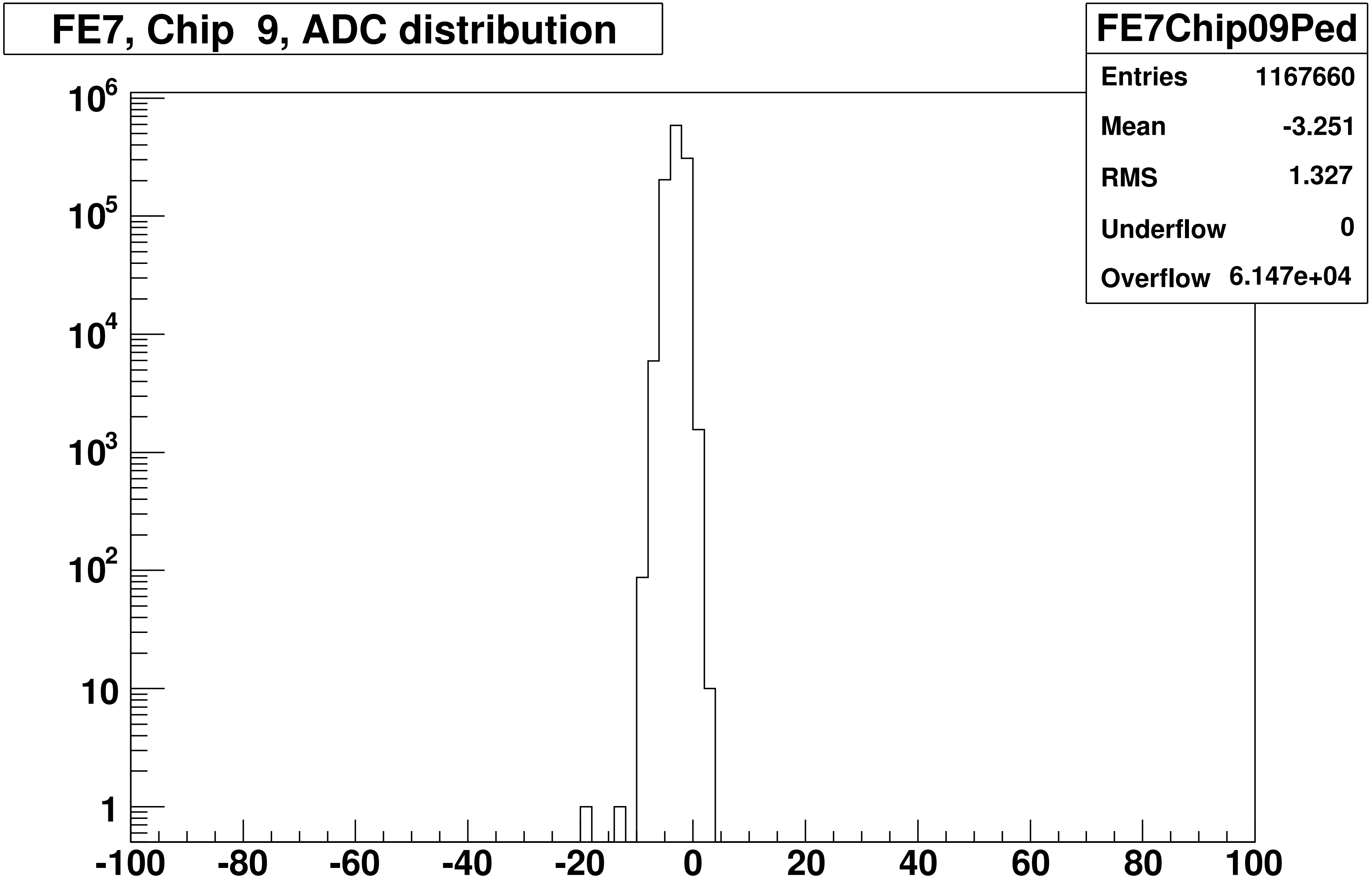

FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 1167660
Mean 1024
RMS 4268
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



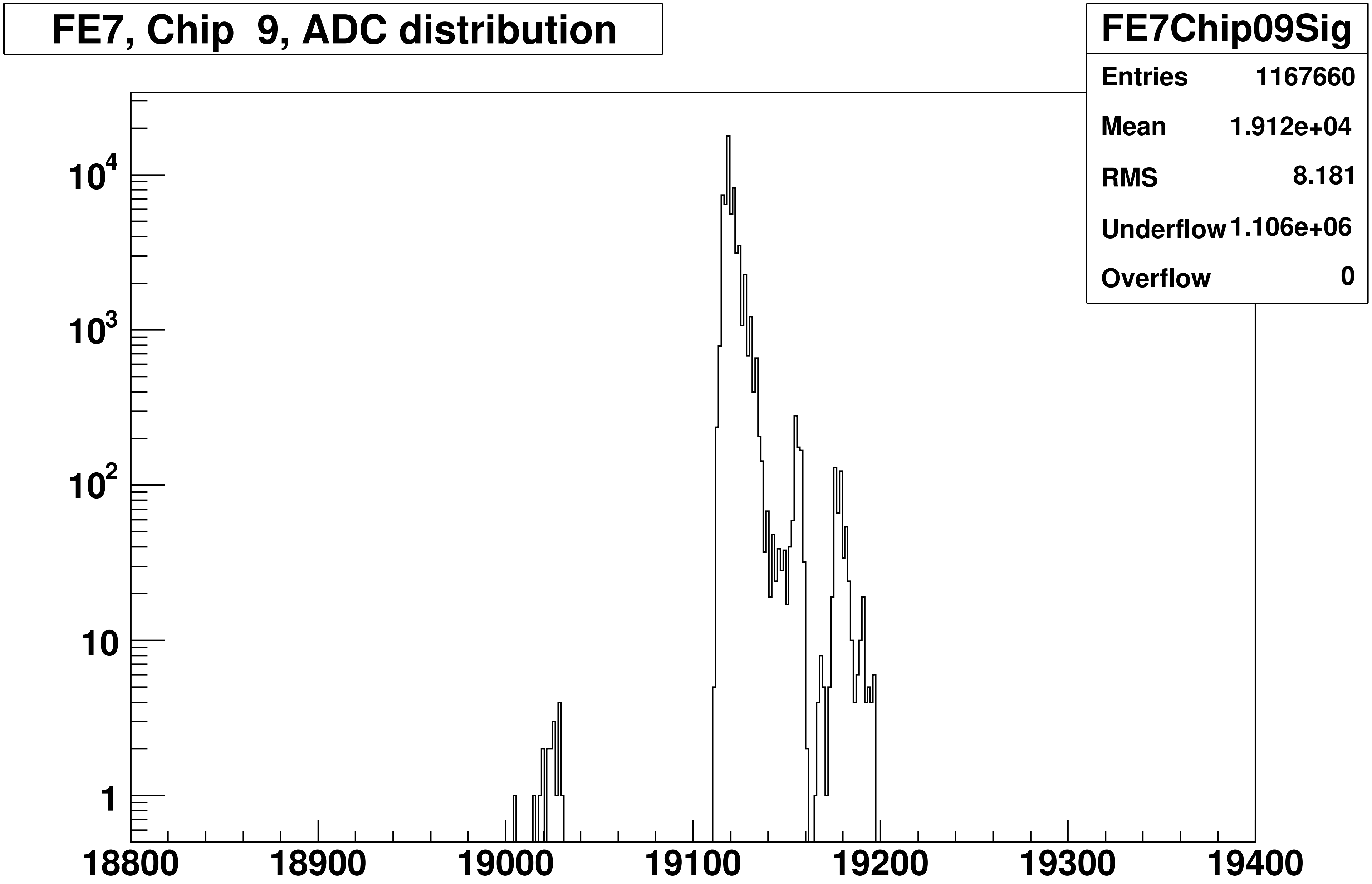
FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 1167660
Mean 22.53
RMS 1.289
Underflow 0
Overflow 6.145e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 1167660
Mean 1.923e+04
RMS 7.035
Underflow 1.107e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

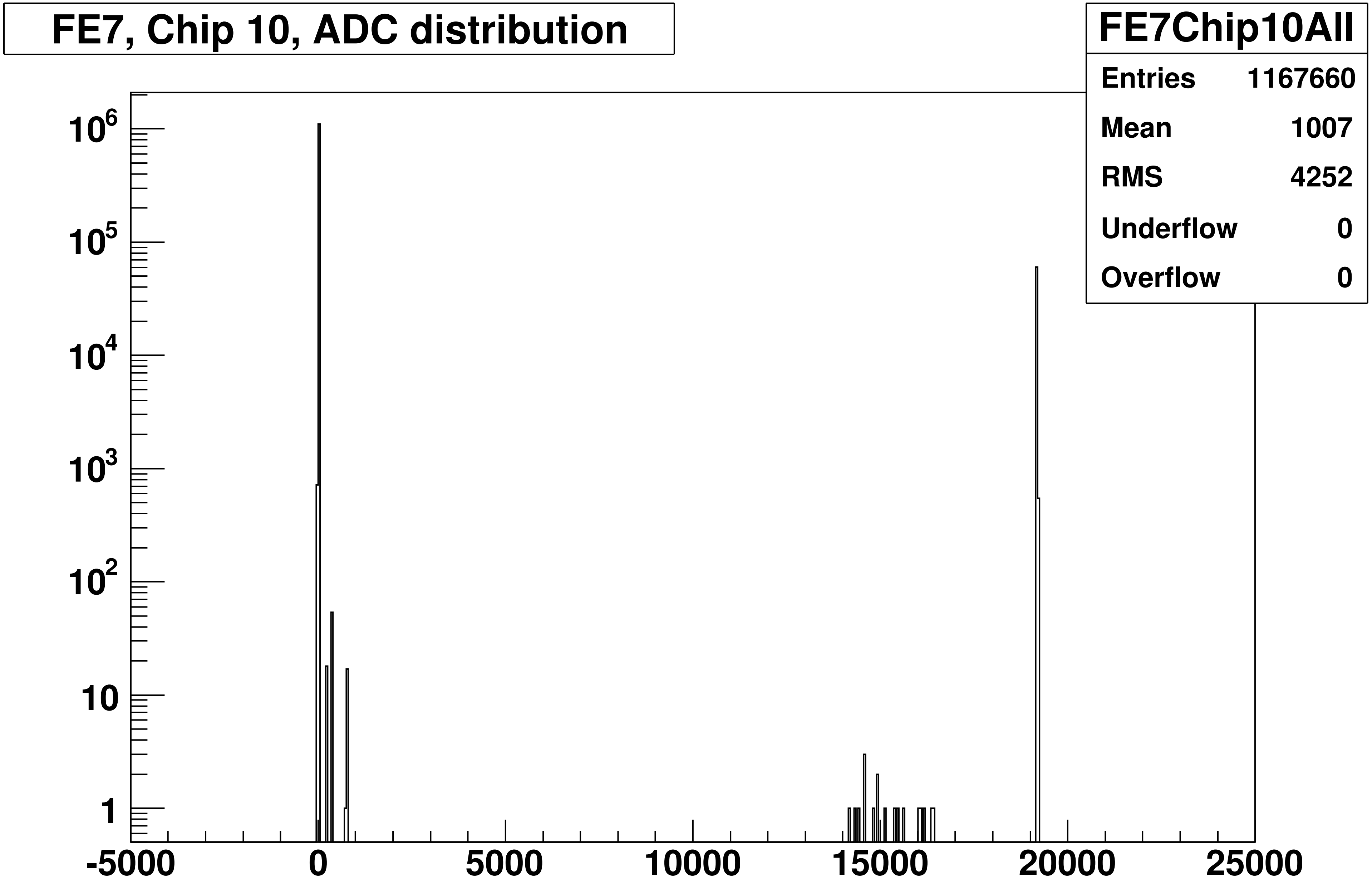

FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 1167660
Mean 1004
RMS 4271
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

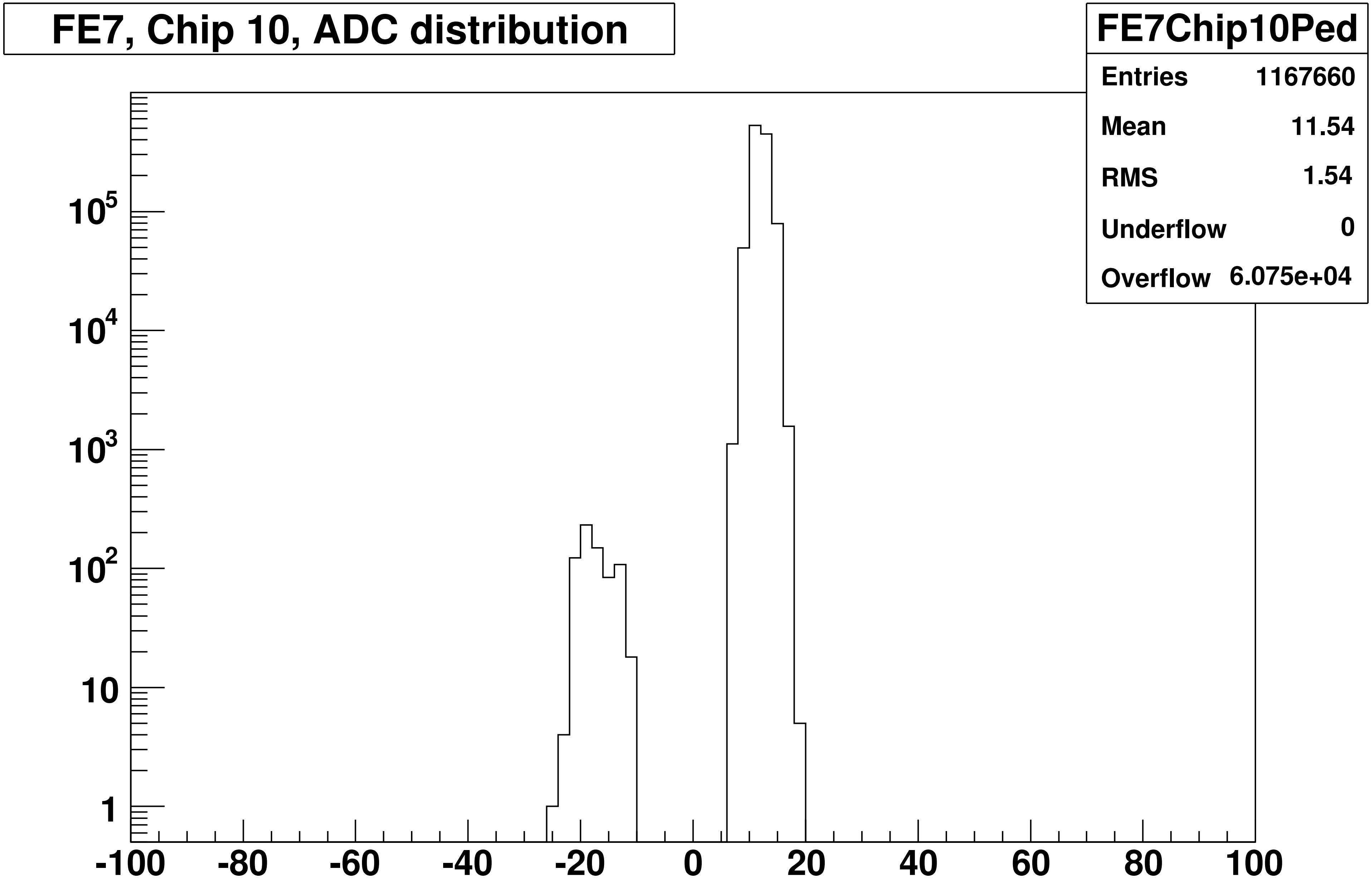


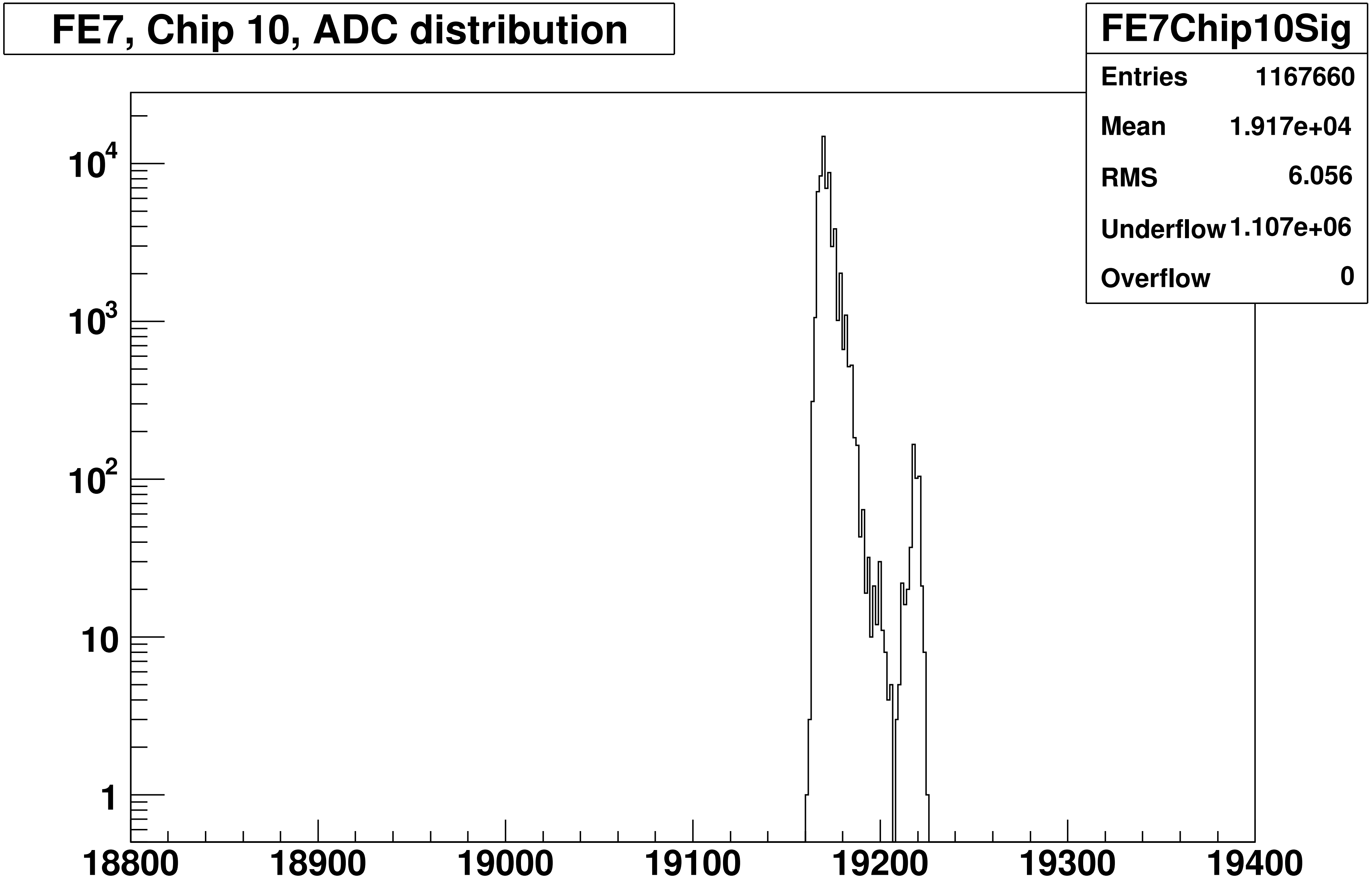
FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 1167660
Mean -3.251
RMS 1.327
Underflow 0
Overflow 6.147e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

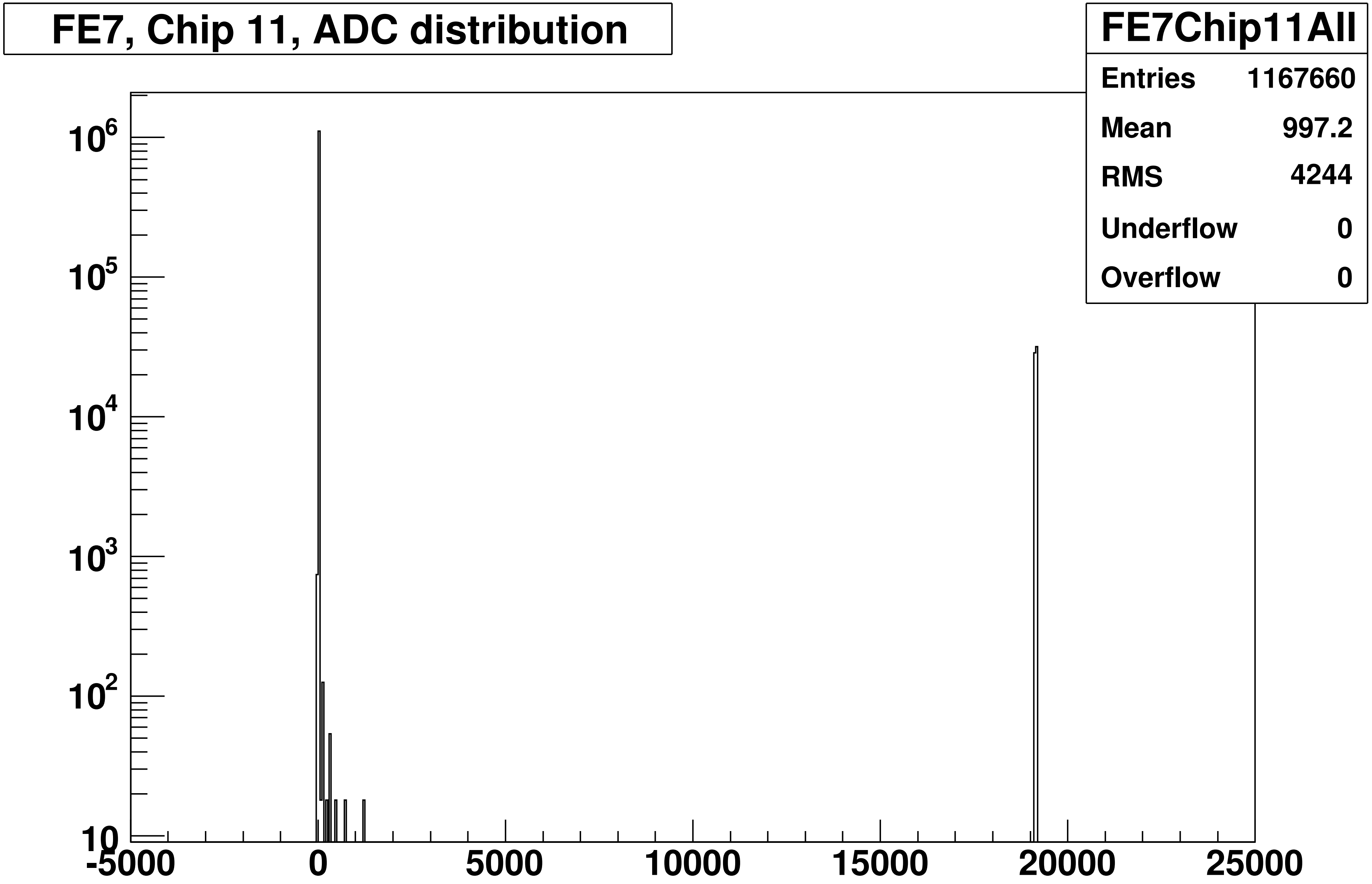


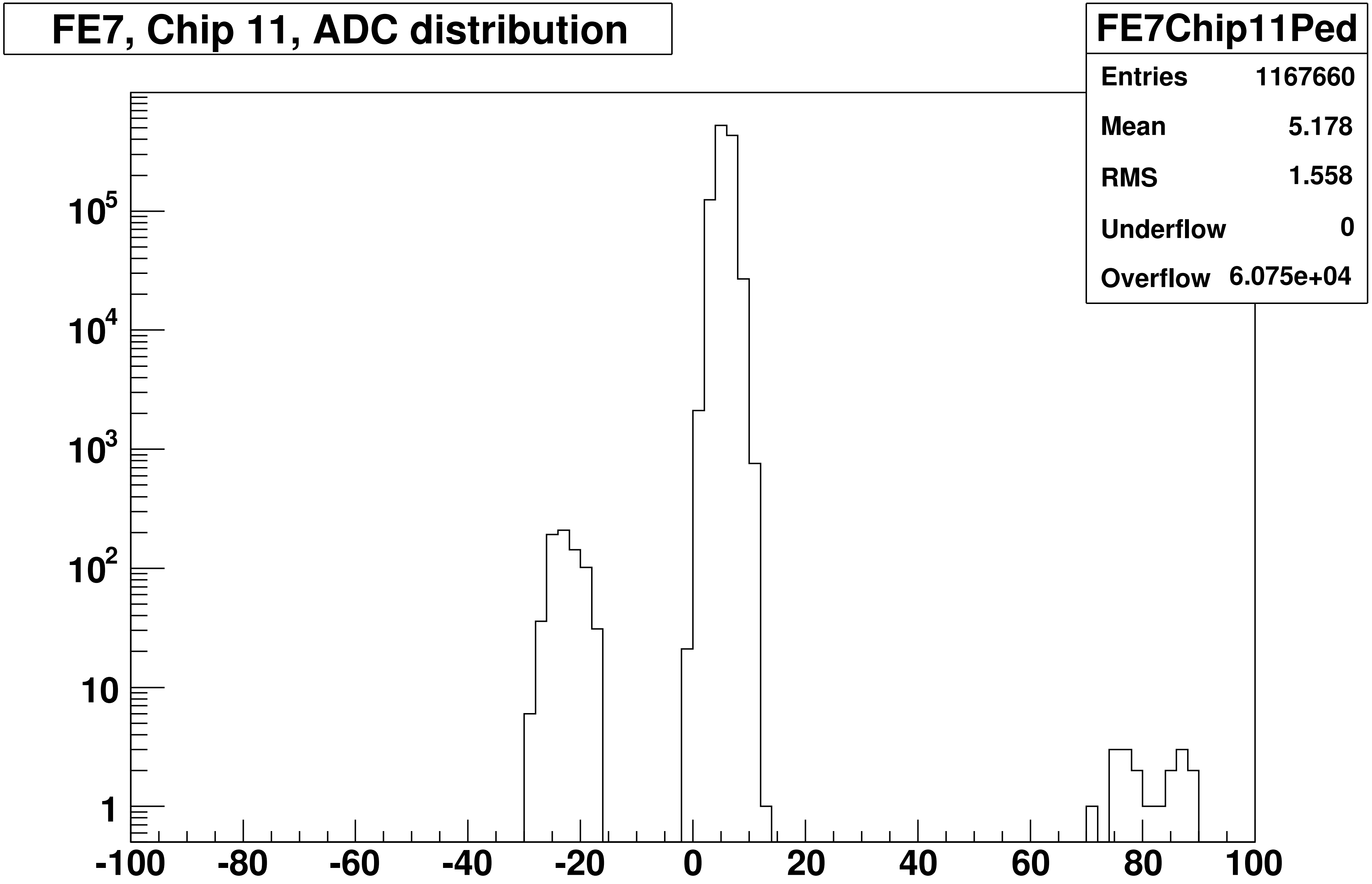
FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 1167660
Mean 1.912e+04
RMS 8.181
Underflow 1.106e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

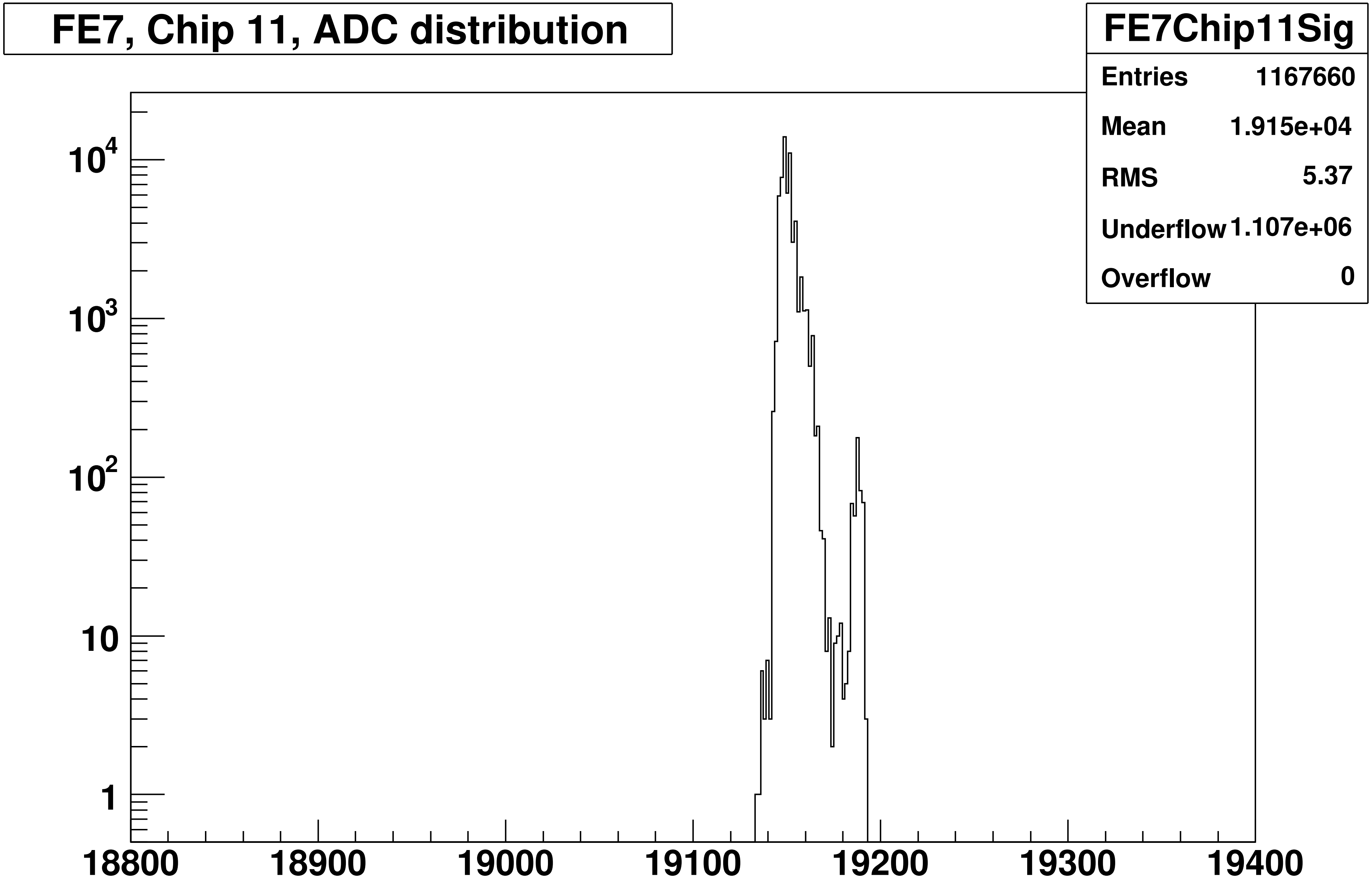

FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 1167660
Mean 1007
RMS 4252
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

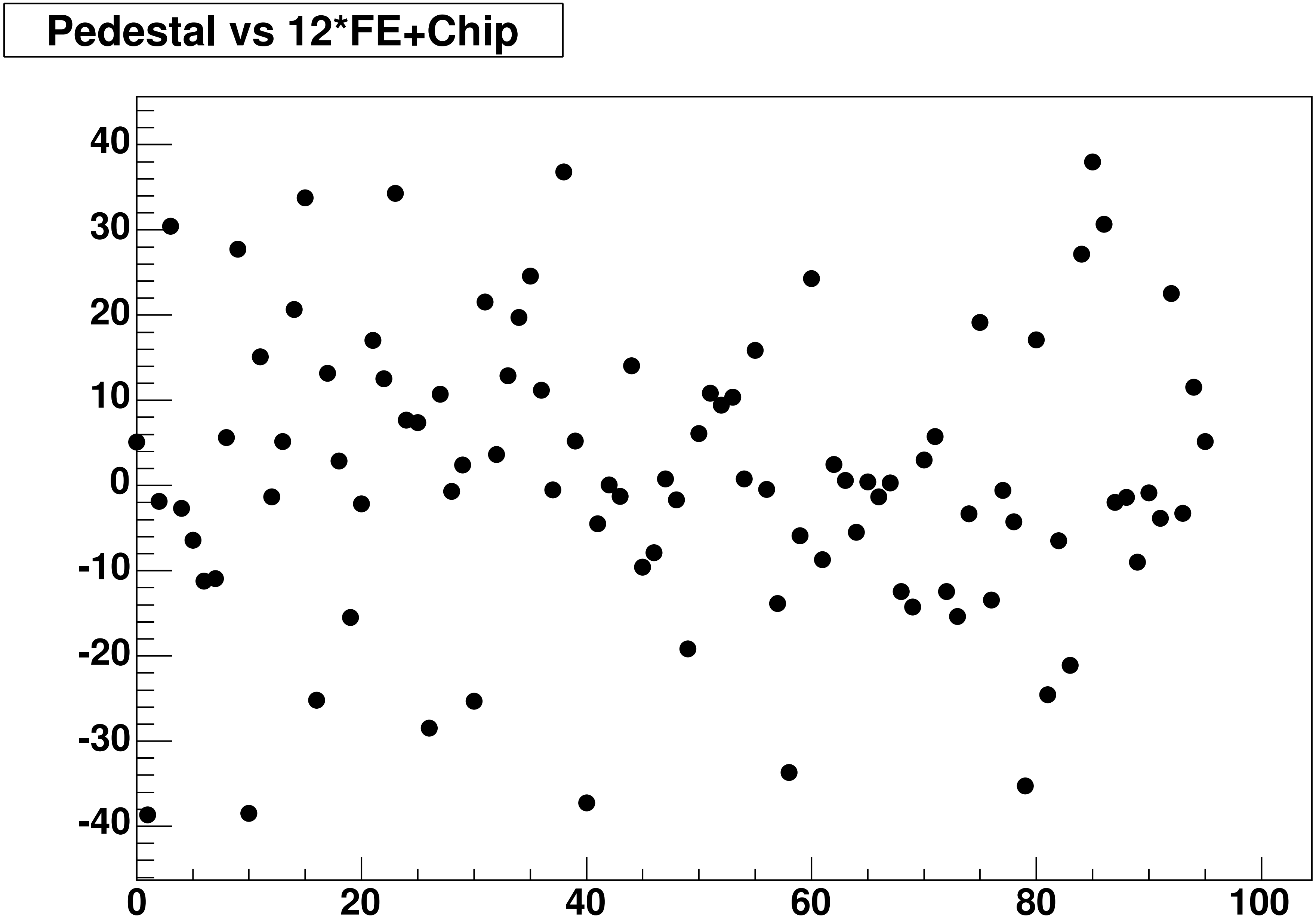

FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 1167660
Mean 11.54
RMS 1.54
Underflow 0
Overflow 6.075e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

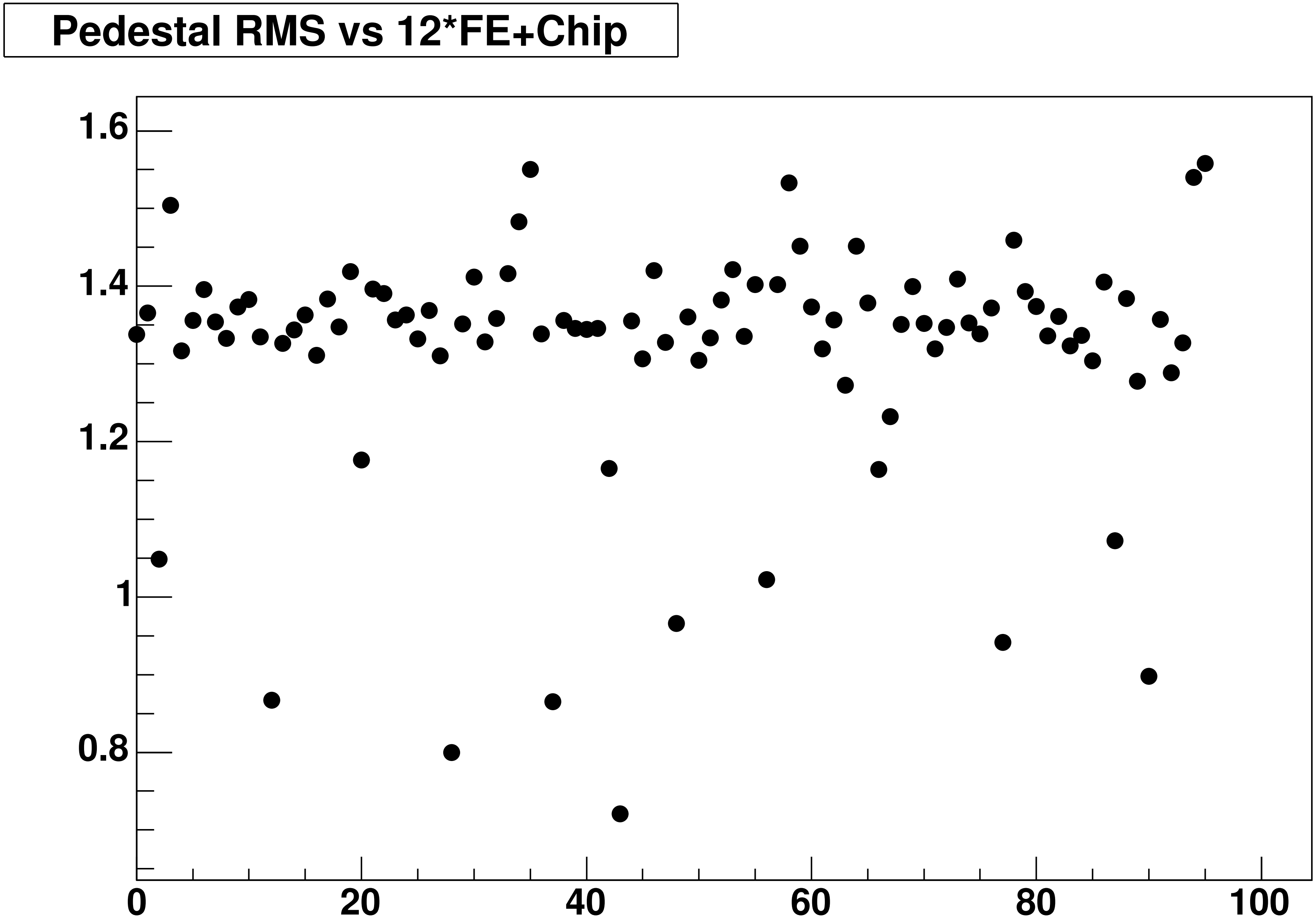

FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 1167660
Mean 1.917e+04
RMS 6.056
Underflow 1.107e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

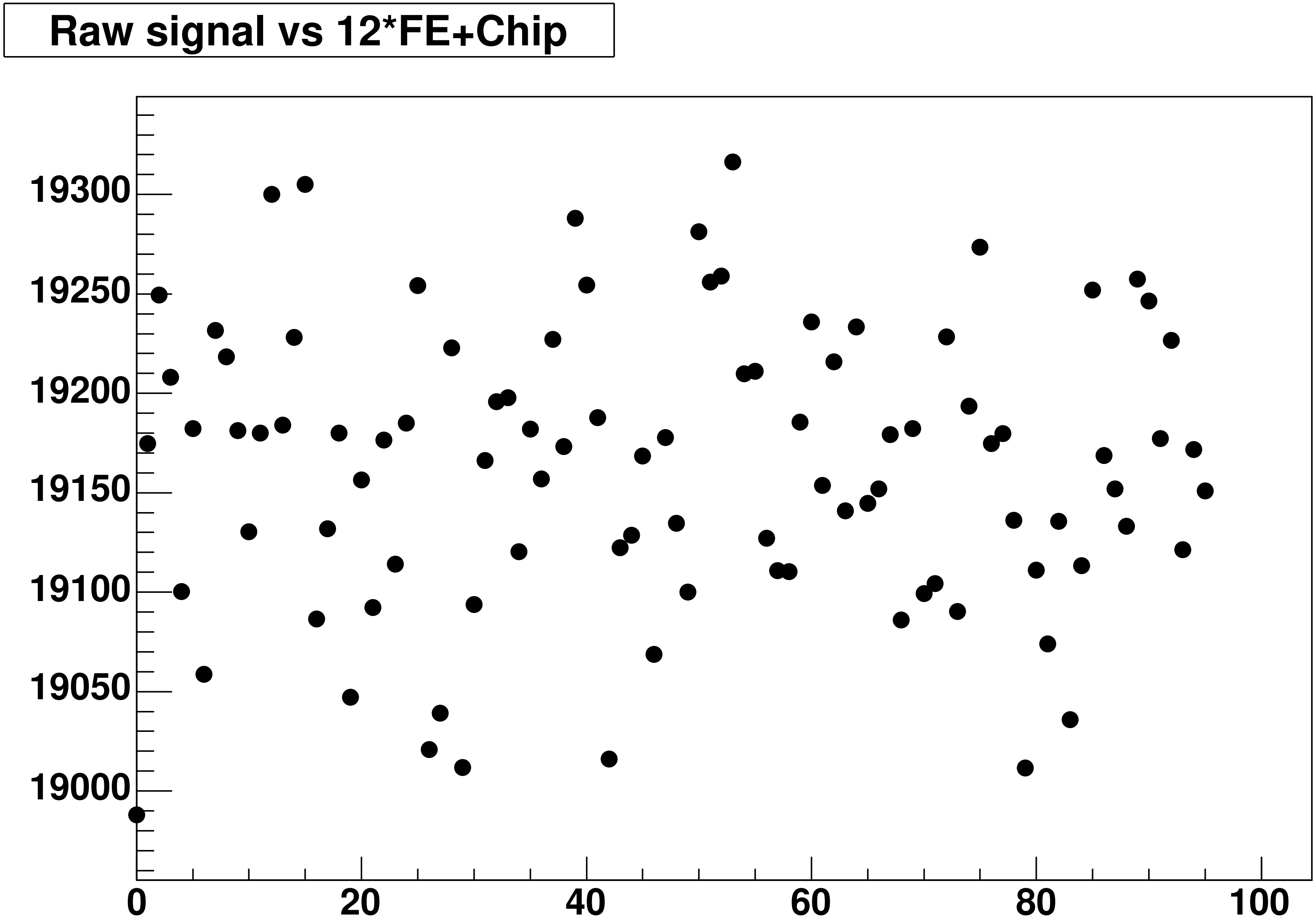

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 1167660
Mean 997.2
RMS 4244
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

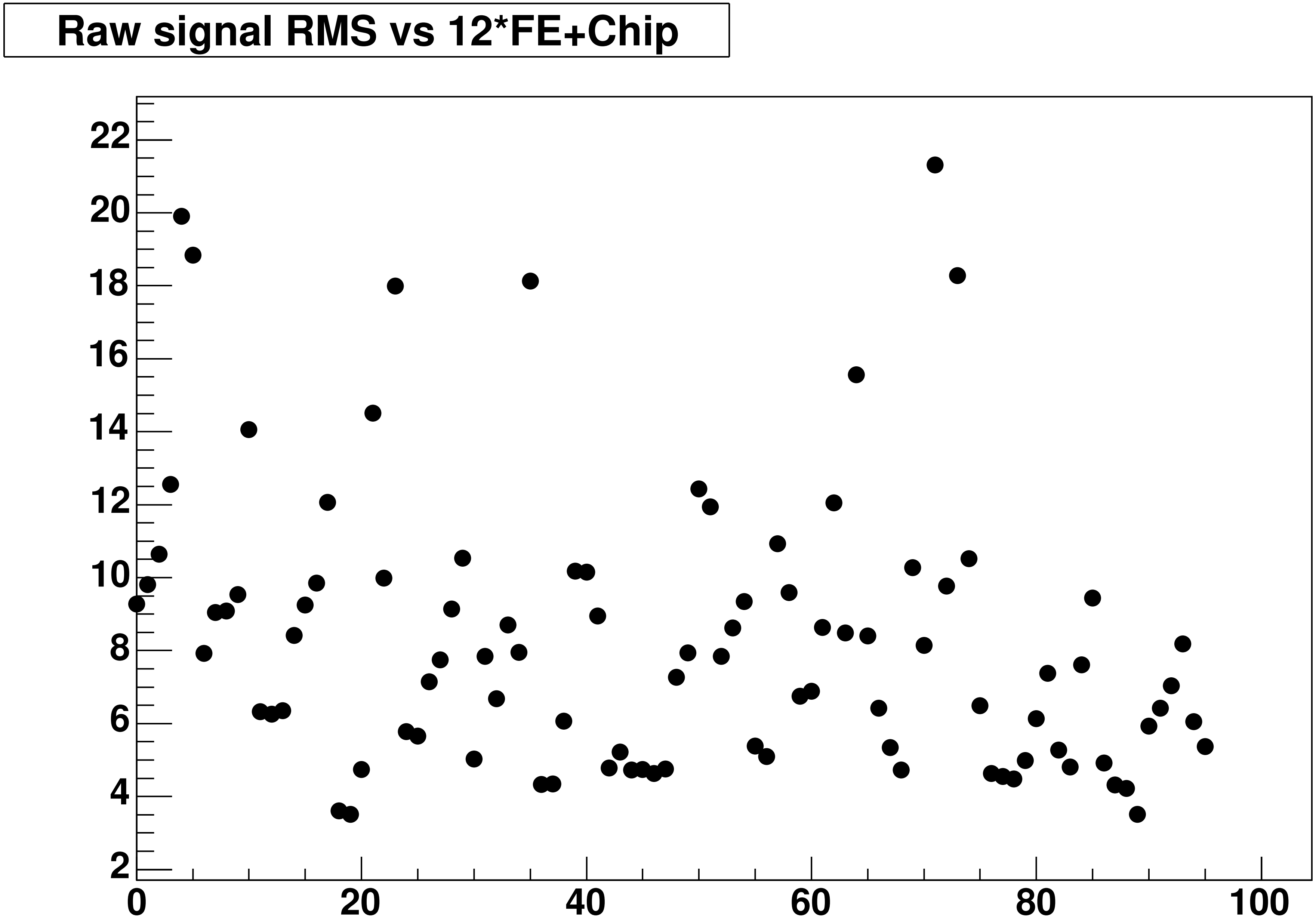

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 1167660
Mean 5.178
RMS 1.558
Underflow 0
Overflow 6.075e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 1167660
Mean 1.915e+04
RMS 5.37
Underflow 1.107e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

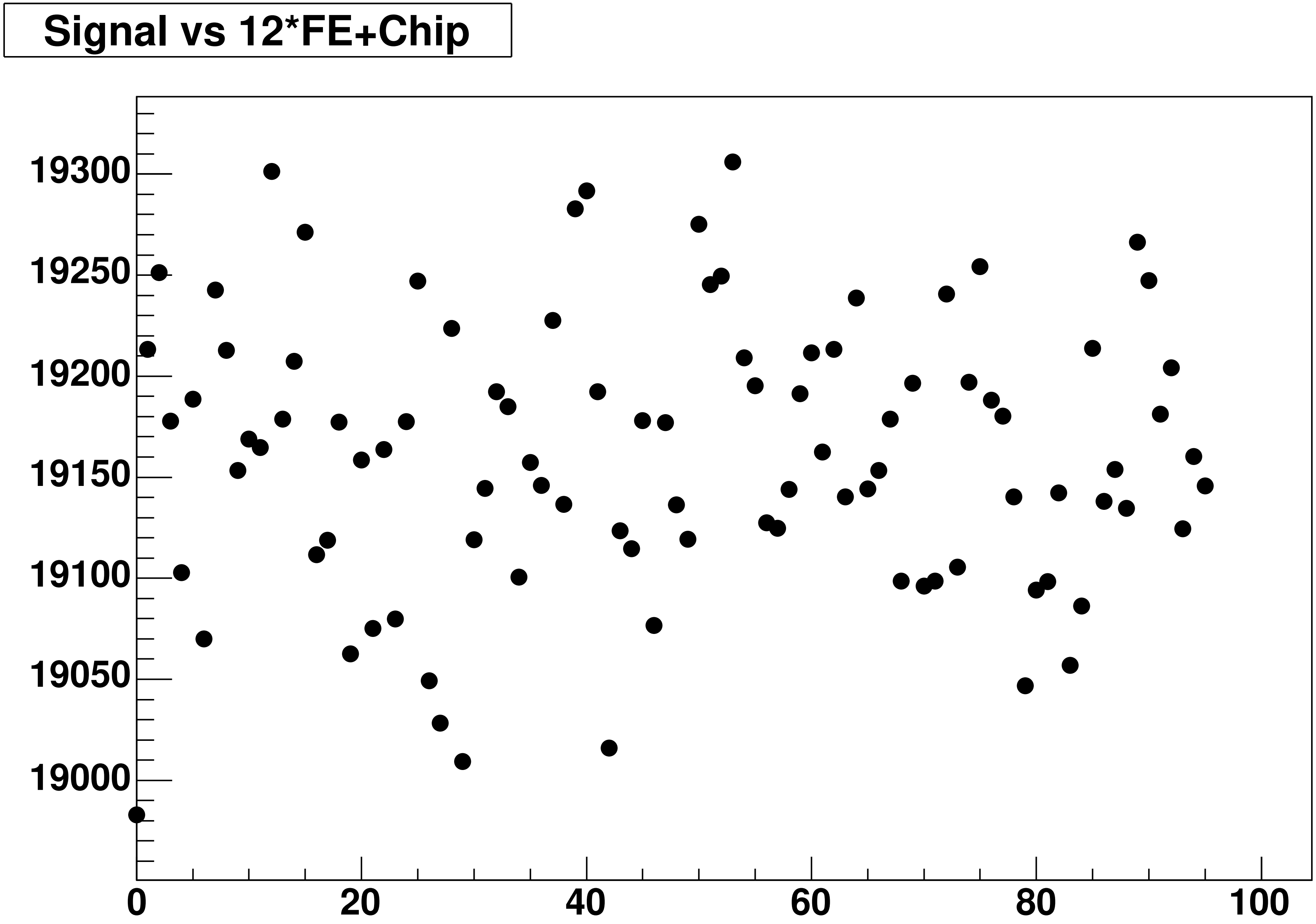

Pedestal vs 12*FE+Chip
40
30
20
10
0
-10
-20
-30
-40
0
20
40
60
80
100

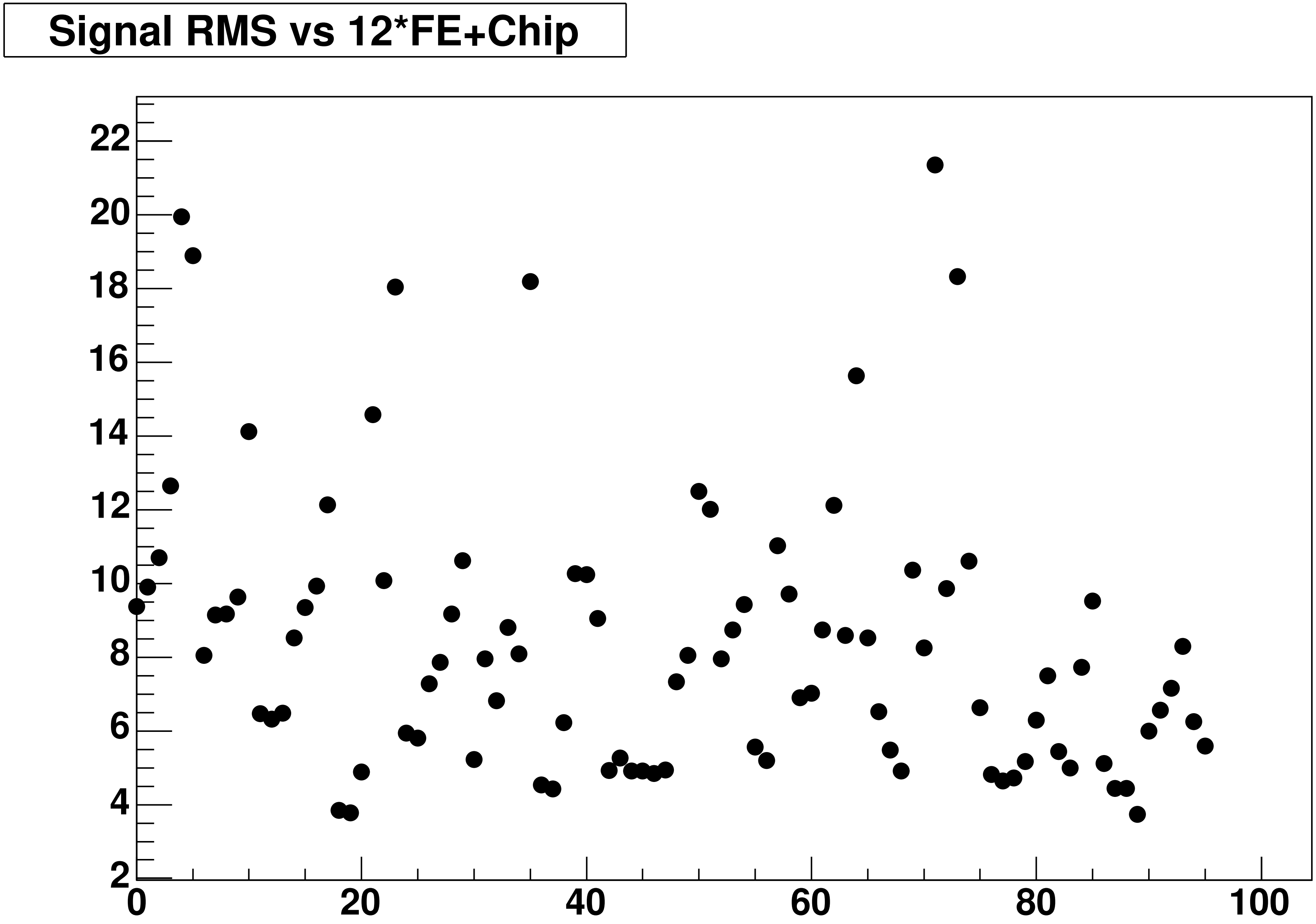

Pedestal RMS vs 12*FE+Chip
1.6
1.4
1.2
1
0.8
0
20
40
60
80
100


Raw signal vs 12*FE+Chip
19300
19250
19200
19150
19100
19050
19000
0
20
40
60
80
100



Raw signal RMS vs 12*FE+Chip
22
20
18
16
14
12
10
8
6
4
2
0
20
40
60
80
100


Signal vs 12*FE+Chip
19300
19250
19200
19150
19100
19050
19000
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
22
20
18
16
14
12
10
8
6
4
2
0
20
40
60
80
100