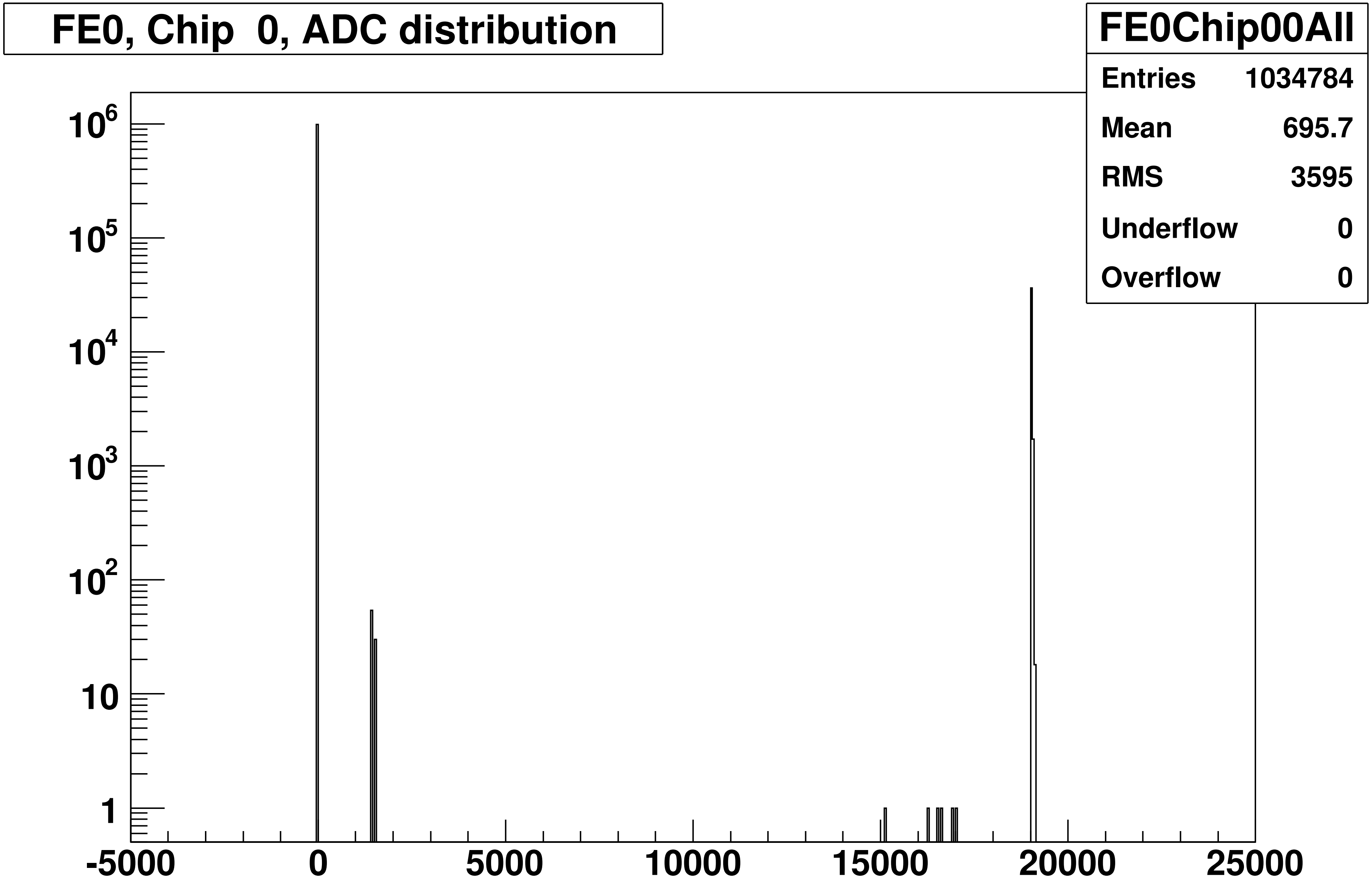

FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 1034784
Mean 695.7
RMS 3595
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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10000
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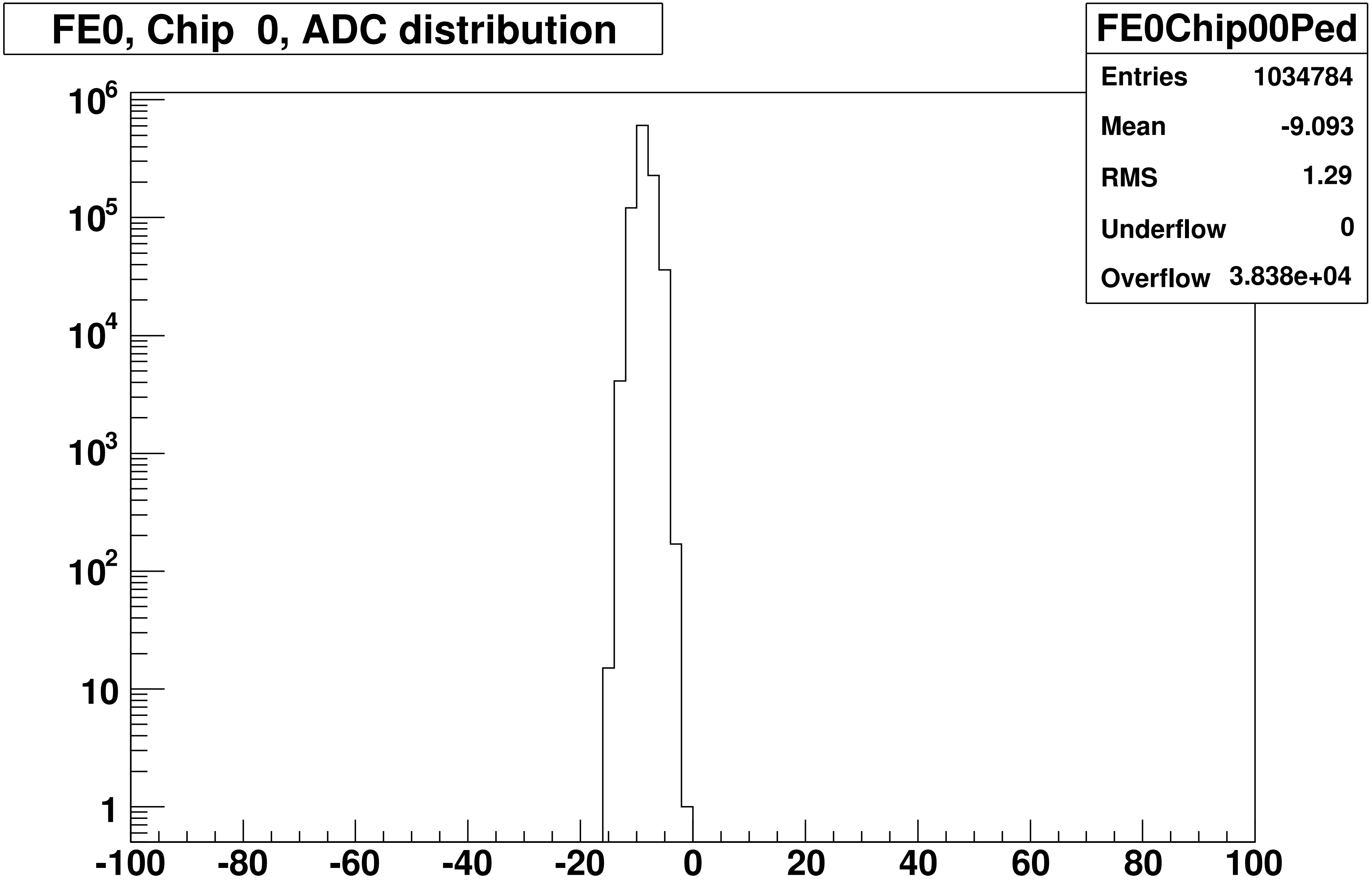
20000
25000


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 1034784
Mean -9.093
RMS 1.29
Underflow 0
Overflow 3.838e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
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-60
-40
-20
0
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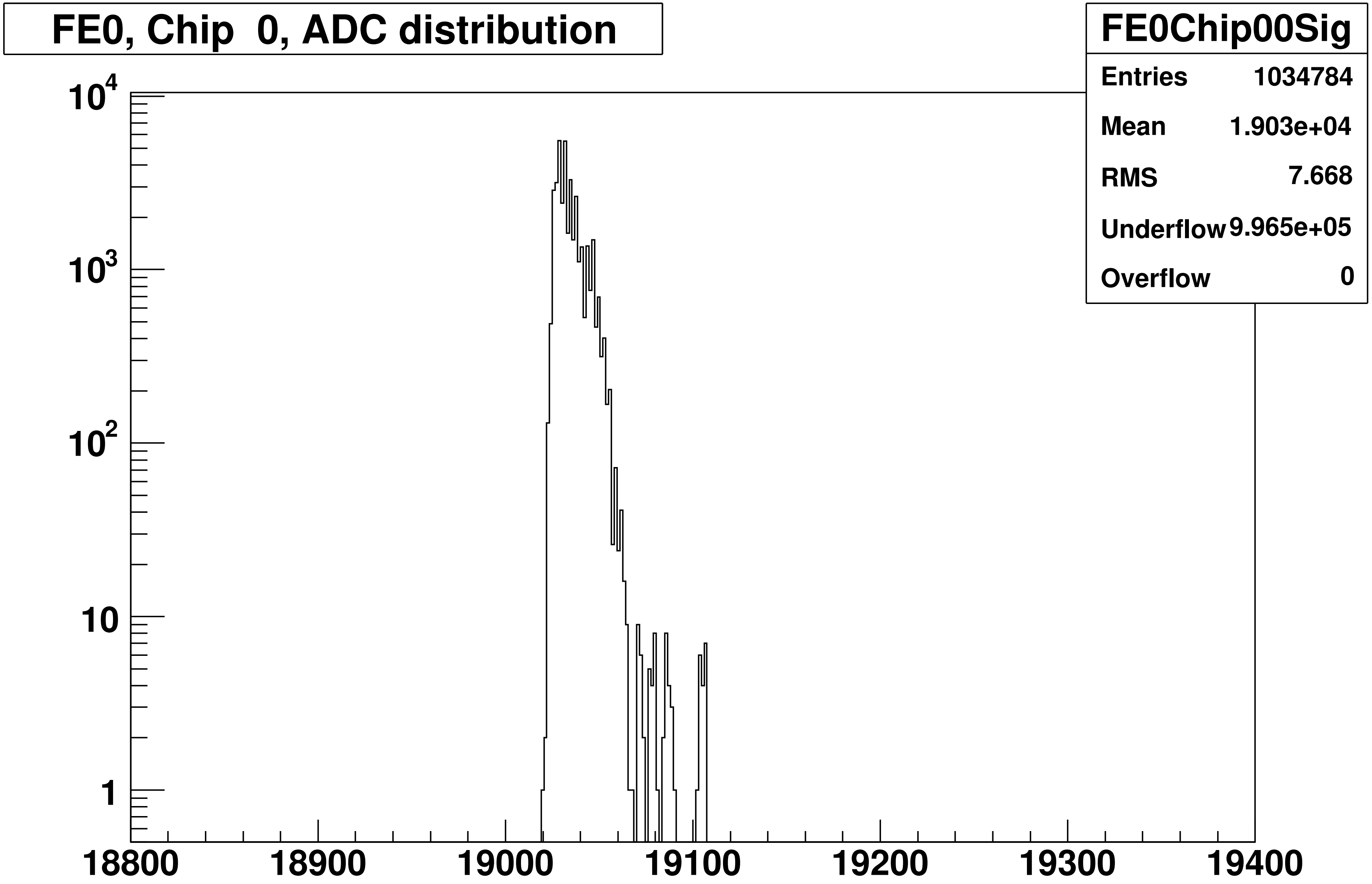
40
60
80
100


FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 1034784
Mean 1.903e+04
RMS 7.668
Underflow 9.965e+05
Overflow 0
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10^3
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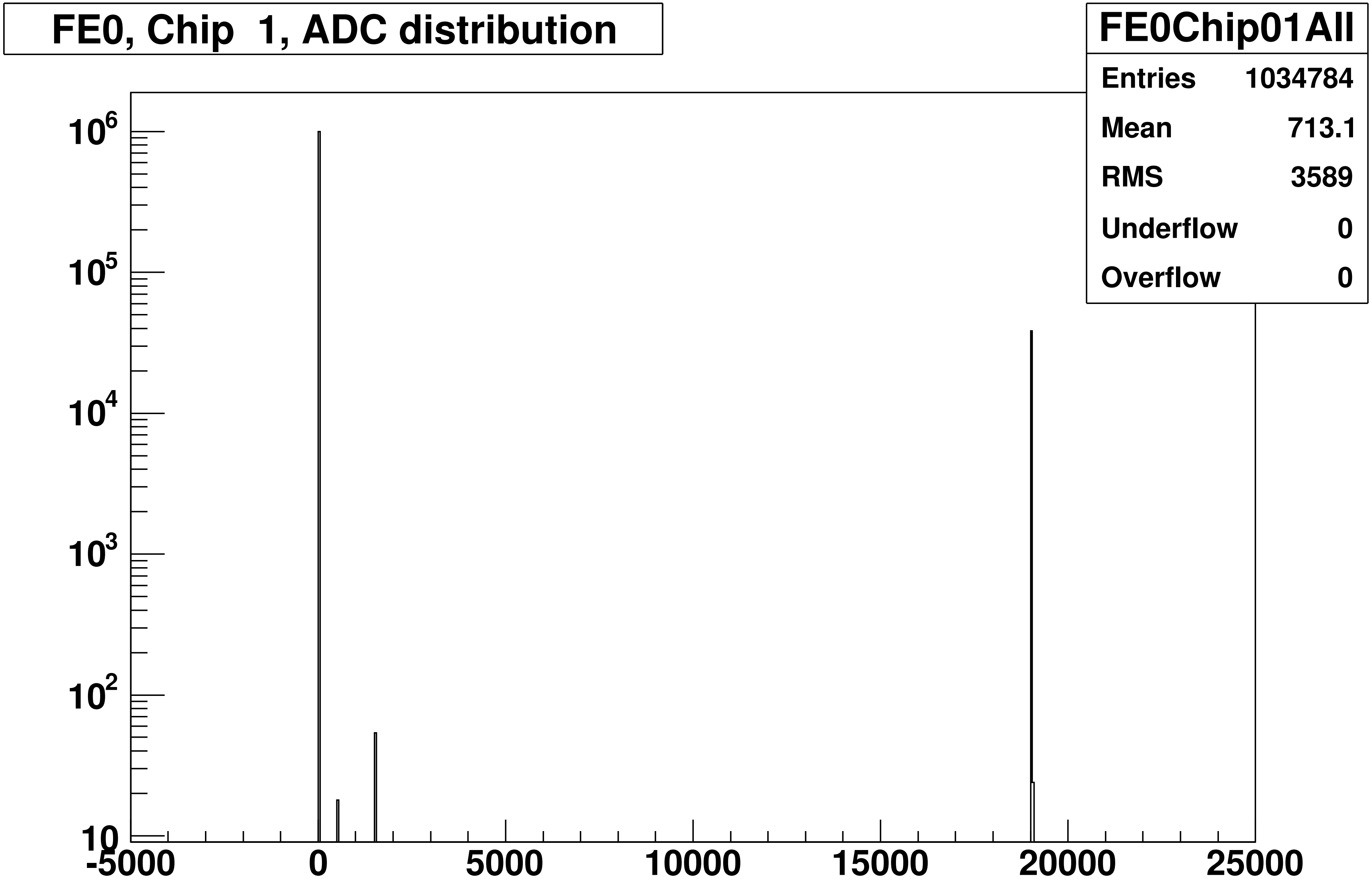

FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 1034784
Mean 713.1
RMS 3589
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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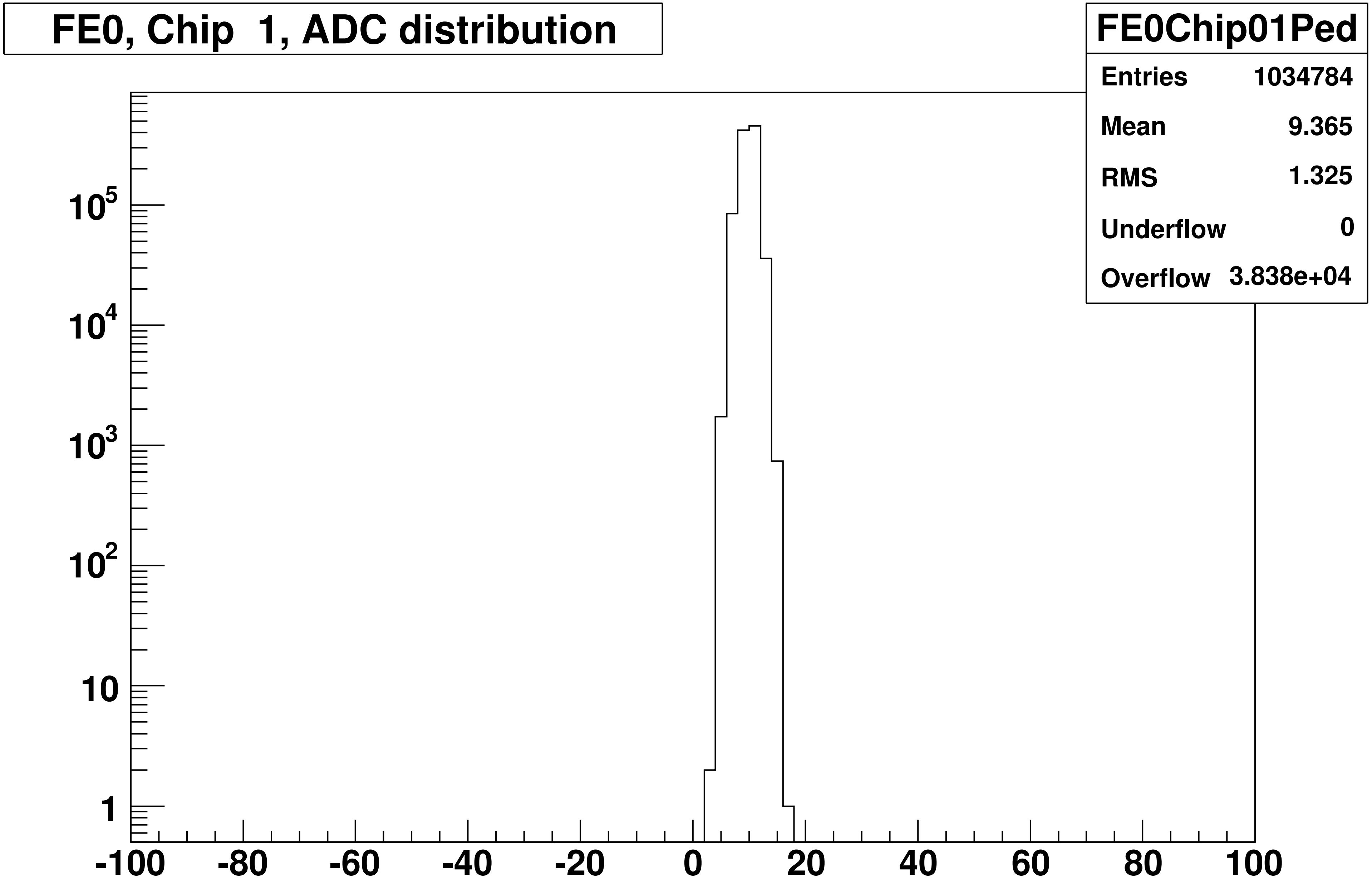

FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 1034784
Mean 9.365
RMS 1.325
Underflow 0
Overflow 3.838e+04
10^5
10^4
10^3
10^2
10
1
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80
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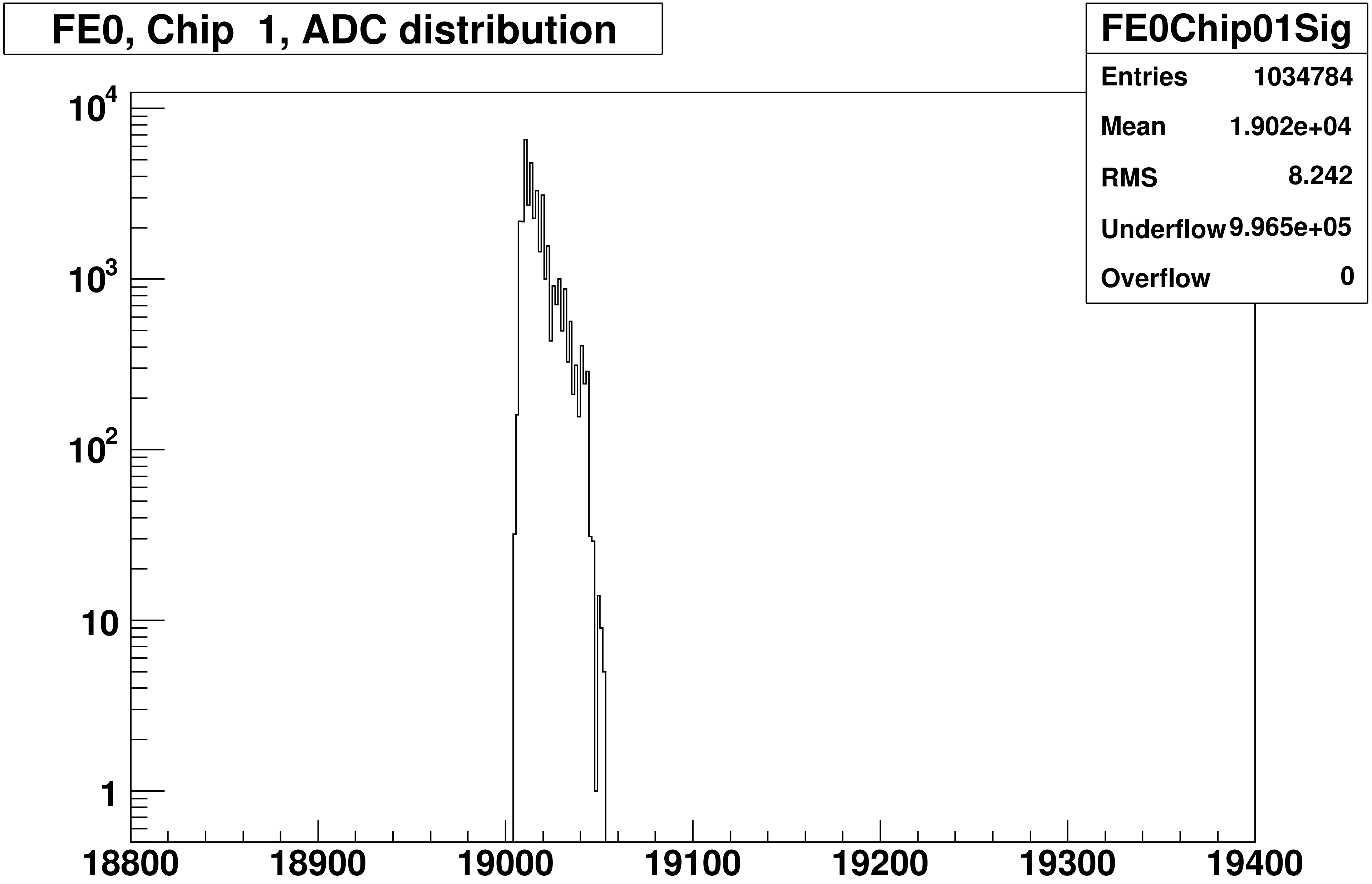

FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 1034784
Mean 1.902e+04
RMS 8.242
Underflow 9.965e+05
Overflow 0
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10^3
10^2
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18900
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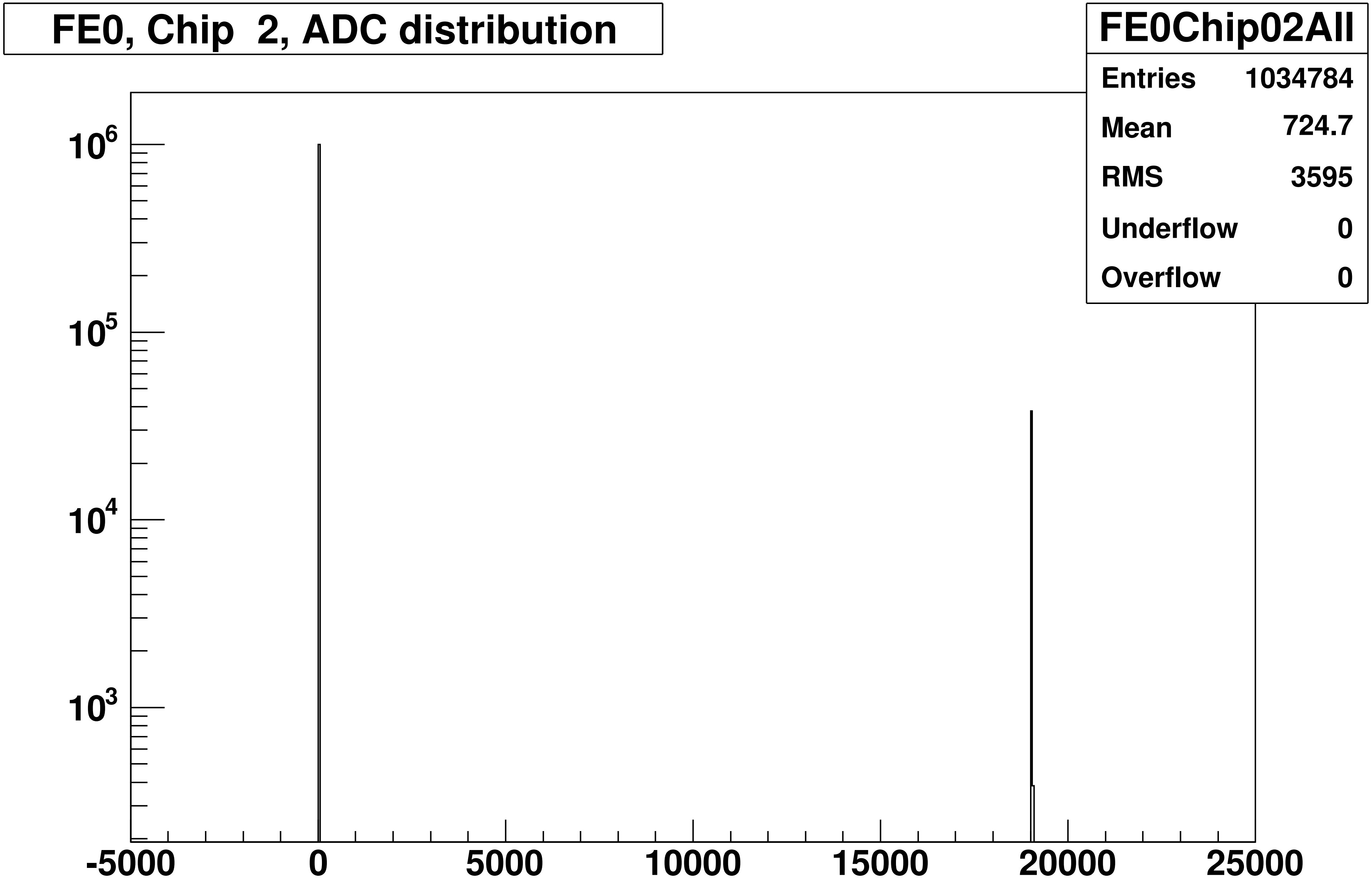
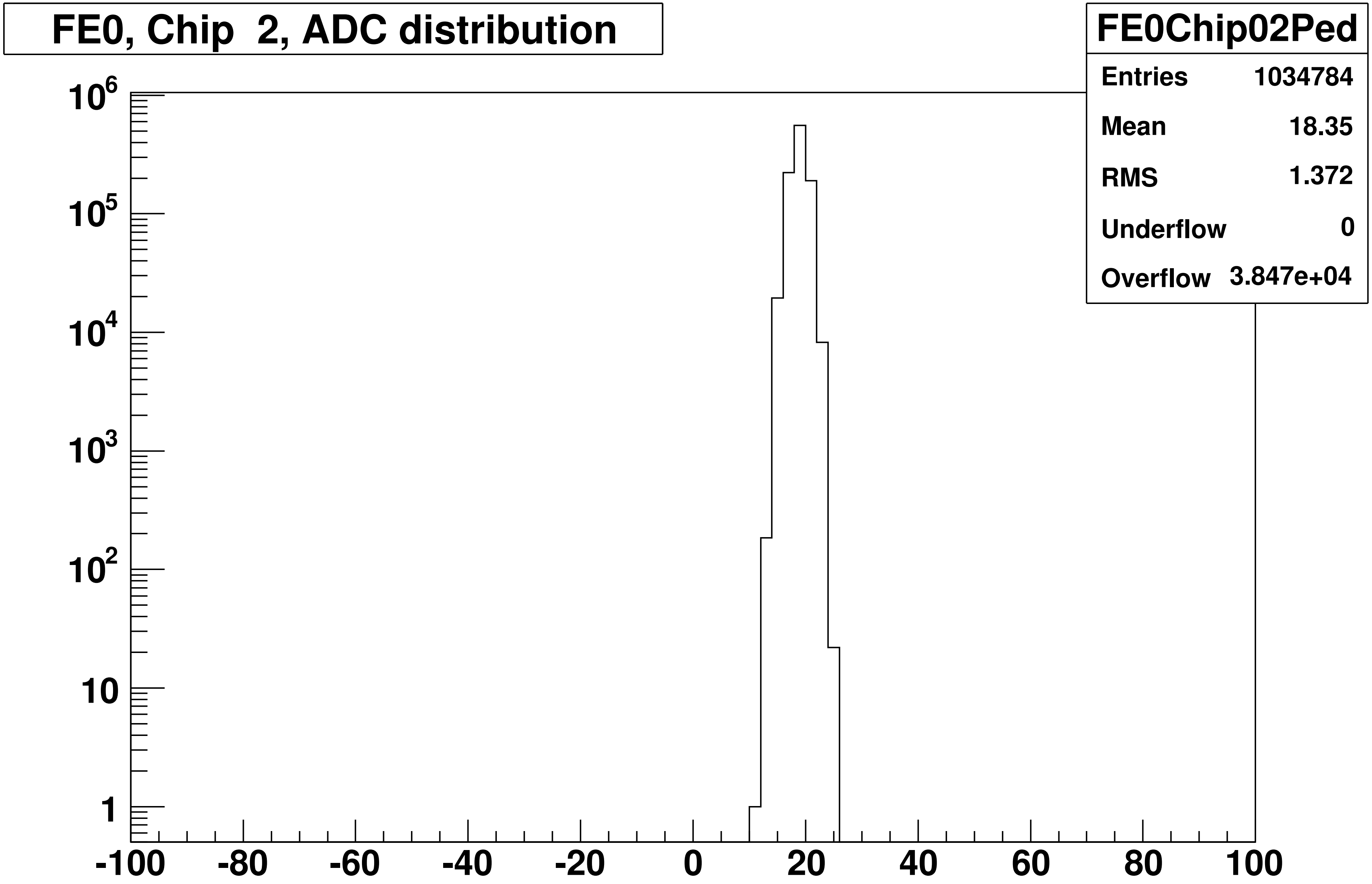

FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 1034784
Mean 724.7
RMS 3595
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 1034784
Mean 18.35
RMS 1.372
Underflow 0
Overflow 3.847e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
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-60
-40
-20
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80
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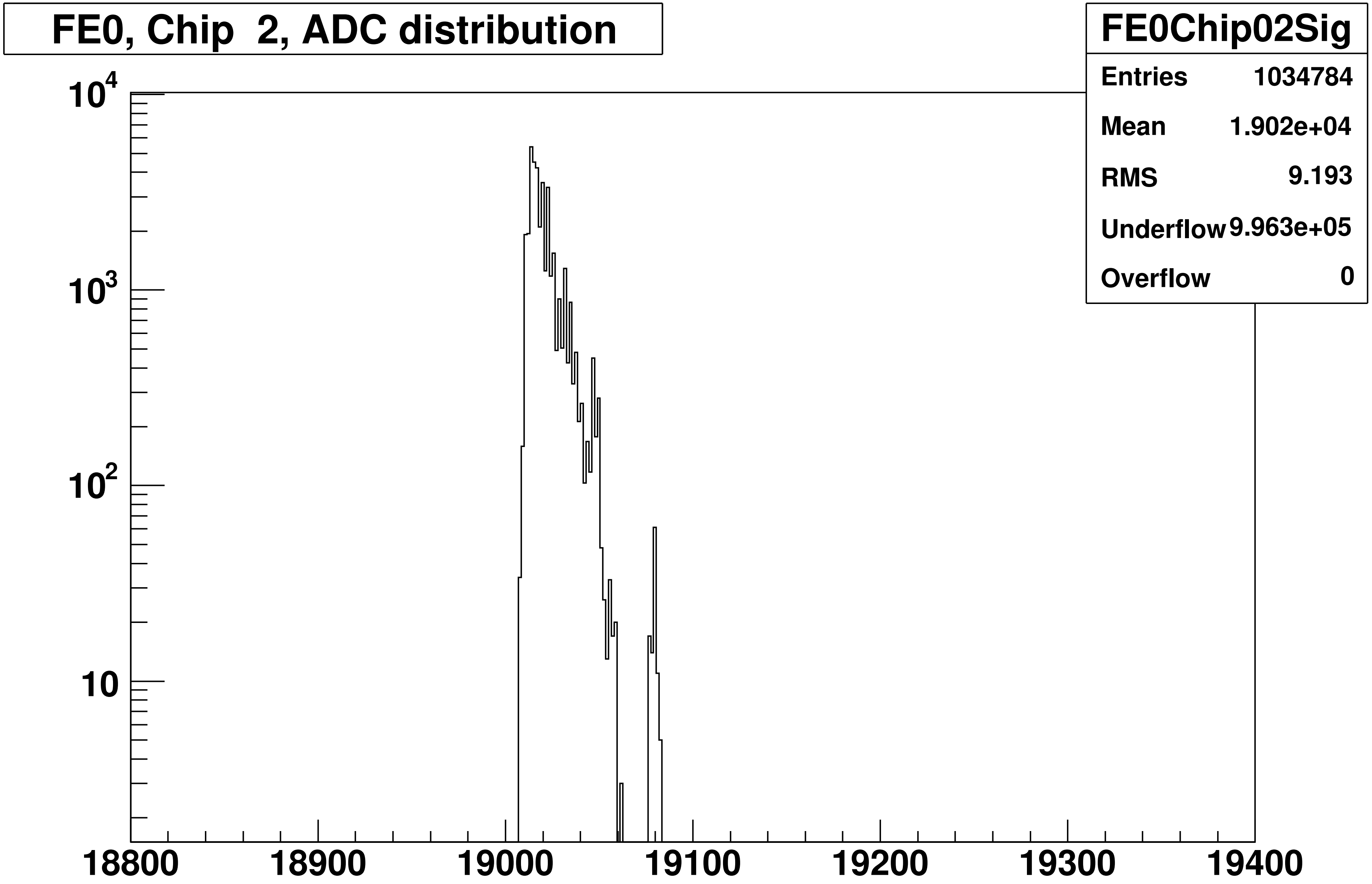

FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 1034784
Mean 1.902e+04
RMS 9.193
Underflow 9.963e+05
Overflow 0
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10^3
10^2
10
18800
18900
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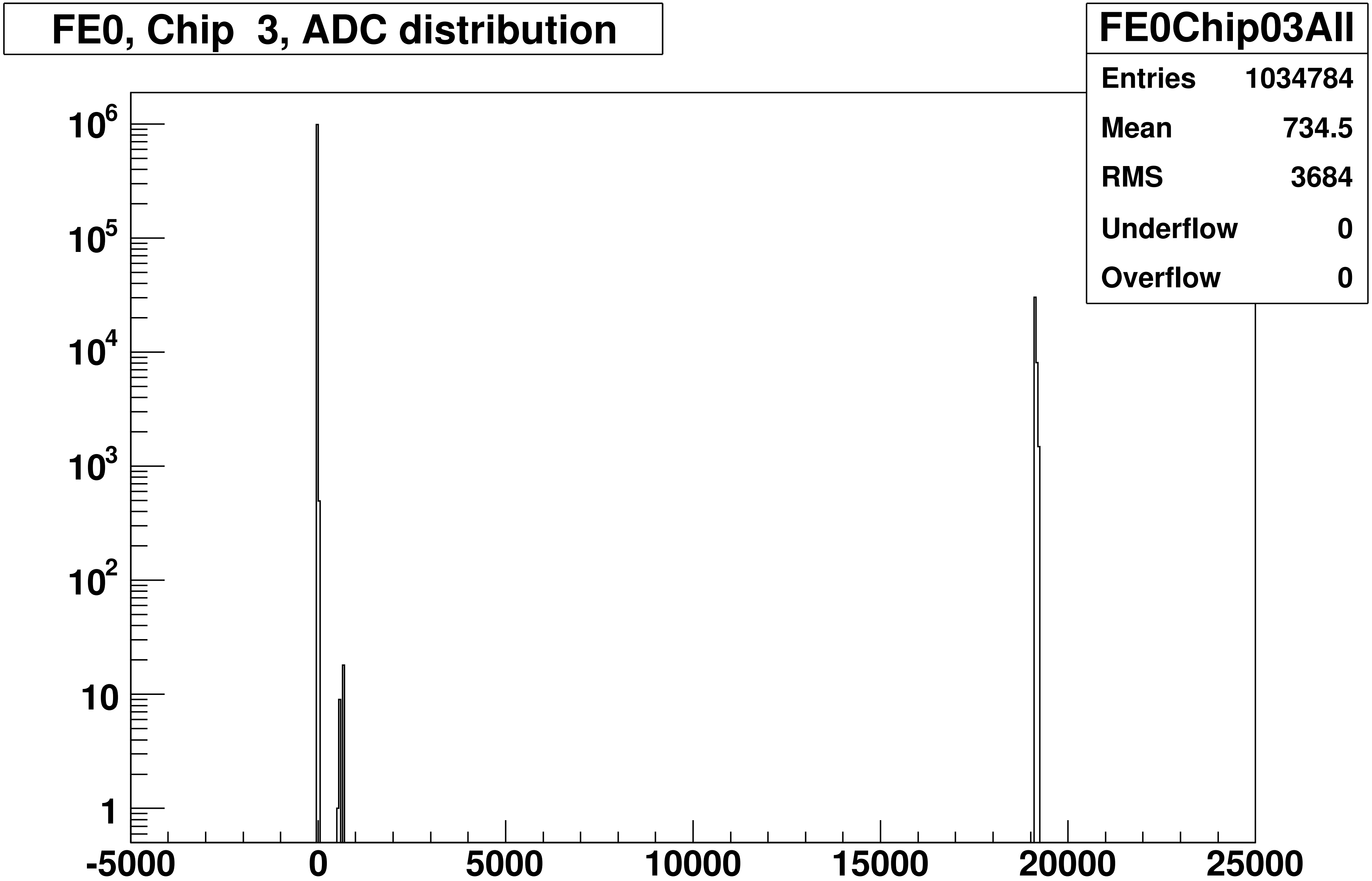

FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 1034784
Mean 734.5
RMS 3684
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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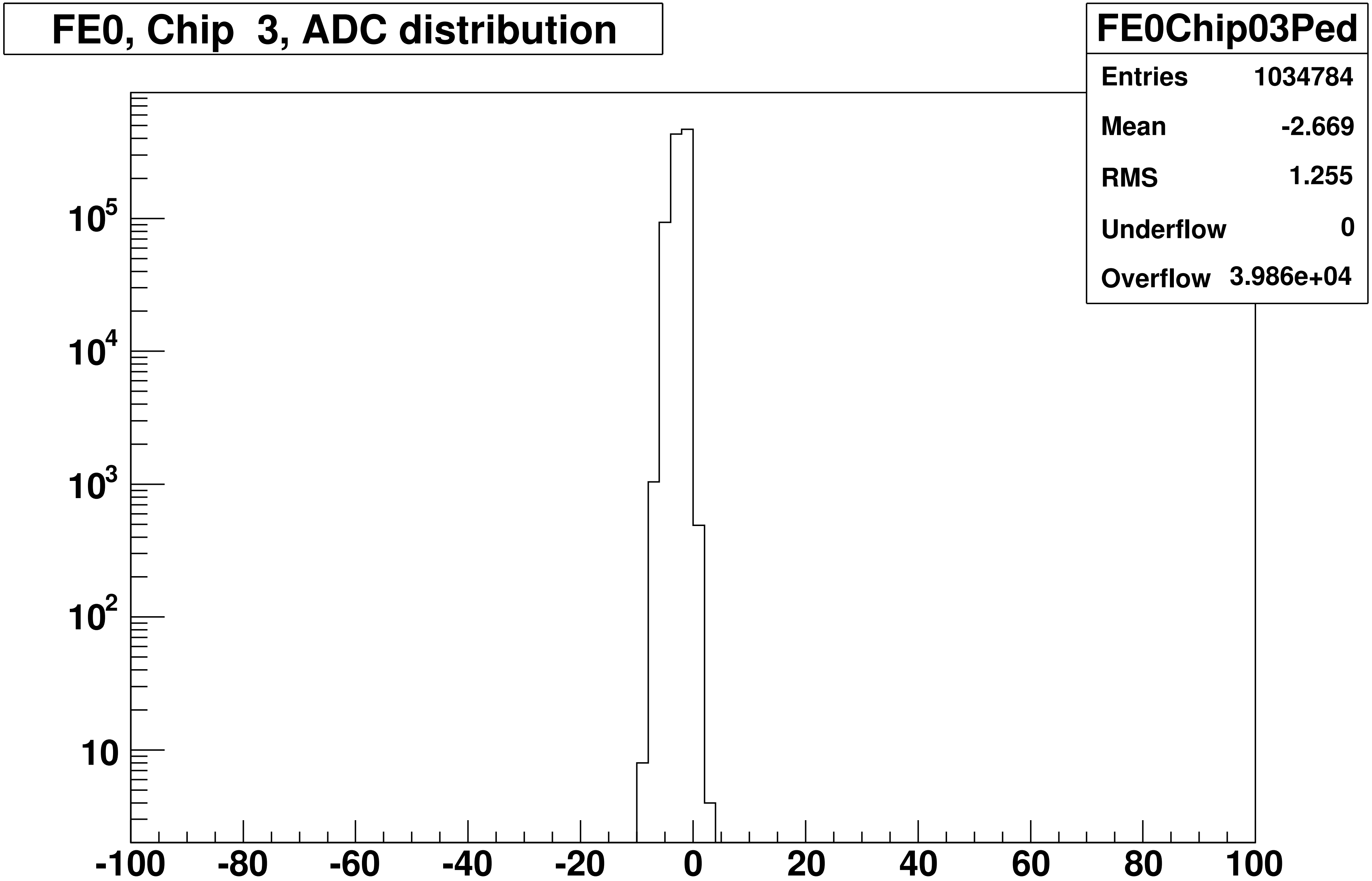
10000
15000
20000
25000



FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 1034784
Mean -2.669
RMS 1.255
Underflow 0
Overflow 3.986e+04
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10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
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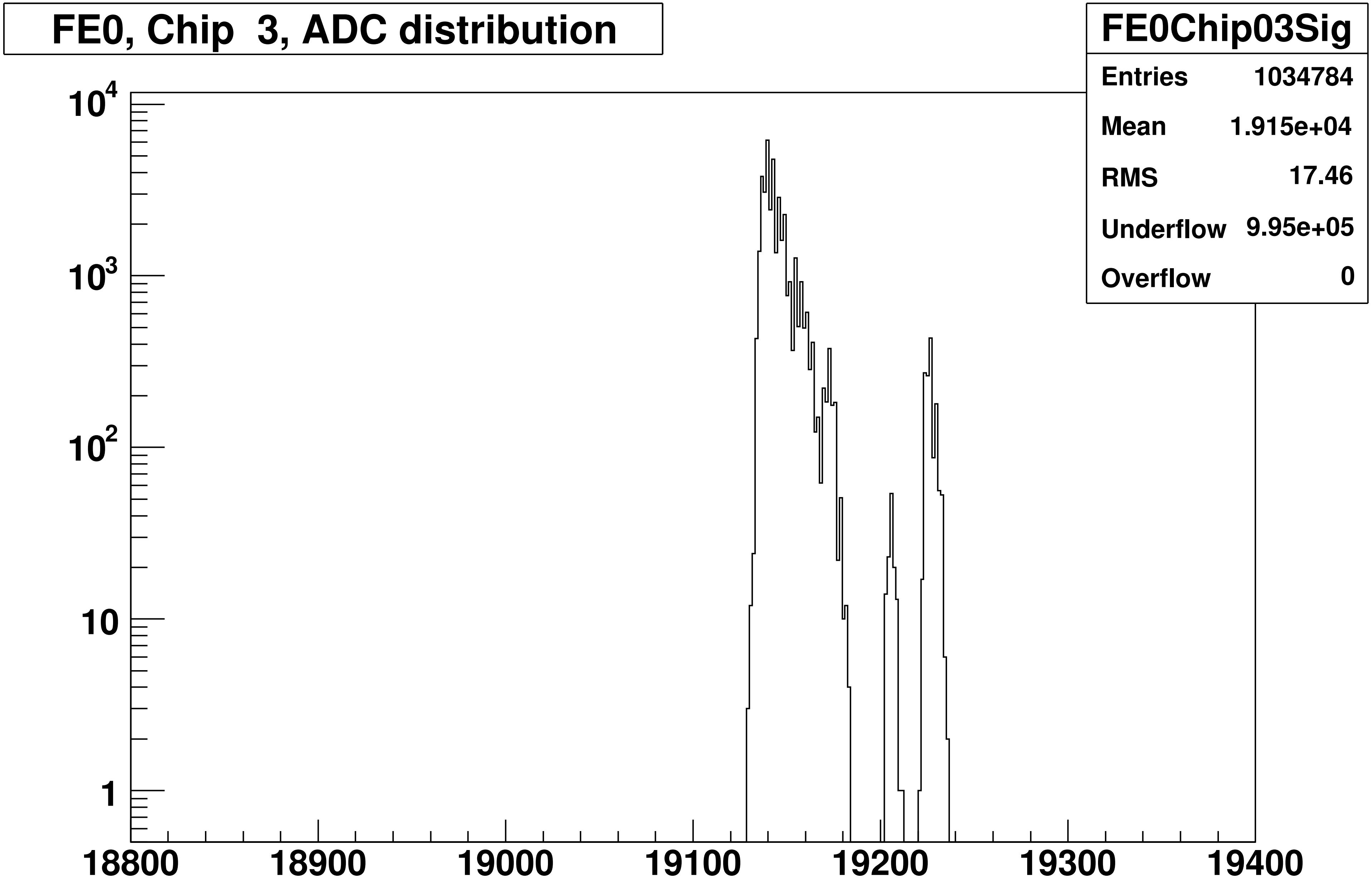
40
60
80
100


FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 1034784
Mean 1.915e+04
RMS 17.46
Underflow 9.95e+05
Overflow 0
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10^3
10^2
10
1
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18900
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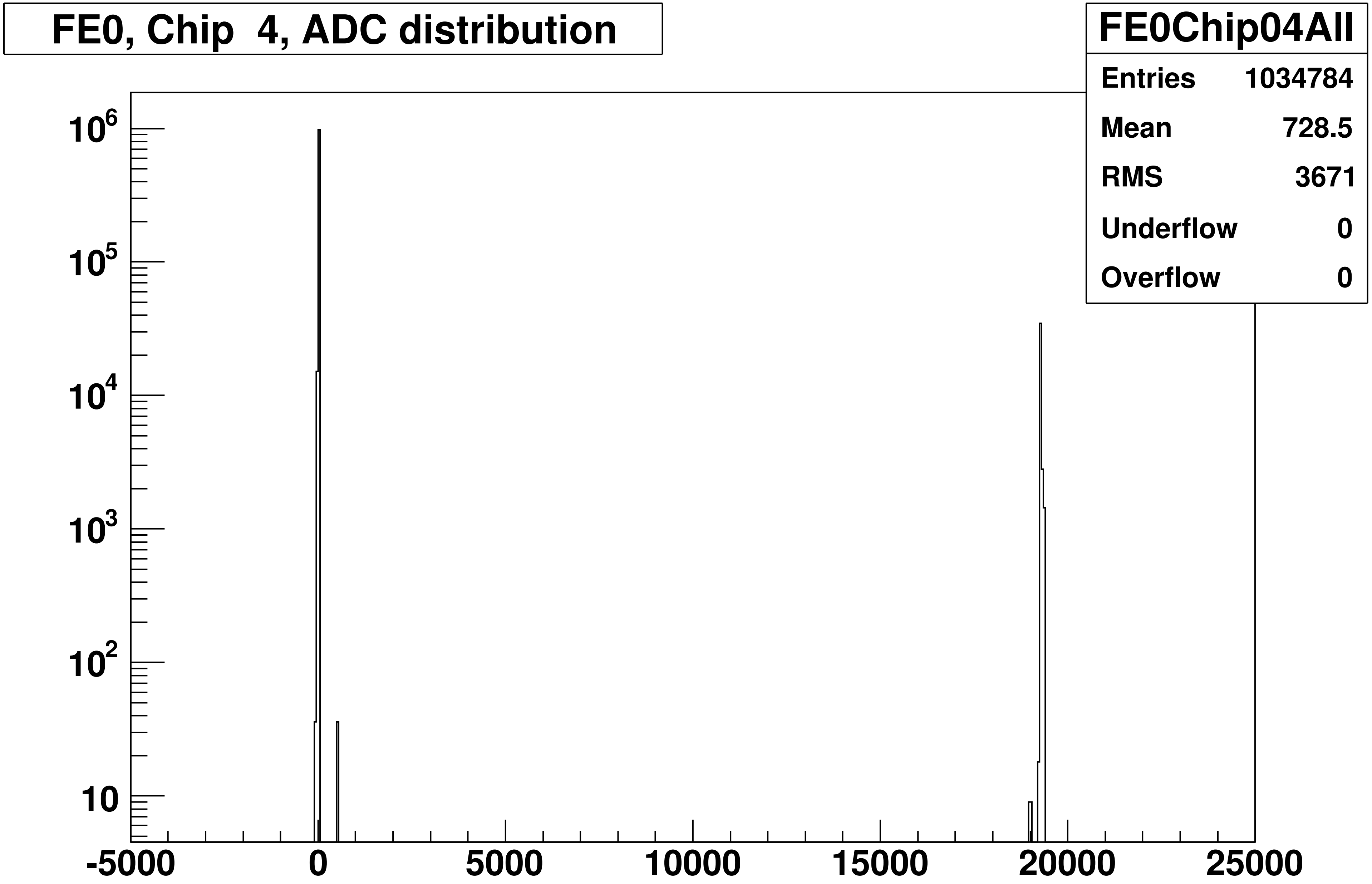
19200
19300
19400


FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 1034784
Mean 728.5
RMS 3671
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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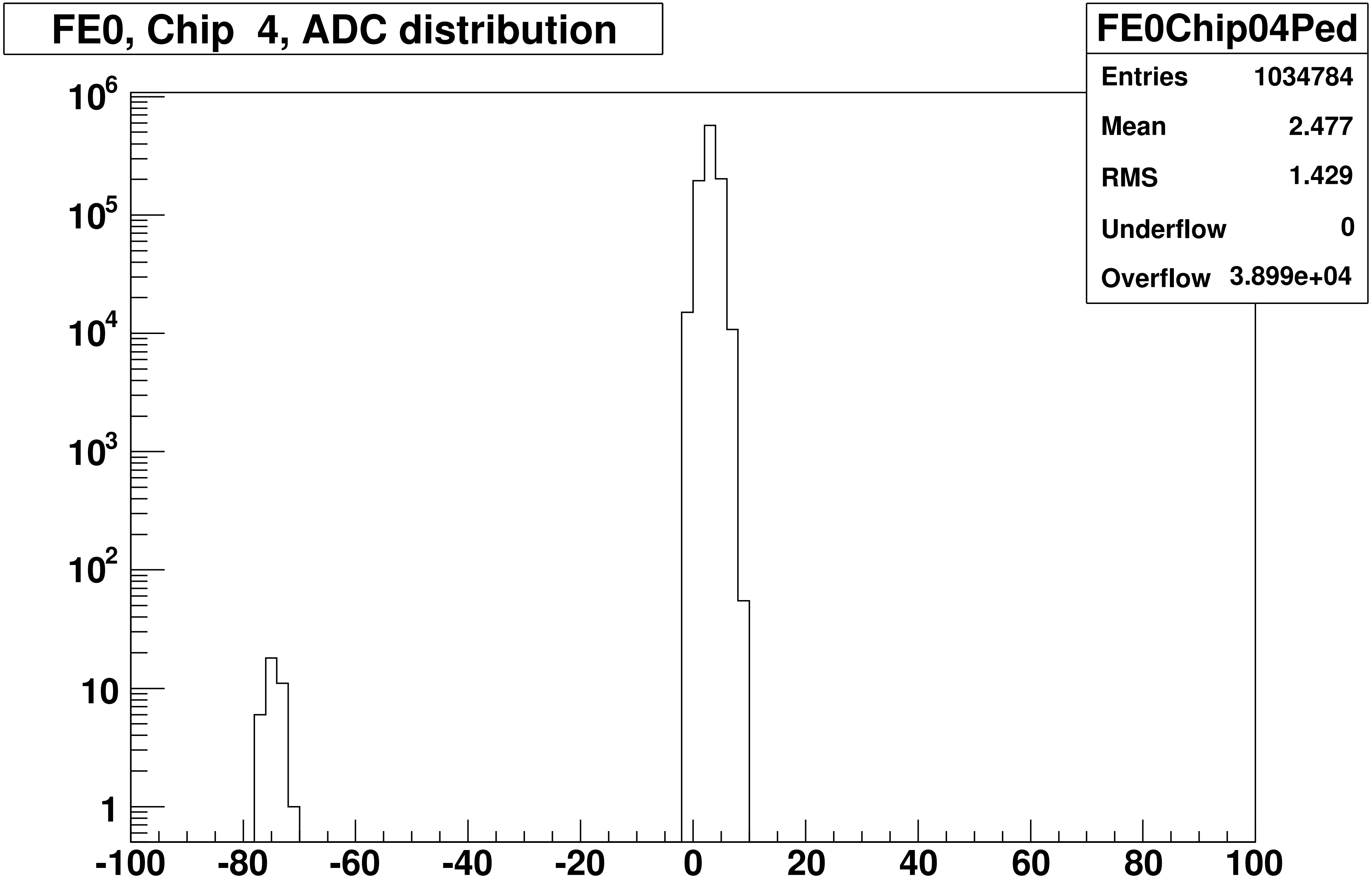
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 1034784
Mean 2.477
RMS 1.429
Underflow 0
Overflow 3.899e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
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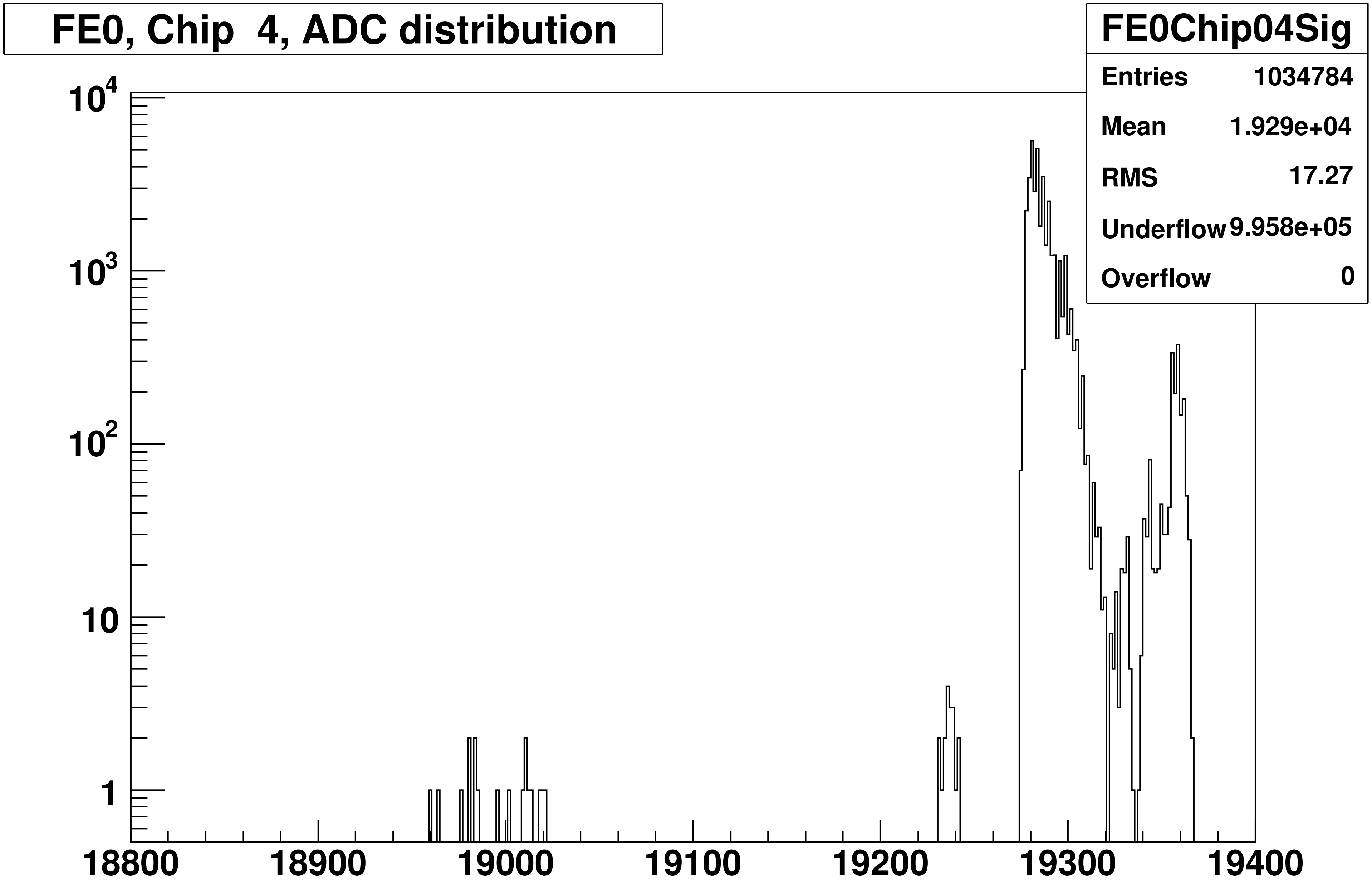
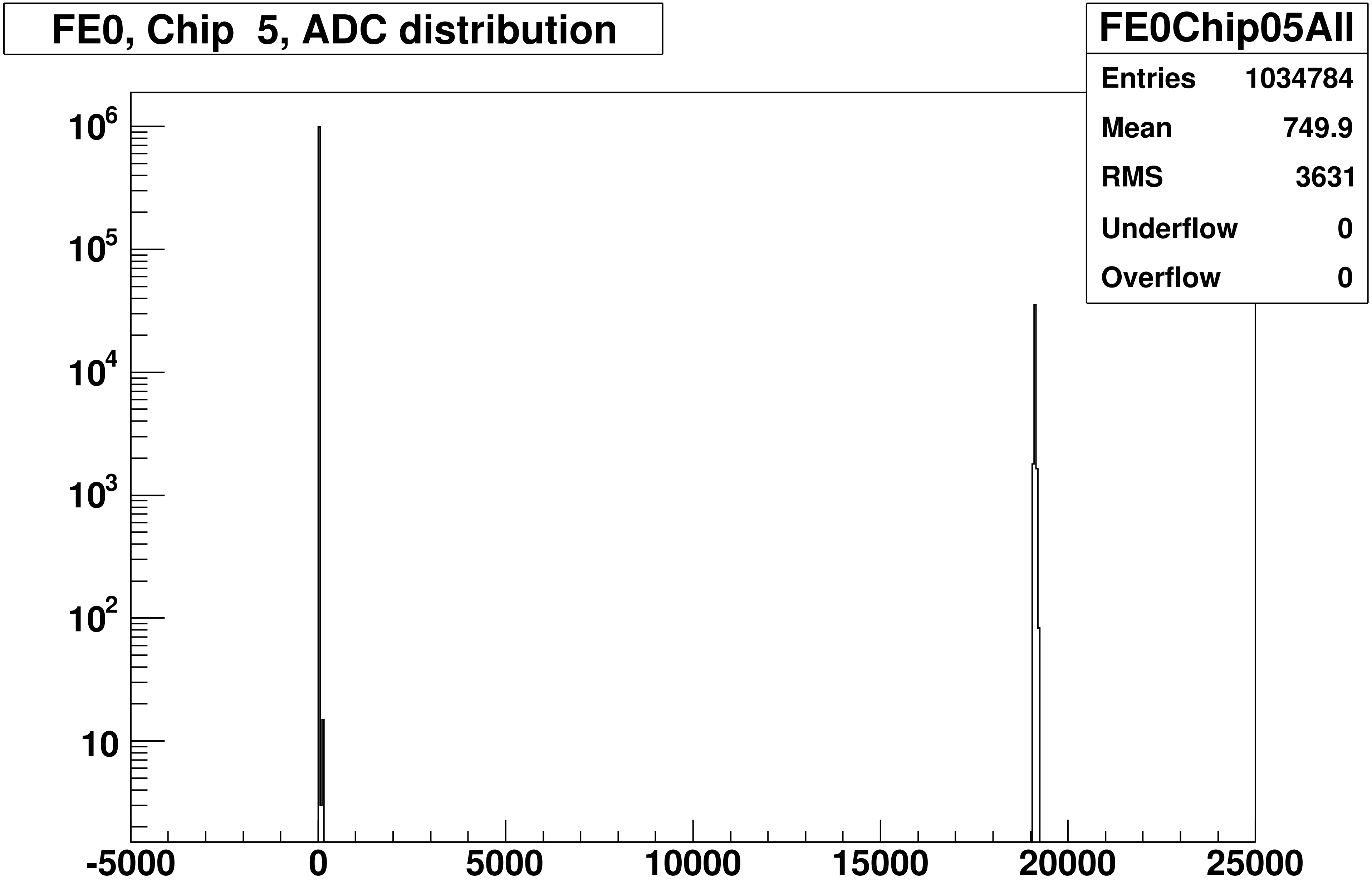
40
60
80
100


FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 1034784
Mean 1.929e+04
RMS 17.27
Underflow 9.958e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 1034784
Mean 749.9
RMS 3631
Underflow 0
Overflow 0
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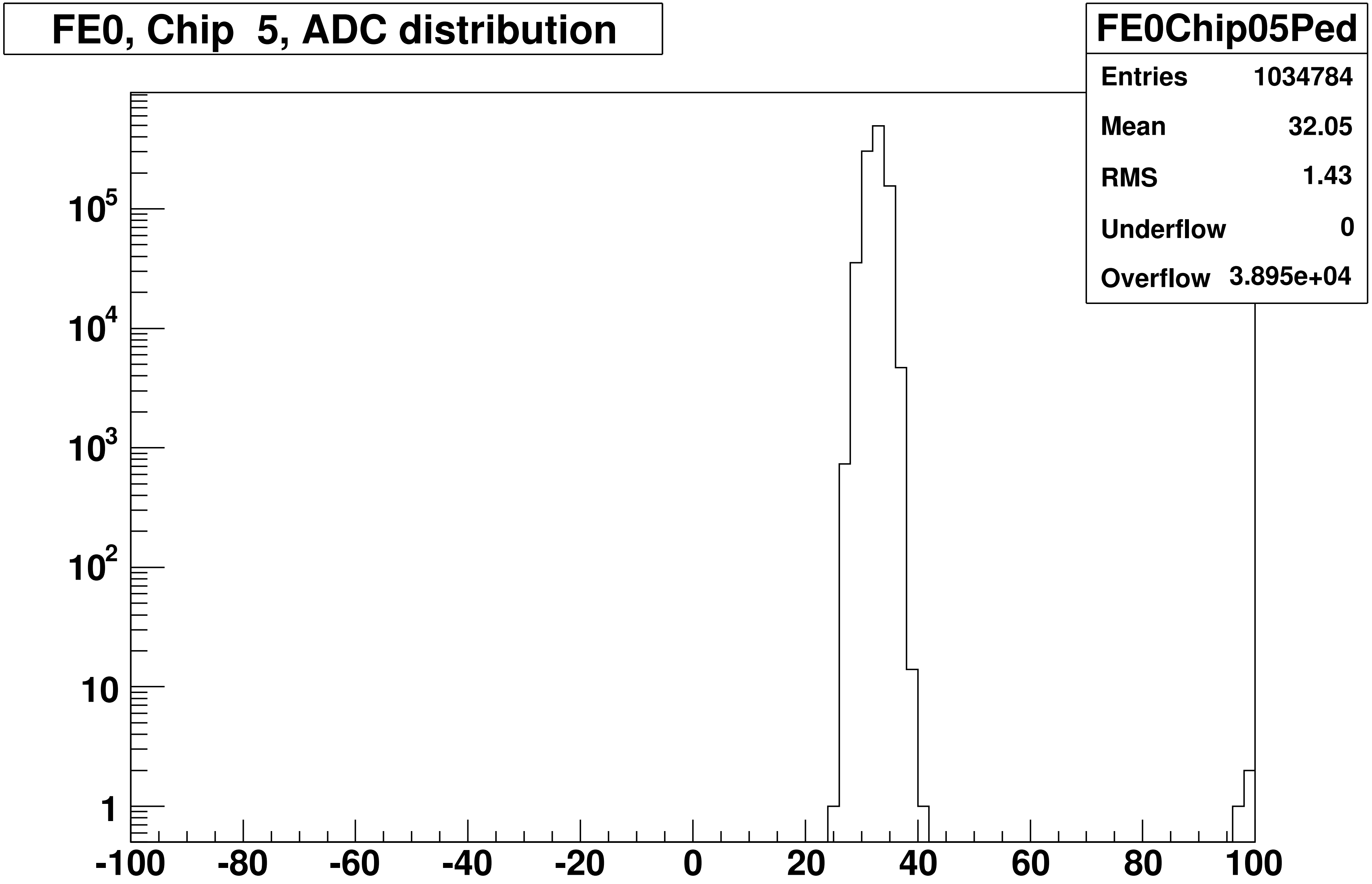
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 1034784
Mean 32.05
RMS 1.43
Underflow 0
Overflow 3.895e+04
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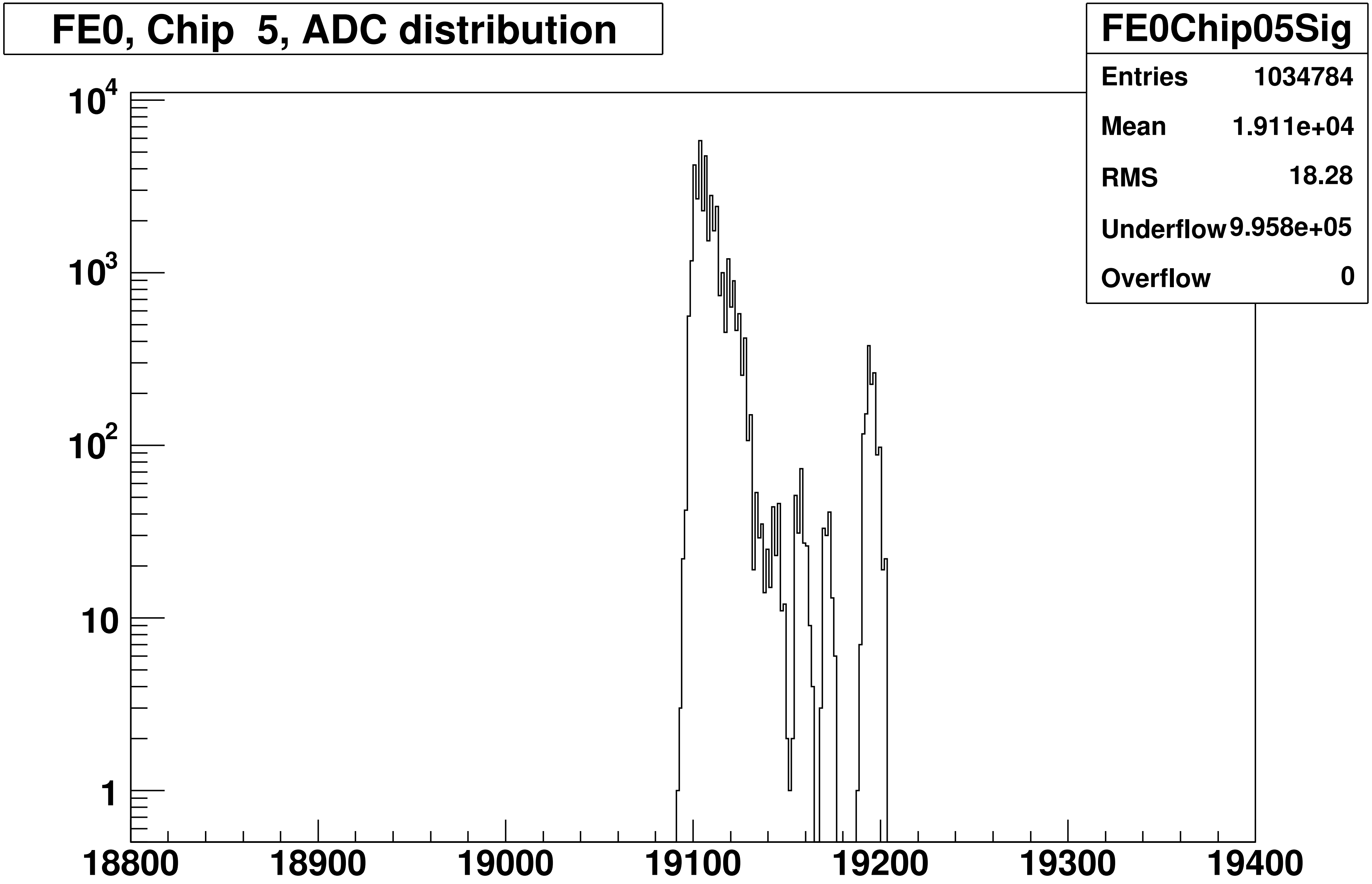
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 1034784
Mean 1.911e+04
RMS 18.28
Underflow 9.958e+05
Overflow 0
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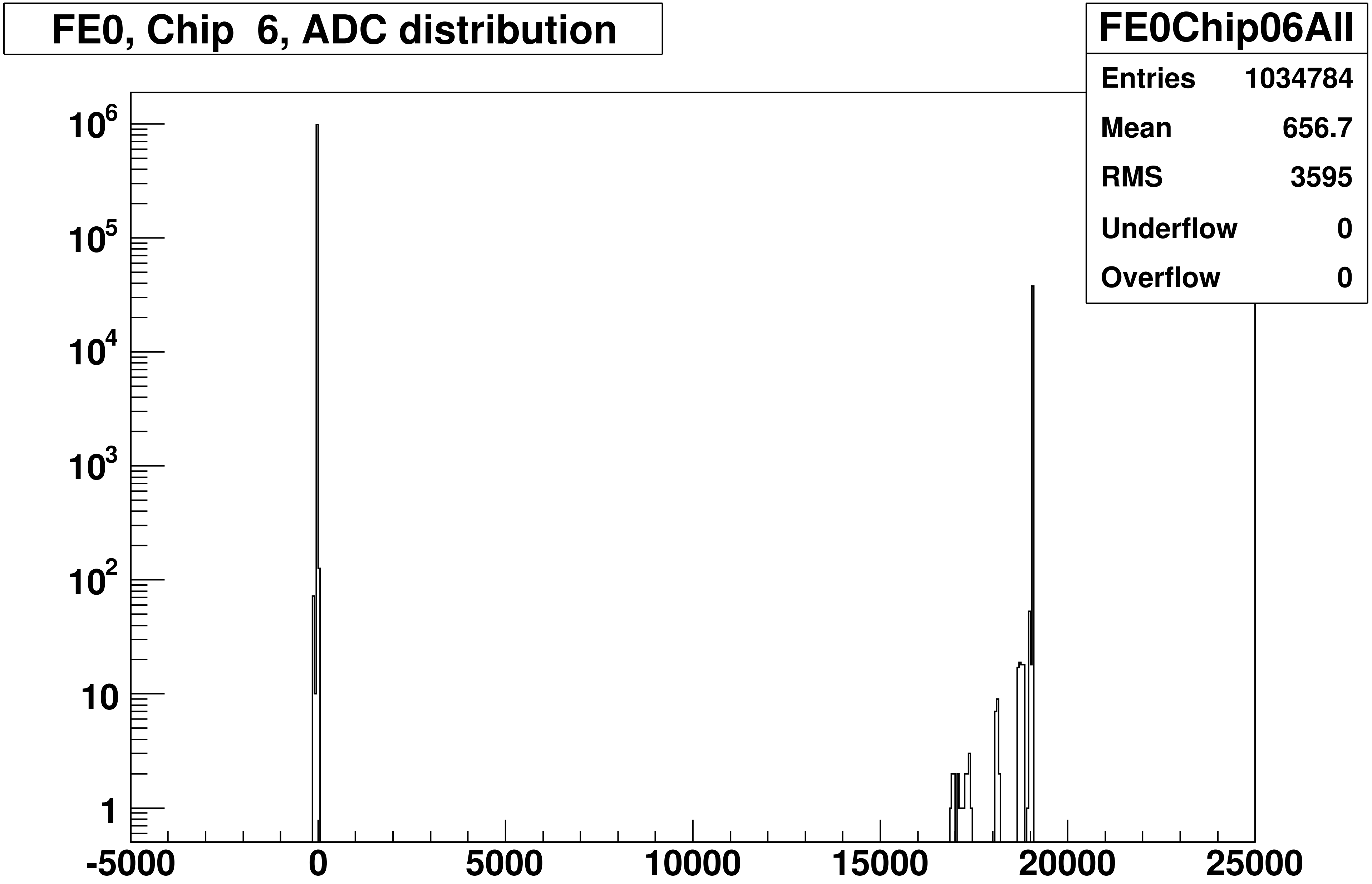
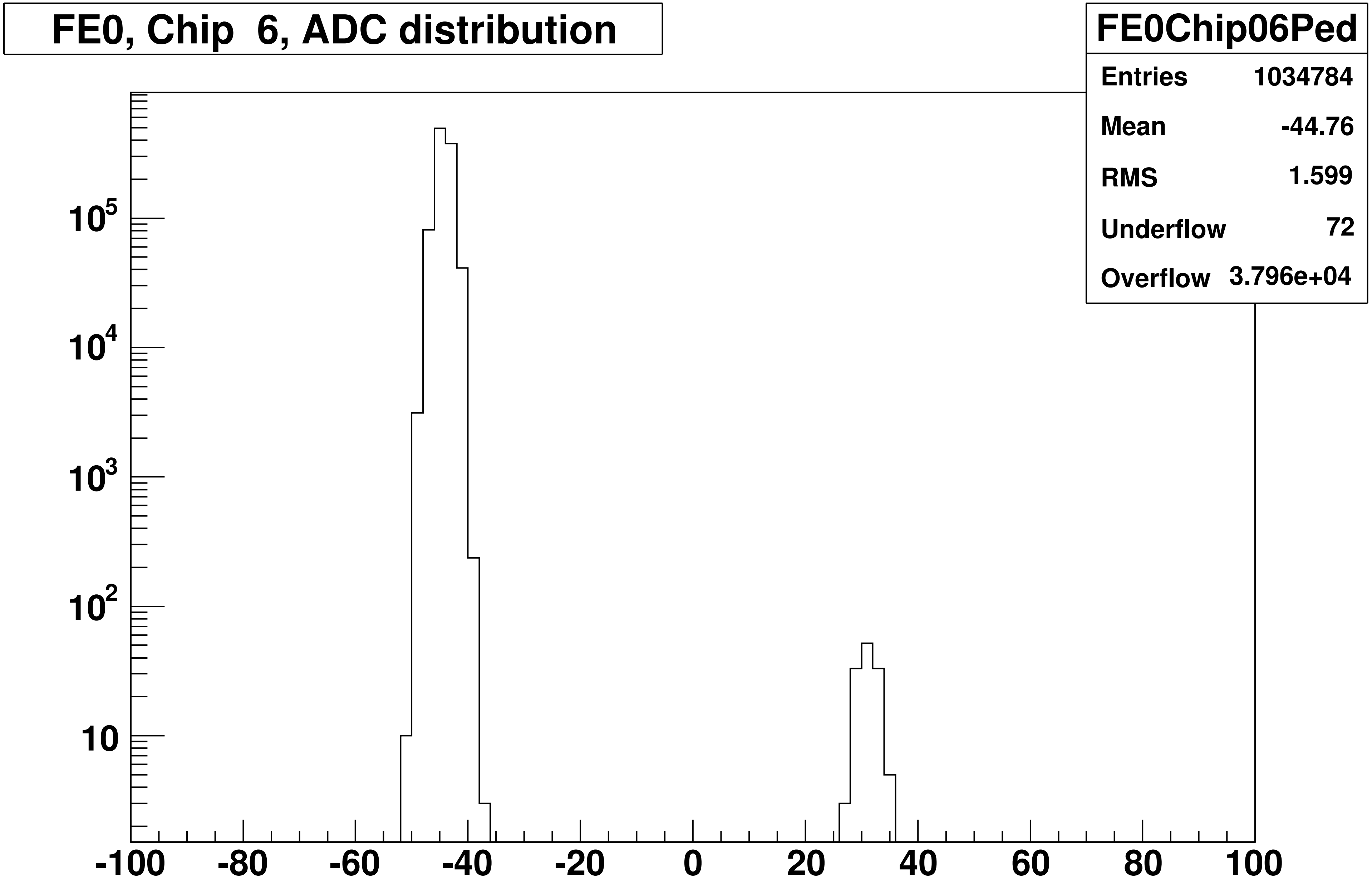
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 1034784
Mean 656.7
RMS 3595
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 1034784
Mean -44.76
RMS 1.599
Underflow 72
Overflow 3.796e+04
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10^4
10^3
10^2
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-100
-80
-60
-40
-20
0
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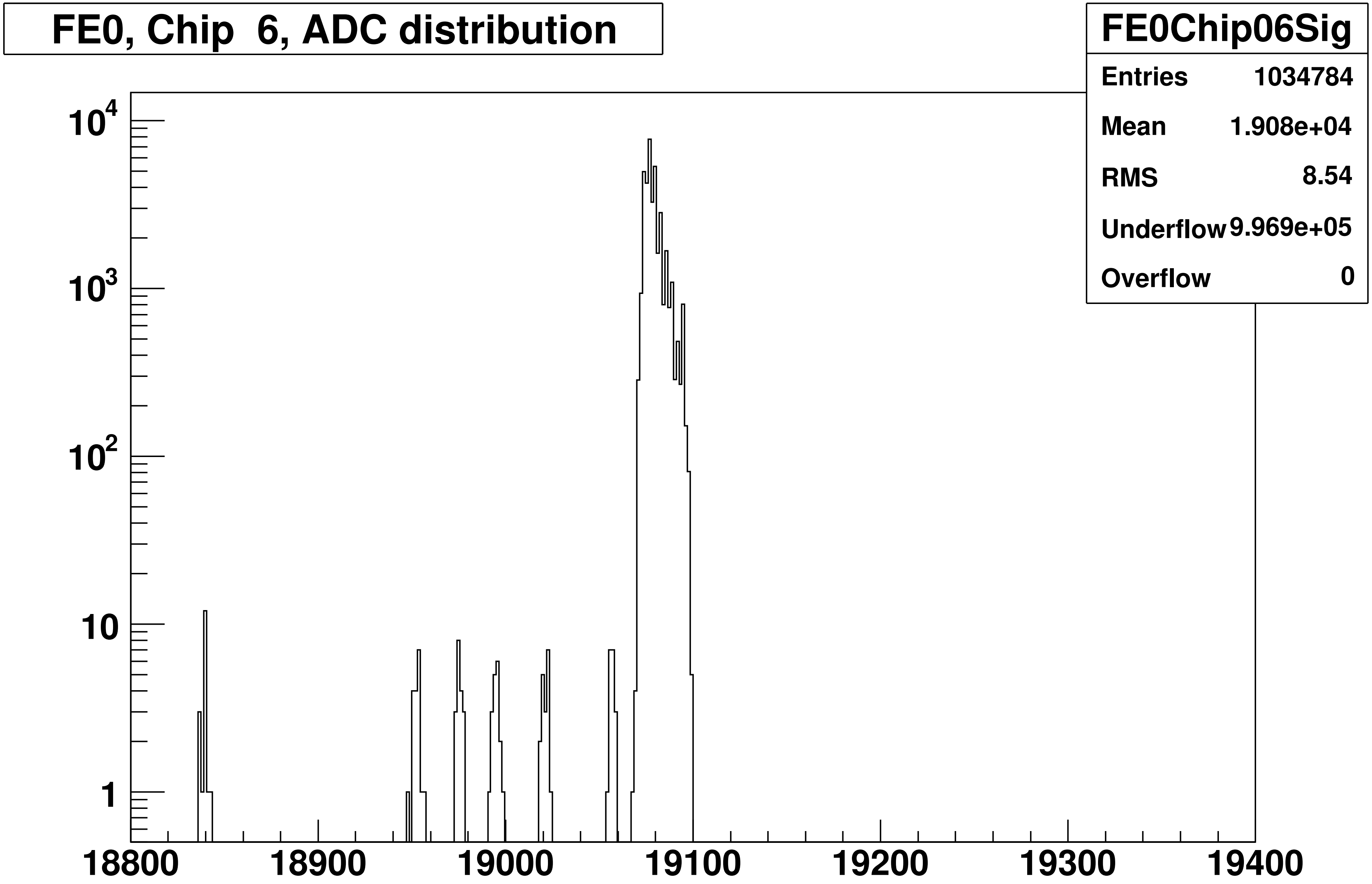
60
80
100


FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 1034784
Mean 1.908e+04
RMS 8.54
Underflow 9.969e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
19100
19200
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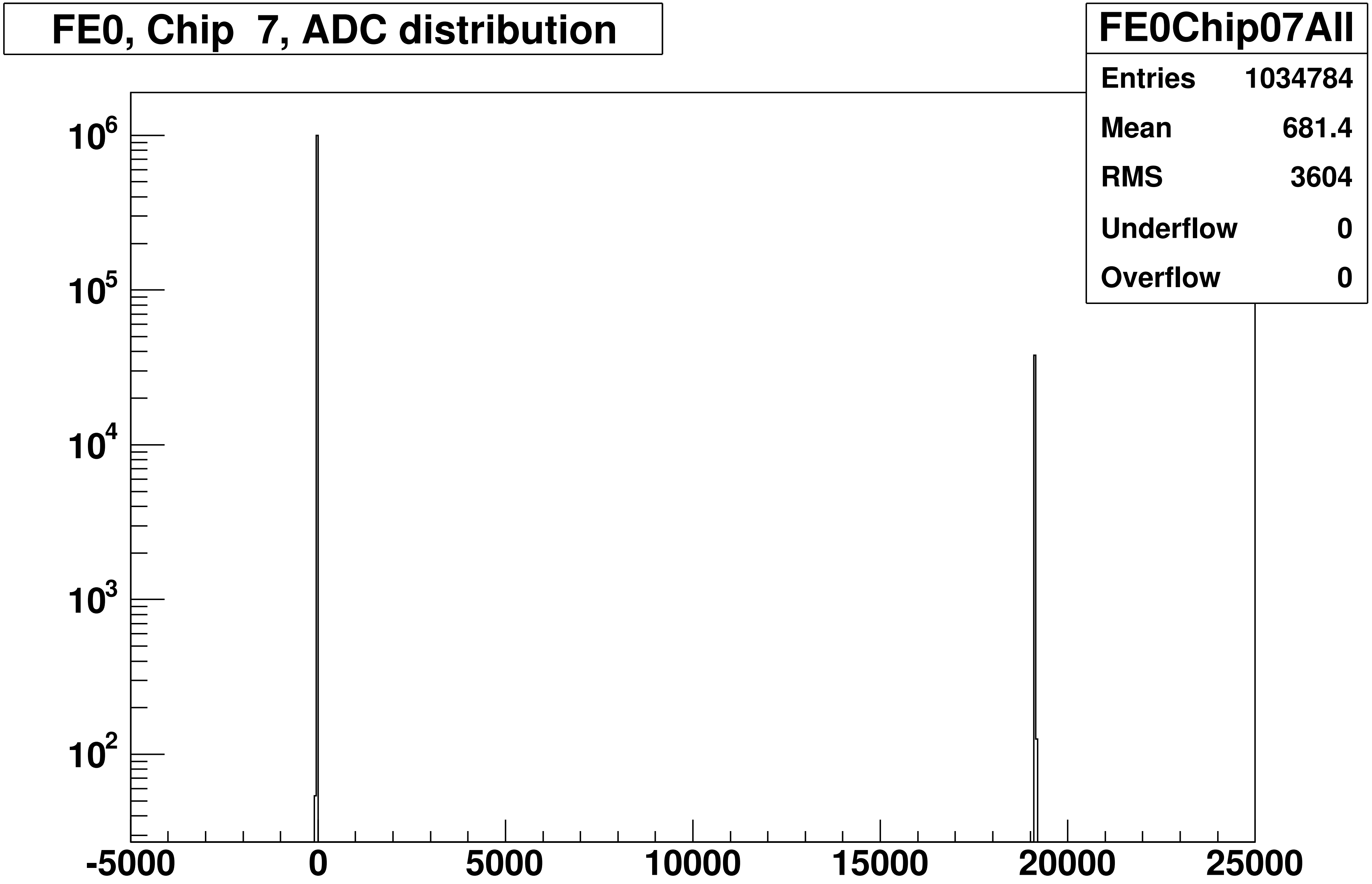
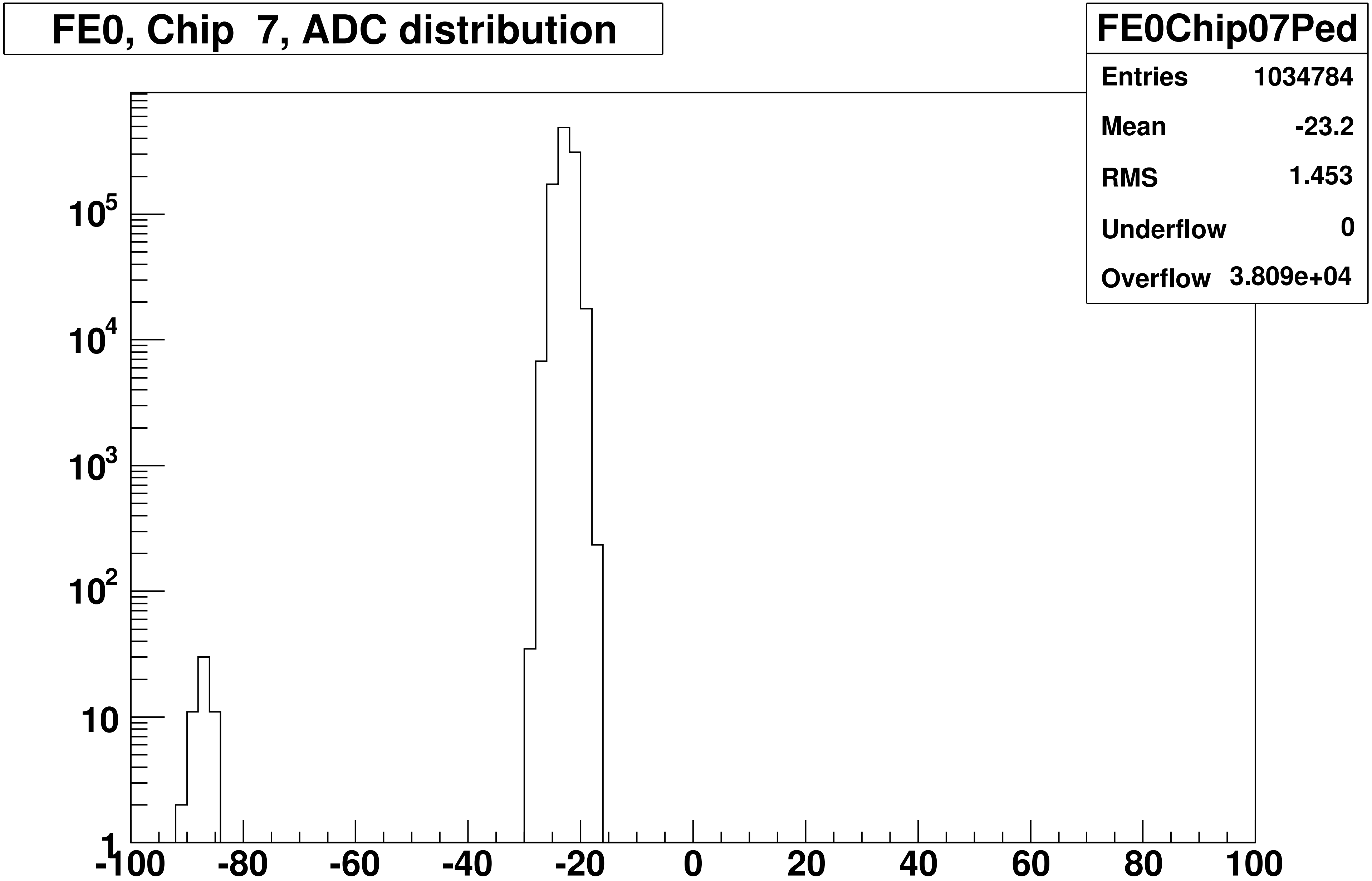
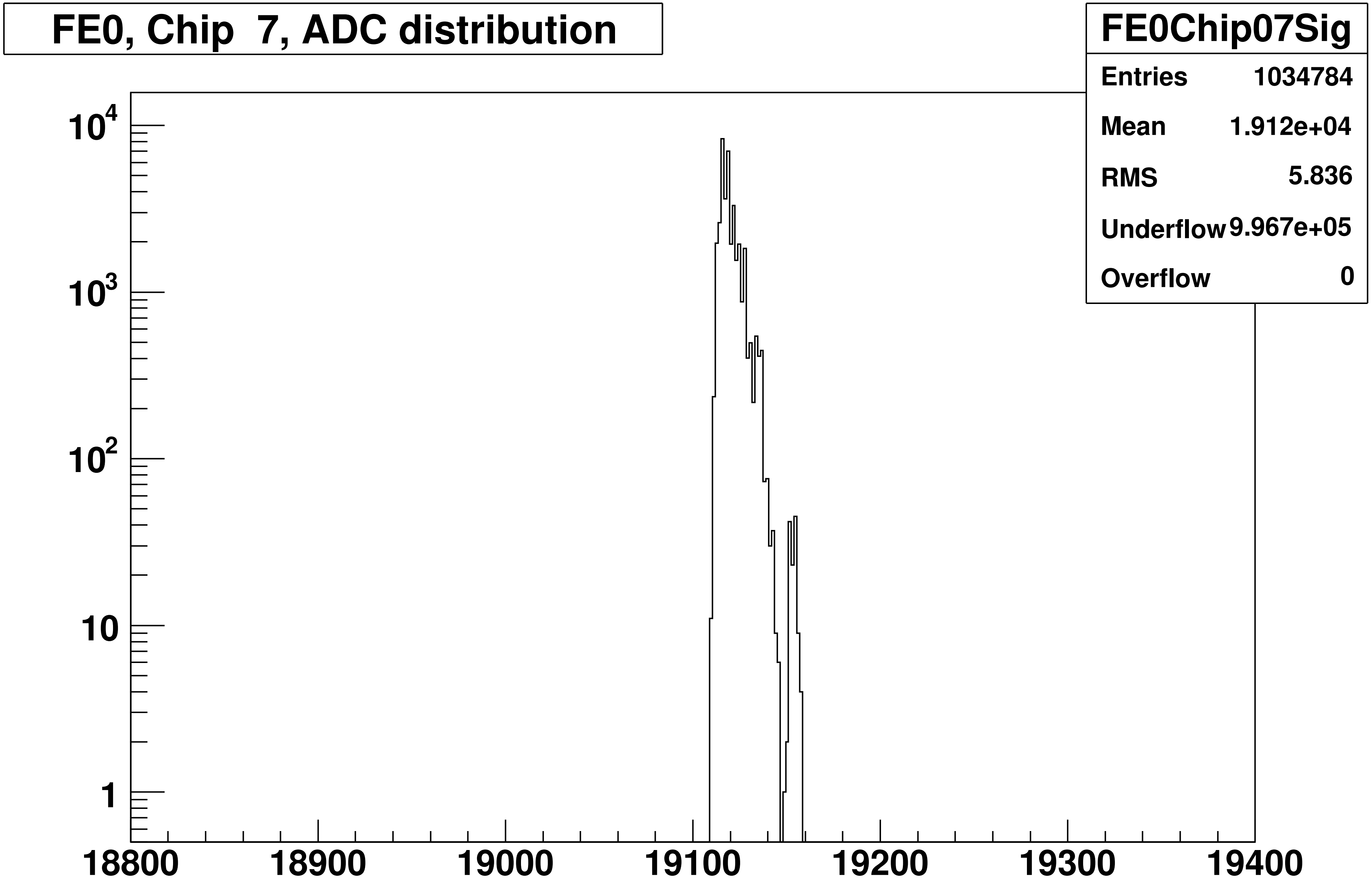
19400



FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 1034784
Mean 681.4
RMS 3604
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 1034784
Mean -23.2
RMS 1.453
Underflow 0
Overflow 3.809e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 1034784
Mean 1.912e+04
RMS 5.836
Underflow 9.967e+05
Overflow 0
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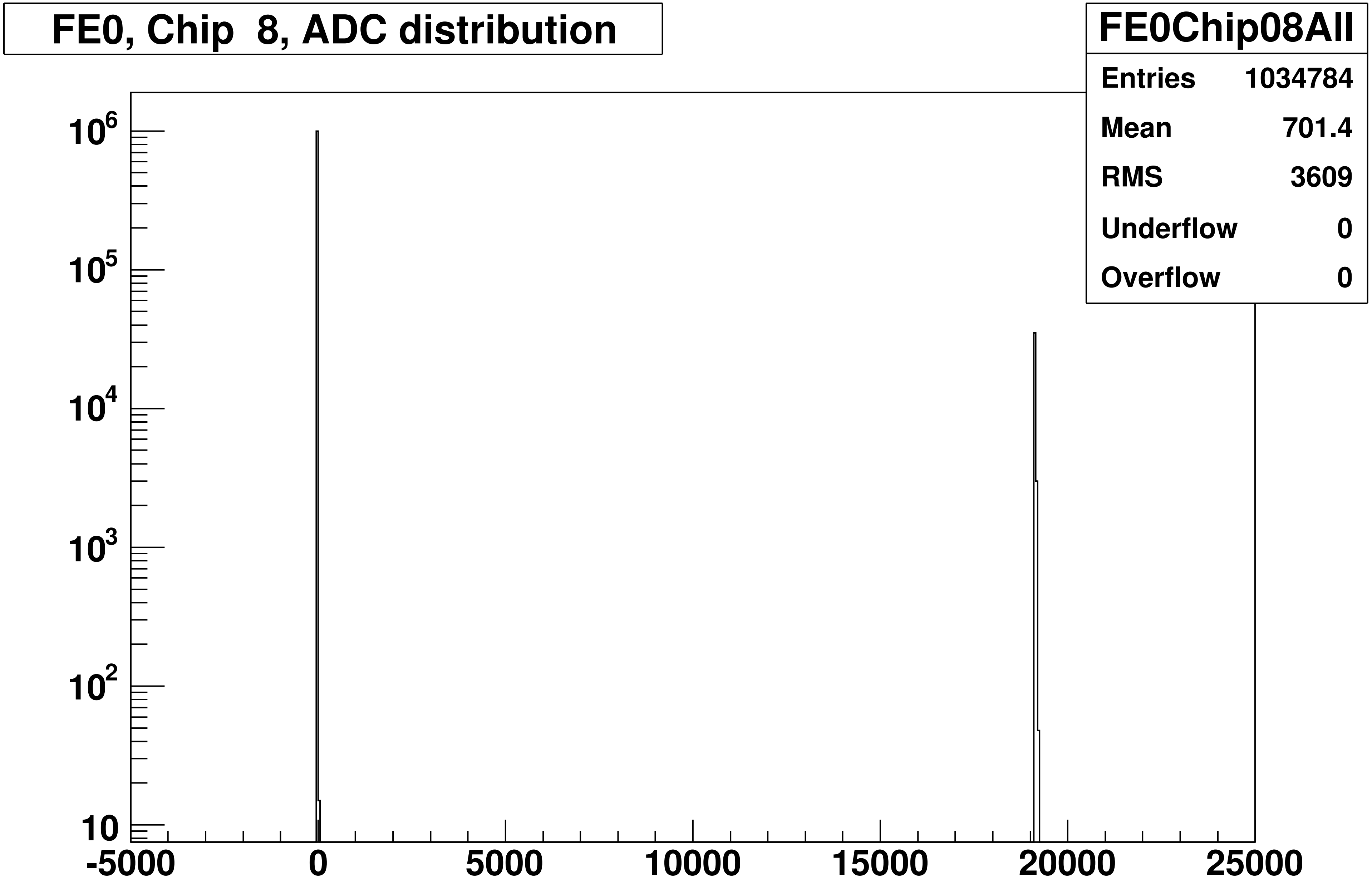
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 1034784
Mean 701.4
RMS 3609
Underflow 0
Overflow 0
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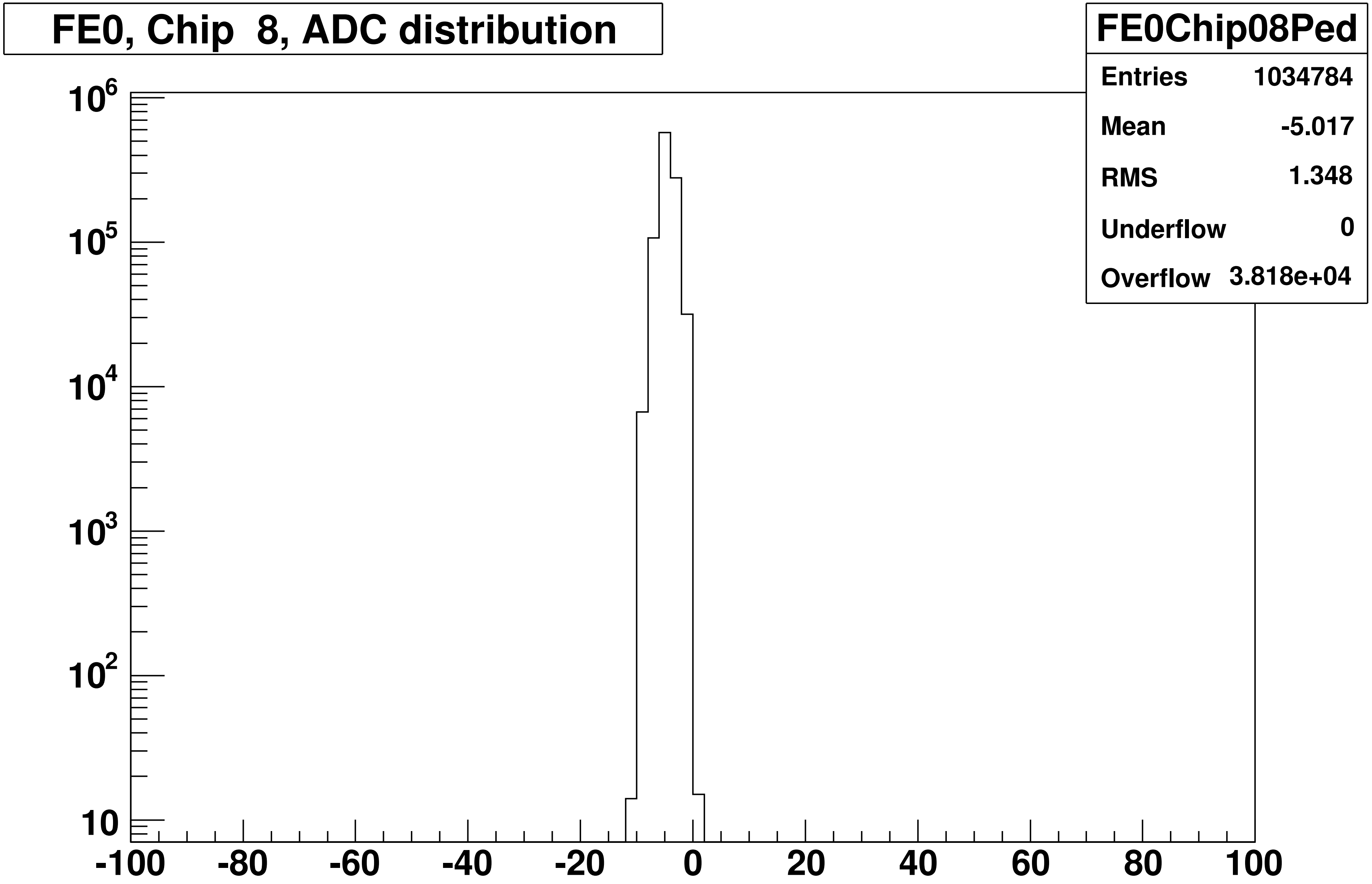
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 1034784
Mean -5.017
RMS 1.348
Underflow 0
Overflow 3.818e+04
10^6
10^5
10^4
10^3
10^2
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-40
-20
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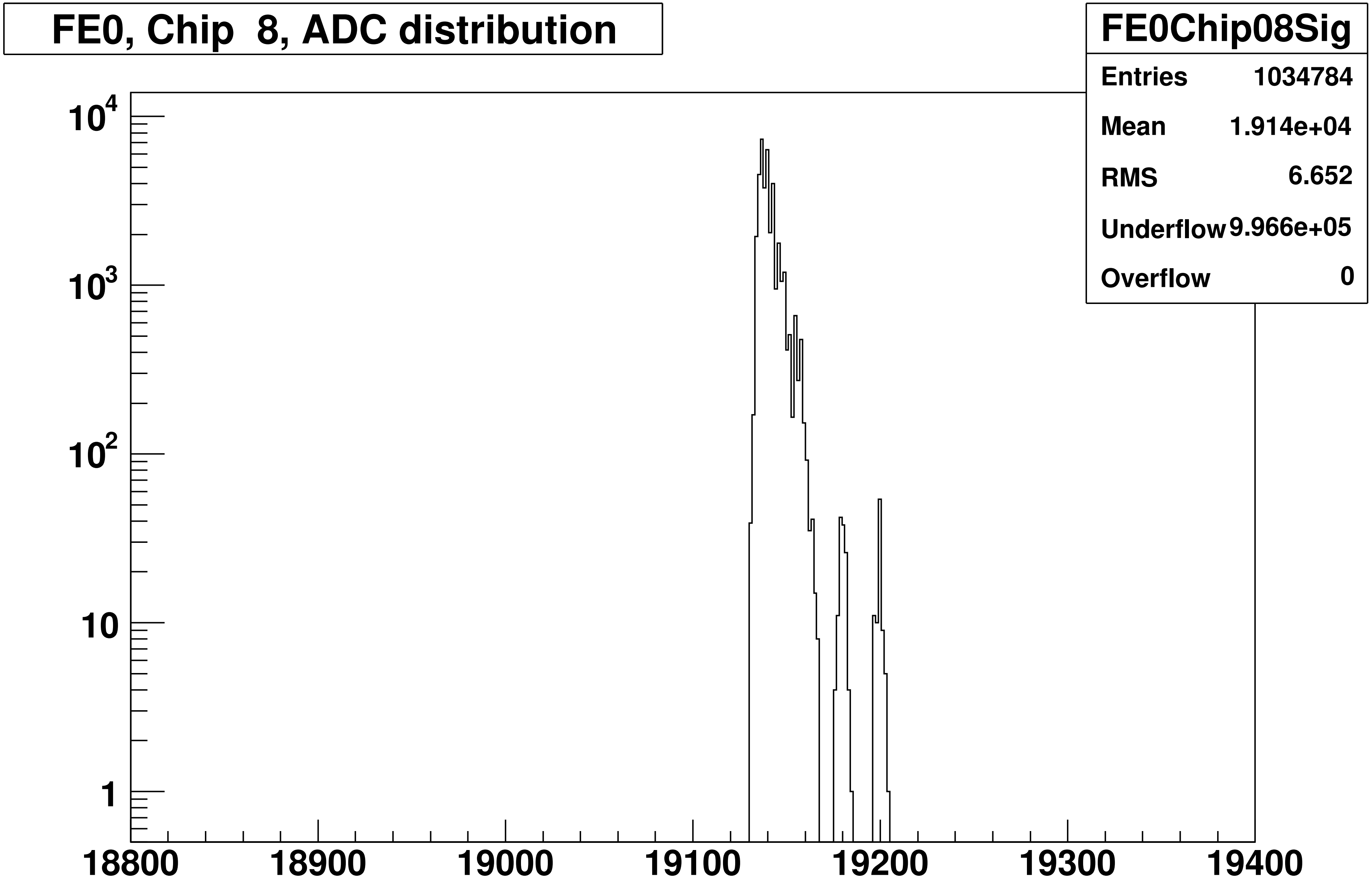
80
100


FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 1034784
Mean 1.914e+04
RMS 6.652
Underflow 9.966e+05
Overflow 0
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10^3
10^2
10
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18800
18900
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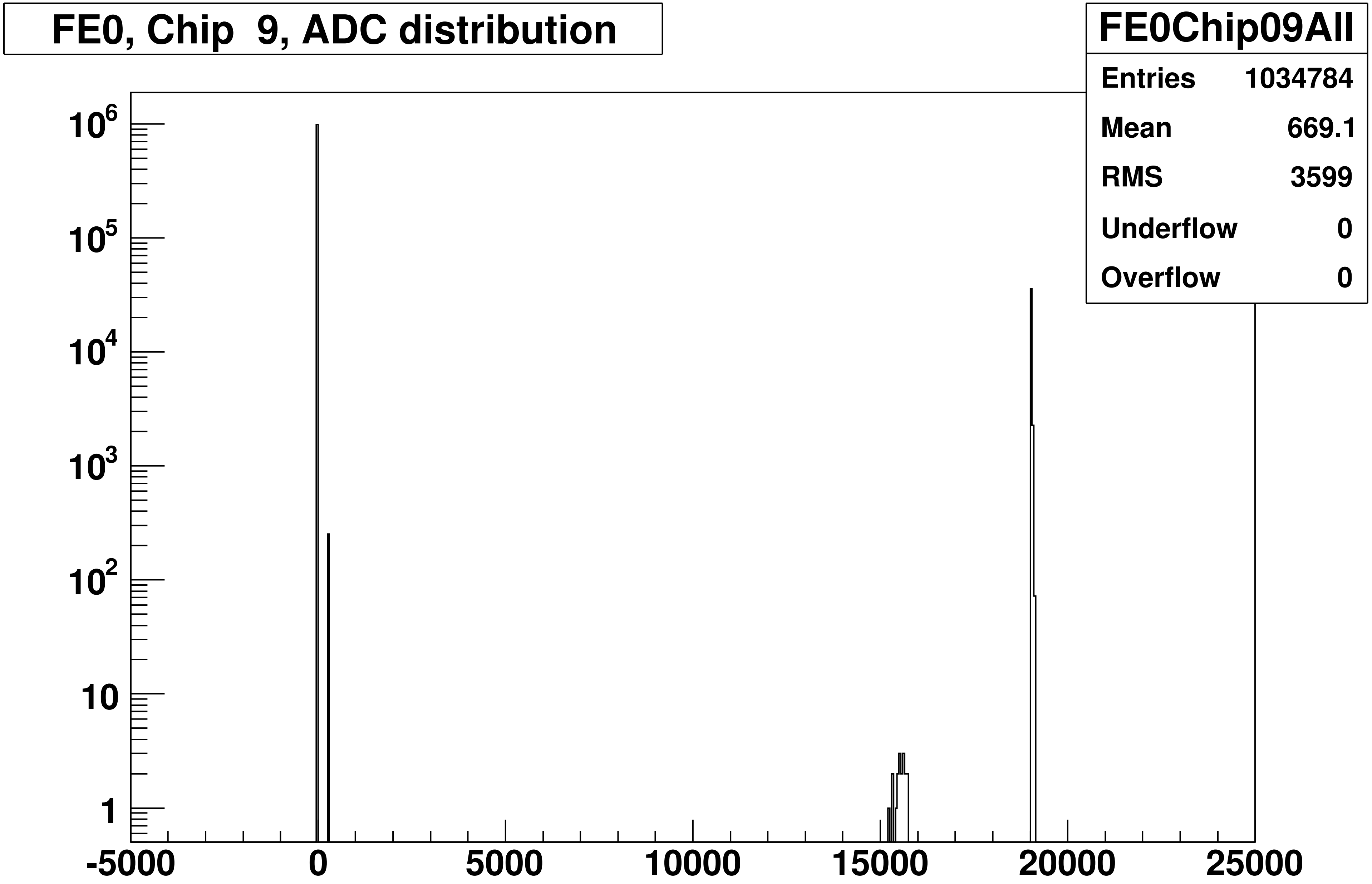
FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 1034784
Mean 669.1
RMS 3599
Underflow 0
Overflow 0
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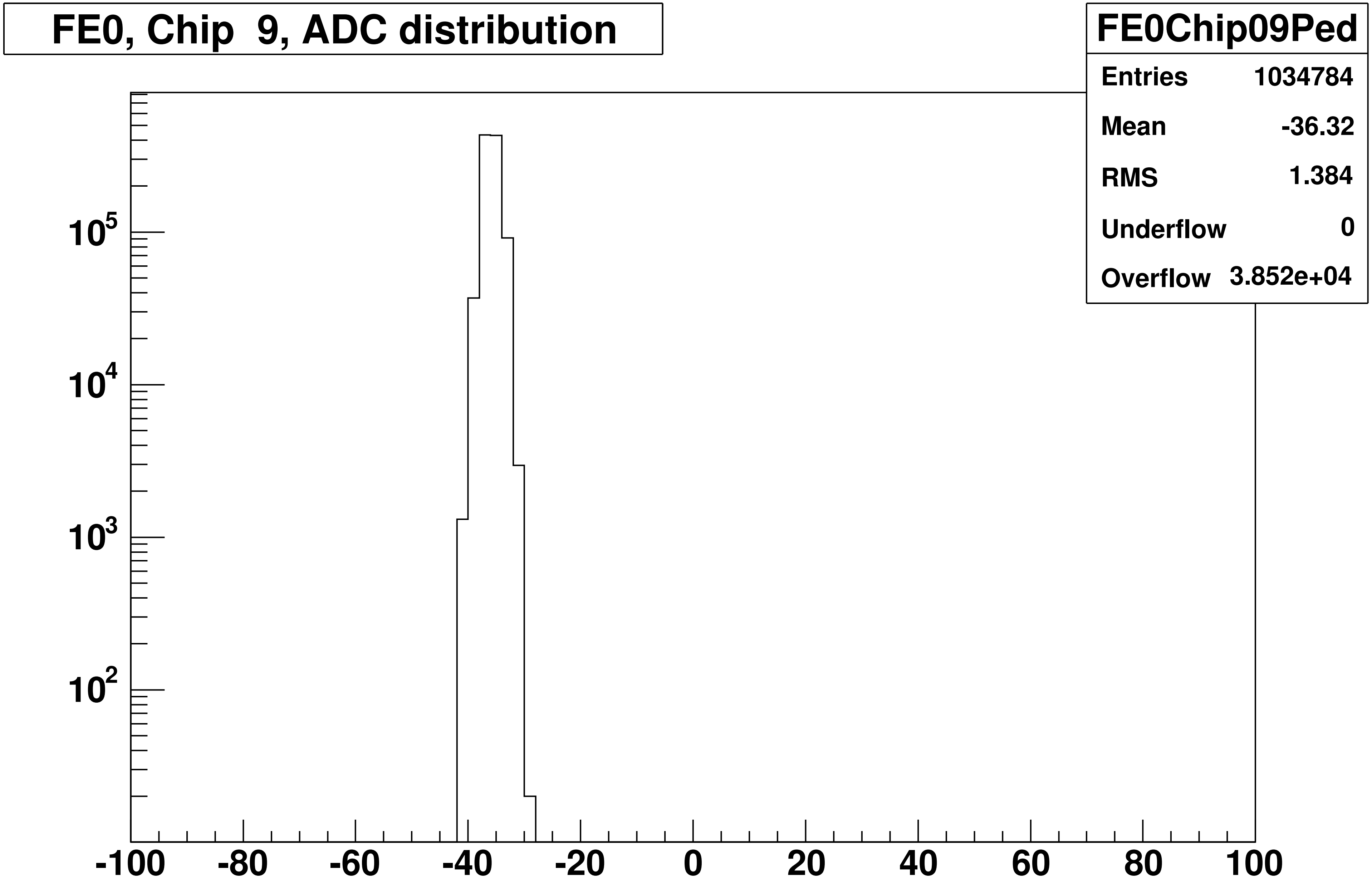
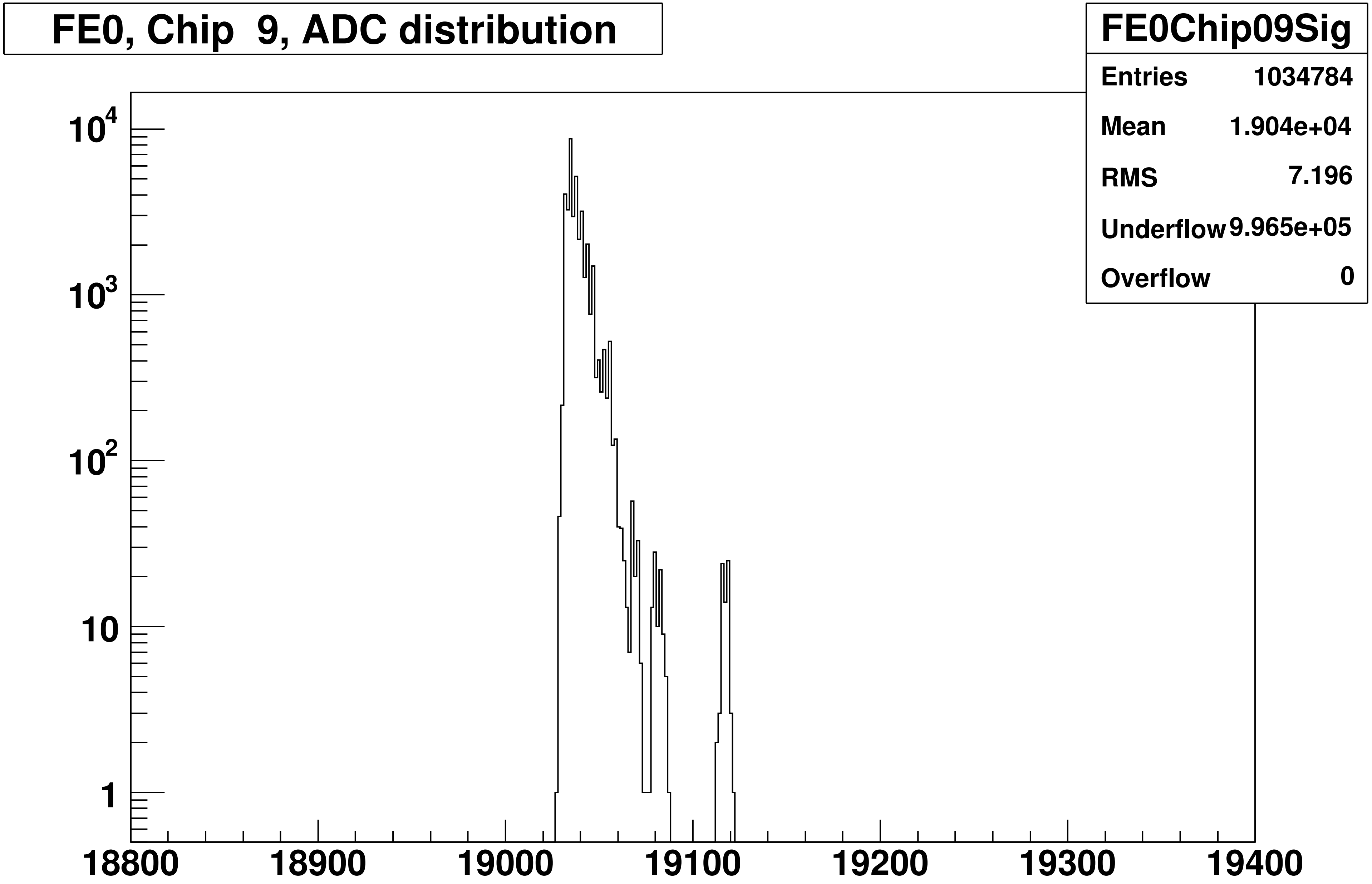
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 1034784
Mean -36.32
RMS 1.384
Underflow 0
Overflow 3.852e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 1034784
Mean 1.904e+04
RMS 7.196
Underflow 9.965e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
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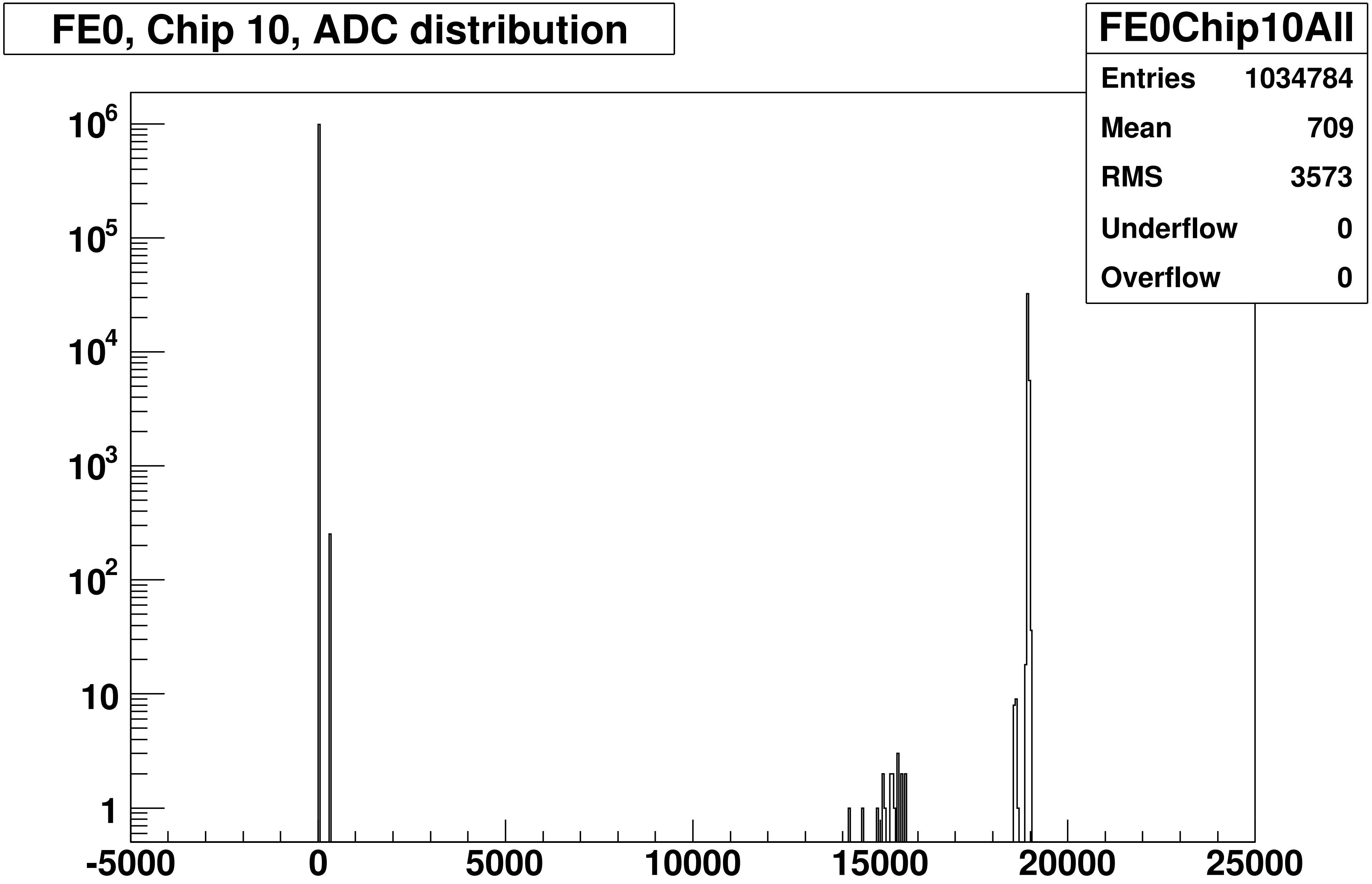
19400



FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 1034784
Mean 709
RMS 3573
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
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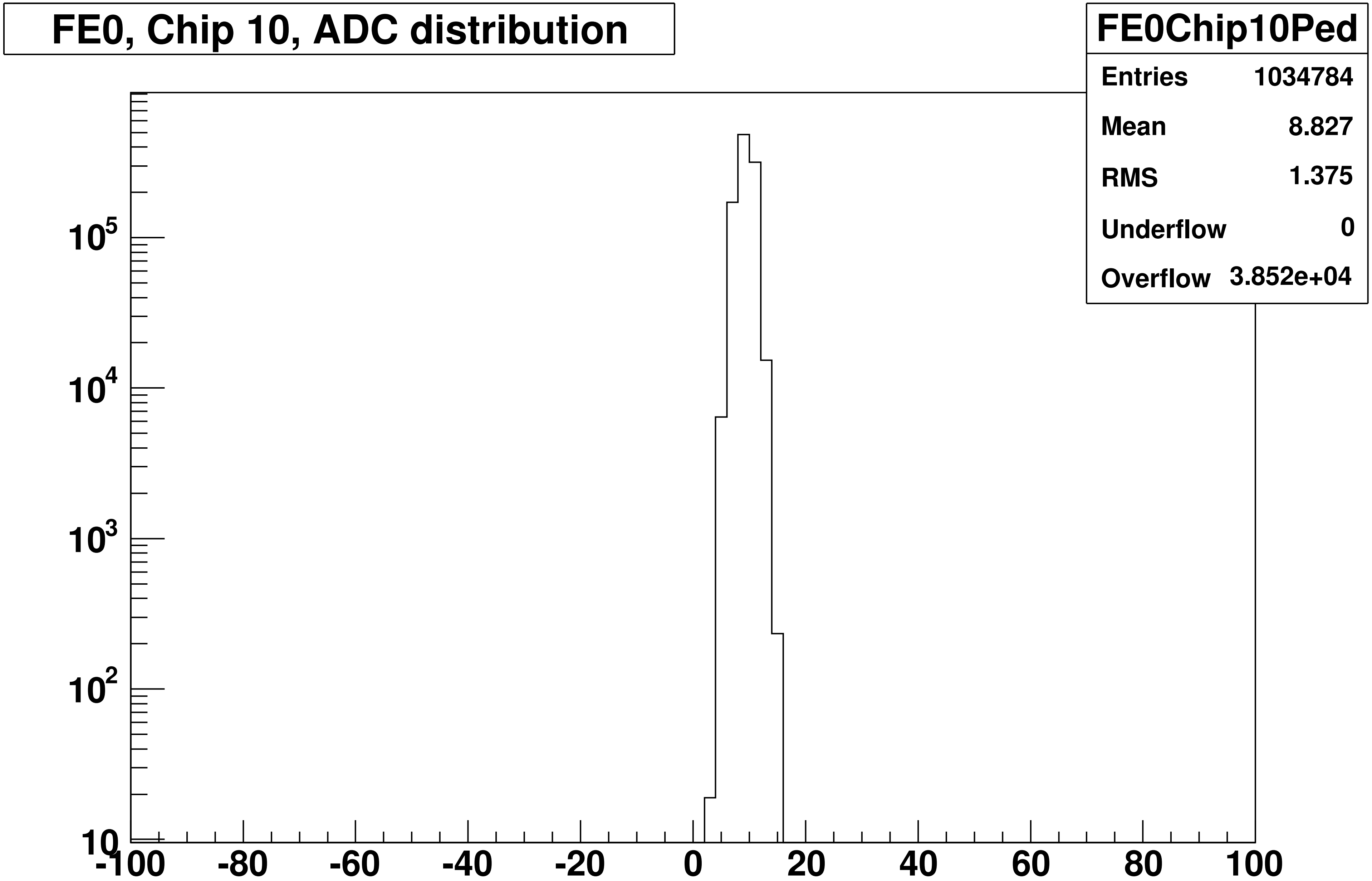
10000
15000
20000
25000


FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 1034784
Mean 8.827
RMS 1.375
Underflow 0
Overflow 3.852e+04
10^5
10^4
10^3
10^2
10
-100
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-20
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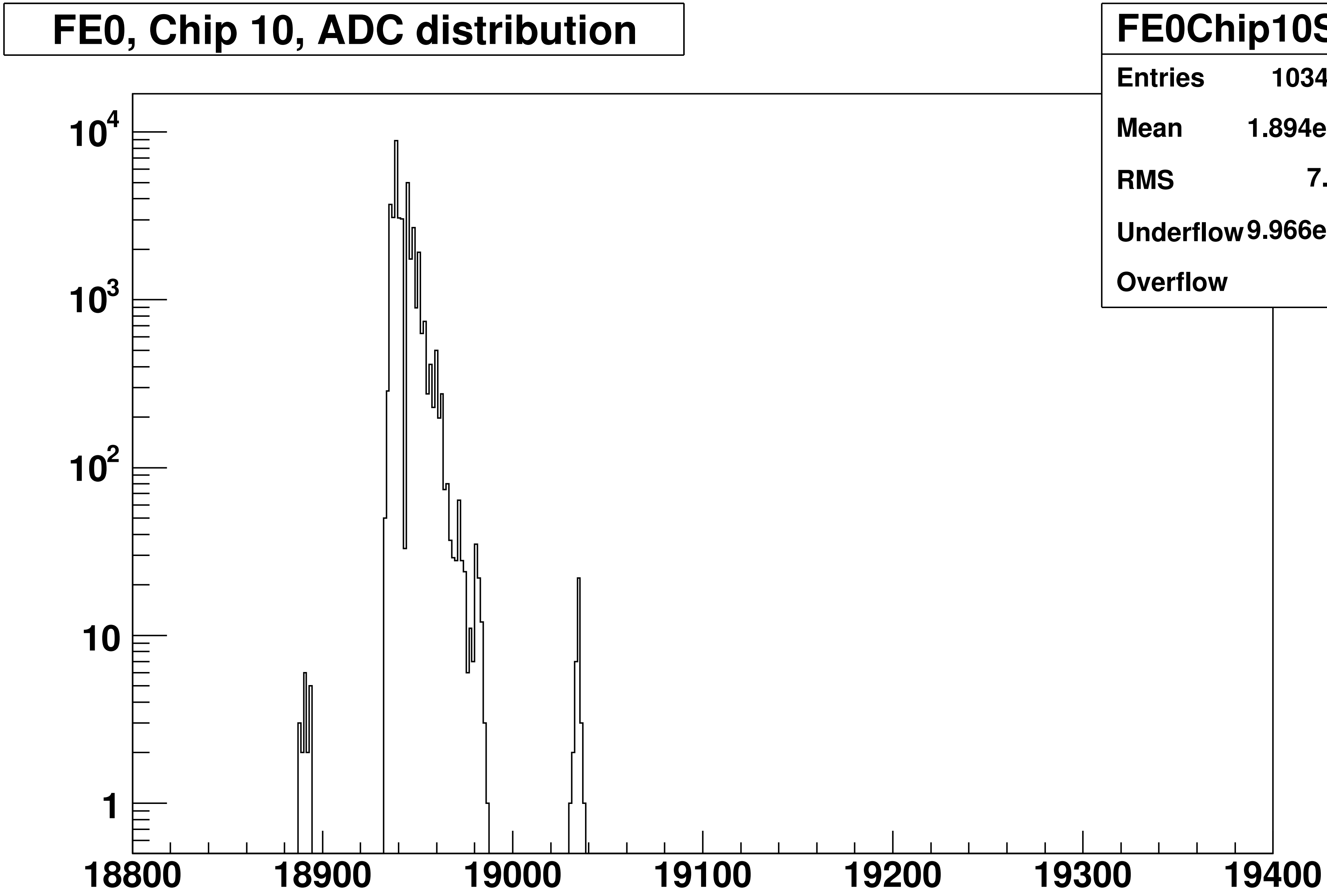
40
60
80
100


FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 1034784
Mean 1.894e+04
RMS 7.429
Underflow 9.966e+05
Overflow 0
10^4
10^3
10^2
10
1
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18900
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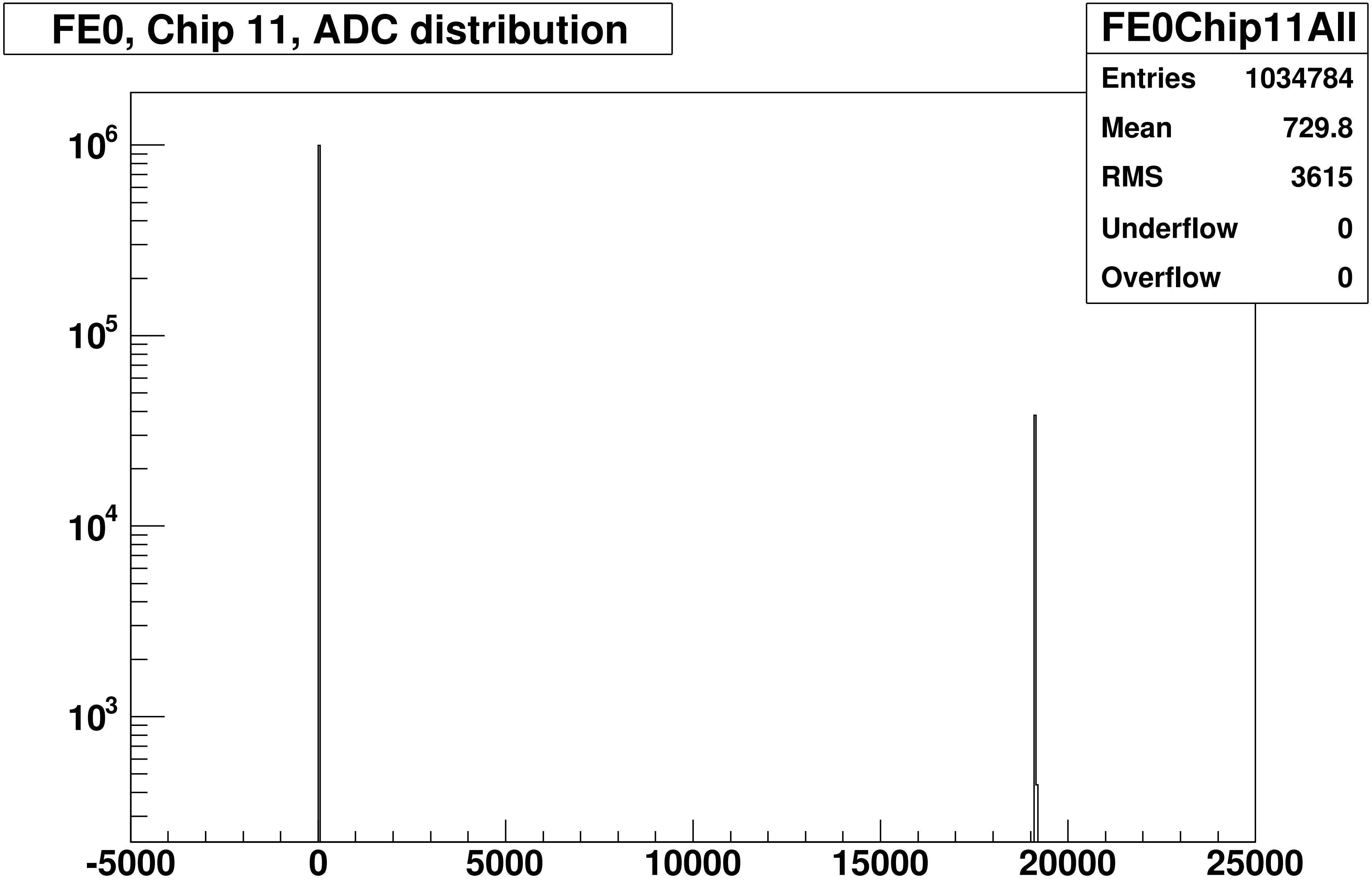
19300
19400


FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 1034784
Mean 729.8
RMS 3615
Underflow 0
Overflow 0
10^6
10^5
10^4
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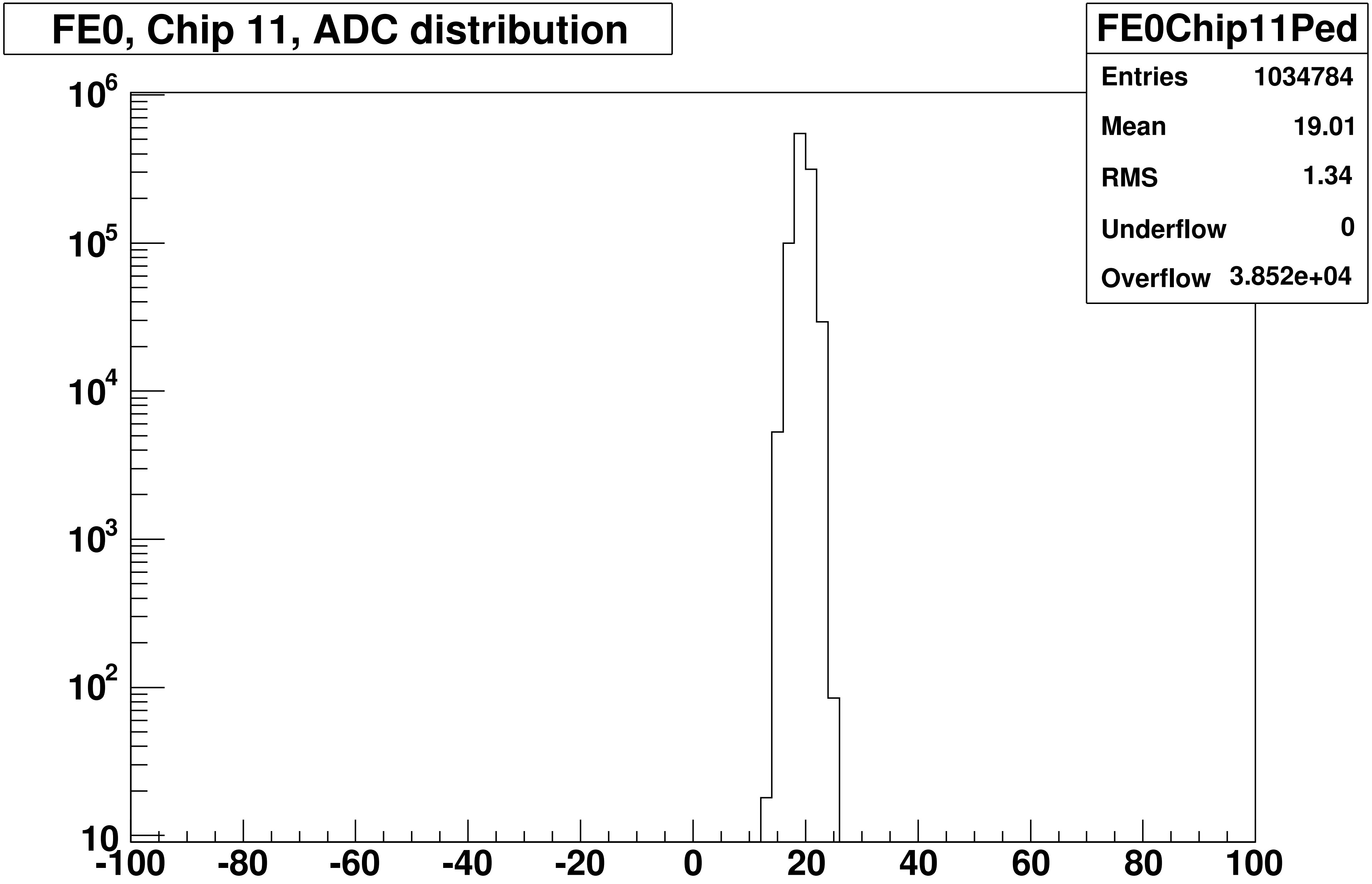
10000
15000
20000
25000



FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 1034784
Mean 19.01
RMS 1.34
Underflow 0
Overflow 3.852e+04
10
10^2
10^3
10^4
10^5
10^6
-100
-80
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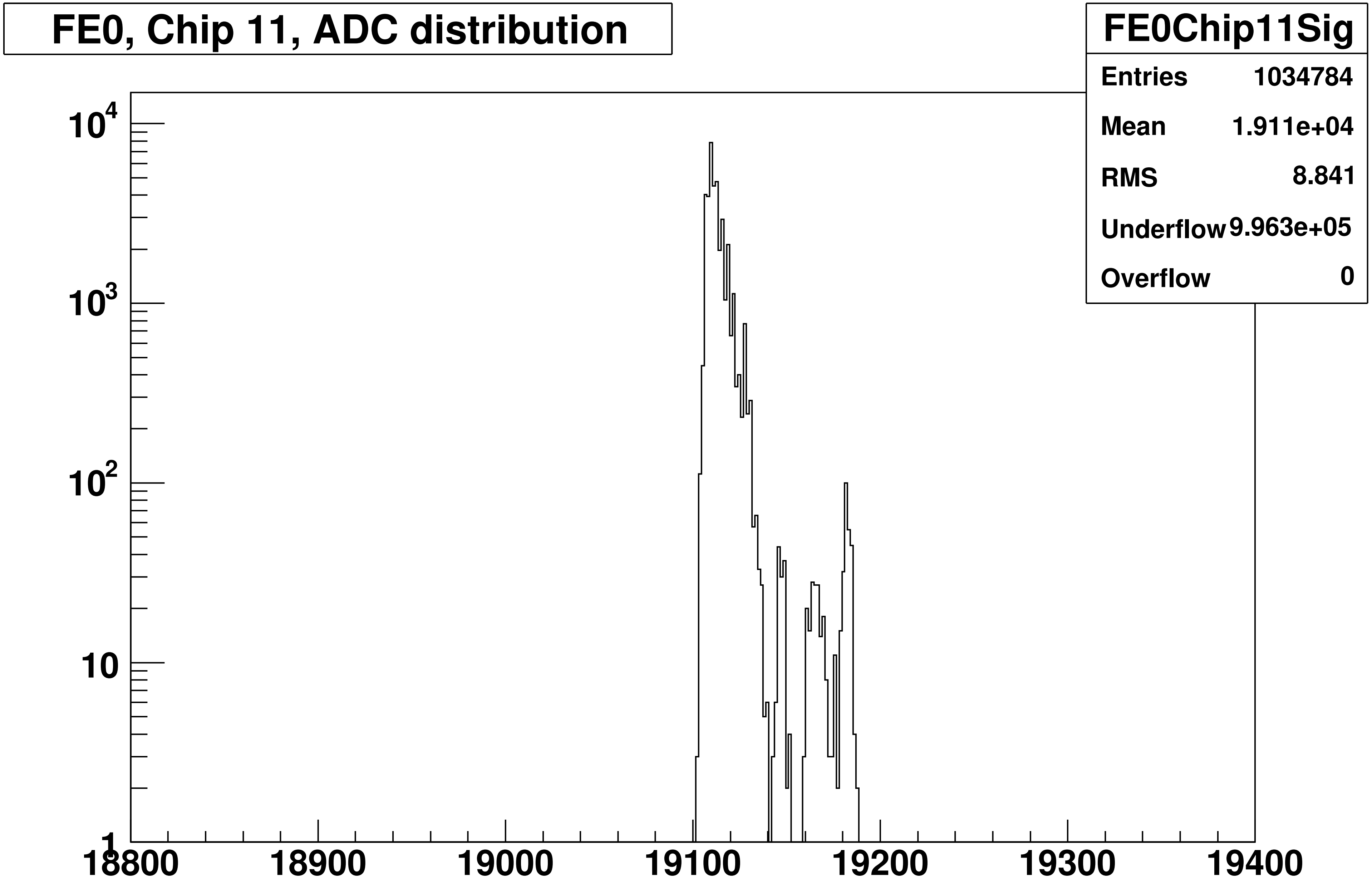
-40
-20
0
20
40
60
80
100


FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 1034784
Mean 1.911e+04
RMS 8.841
Underflow 9.963e+05
Overflow 0
10^4
10^3
10^2
10
1
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18900
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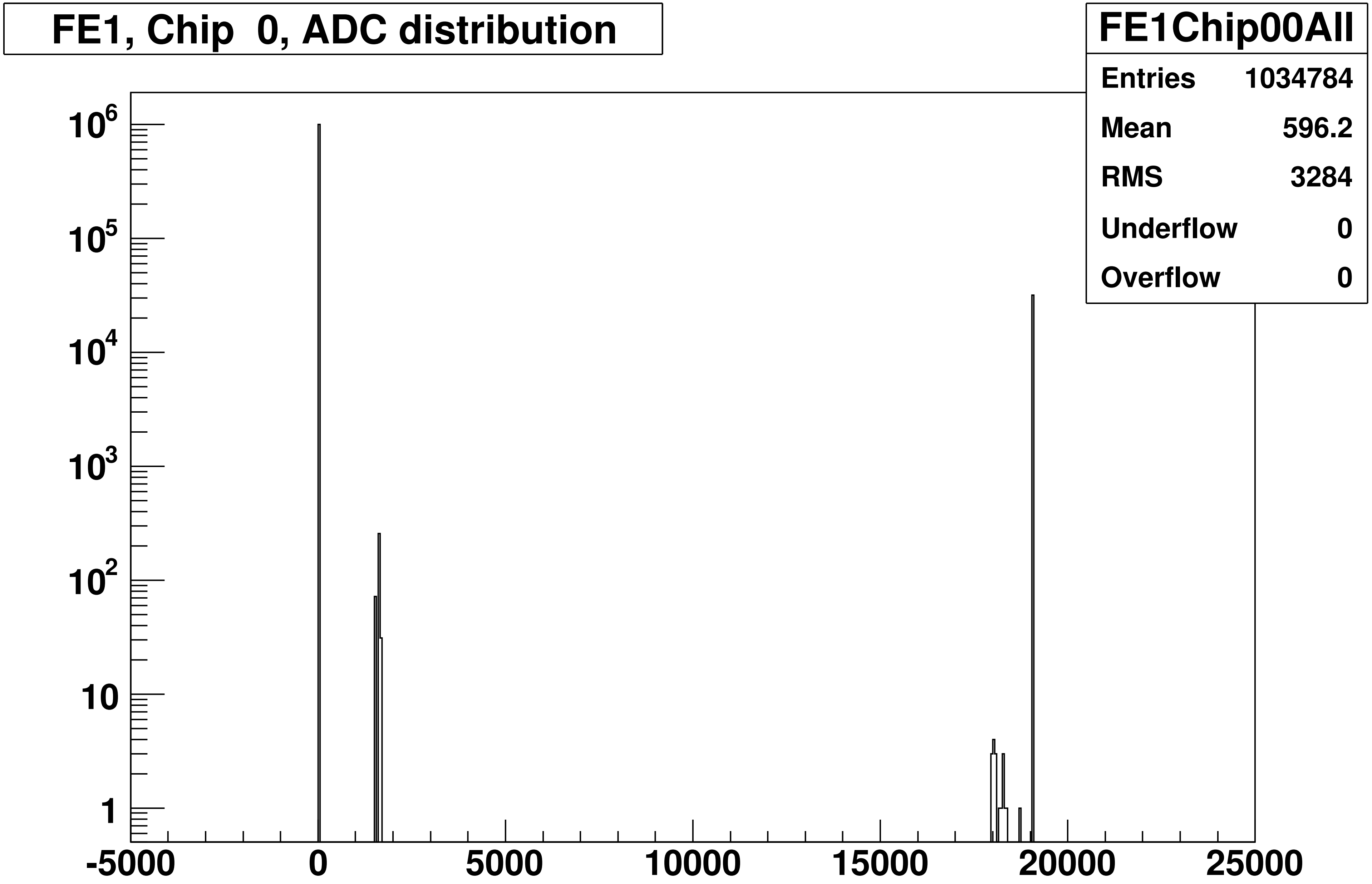
19400



FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 1034784
Mean 596.2
RMS 3284
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
5000
10000
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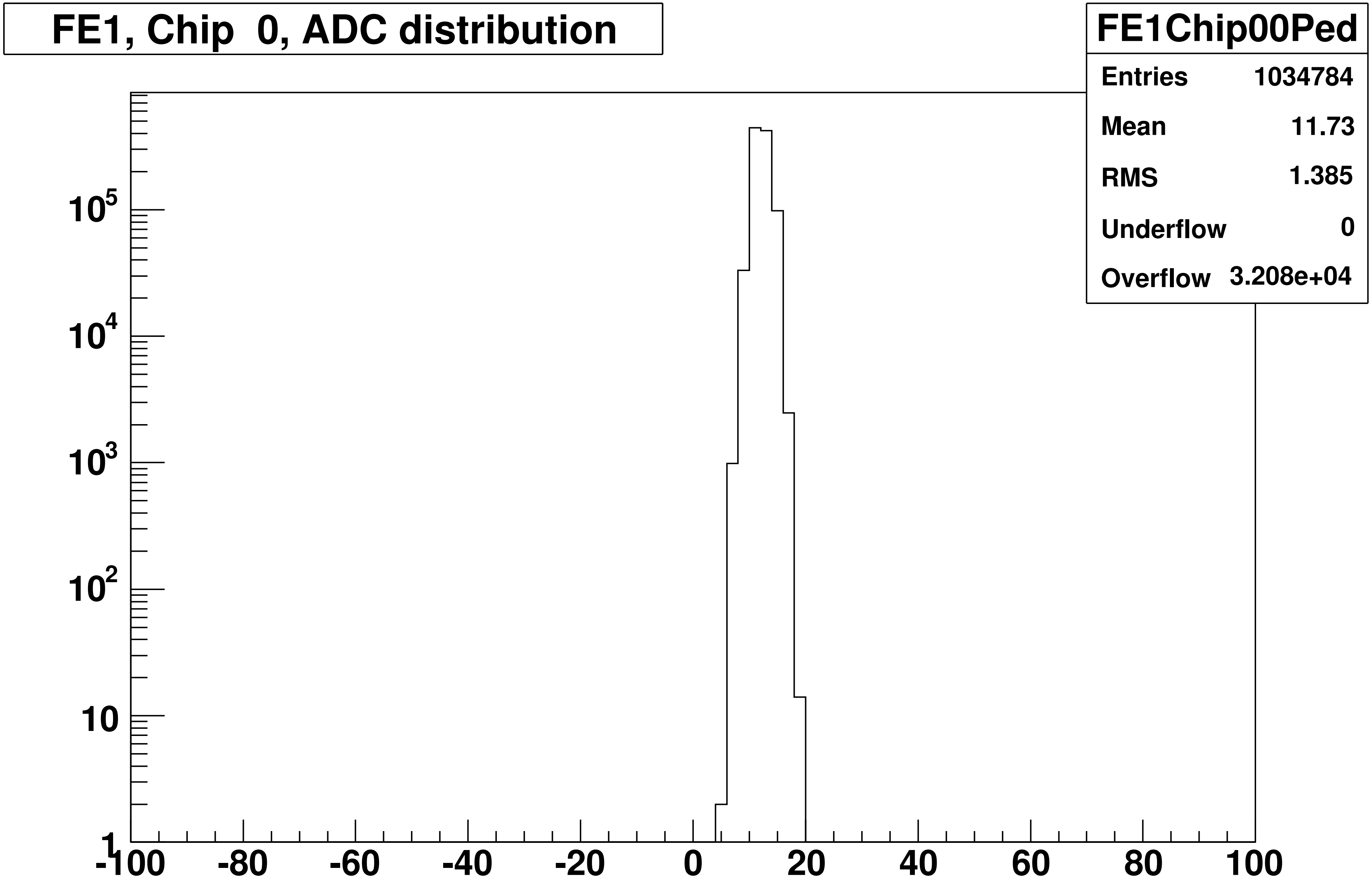
20000
25000


FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 1034784
Mean 11.73
RMS 1.385
Underflow 0
Overflow 3.208e+04
10^5
10^4
10^3
10^2
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-20
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40
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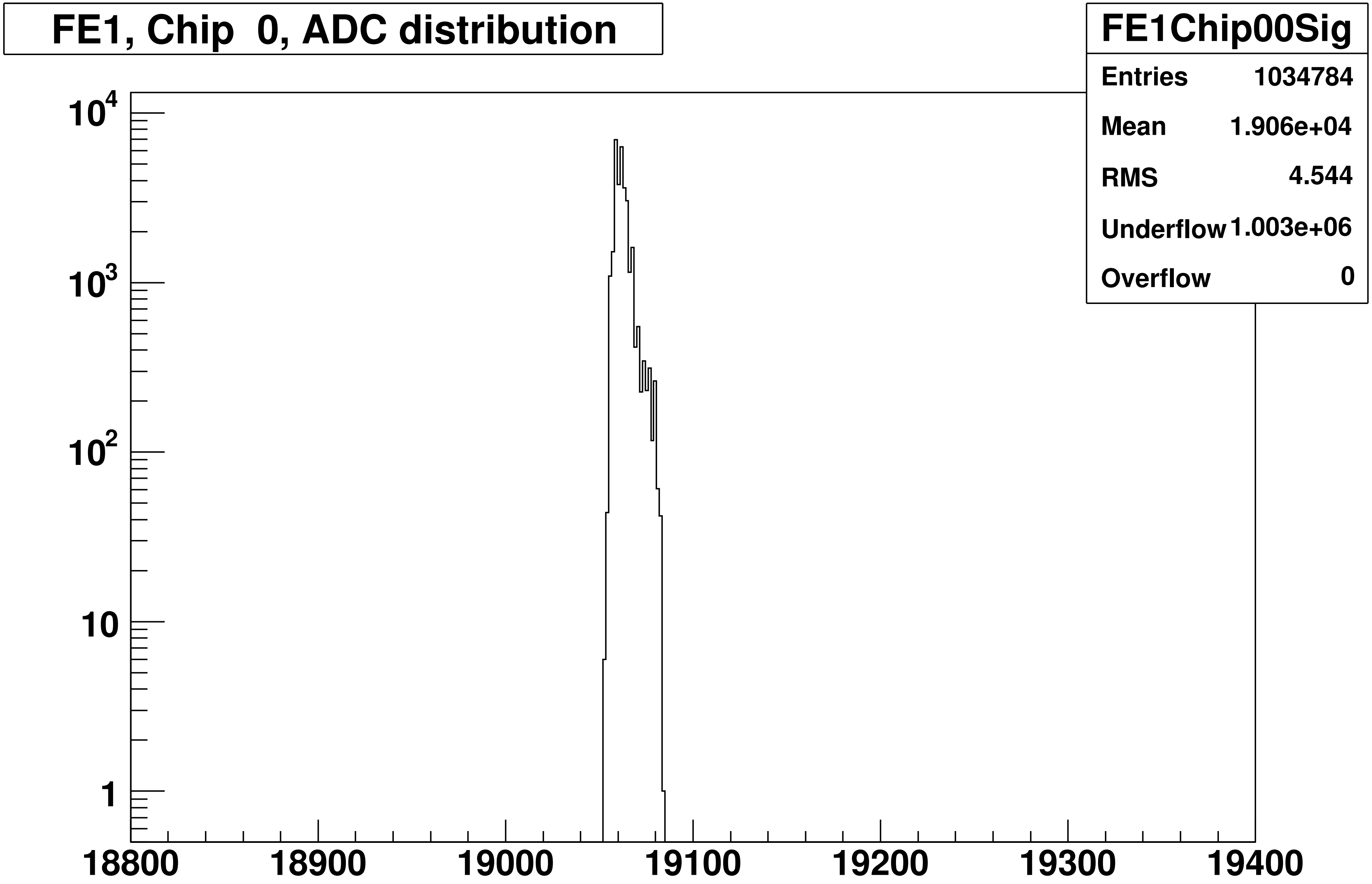
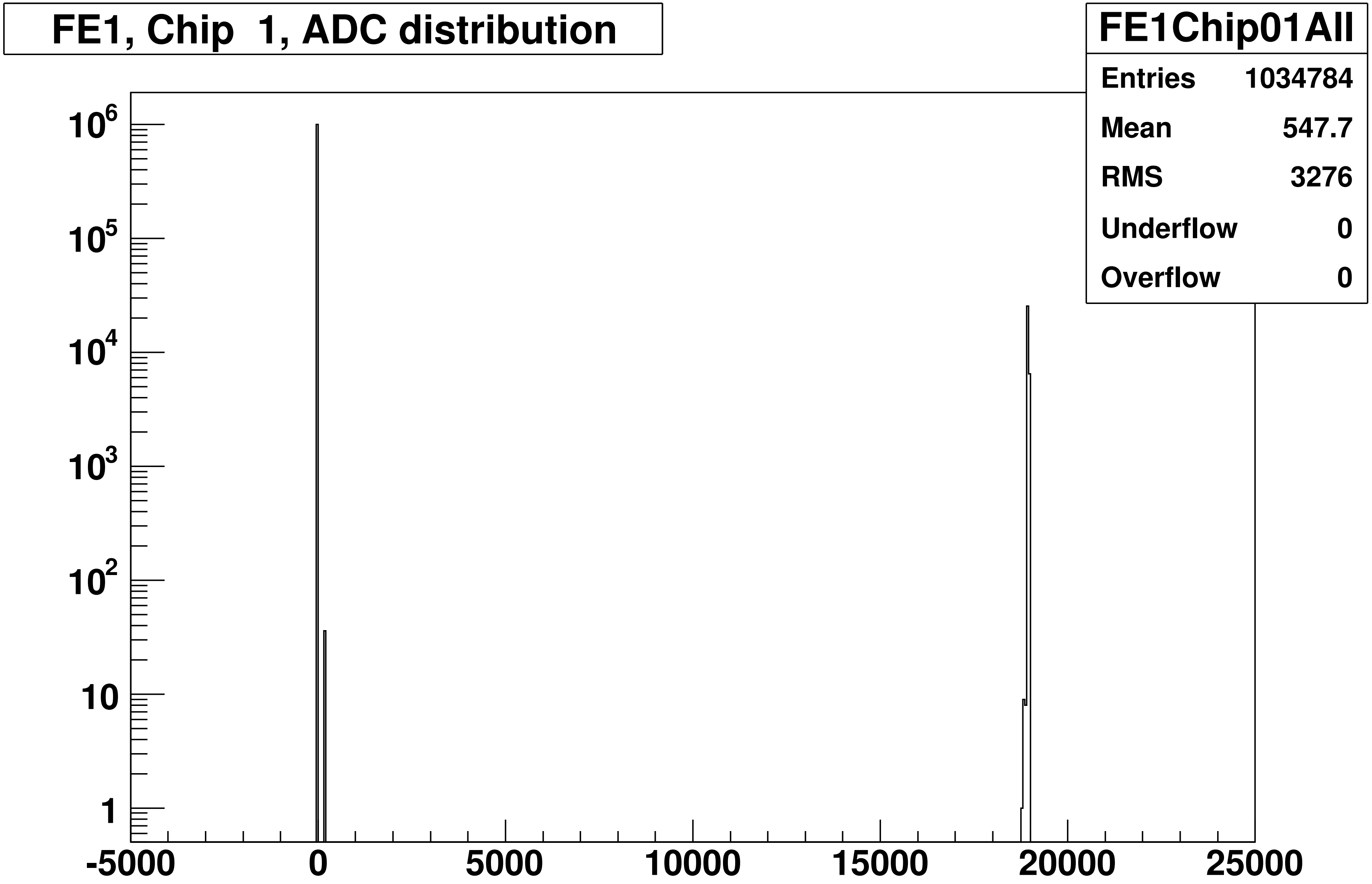
80
100



FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 1034784
Mean 1.906e+04
RMS 4.544
Underflow 1.003e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 1034784
Mean 547.7
RMS 3276
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
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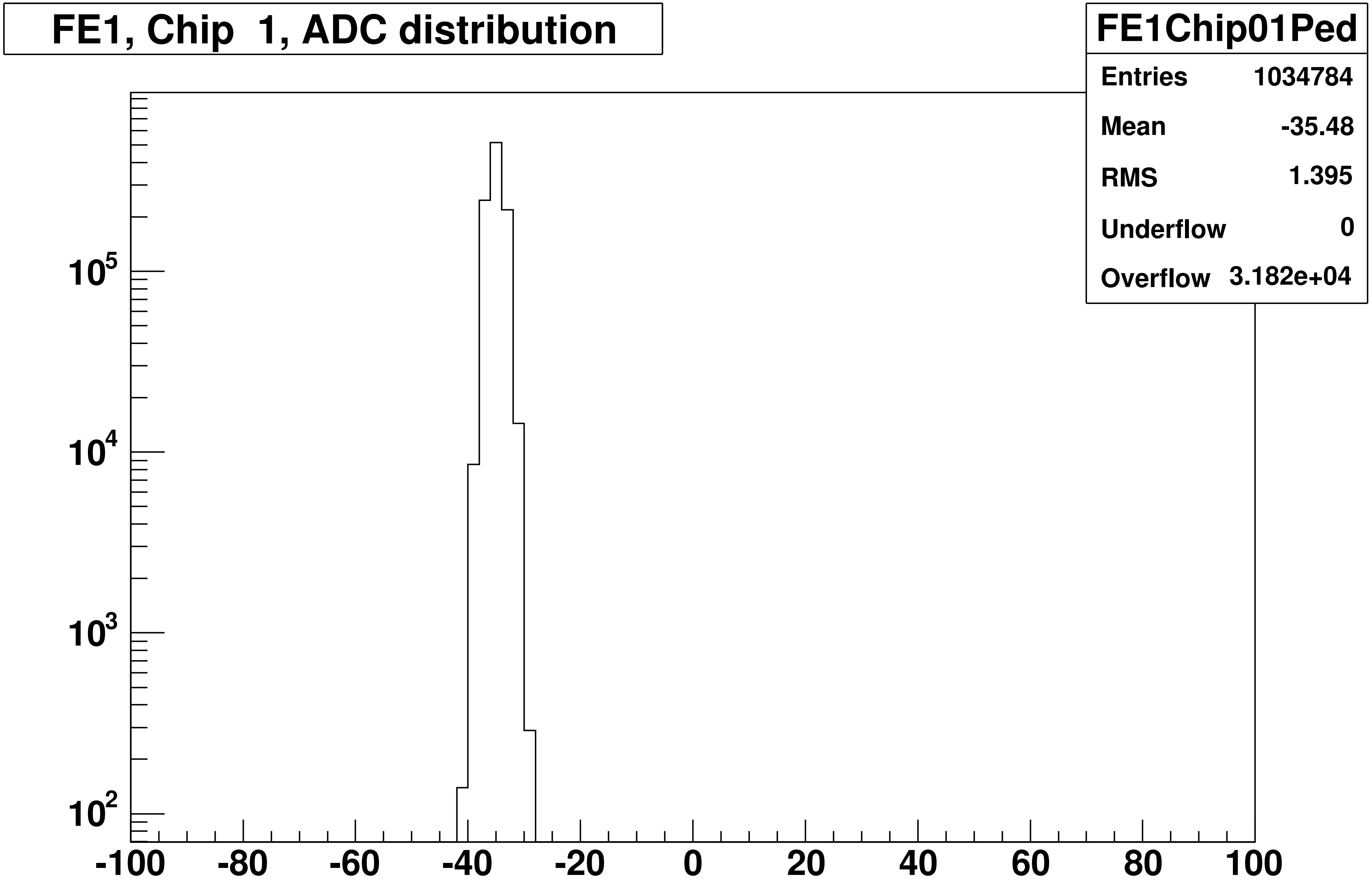
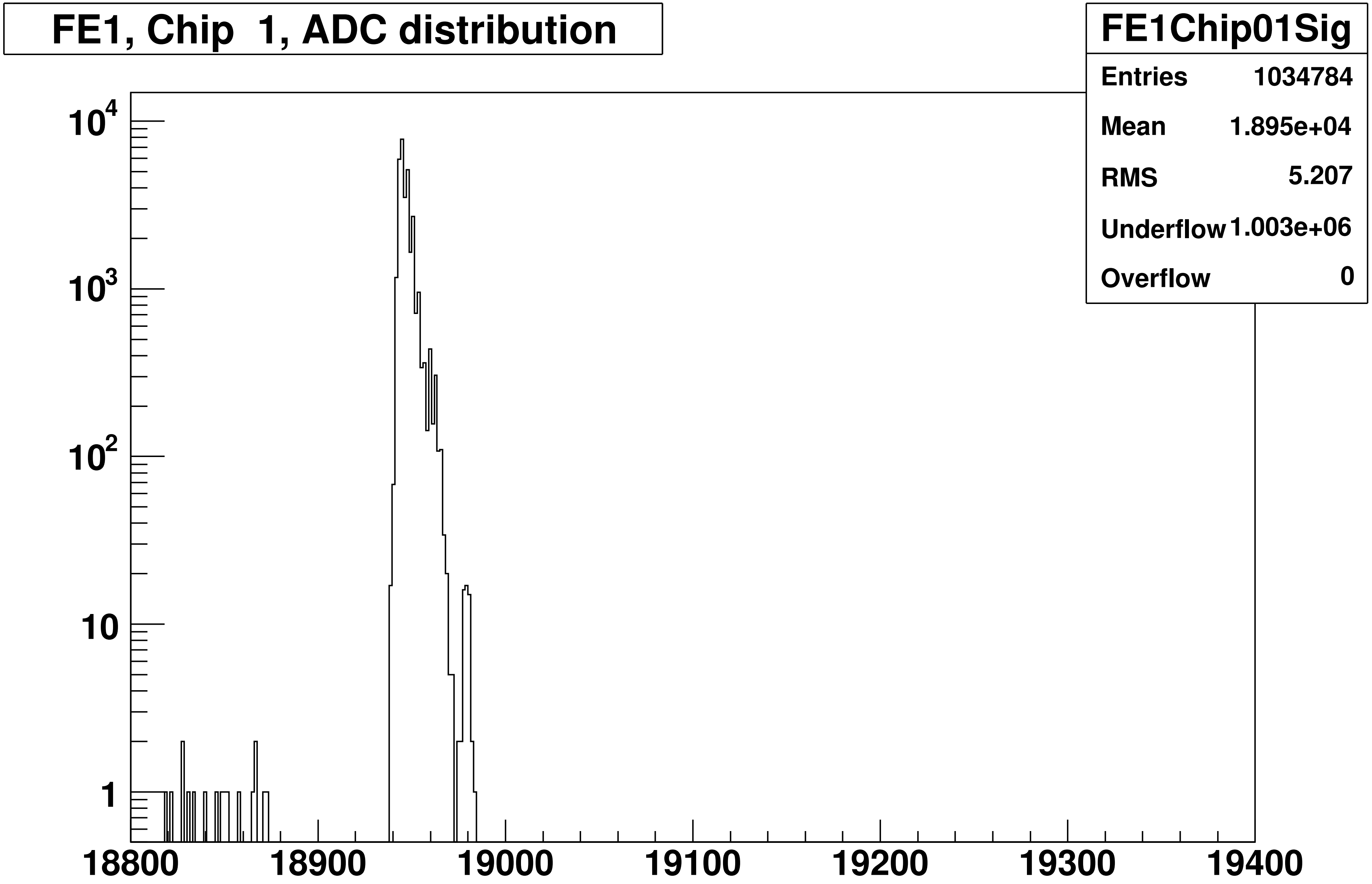
20000
25000



FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 1034784
Mean -35.48
RMS 1.395
Underflow 0
Overflow 3.182e+04
10^2
10^3
10^4
10^5
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 1034784
Mean 1.895e+04
RMS 5.207
Underflow 1.003e+06
Overflow 0
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10^3
10^2
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1
18800
18900
19000
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19200
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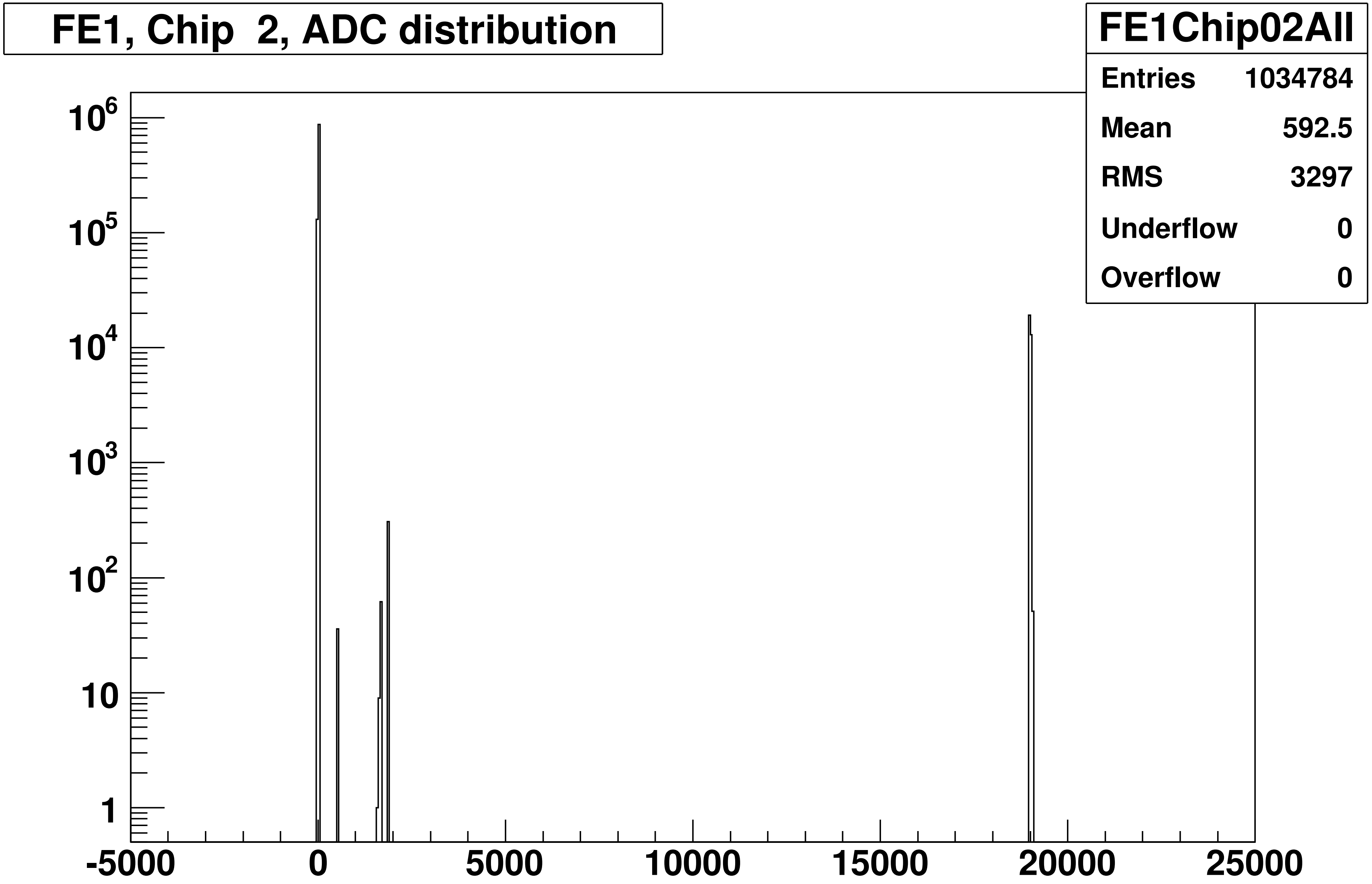
19400



FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 1034784
Mean 592.5
RMS 3297
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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-5000
0
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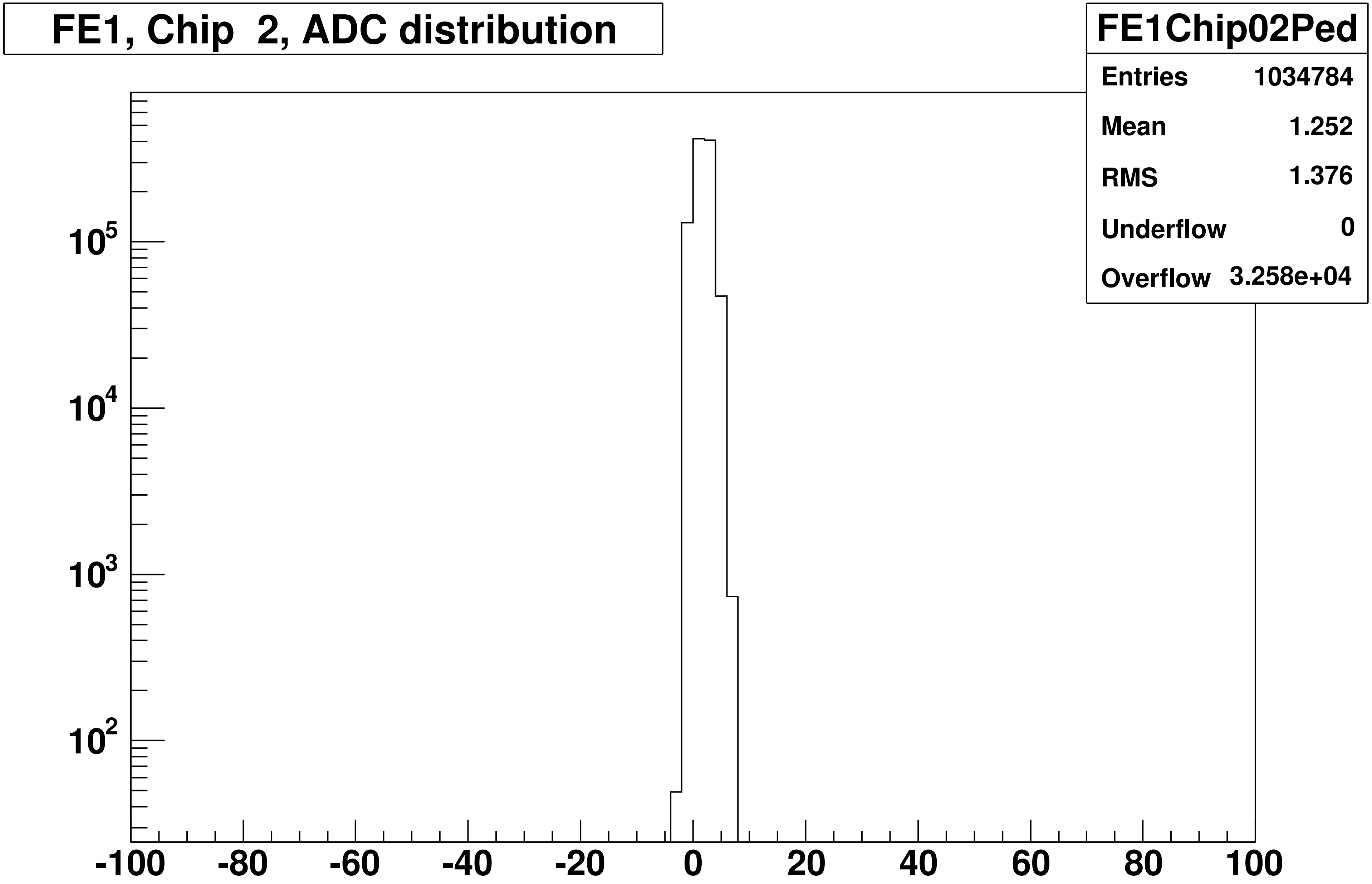
20000
25000


FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 1034784
Mean 1.252
RMS 1.376
Underflow 0
Overflow 3.258e+04
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10^3
10^2
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-40
-20
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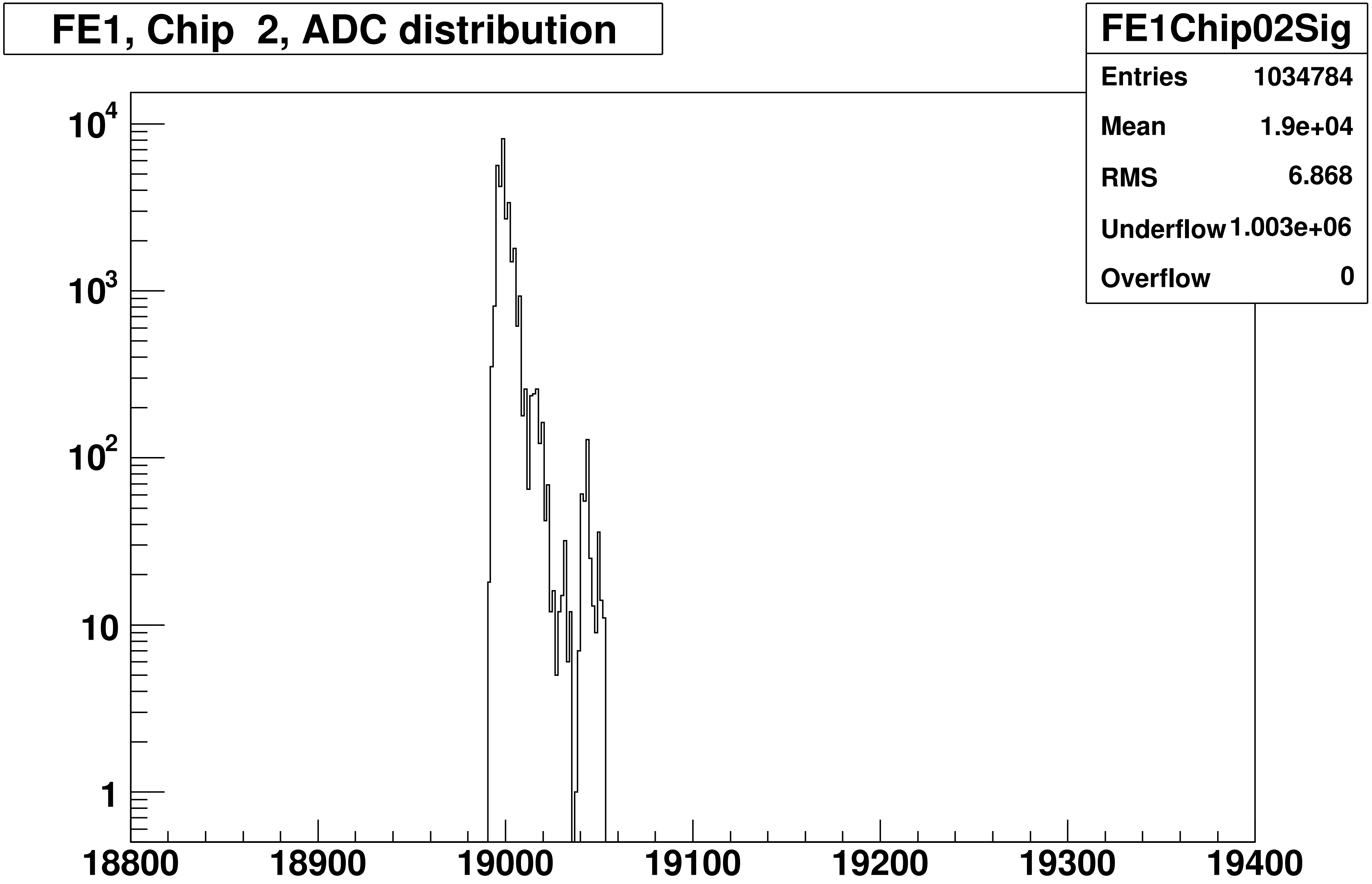
40
60
80
100


FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 1034784
Mean 1.9e+04
RMS 6.868
Underflow 1.003e+06
Overflow 0
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10^3
10^2
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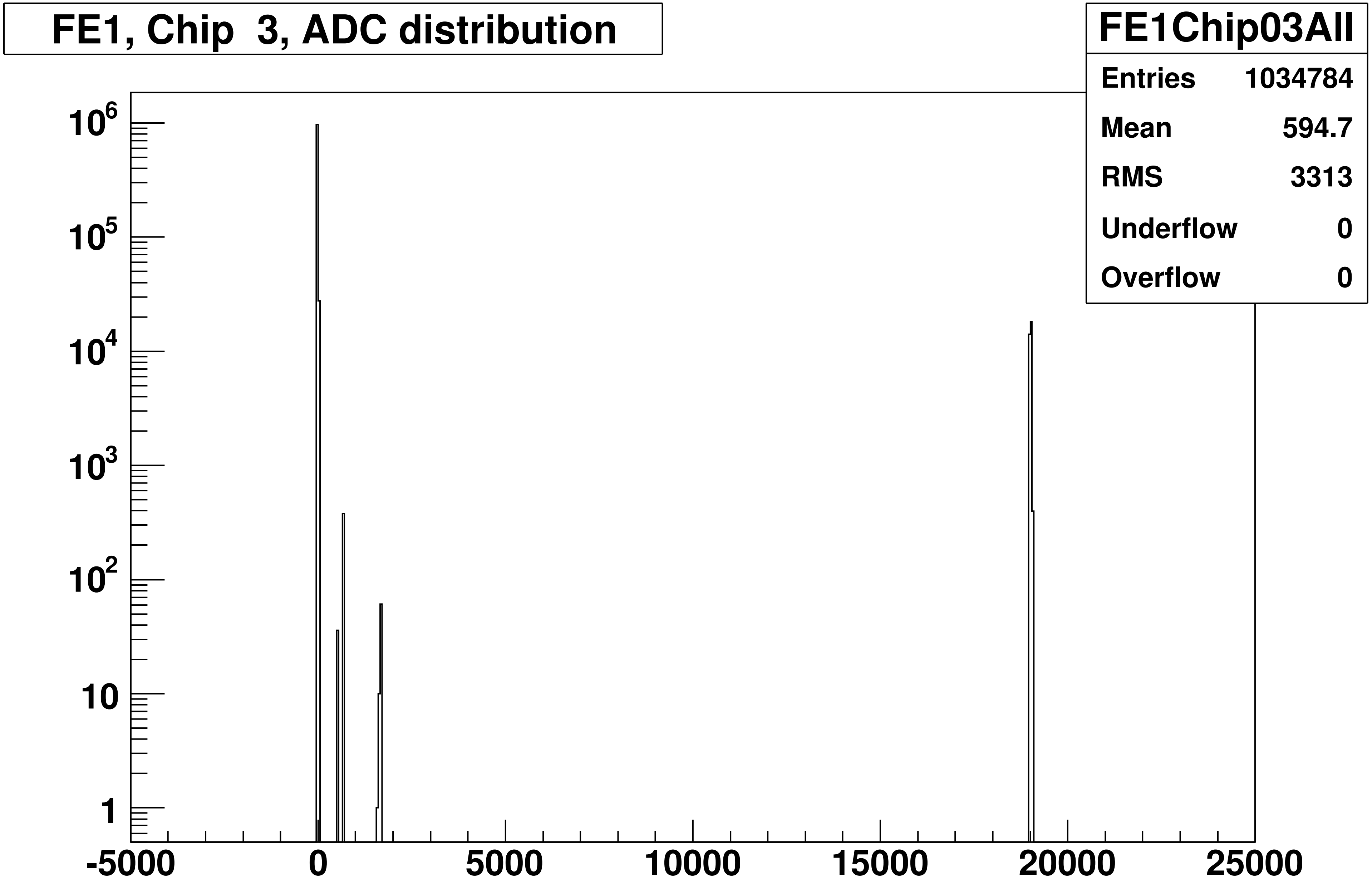

FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 1034784
Mean 594.7
RMS 3313
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
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0
5000
10000
15000
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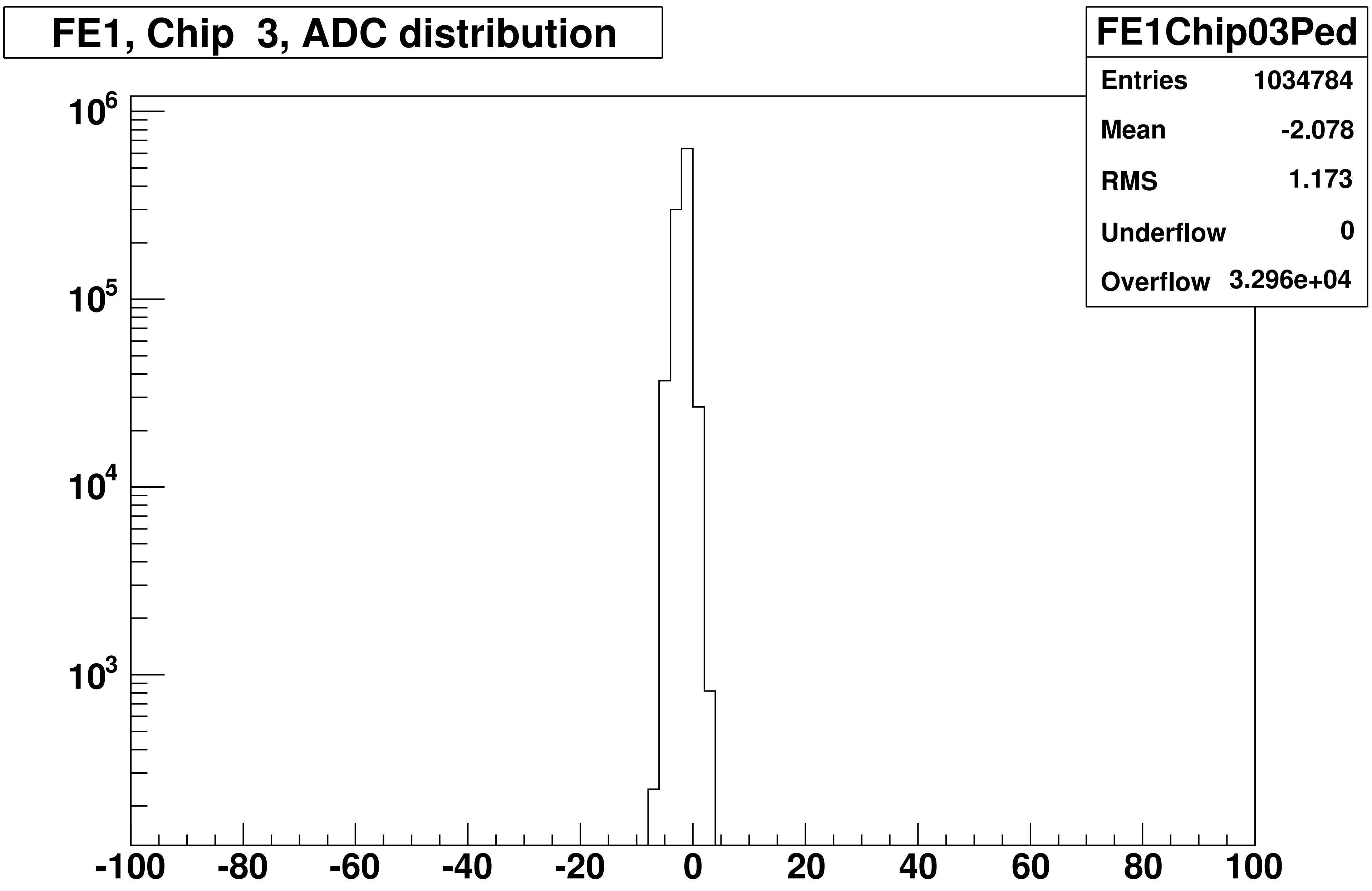
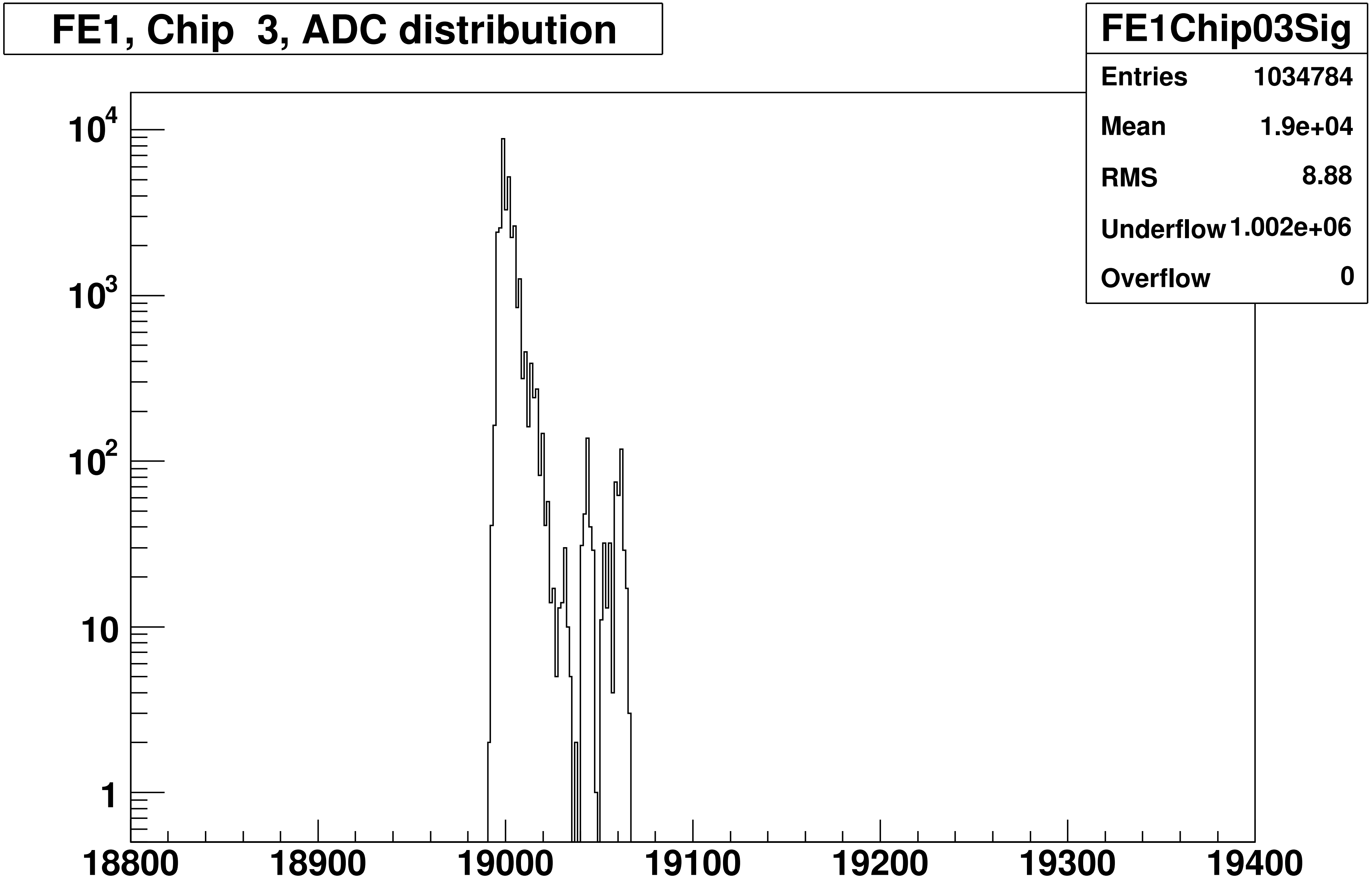
25000


FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 1034784
Mean -2.078
RMS 1.173
Underflow 0
Overflow 3.296e+04
10^6
10^5
10^4
10^3
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 1034784
Mean 1.9e+04
RMS 8.88
Underflow 1.002e+06
Overflow 0
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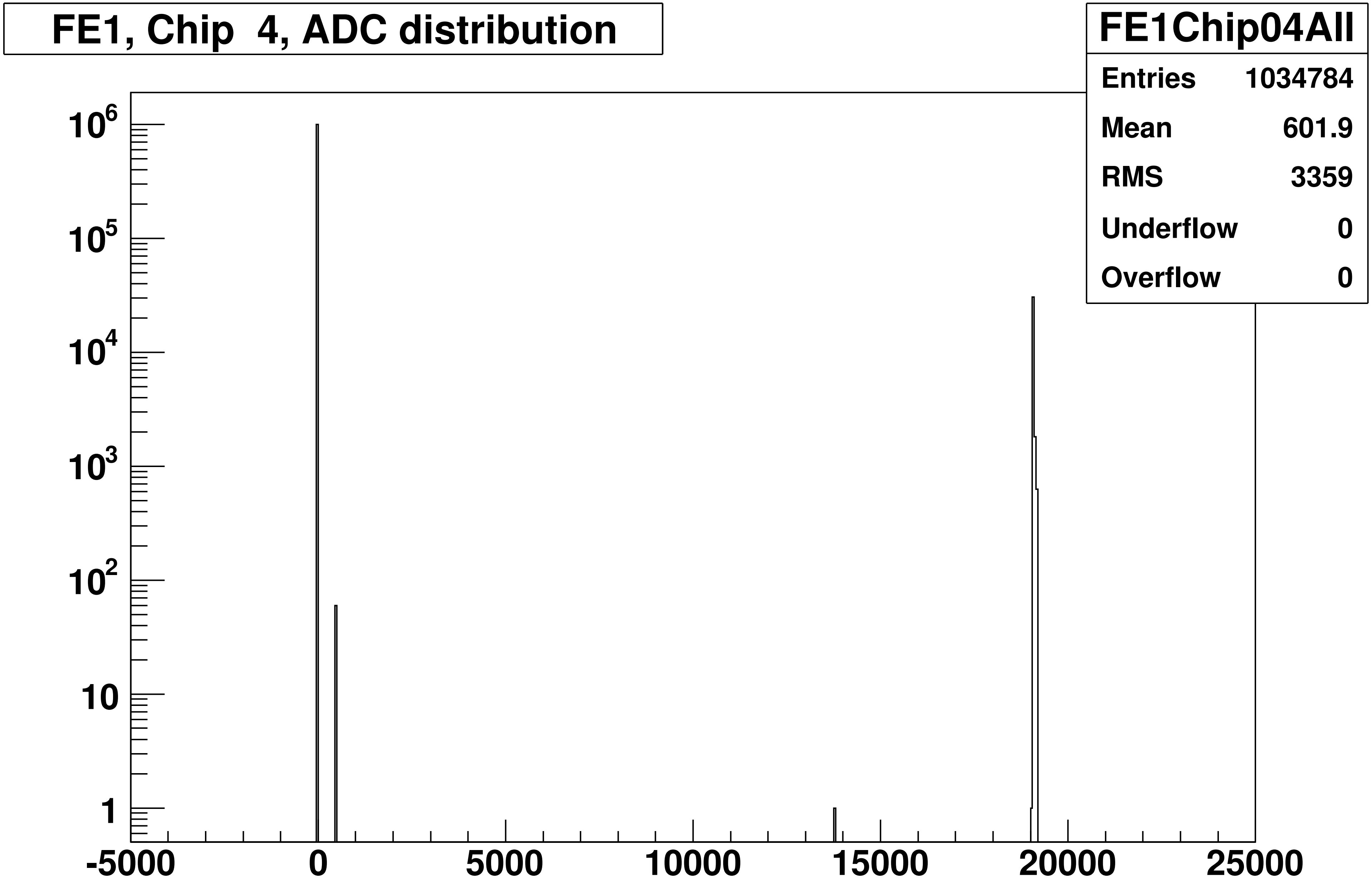
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 1034784
Mean 601.9
RMS 3359
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
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10000
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20000
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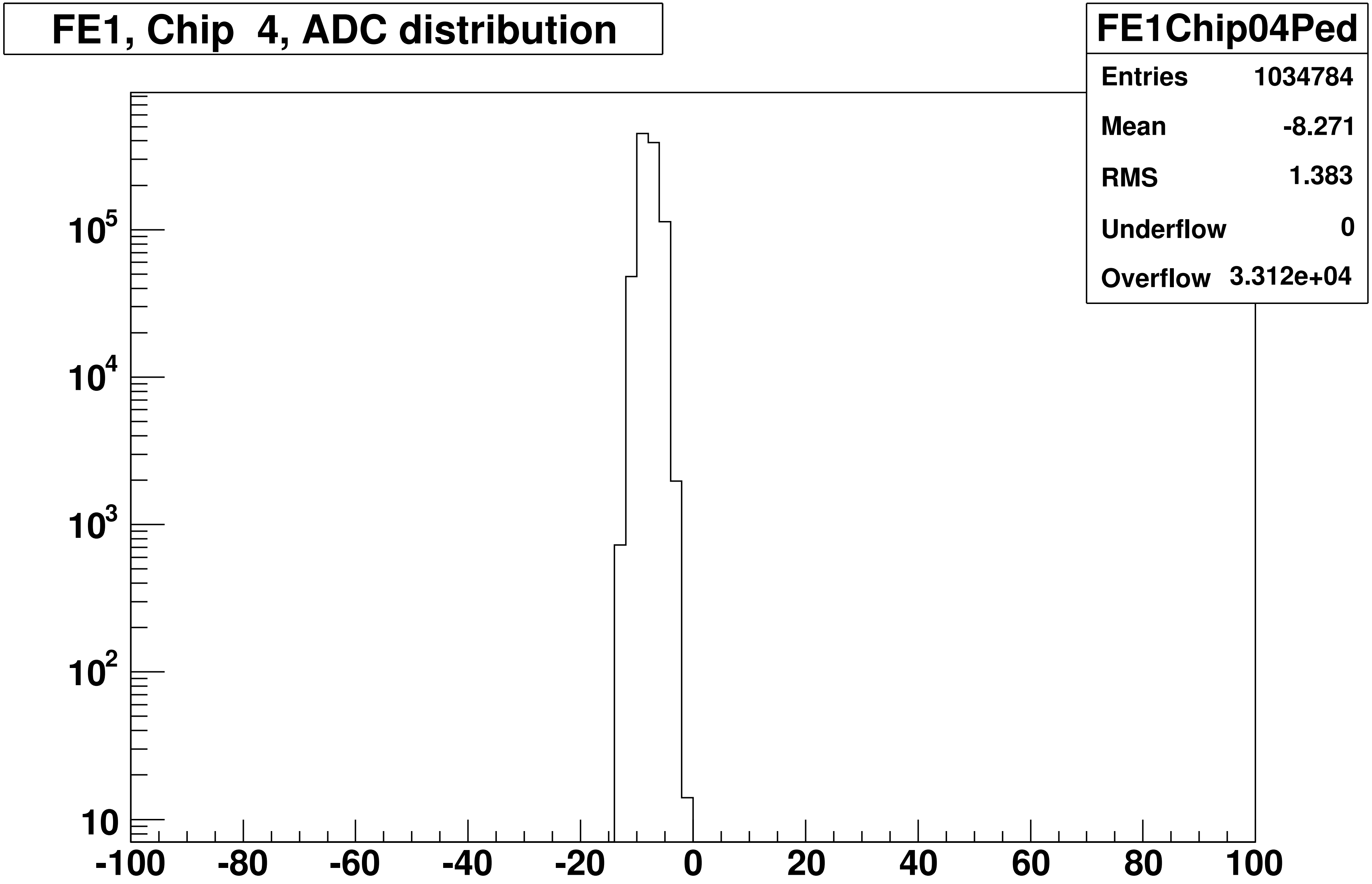
FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 1034784
Mean -8.271
RMS 1.383
Underflow 0
Overflow 3.312e+04
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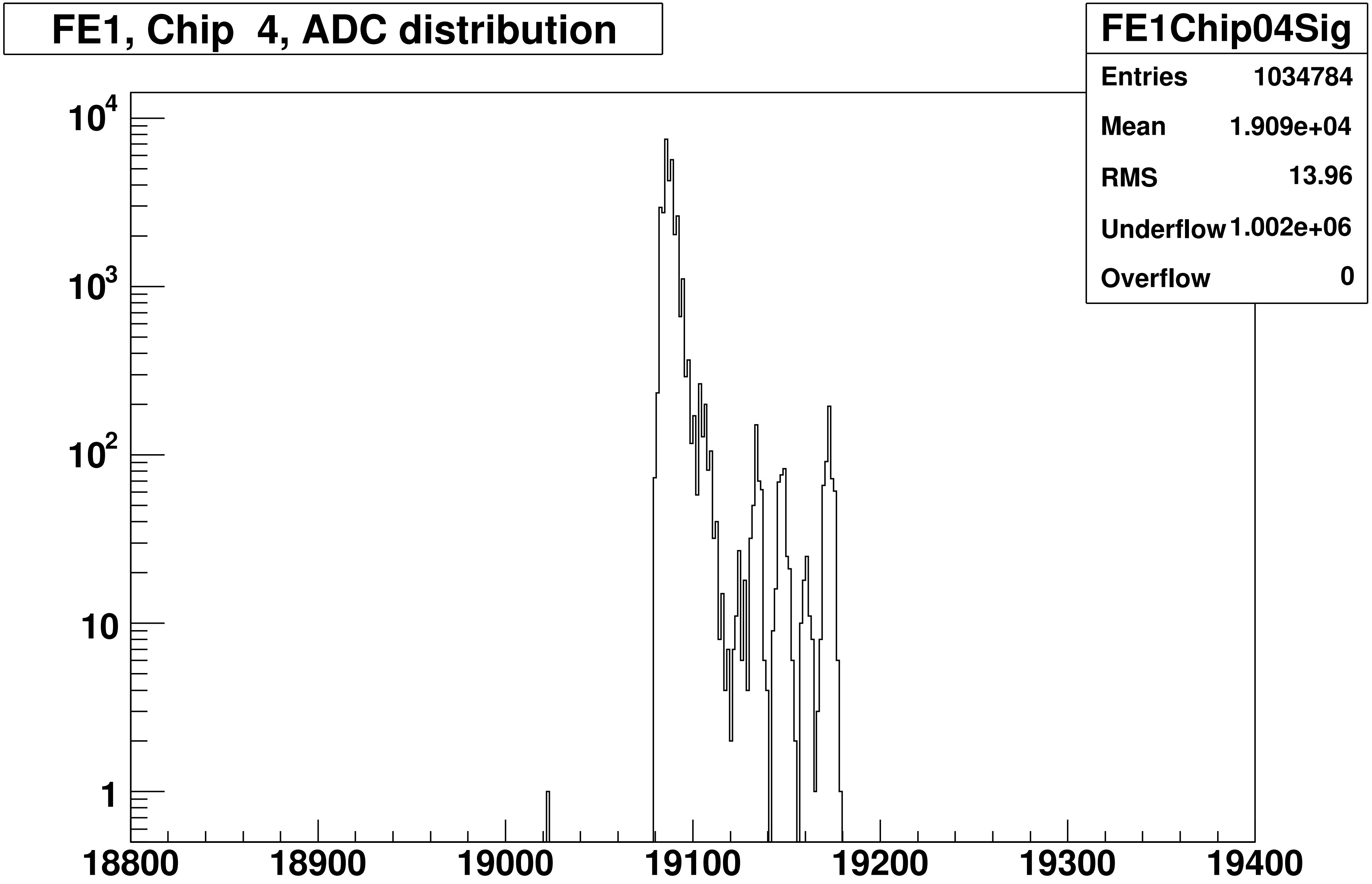
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 1034784
Mean 1.909e+04
RMS 13.96
Underflow 1.002e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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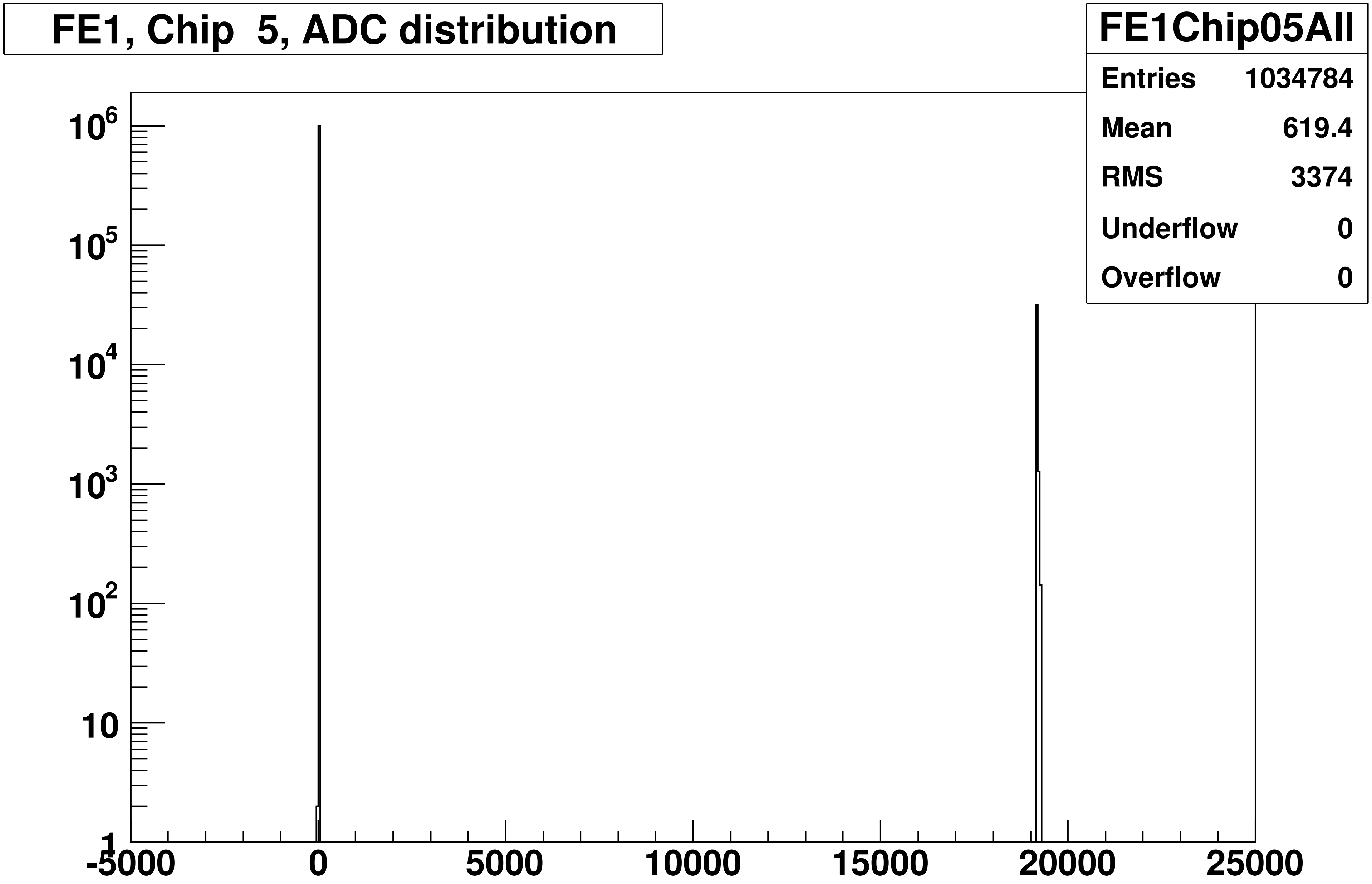
19400


FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 1034784
Mean 619.4
RMS 3374
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
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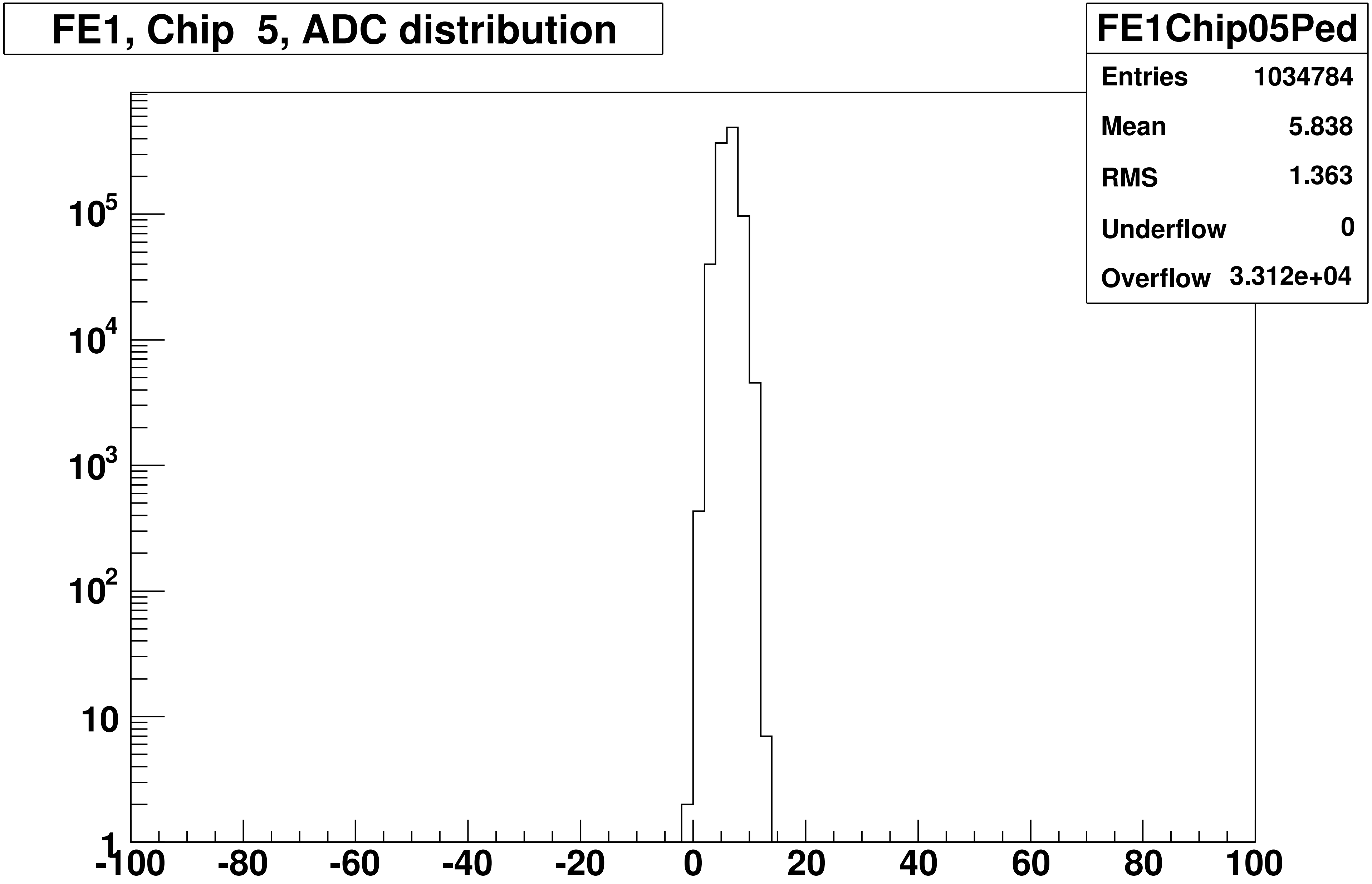
20000
25000



FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 1034784
Mean 5.838
RMS 1.363
Underflow 0
Overflow 3.312e+04
1
10
10^2
10^3
10^4
10^5
-100
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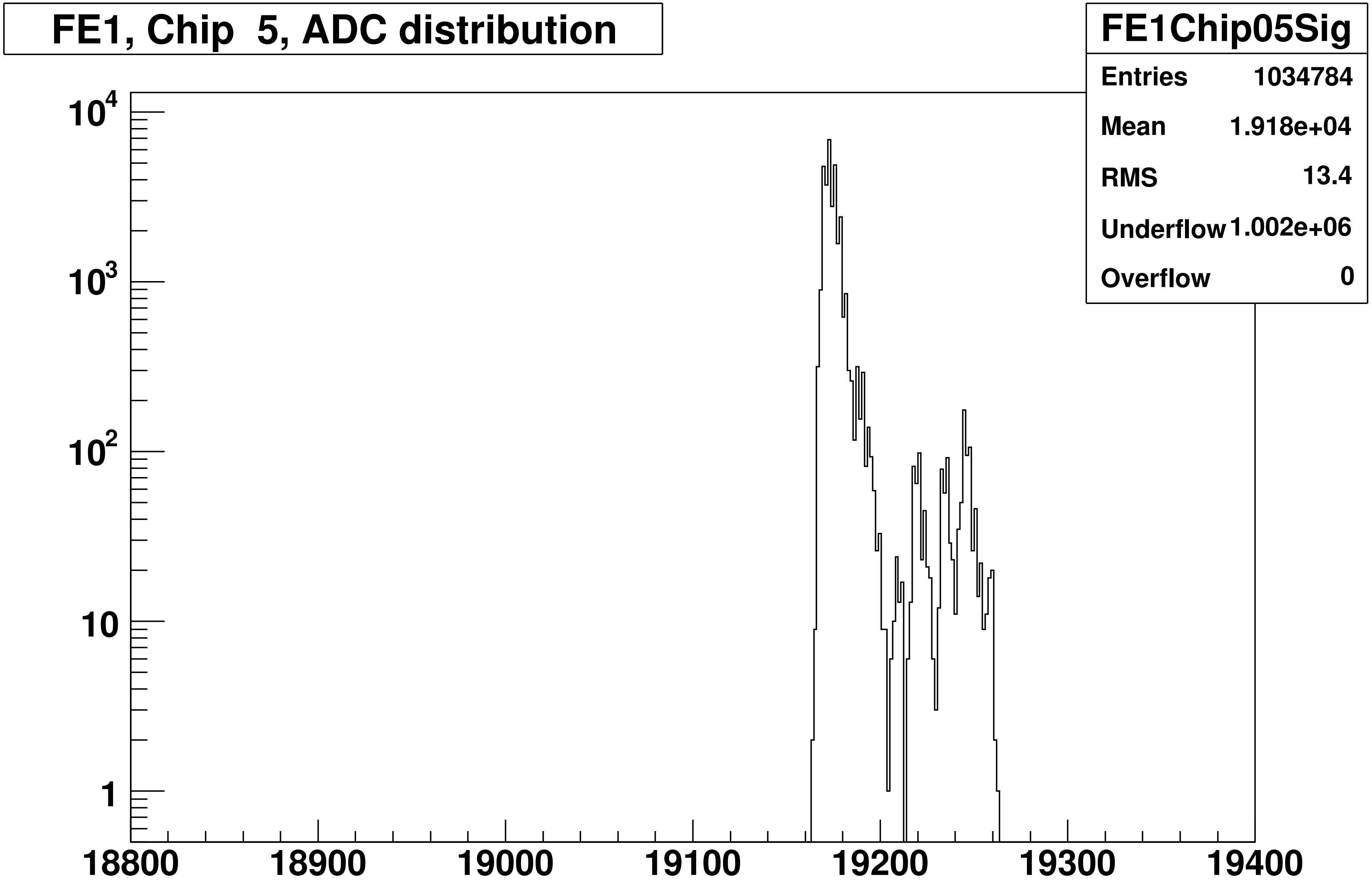
60
80
100


FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 1034784
Mean 1.918e+04
RMS 13.4
Underflow 1.002e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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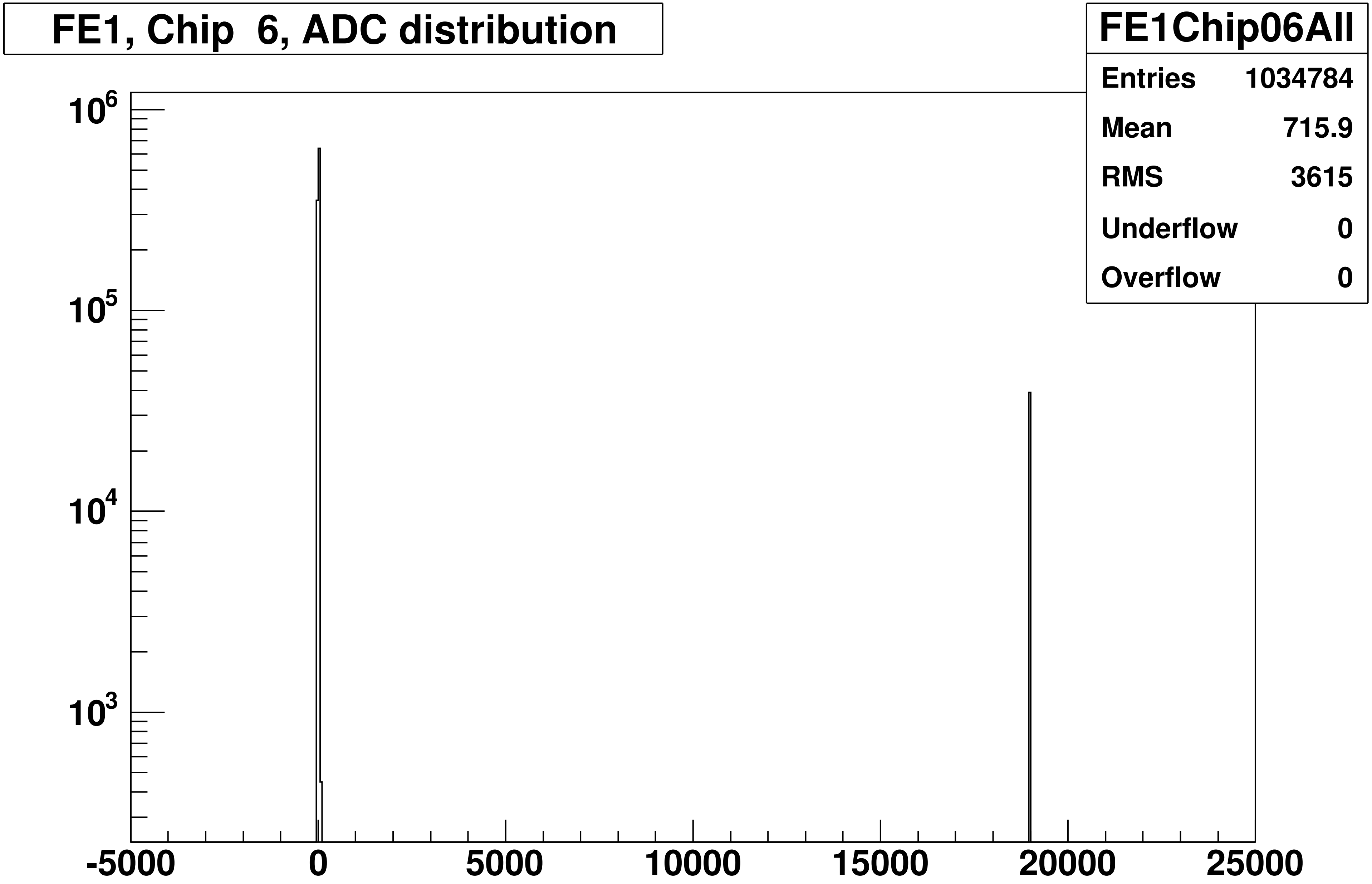
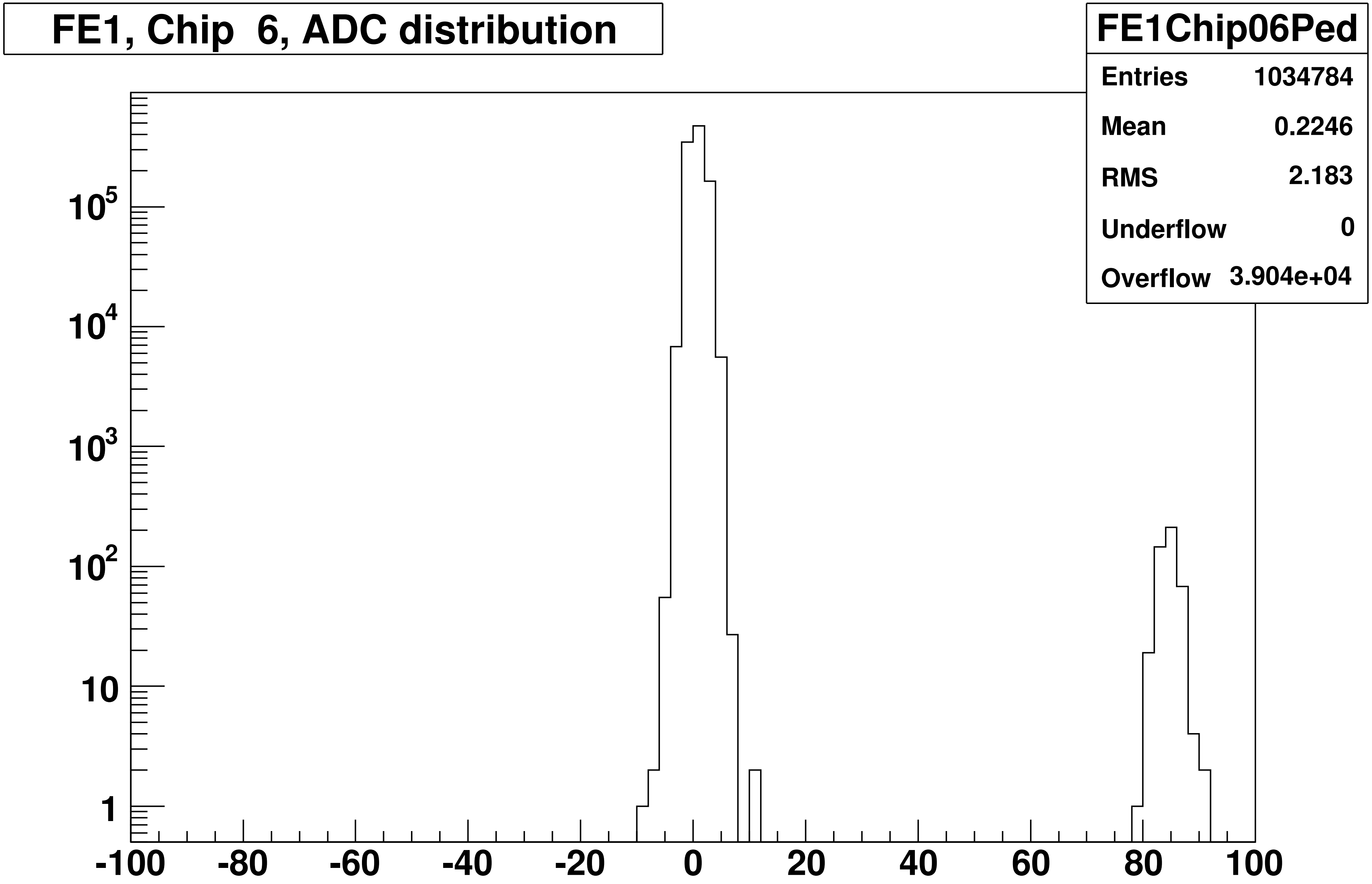
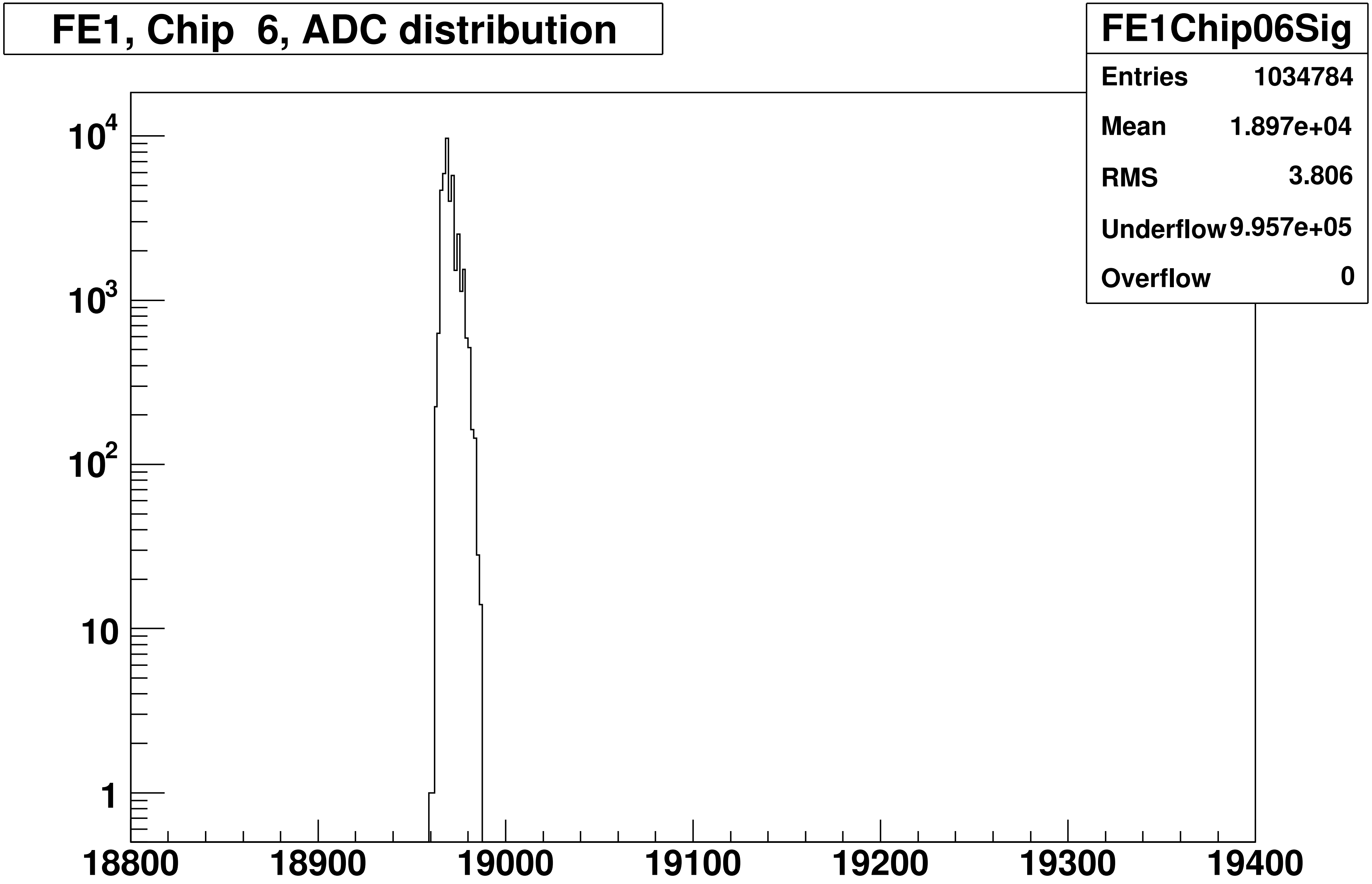
19400



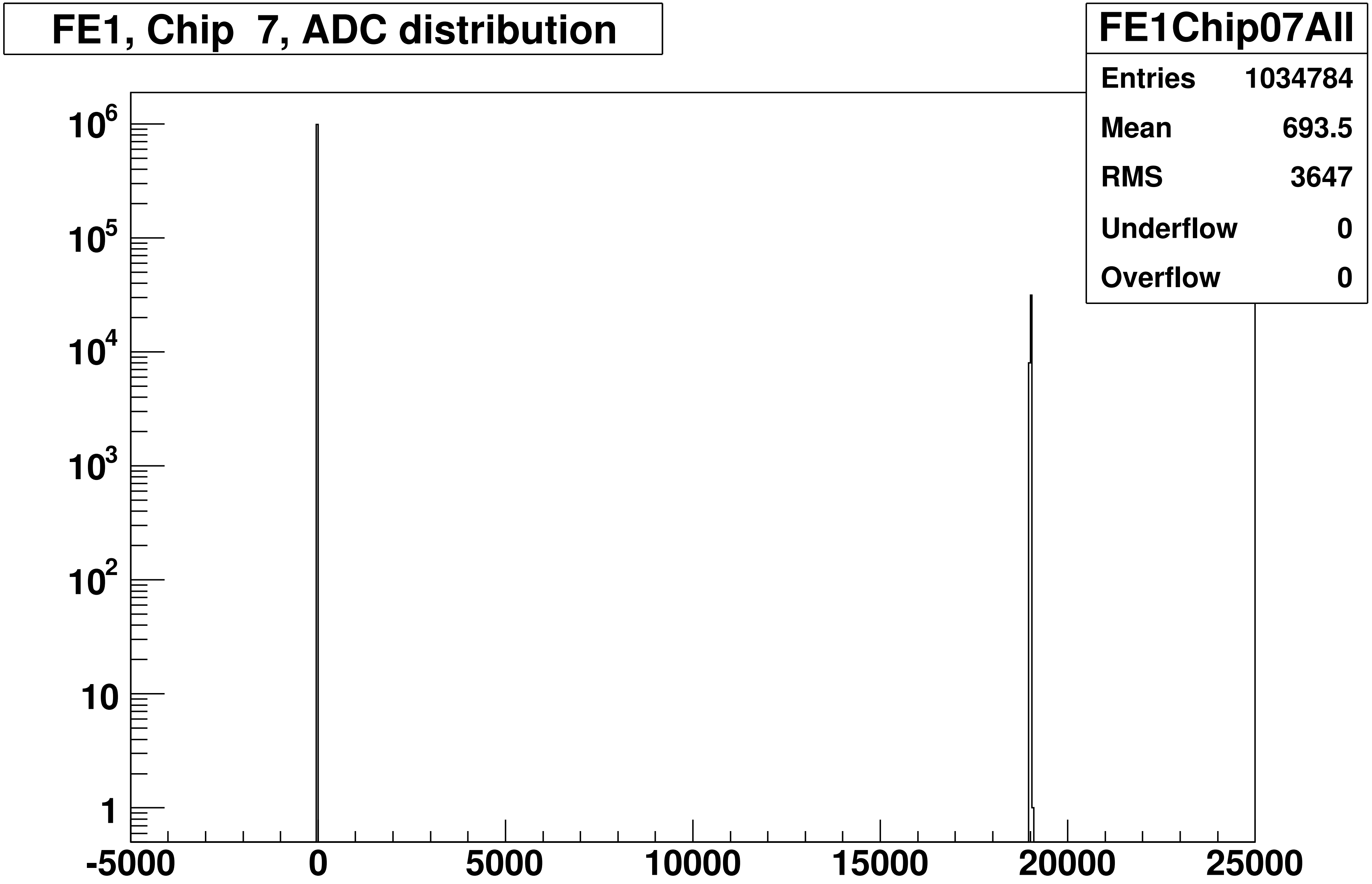
FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 1034784
Mean 715.9
RMS 3615
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 1034784
Mean 0.2246
RMS 2.183
Underflow 0
Overflow 3.904e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 1034784
Mean 1.897e+04
RMS 3.806
Underflow 9.957e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 1034784
Mean 693.5
RMS 3647
Underflow 0
Overflow 0
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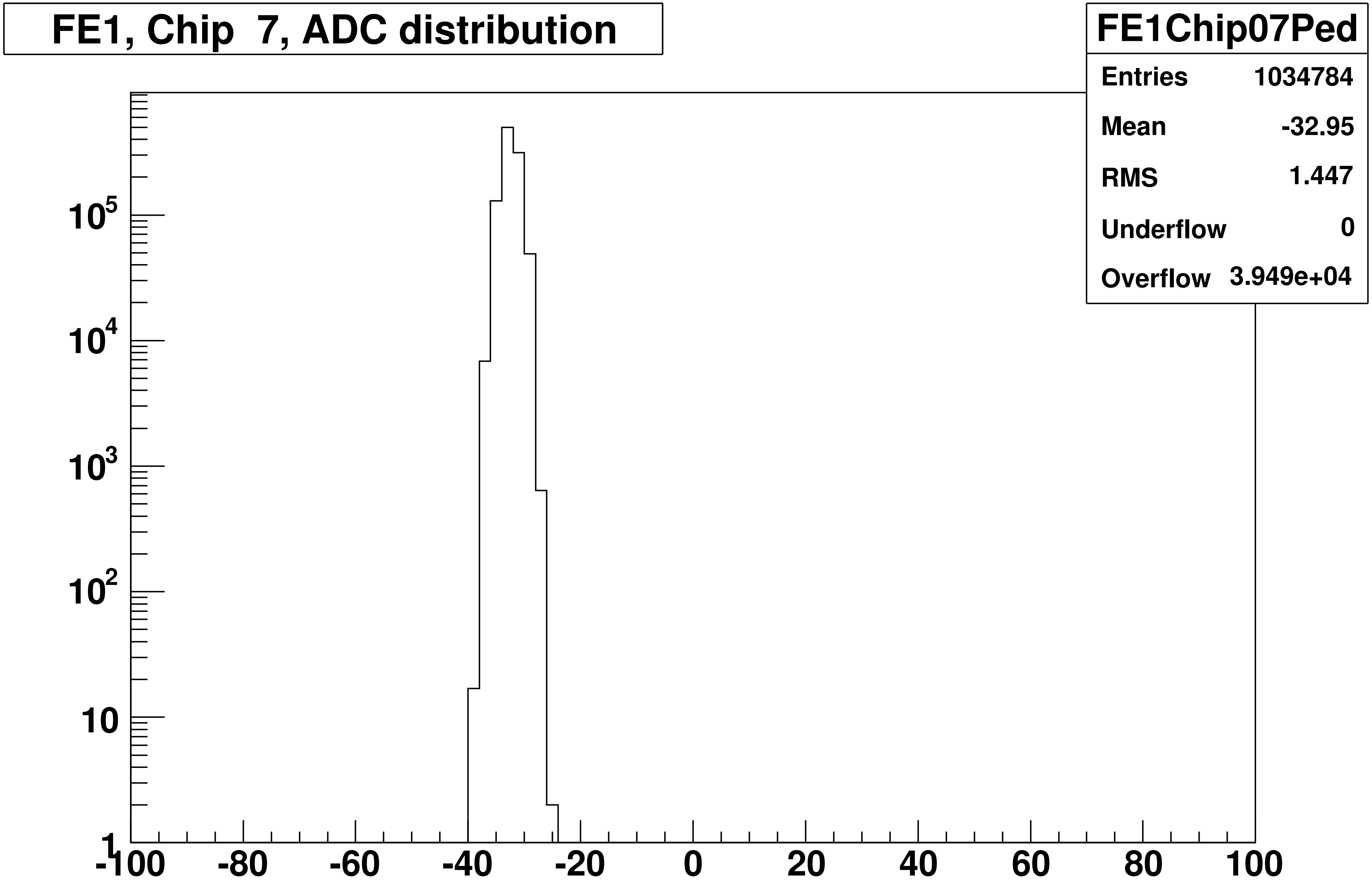
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 1034784
Mean -32.95
RMS 1.447
Underflow 0
Overflow 3.949e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
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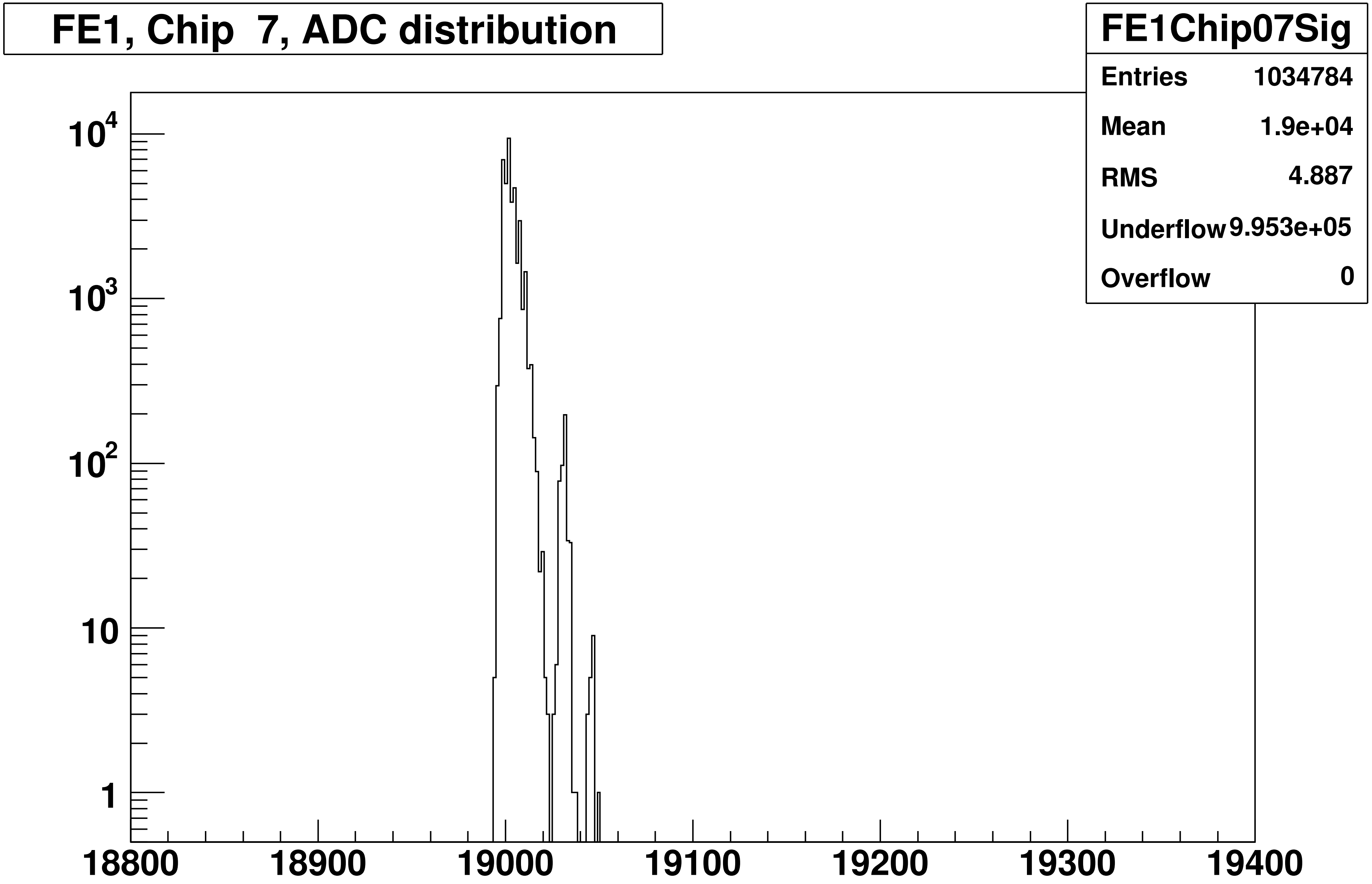
40
60
80
100



FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 1034784
Mean 1.9e+04
RMS 4.887
Underflow 9.953e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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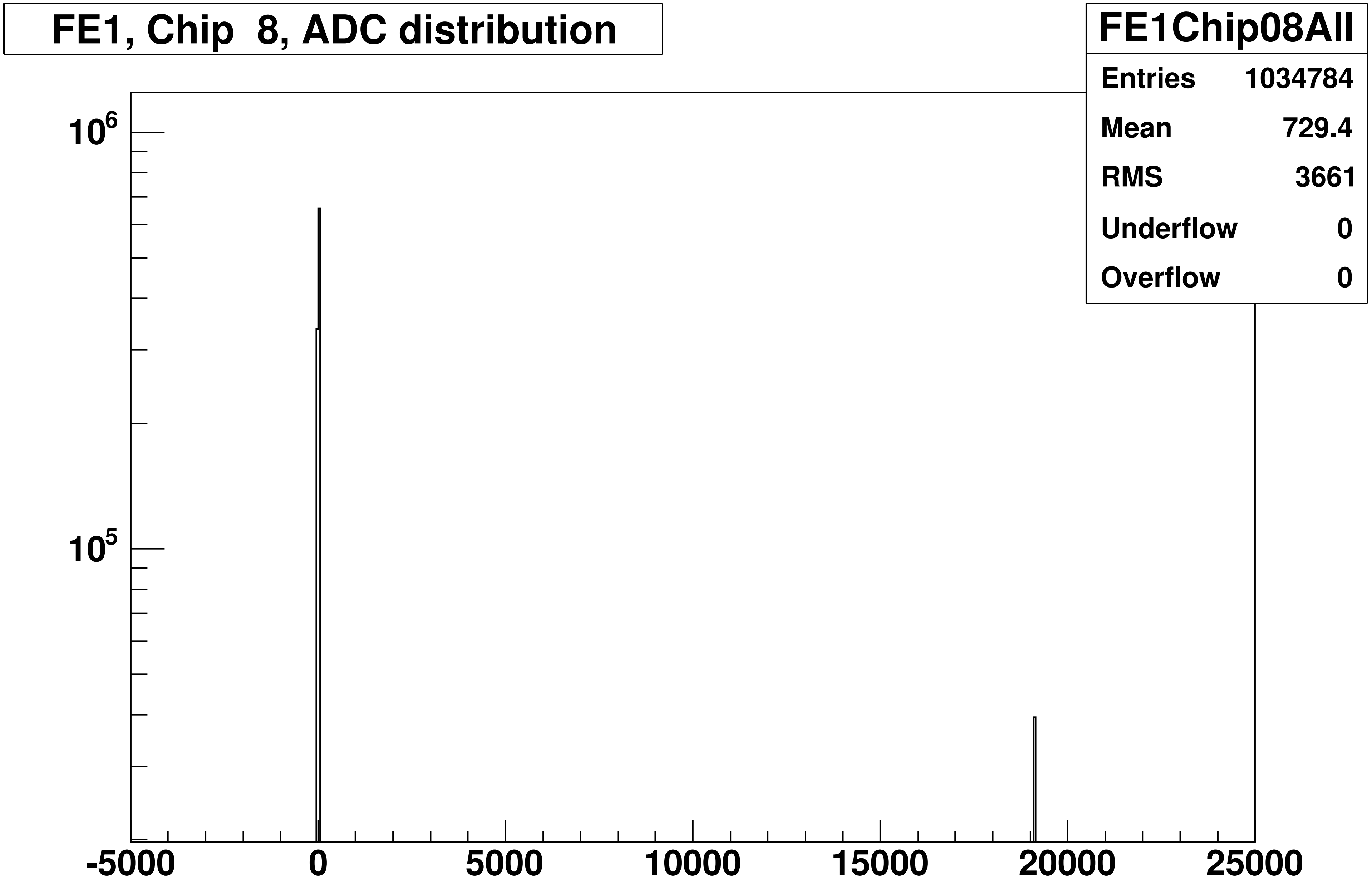
19100
19200
19300
19400


FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 1034784
Mean 729.4
RMS 3661
Underflow 0
Overflow 0
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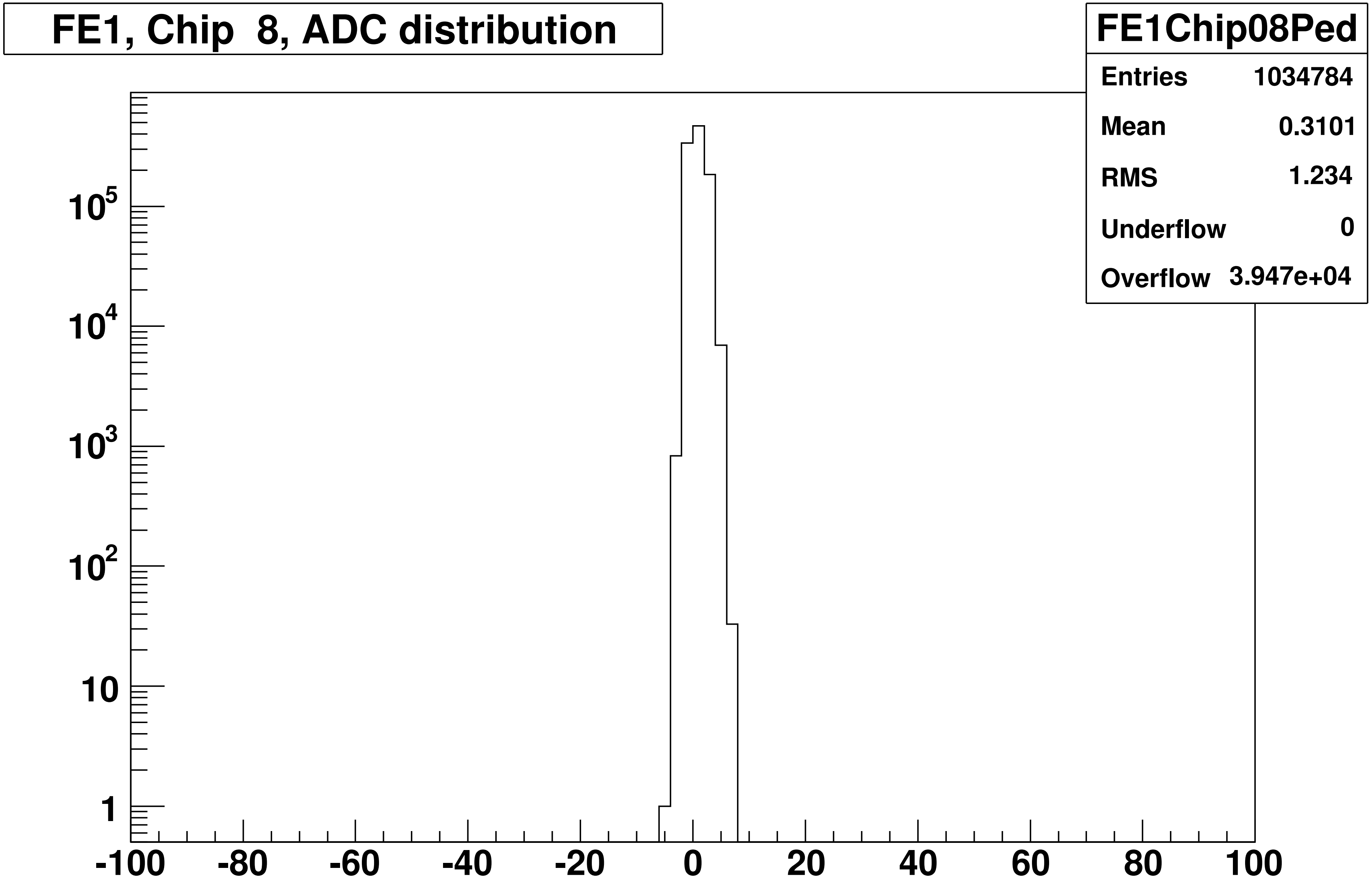
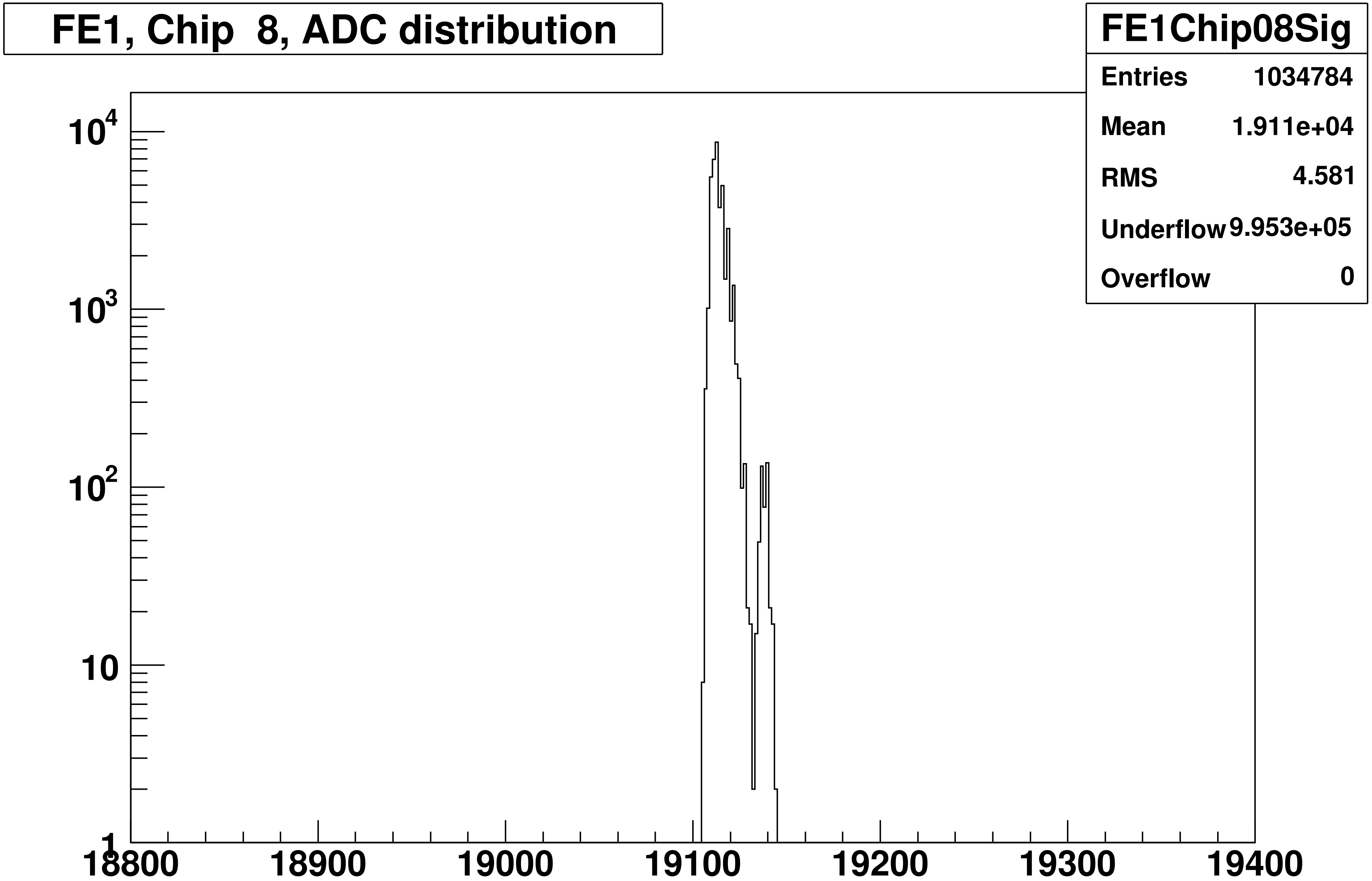
10^5
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 1034784
Mean 0.3101
RMS 1.234
Underflow 0
Overflow 3.947e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 1034784
Mean 1.911e+04
RMS 4.581
Underflow 9.953e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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19100
19200
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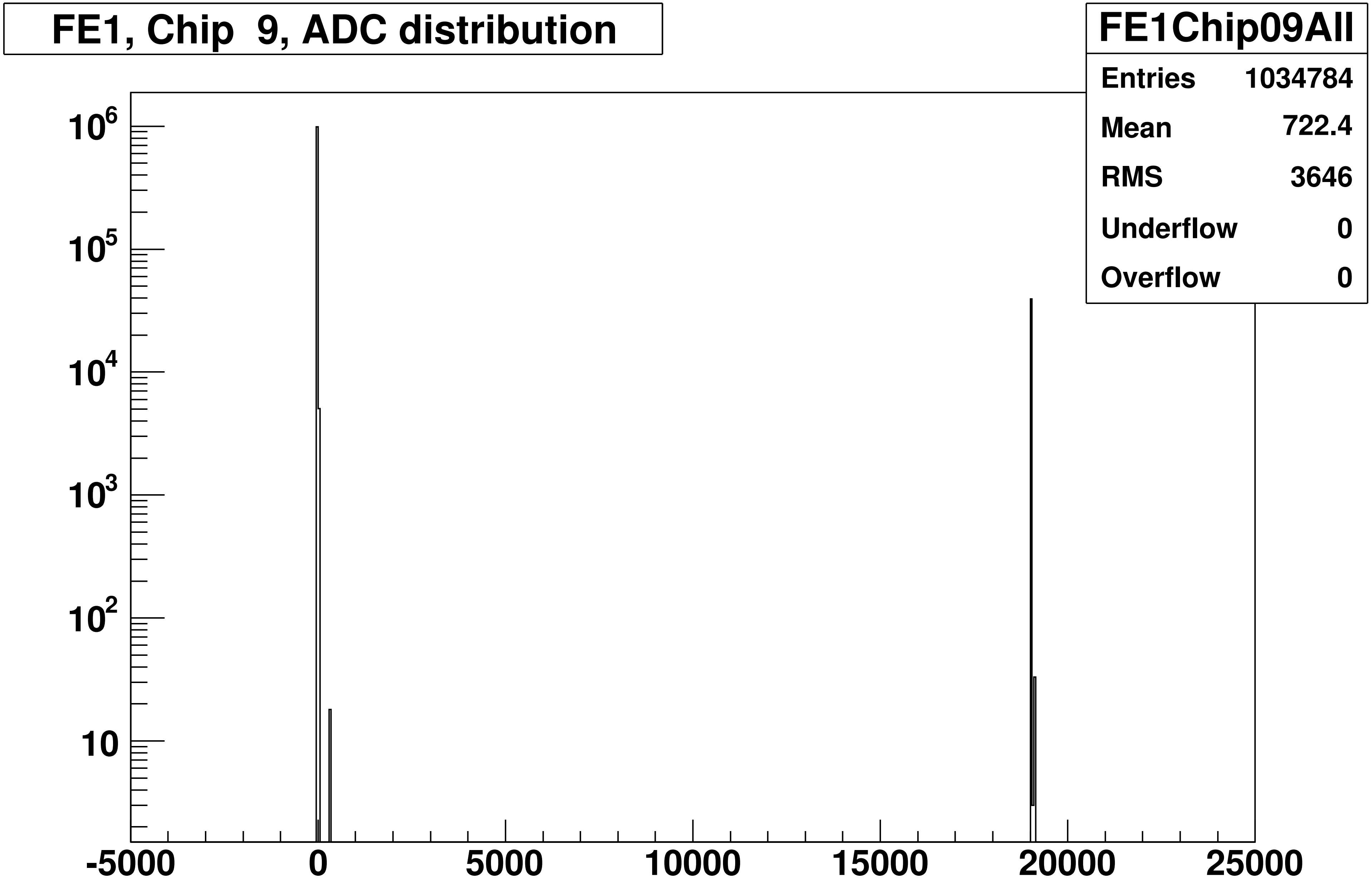
19400



FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 1034784
Mean 722.4
RMS 3646
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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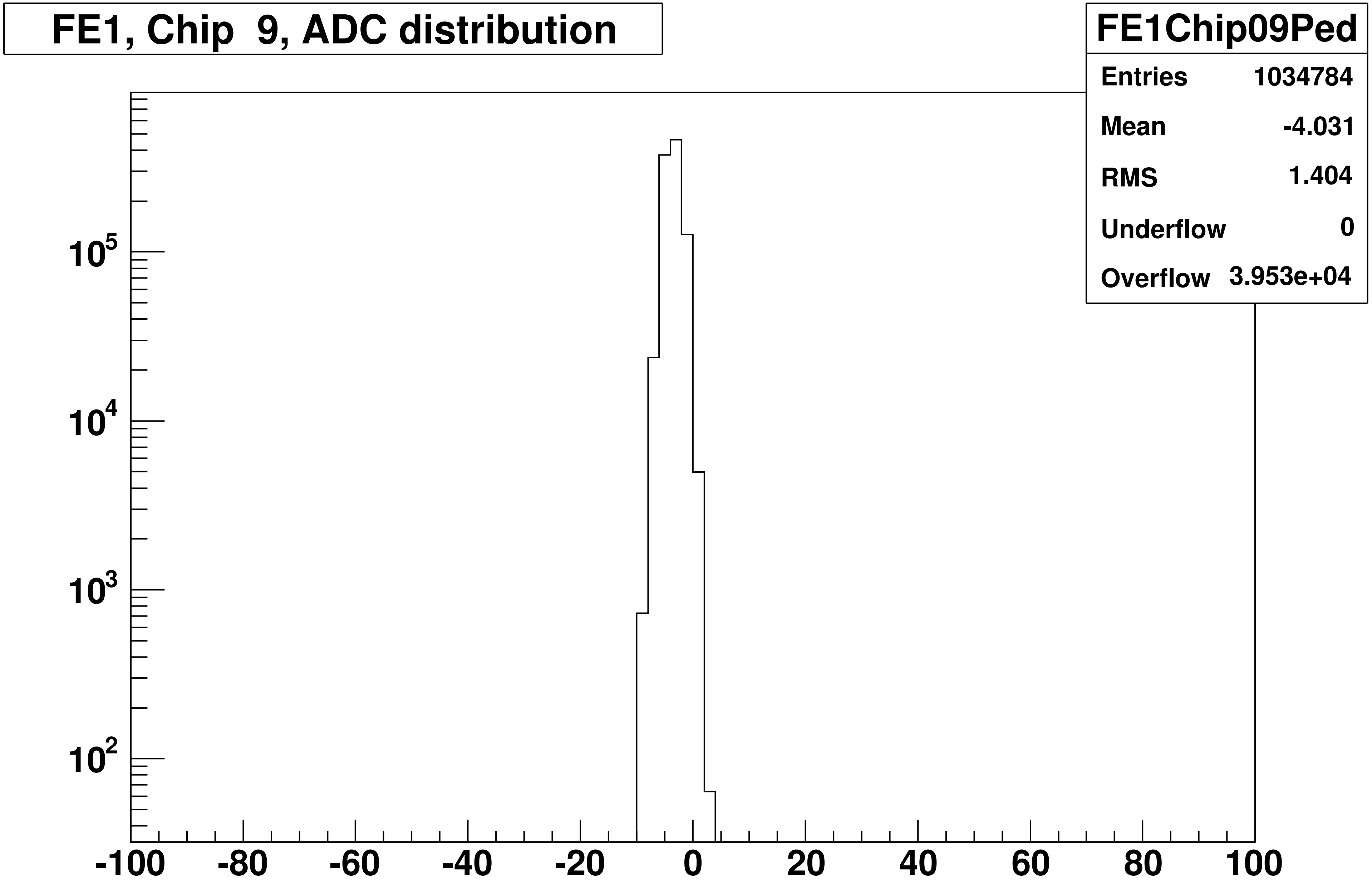
10000
15000
20000
25000



FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 1034784
Mean -4.031
RMS 1.404
Underflow 0
Overflow 3.953e+04
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10^4
10^3
10^2
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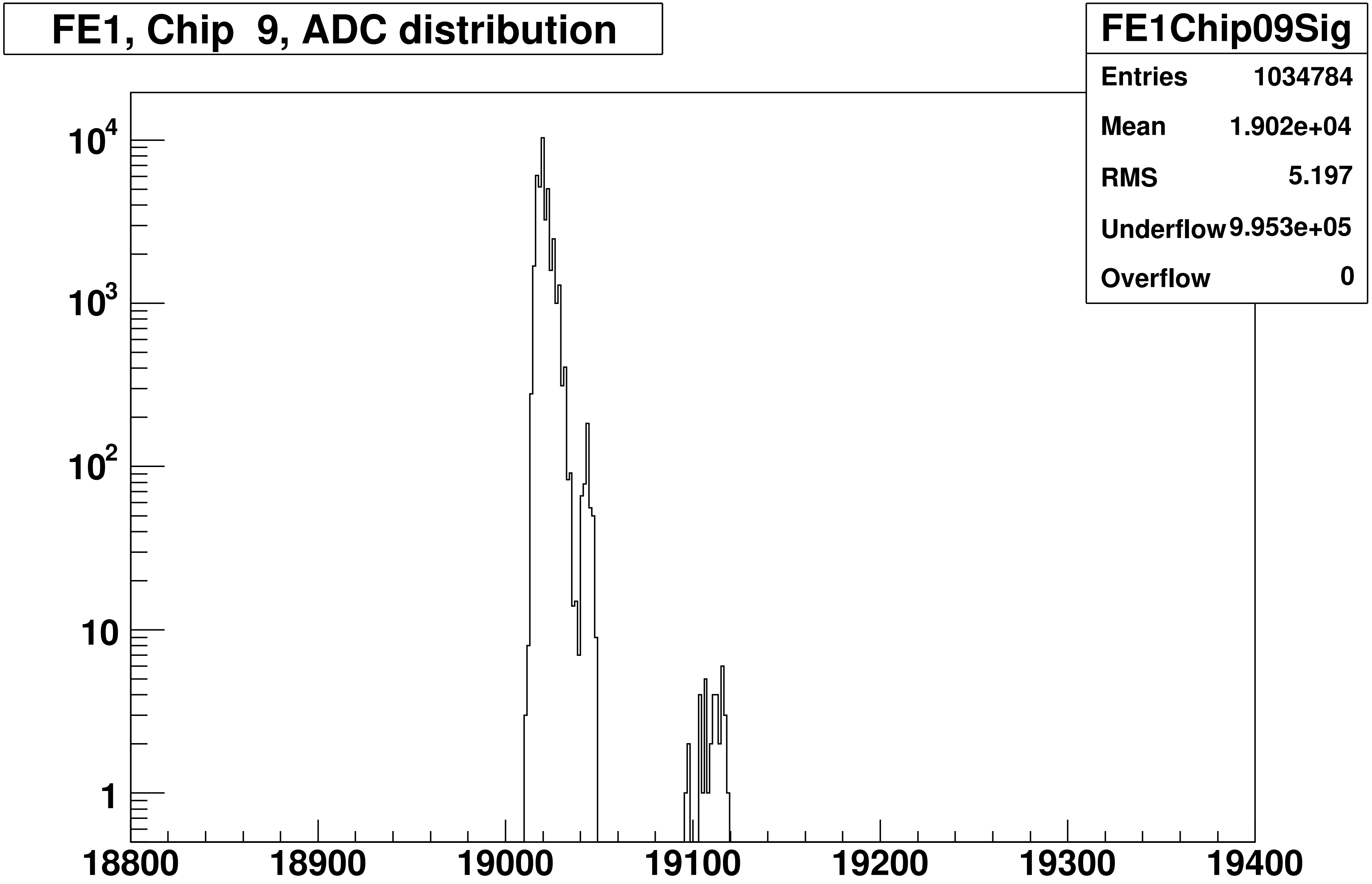
FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 1034784
Mean 1.902e+04
RMS 5.197
Underflow 9.953e+05
Overflow 0
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10^3
10^2
10
1
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18900
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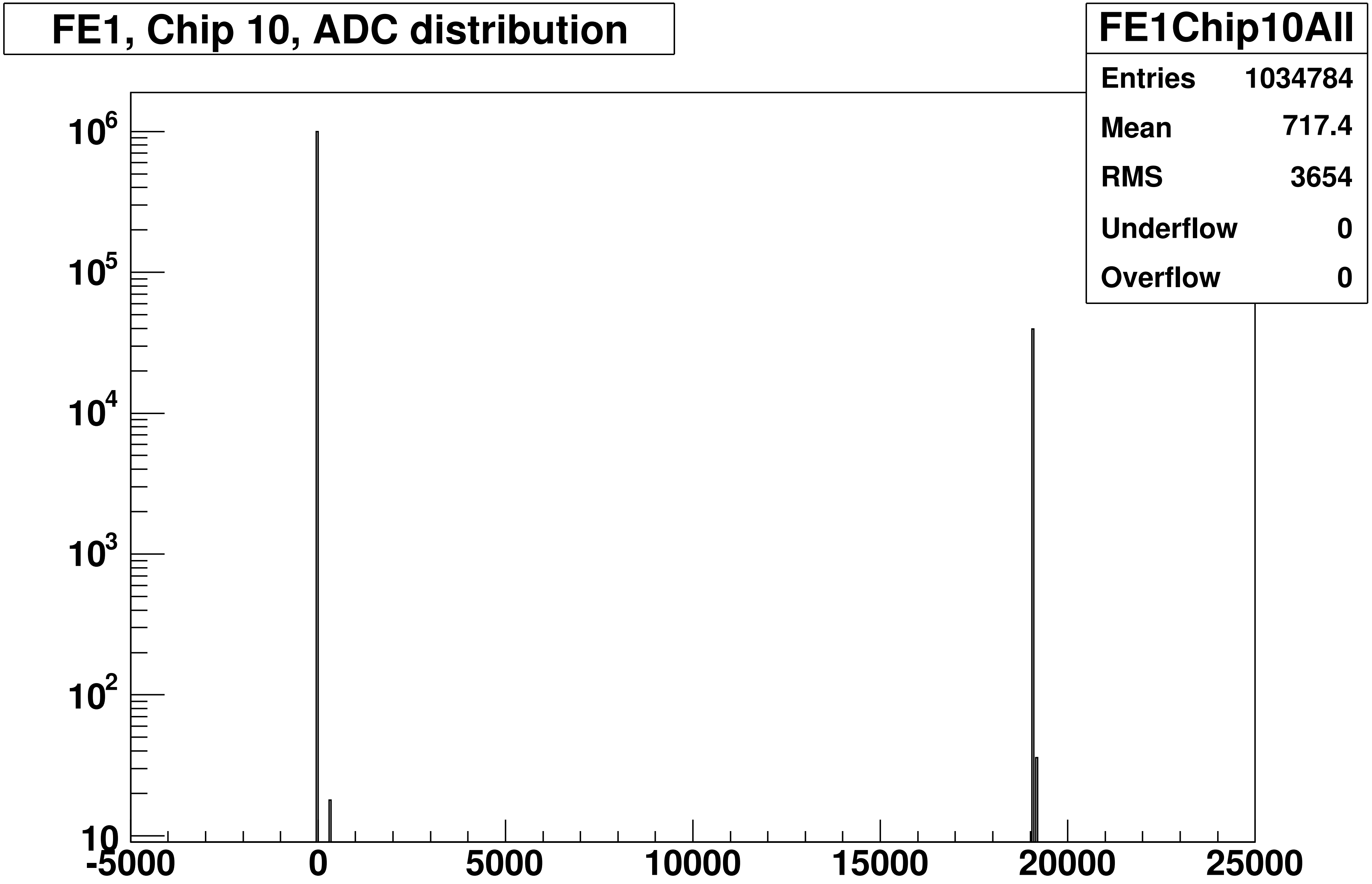
FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 1034784
Mean 717.4
RMS 3654
Underflow 0
Overflow 0
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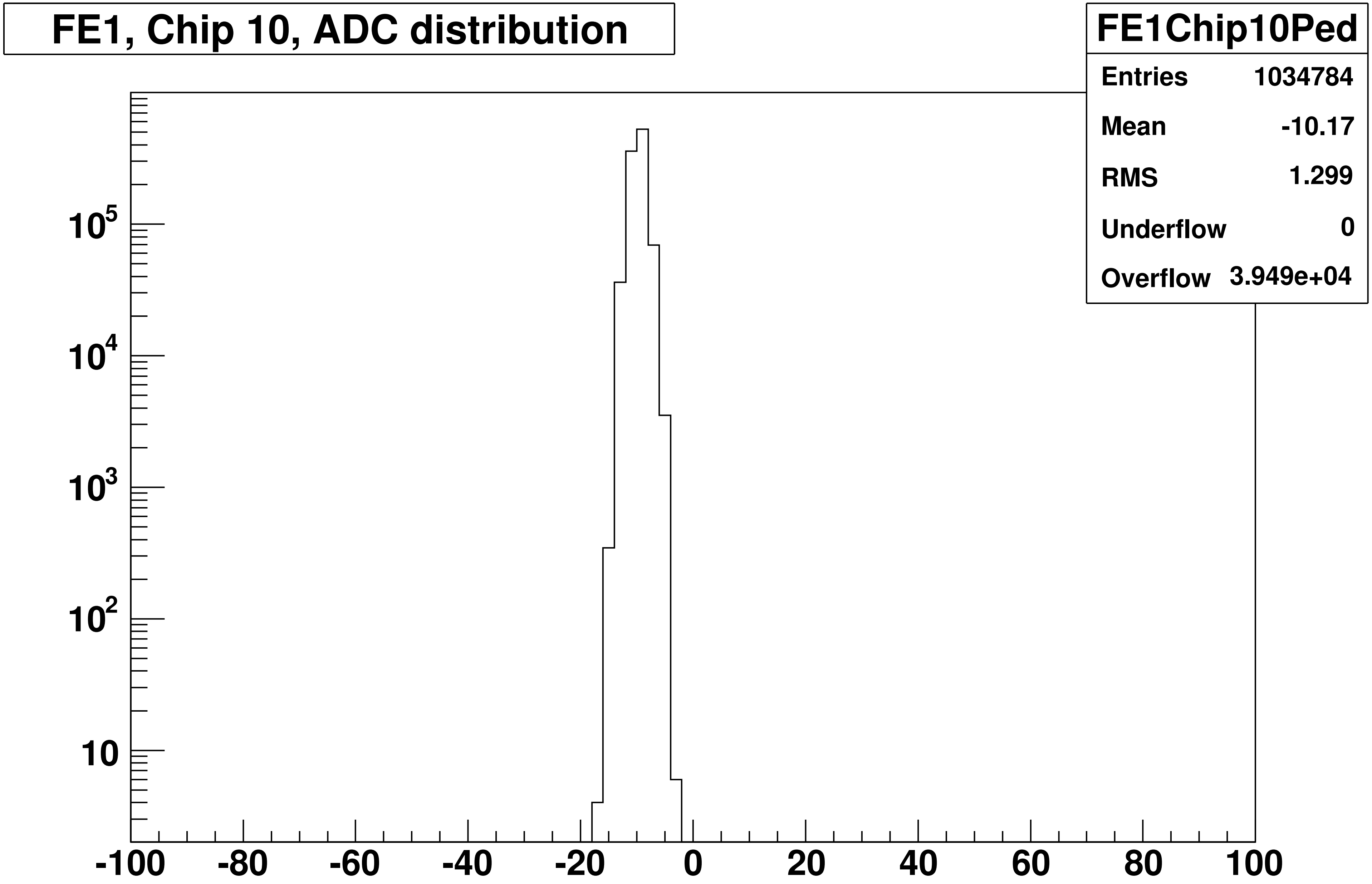
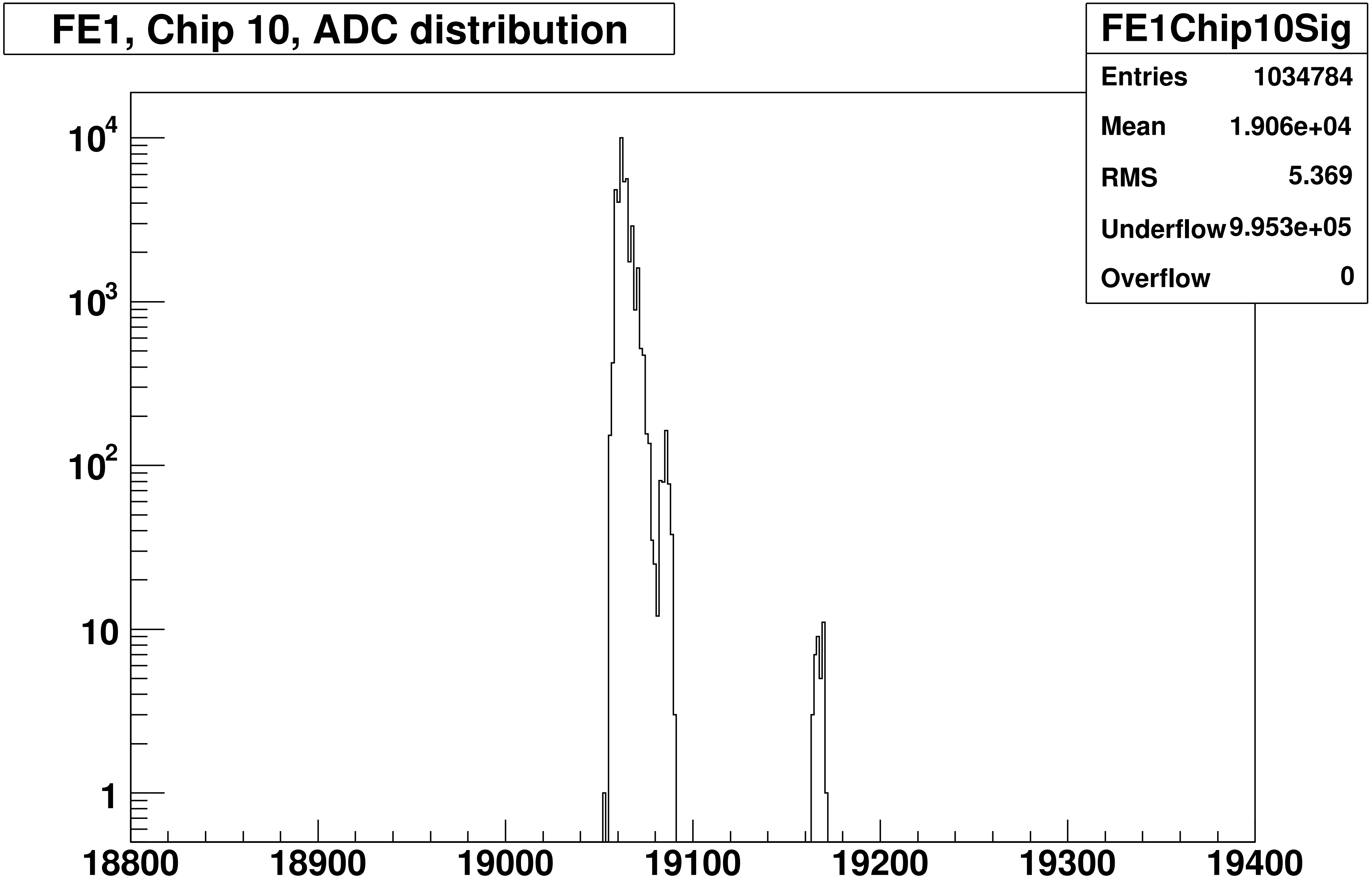
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 1034784
Mean -10.17
RMS 1.299
Underflow 0
Overflow 3.949e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 1034784
Mean 1.906e+04
RMS 5.369
Underflow 9.953e+05
Overflow 0
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10^3
10^2
10
1
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18900
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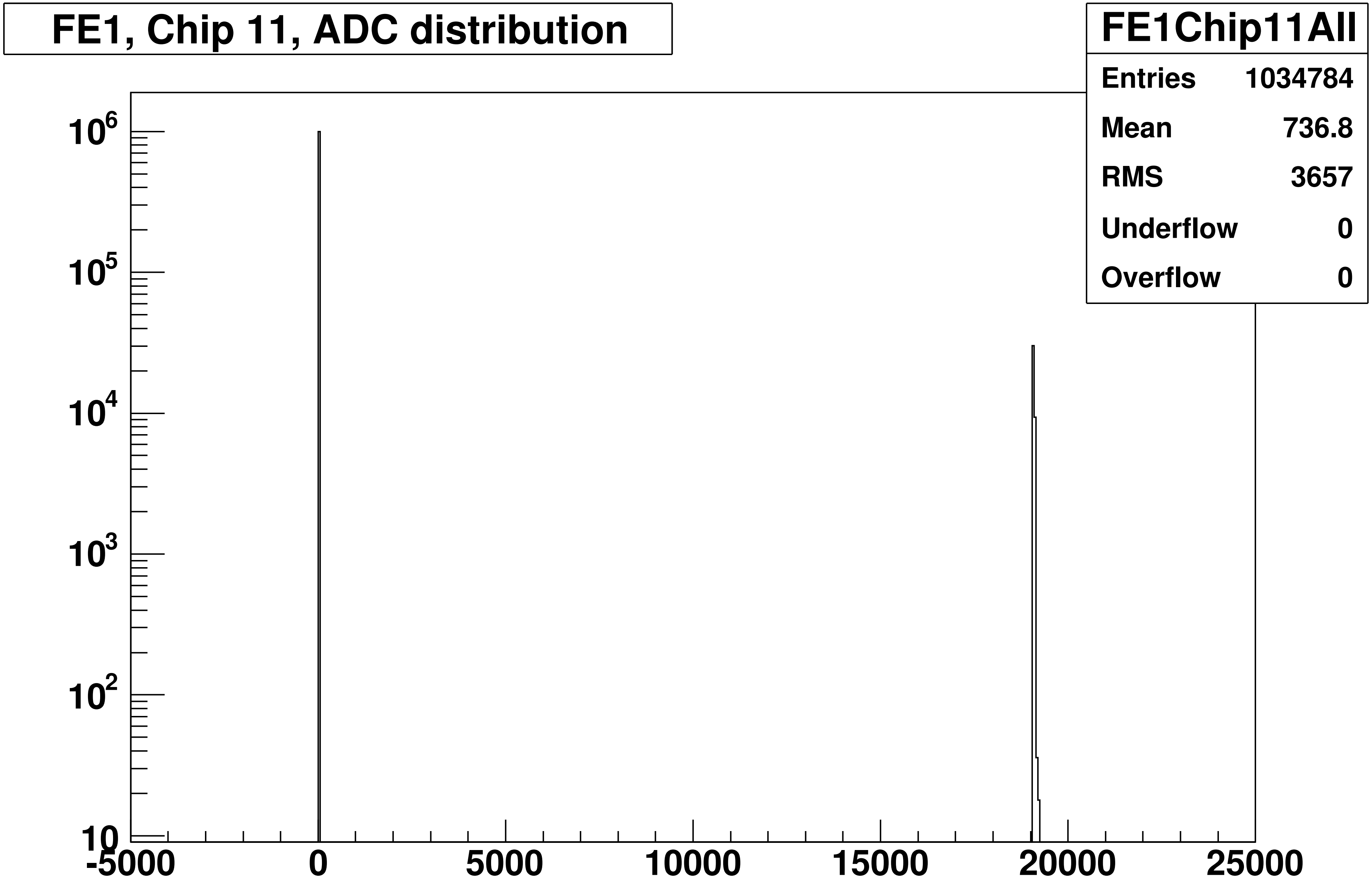
19100
19200
19300
19400



FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 1034784
Mean 736.8
RMS 3657
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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10000
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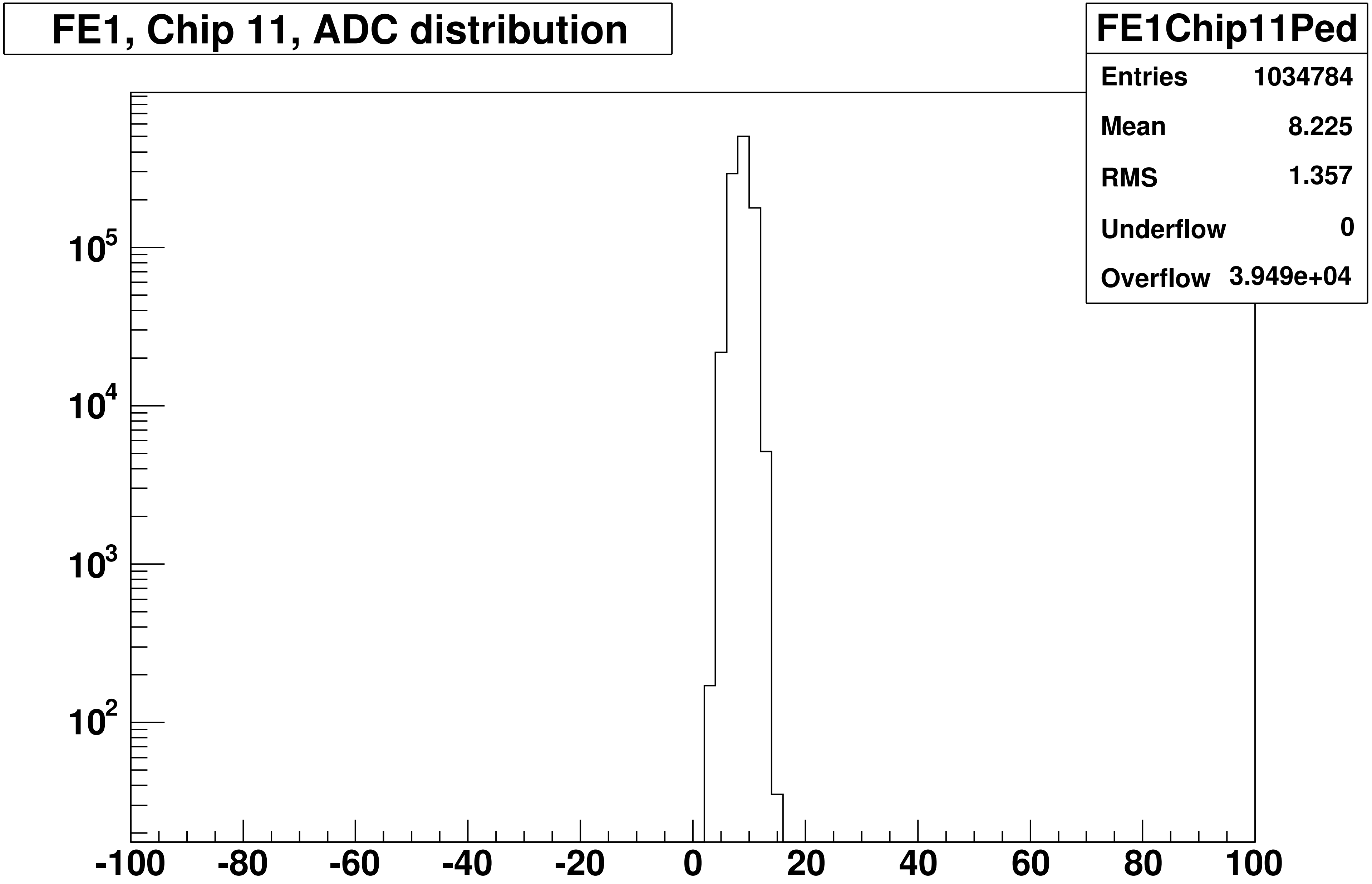
20000
25000



FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 1034784
Mean 8.225
RMS 1.357
Underflow 0
Overflow 3.949e+04
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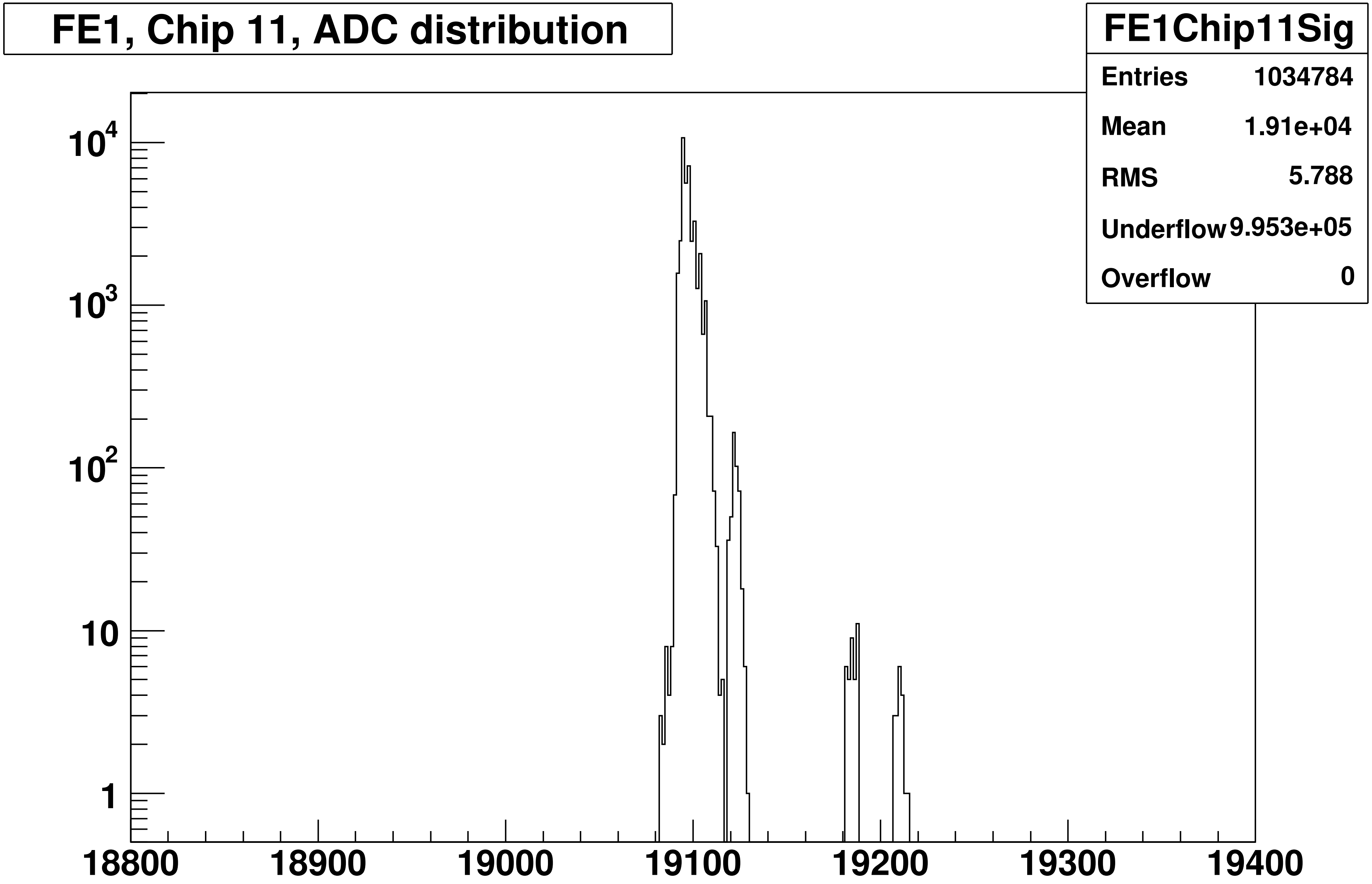
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 1034784
Mean 1.91e+04
RMS 5.788
Underflow 9.953e+05
Overflow 0
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10^3
10^2
10
1
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18900
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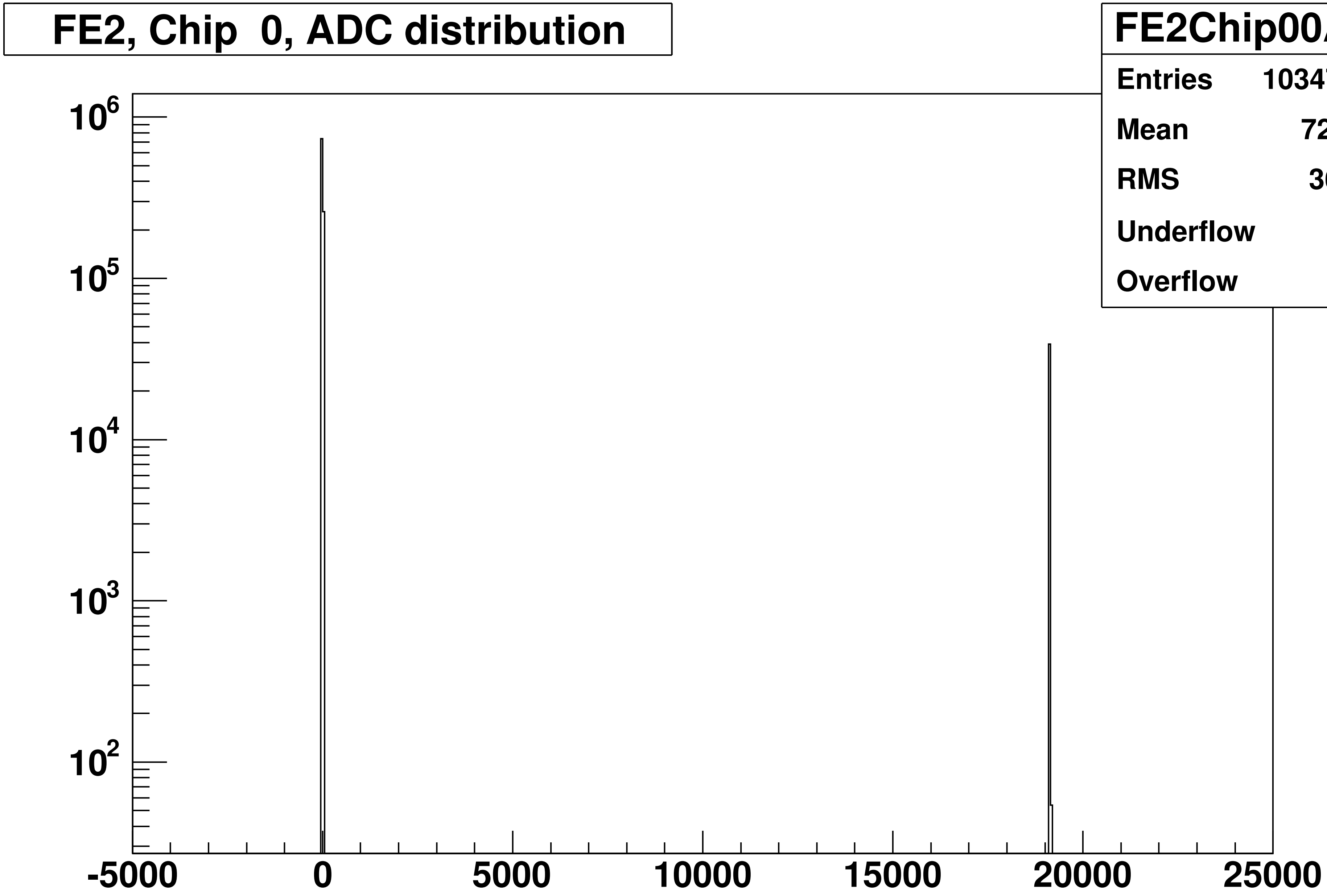
FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 1034784
Mean 723.8
RMS 3650
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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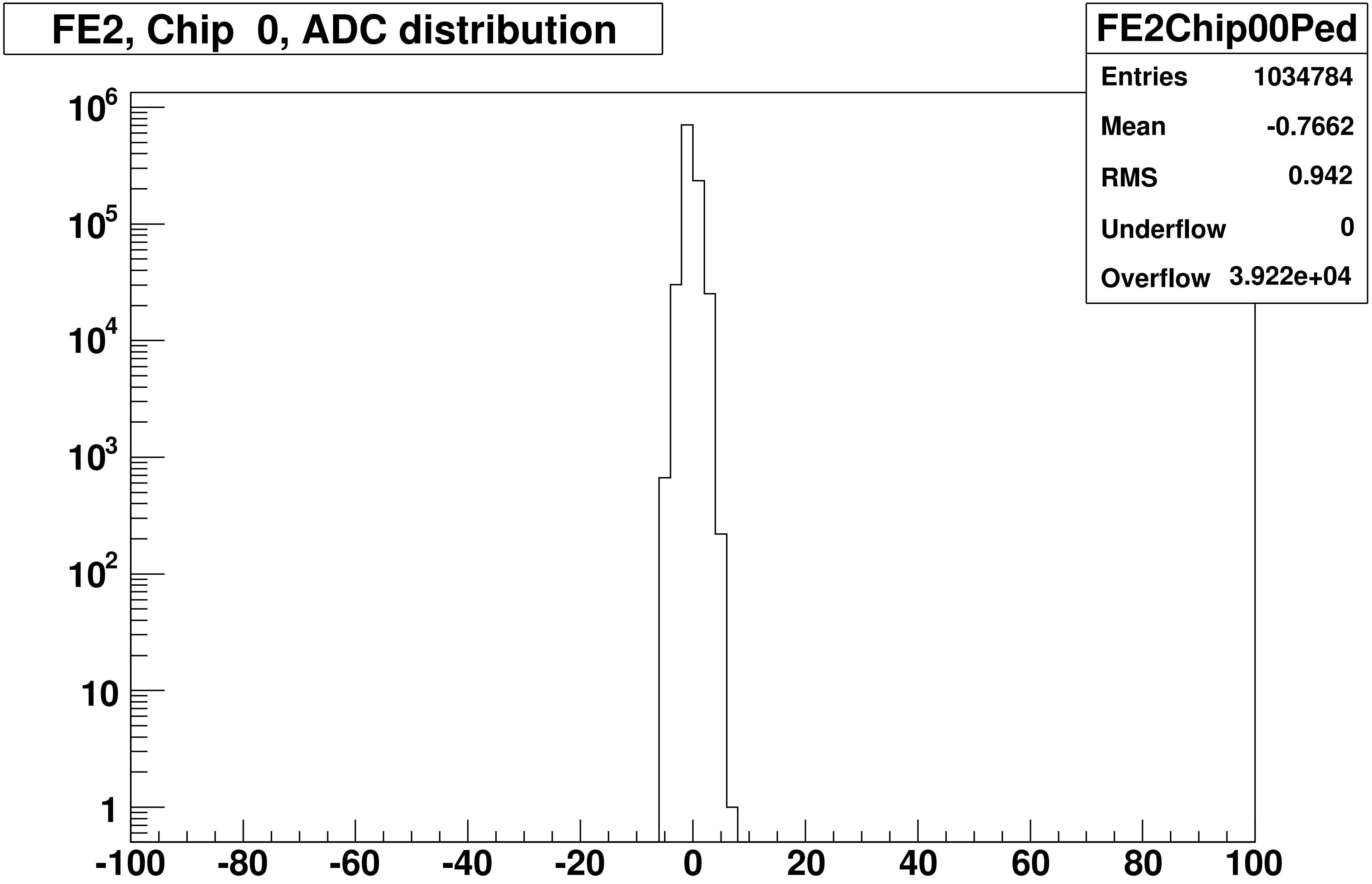
25000



FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 1034784
Mean -0.7662
RMS 0.942
Underflow 0
Overflow 3.922e+04
10^6
10^5
10^4
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10
1
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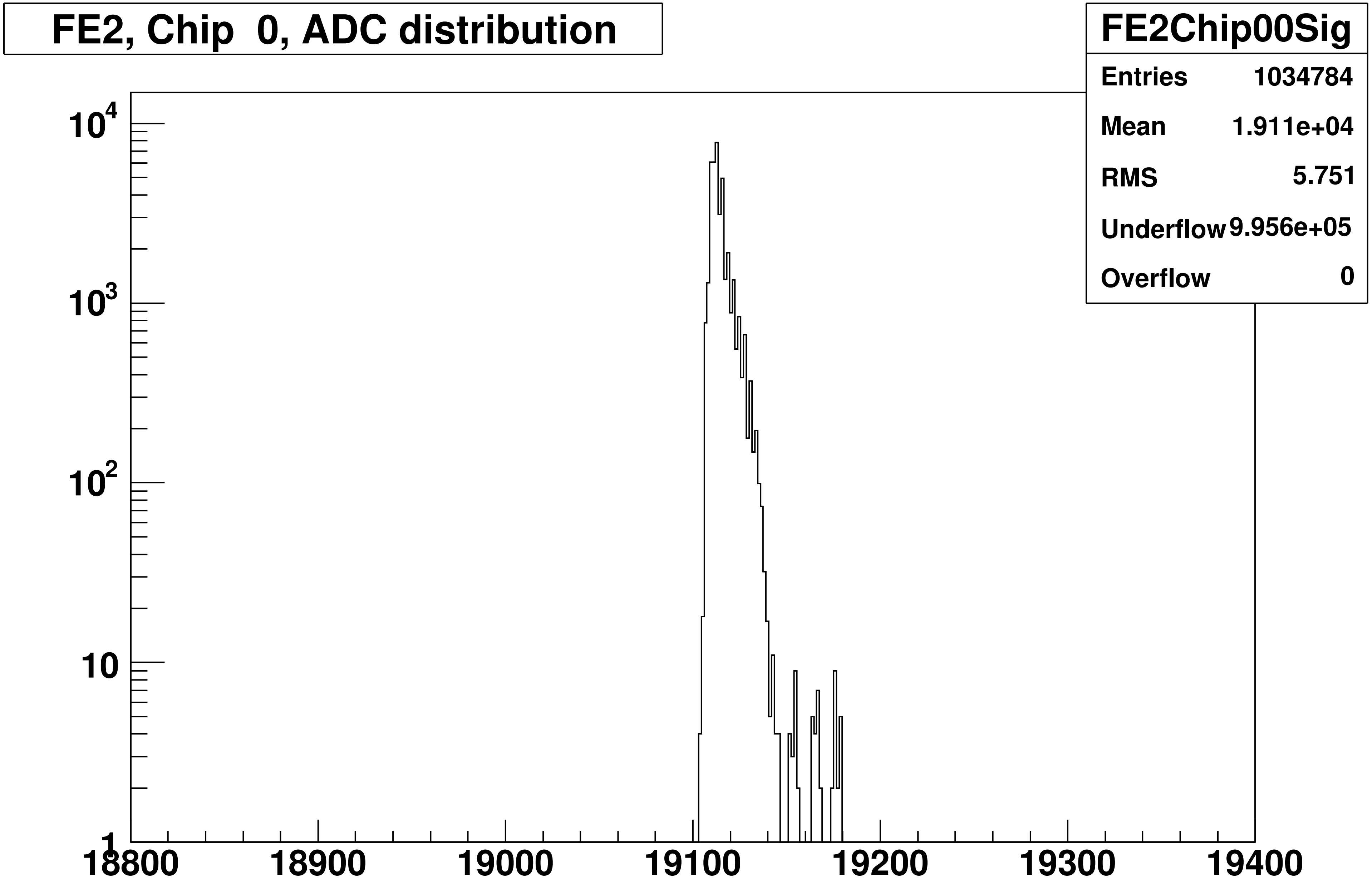
-20
0
20
40
60
80
100


FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 1034784
Mean 1.911e+04
RMS 5.751
Underflow 9.956e+05
Overflow 0
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10^3
10^2
10
1
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18900
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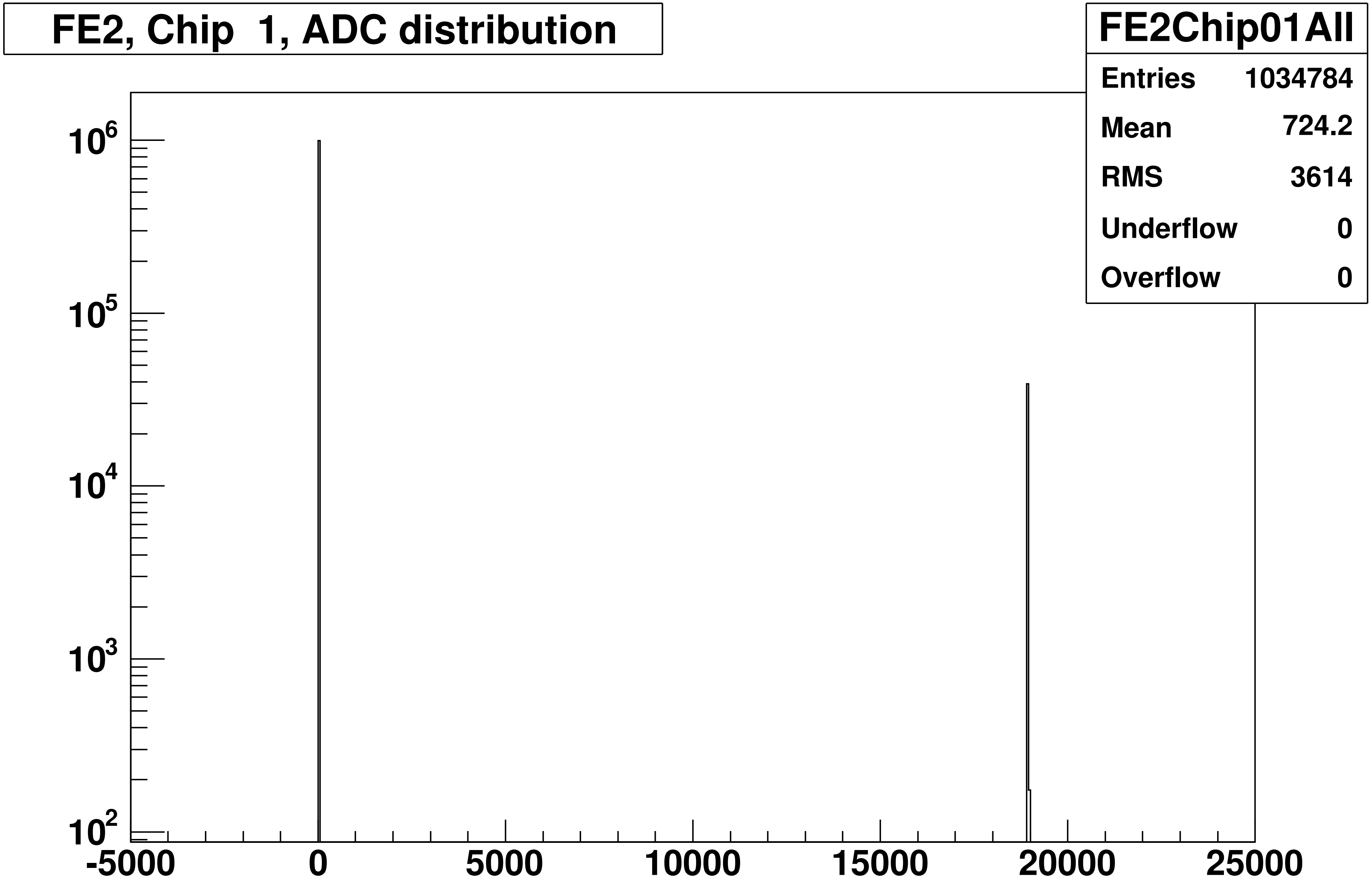
FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 1034784
Mean 724.2
RMS 3614
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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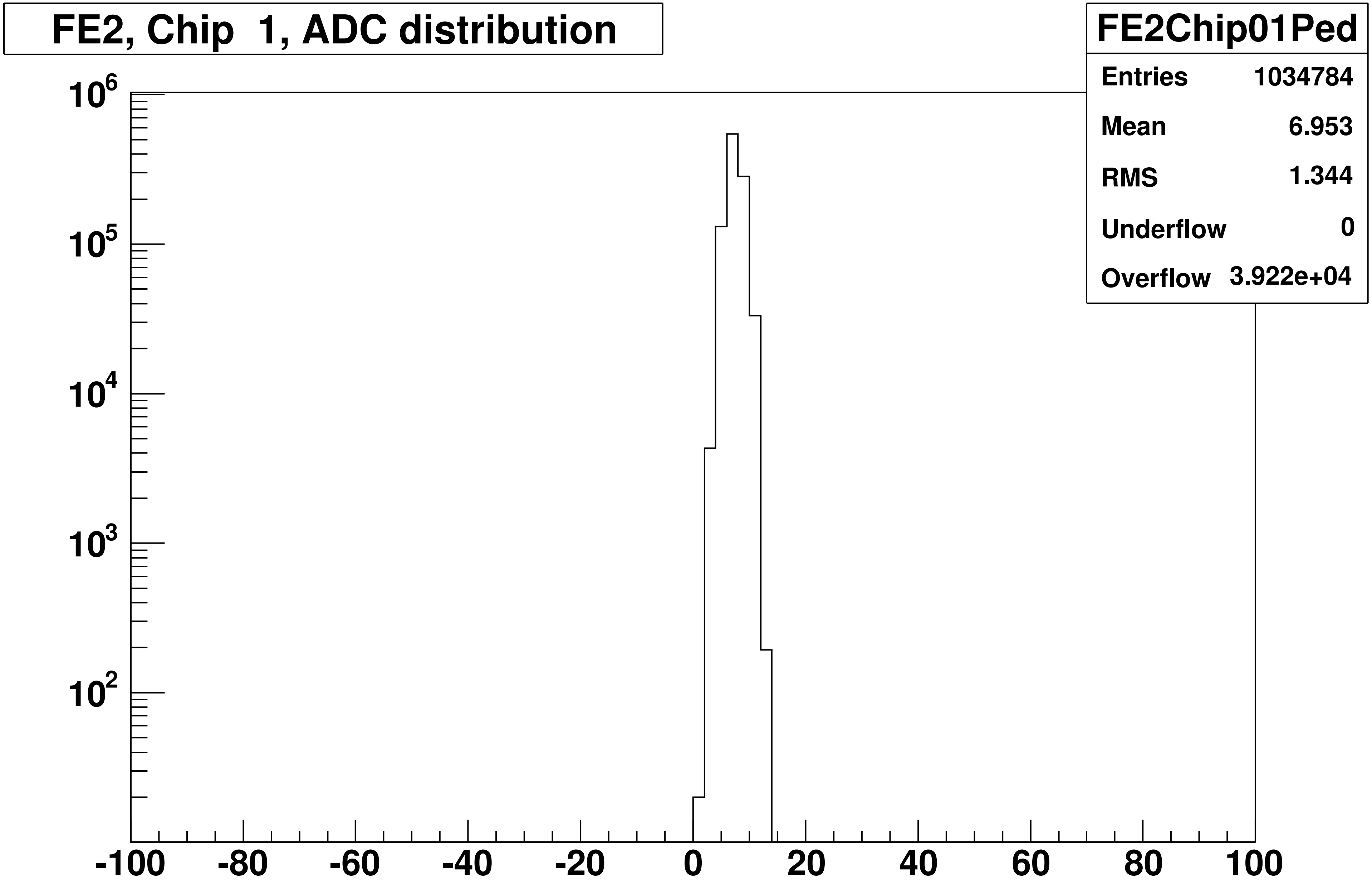
20000
25000


FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 1034784
Mean 6.953
RMS 1.344
Underflow 0
Overflow 3.922e+04
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10^5
10^4
10^3
10^2
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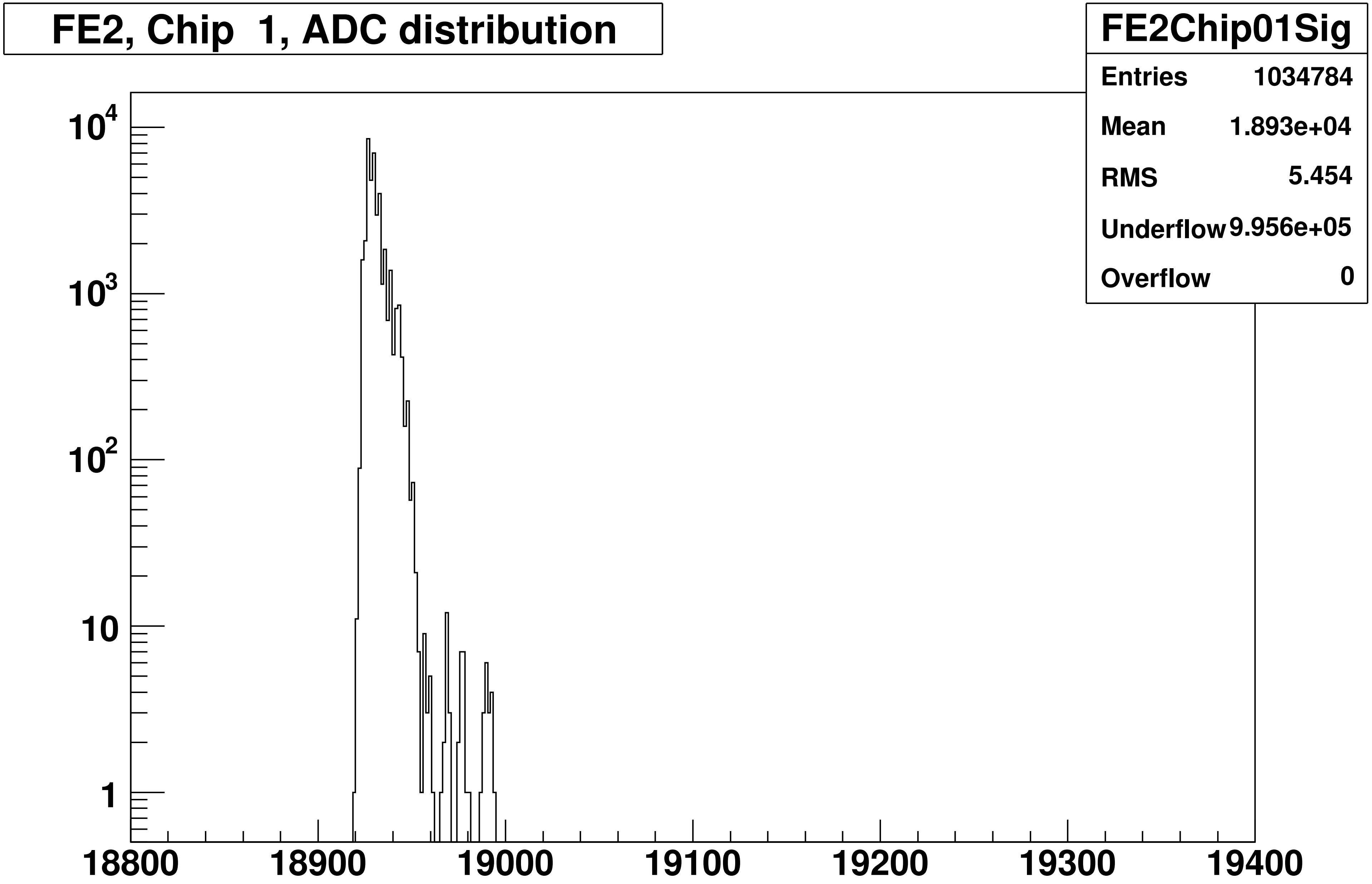
-20
0
20
40
60
80
100


FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 1034784
Mean 1.893e+04
RMS 5.454
Underflow 9.956e+05
Overflow 0
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10^3
10^2
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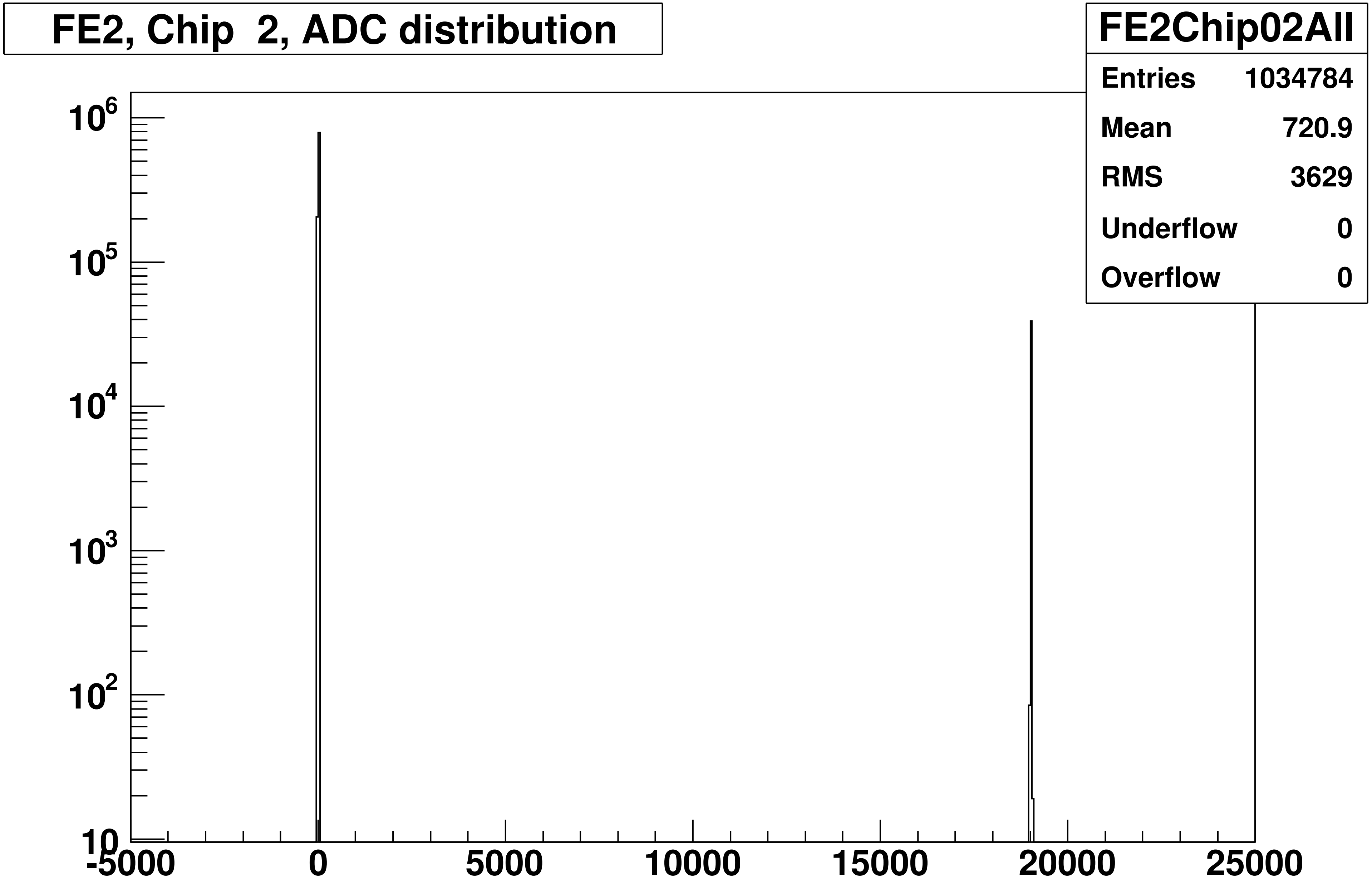

FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 1034784
Mean 720.9
RMS 3629
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
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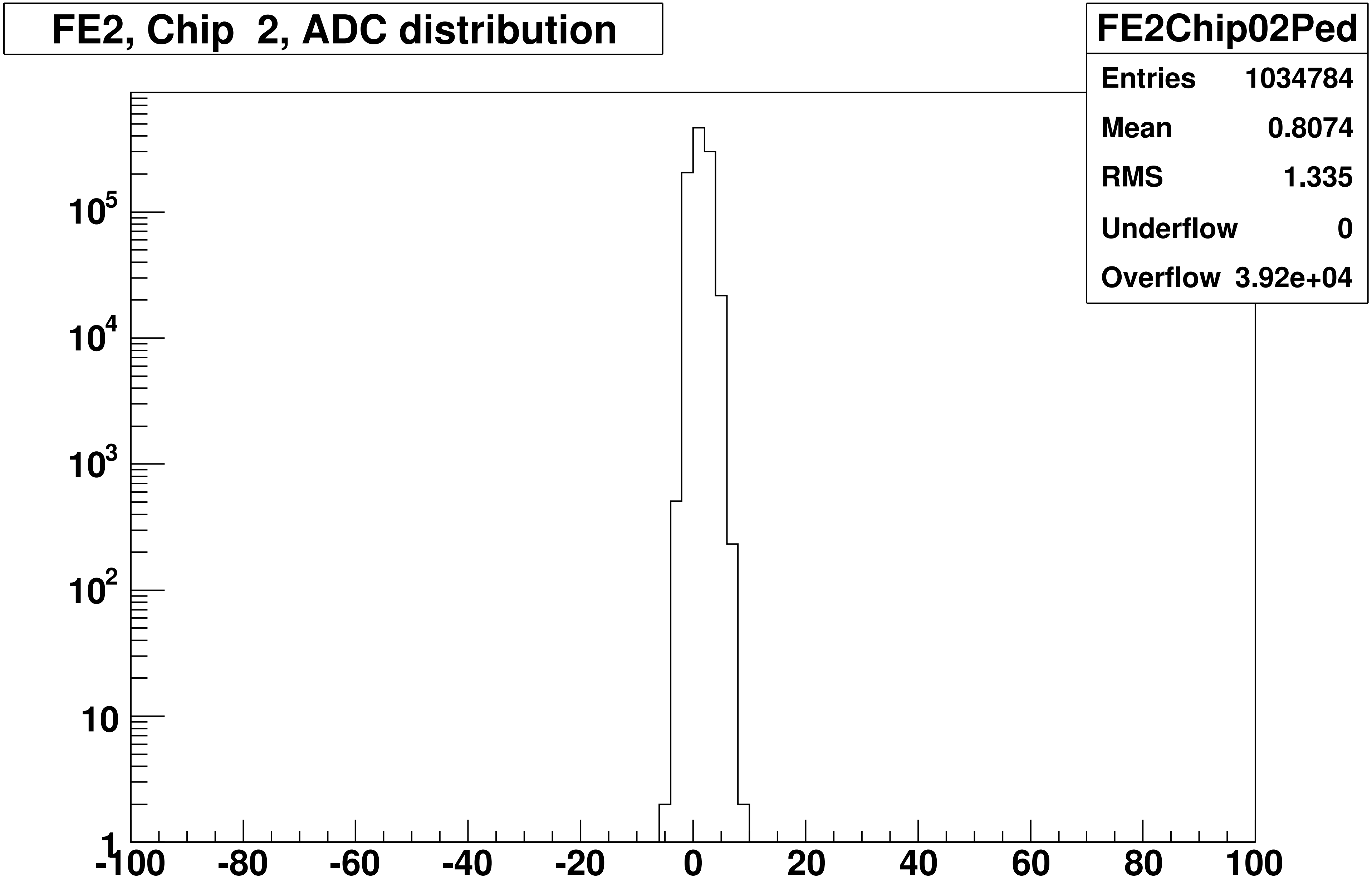
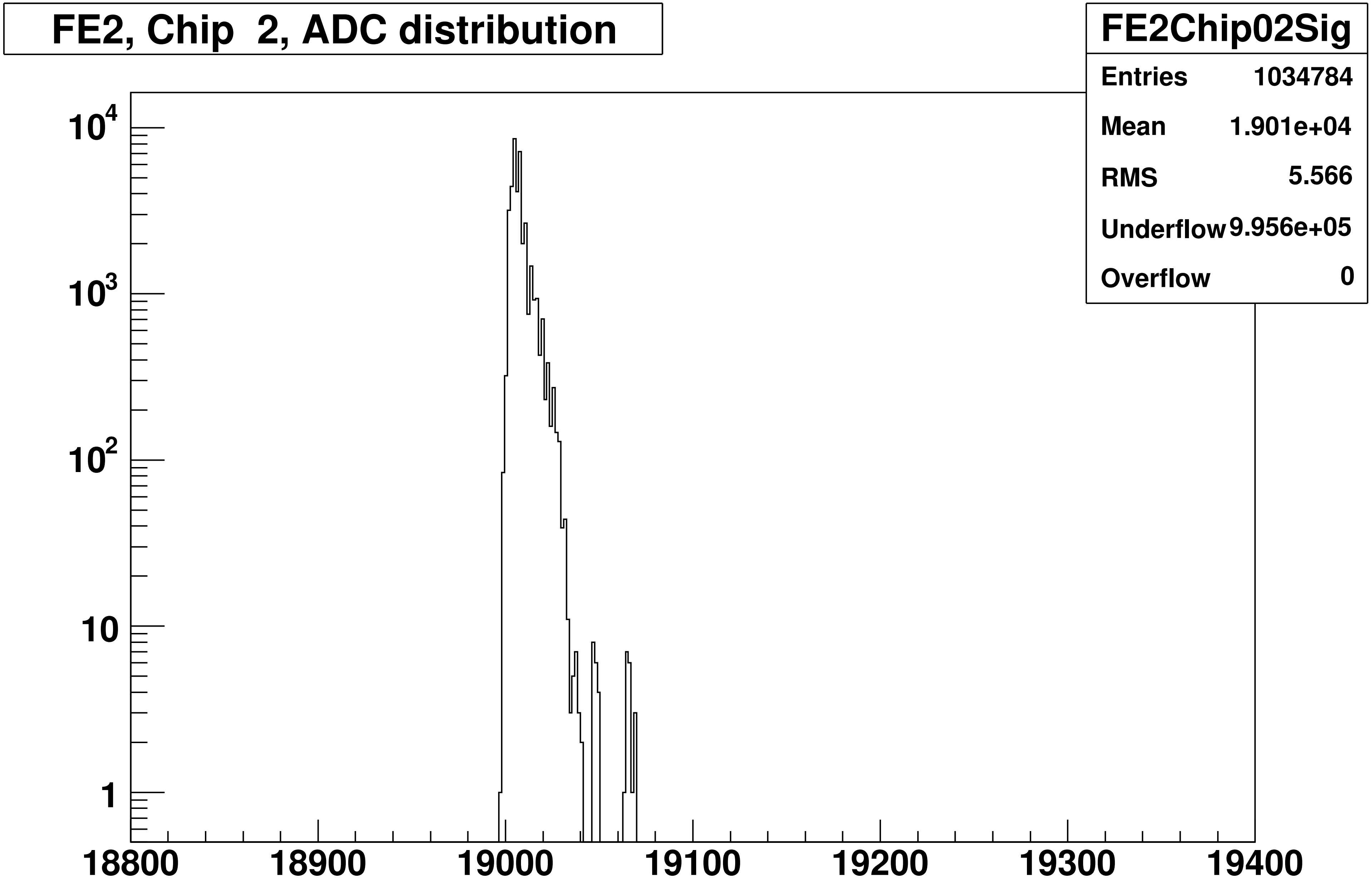

FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 1034784
Mean 0.8074
RMS 1.335
Underflow 0
Overflow 3.92e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 1034784
Mean 1.901e+04
RMS 5.566
Underflow 9.956e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
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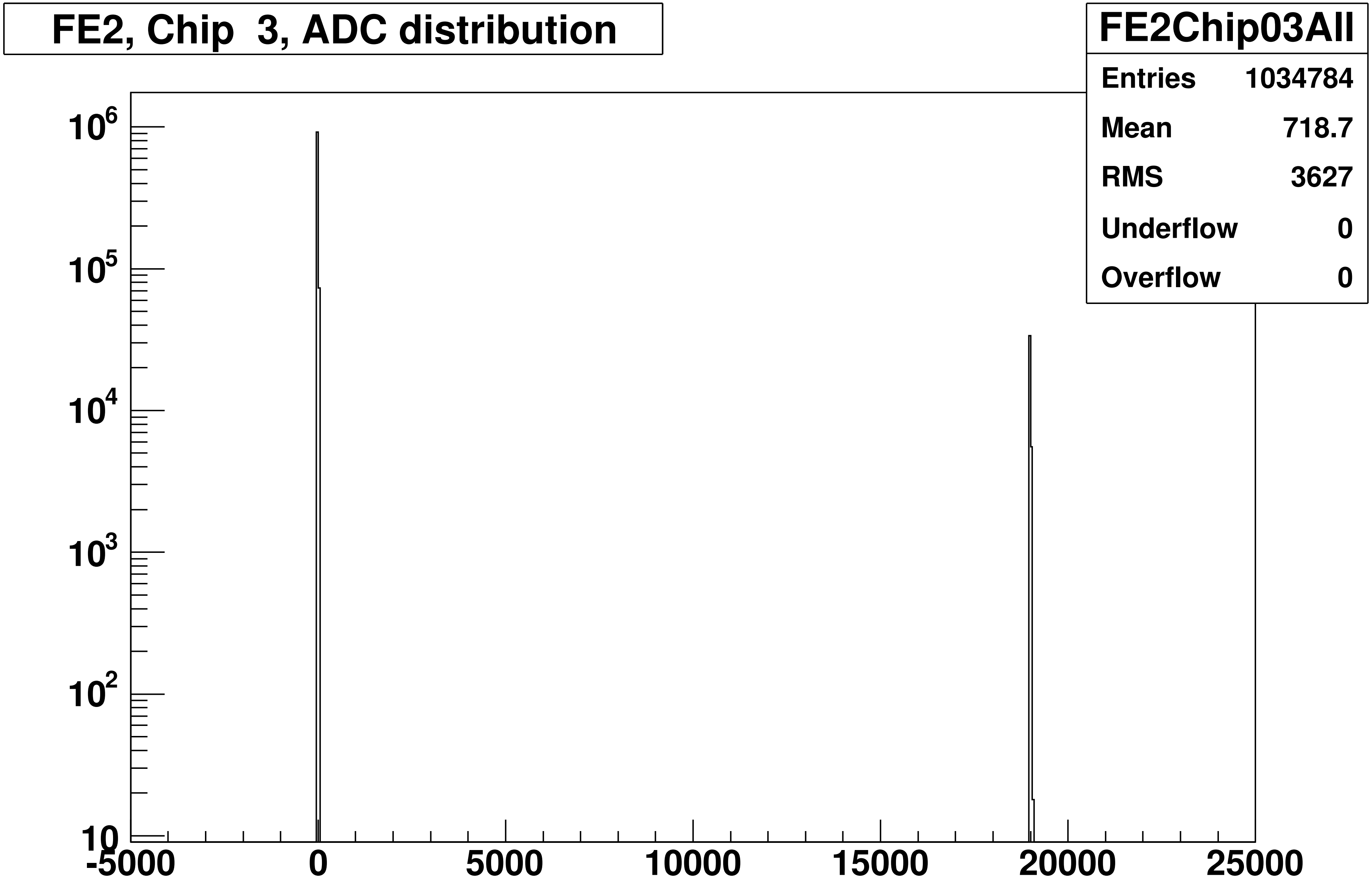
19300
19400


FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 1034784
Mean 718.7
RMS 3627
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
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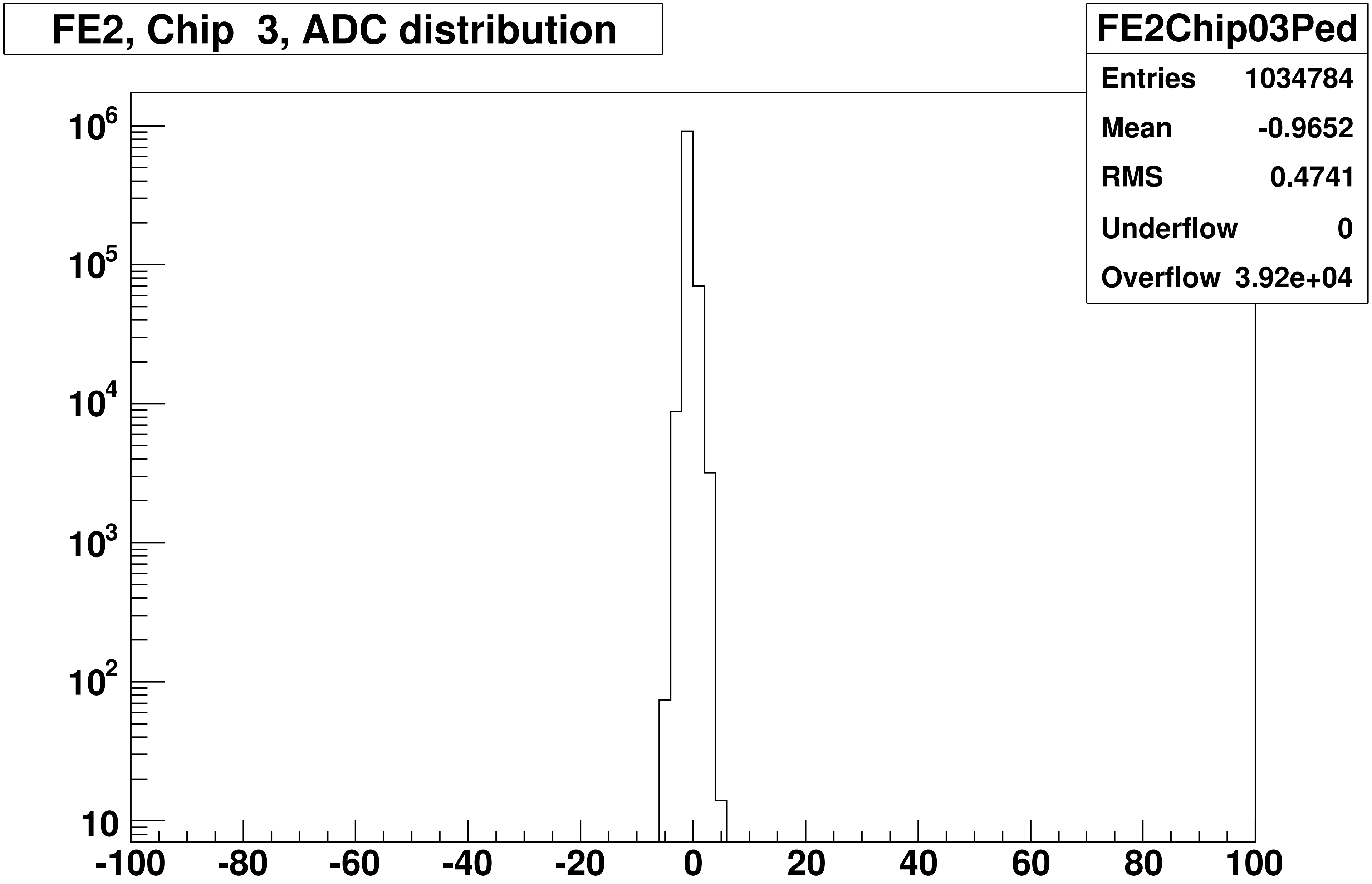
20000
25000


FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 1034784
Mean -0.9652
RMS 0.4741
Underflow 0
Overflow 3.92e+04
10^6
10^5
10^4
10^3
10^2
10
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-40
-20
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80
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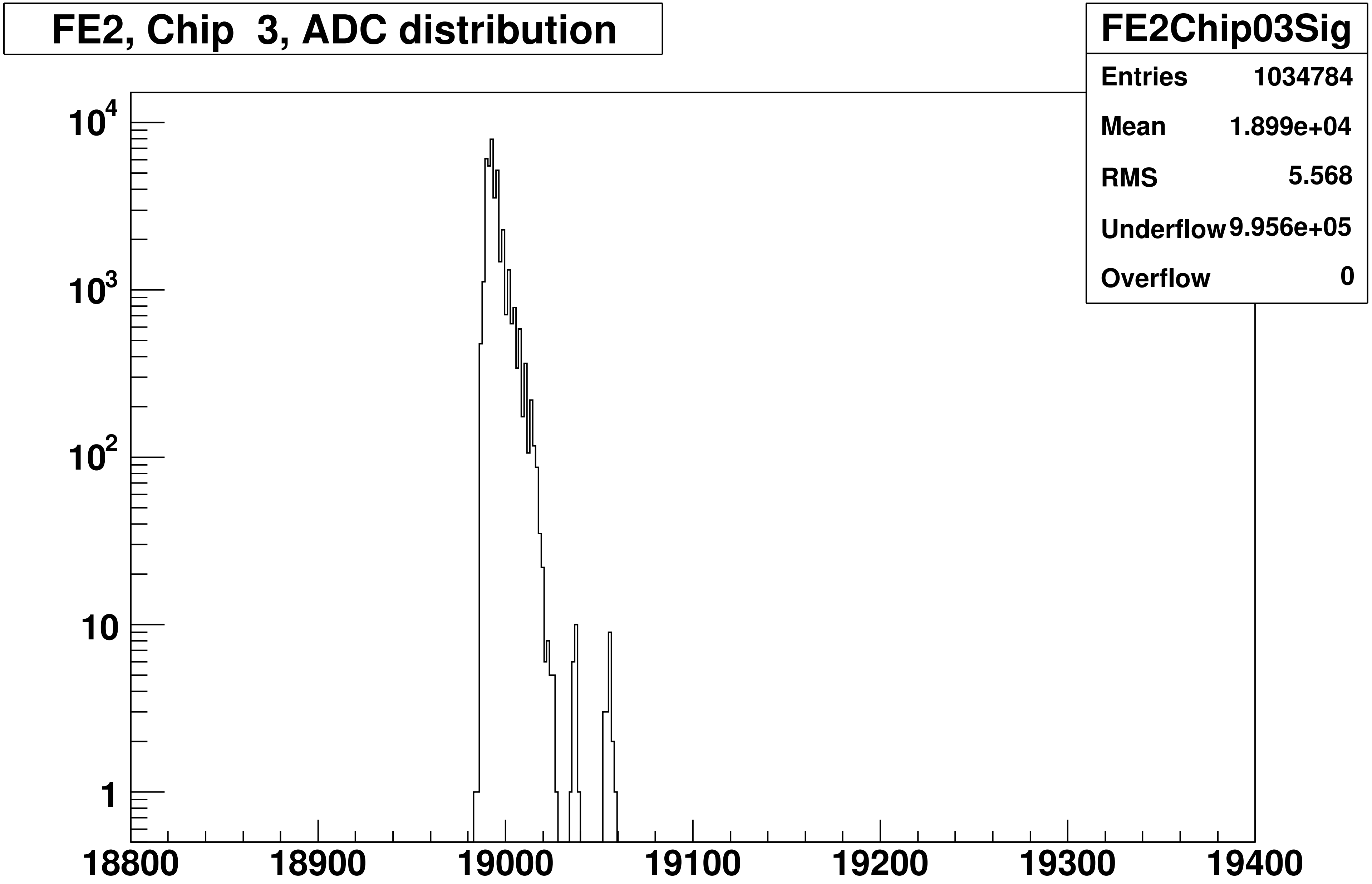
FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 1034784
Mean 1.899e+04
RMS 5.568
Underflow 9.956e+05
Overflow 0
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10^3
10^2
10
1
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18900
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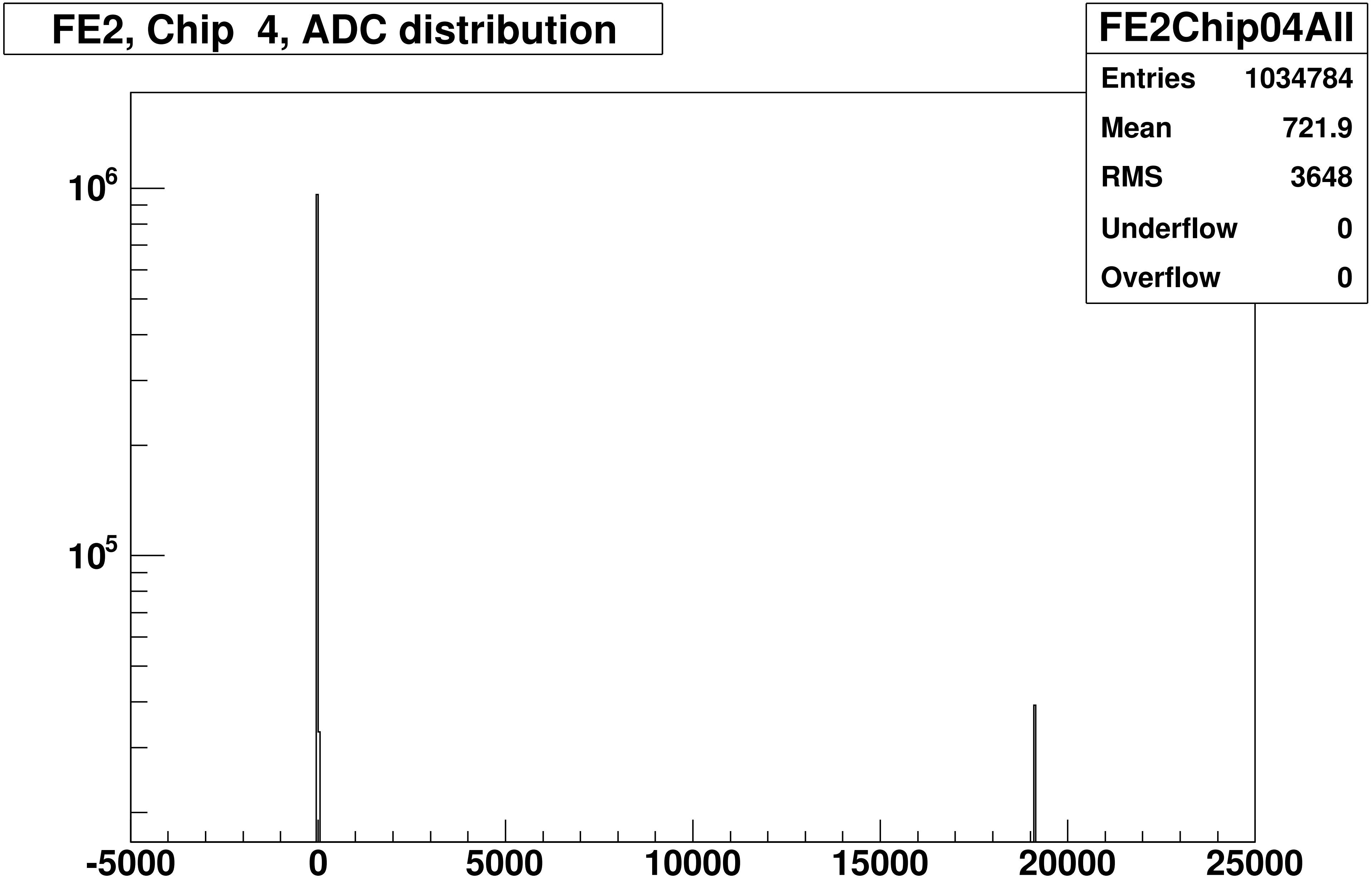
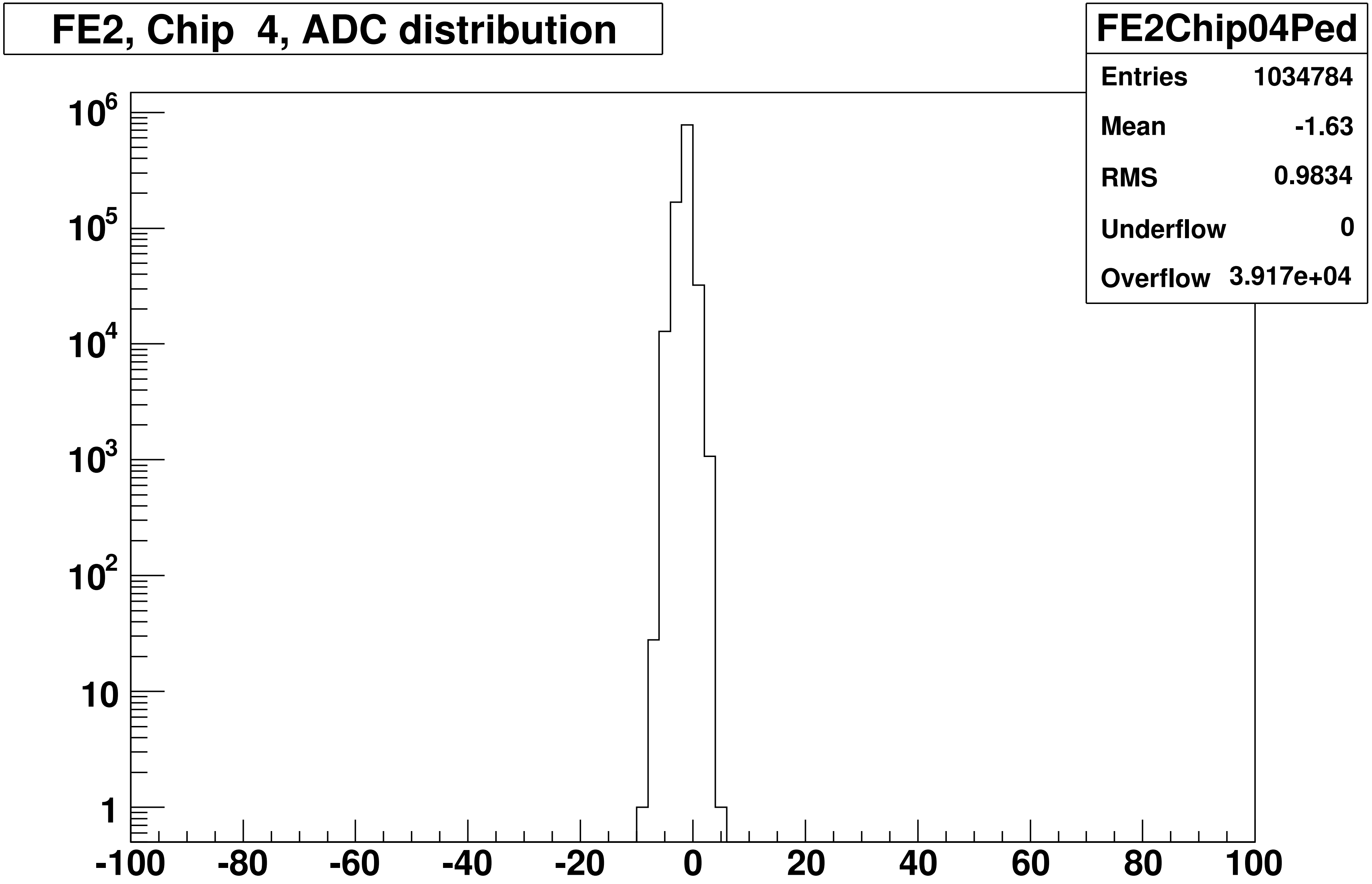


FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 1034784
Mean 721.9
RMS 3648
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 1034784
Mean -1.63
RMS 0.9834
Underflow 0
Overflow 3.917e+04
10^6
10^5
10^4
10^3
10^2
10
1
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-20
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80
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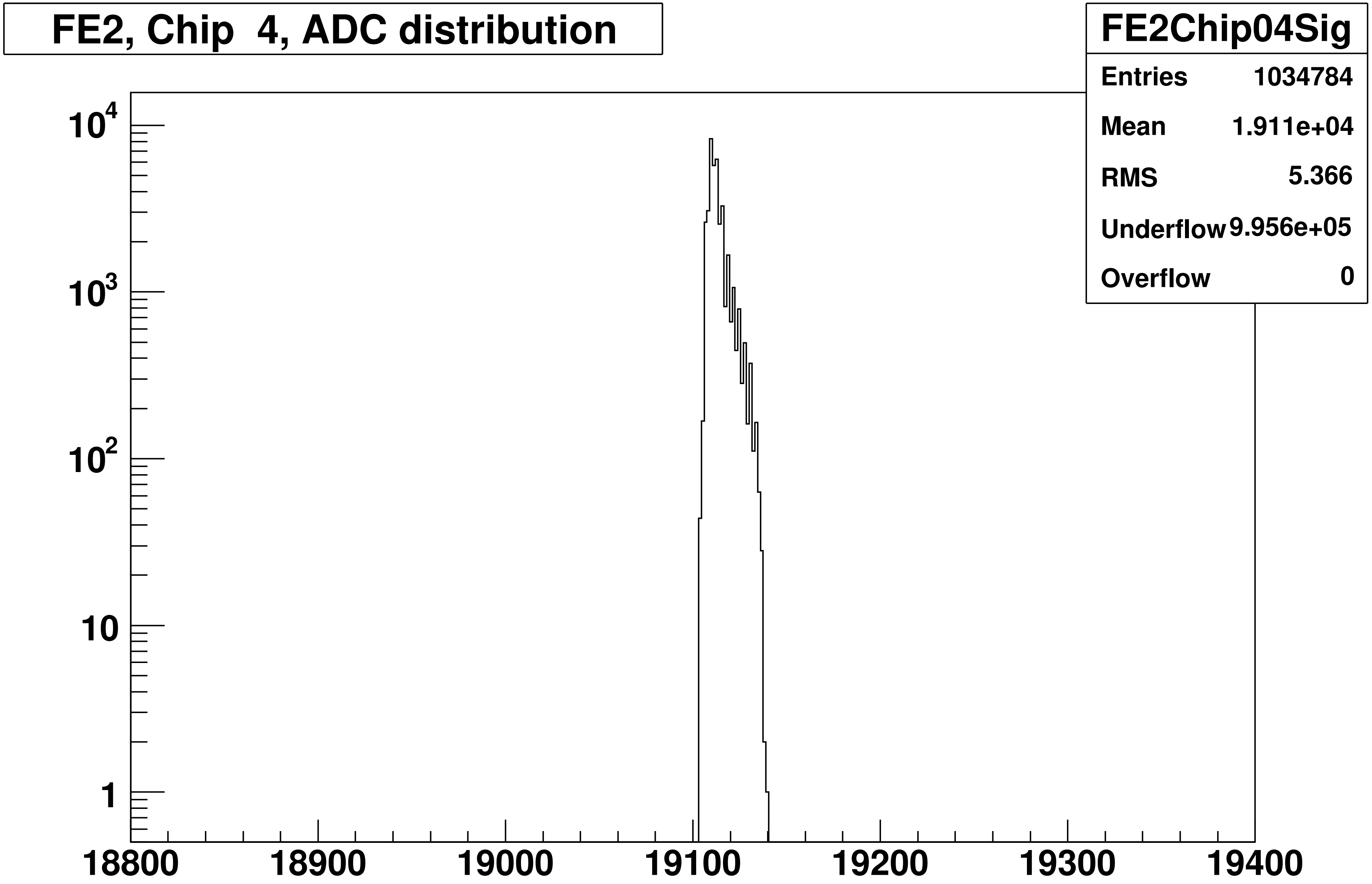

FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 1034784
Mean 1.911e+04
RMS 5.366
Underflow 9.956e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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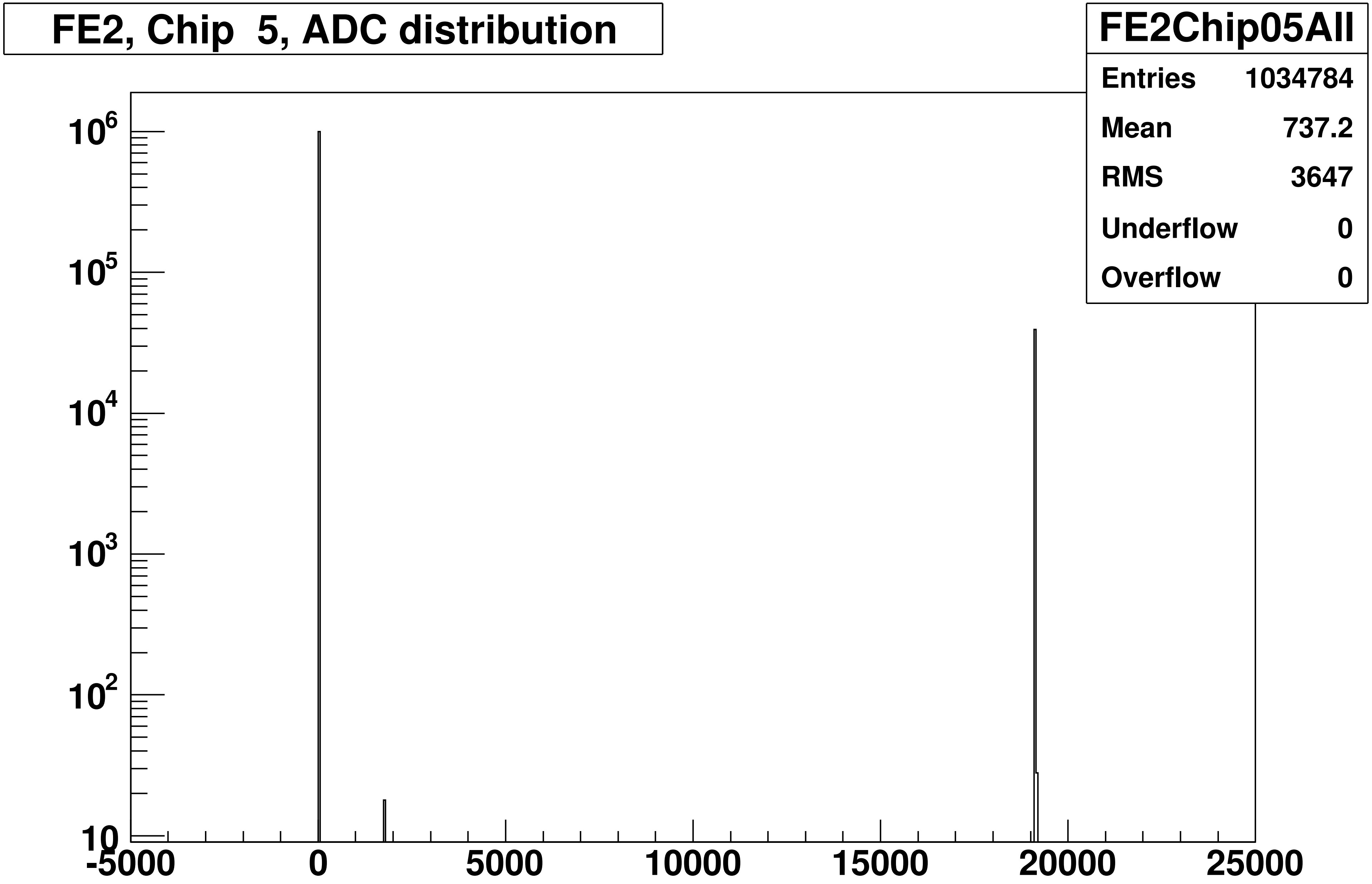
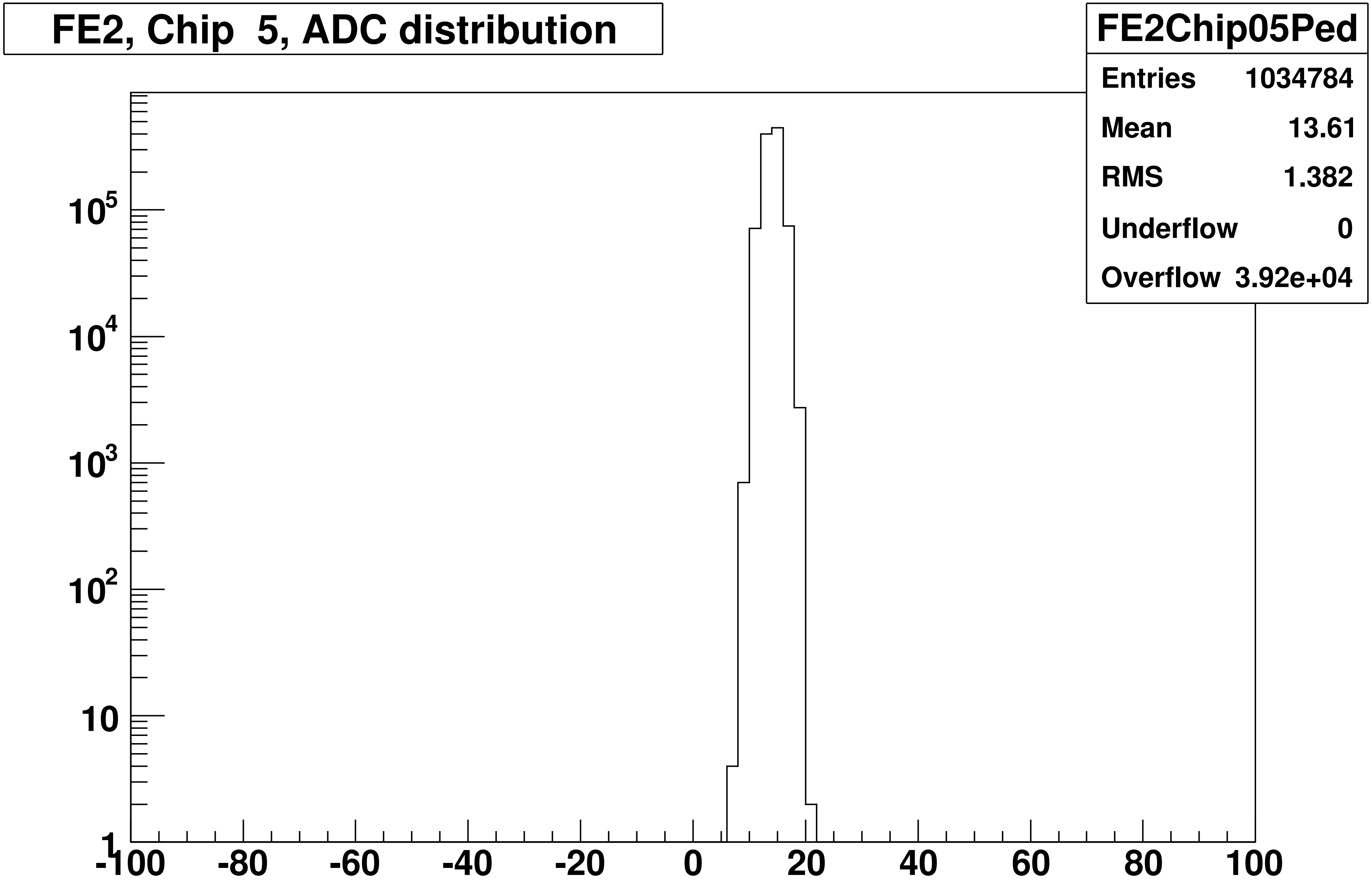


FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 1034784
Mean 737.2
RMS 3647
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 1034784
Mean 13.61
RMS 1.382
Underflow 0
Overflow 3.92e+04
10^5
10^4
10^3
10^2
10
1
-100
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-20
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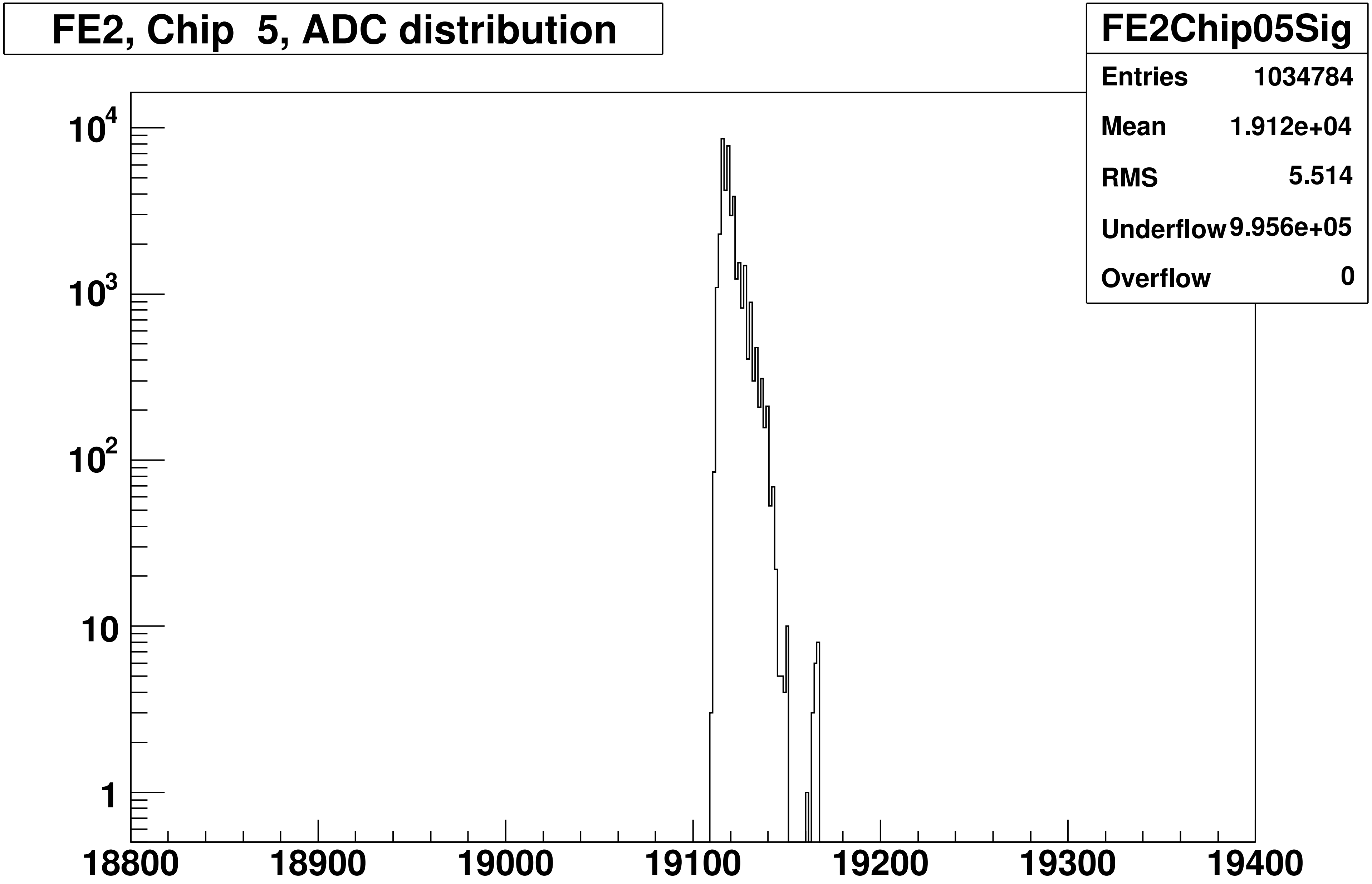
20
40
60
80
100



FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 1034784
Mean 1.912e+04
RMS 5.514
Underflow 9.956e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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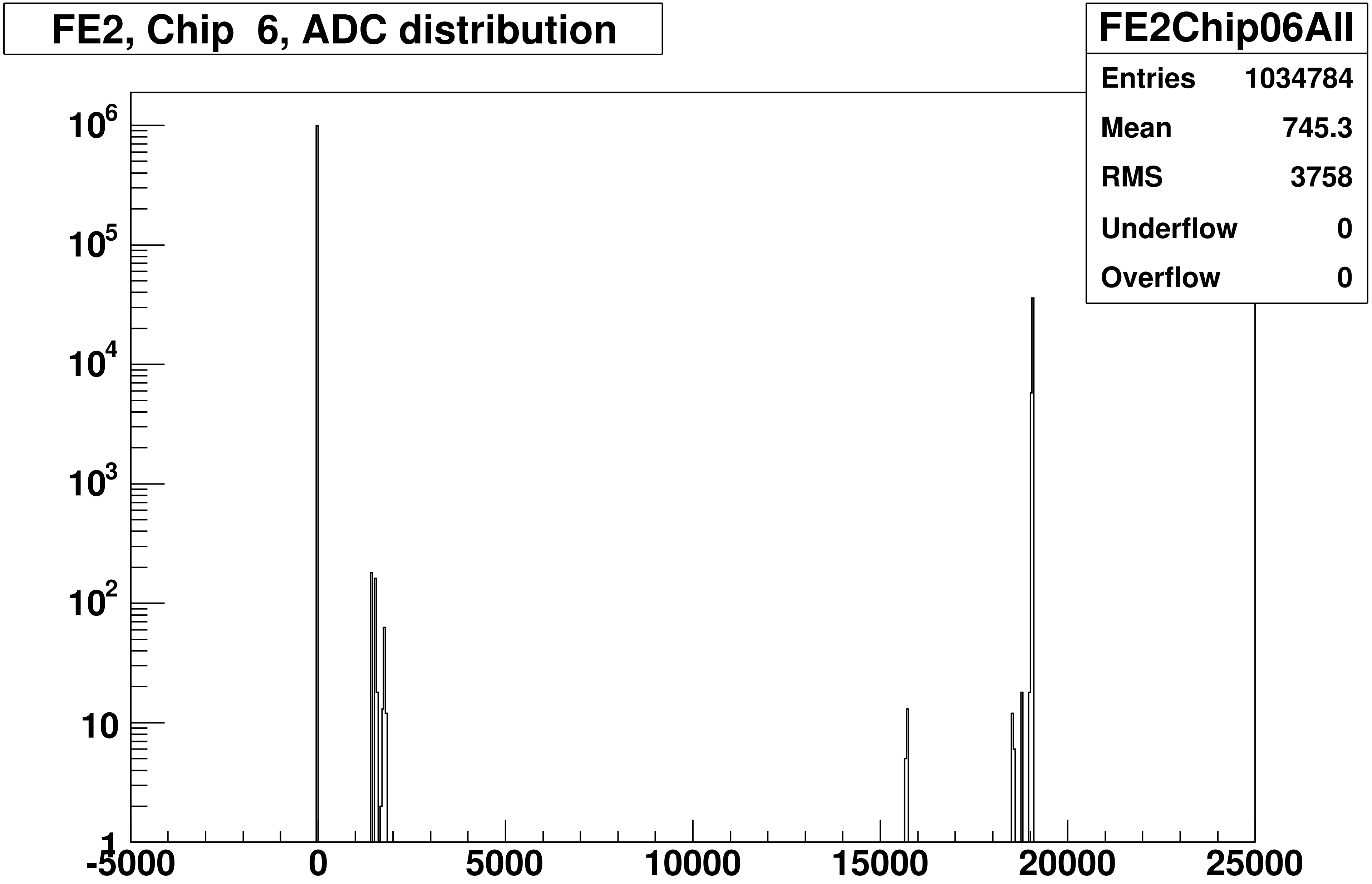
19100
19200
19300
19400


FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 1034784
Mean 745.3
RMS 3758
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
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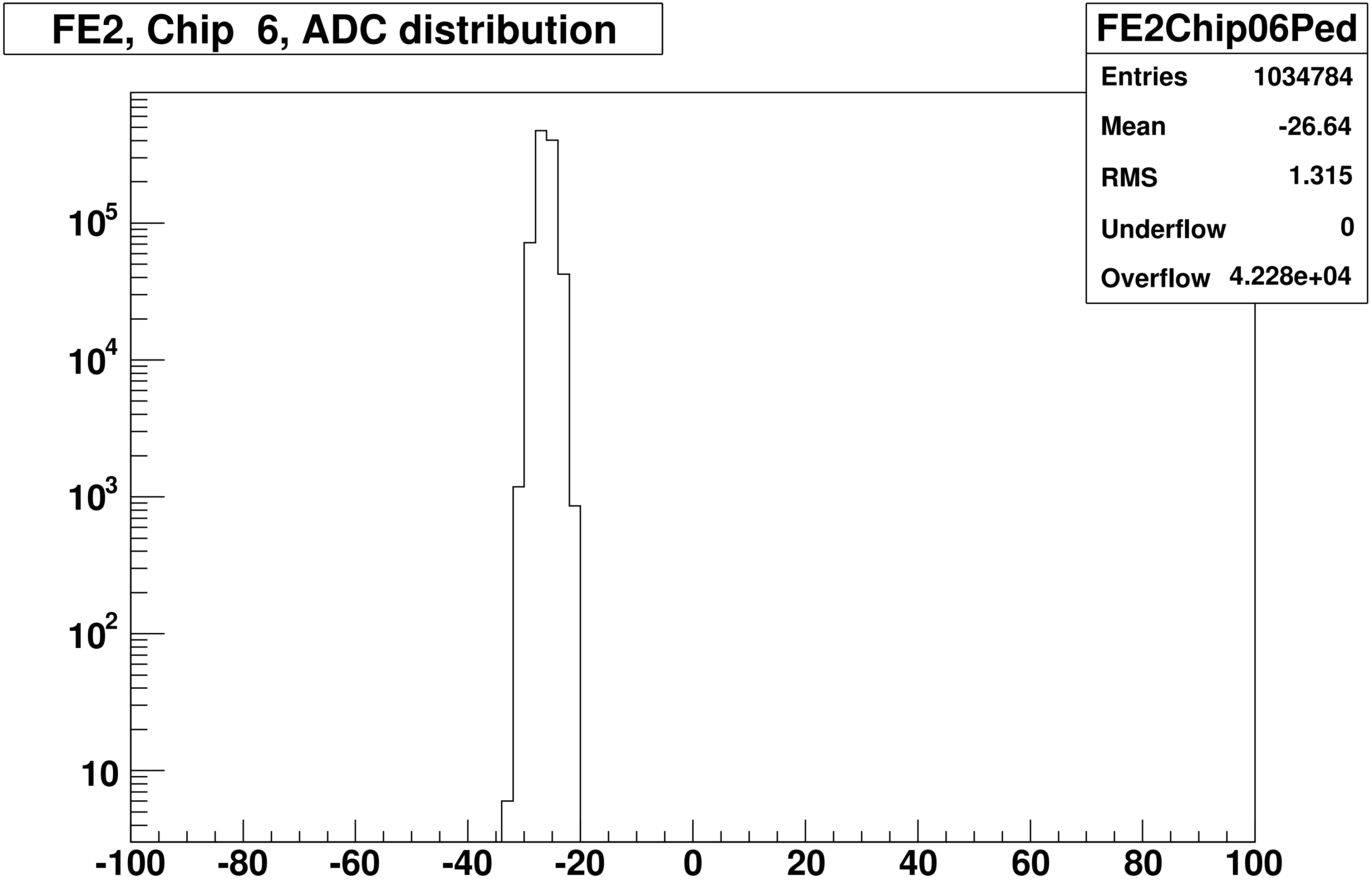
20000
25000



FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 1034784
Mean -26.64
RMS 1.315
Underflow 0
Overflow 4.228e+04
10
10^2
10^3
10^4
10^5
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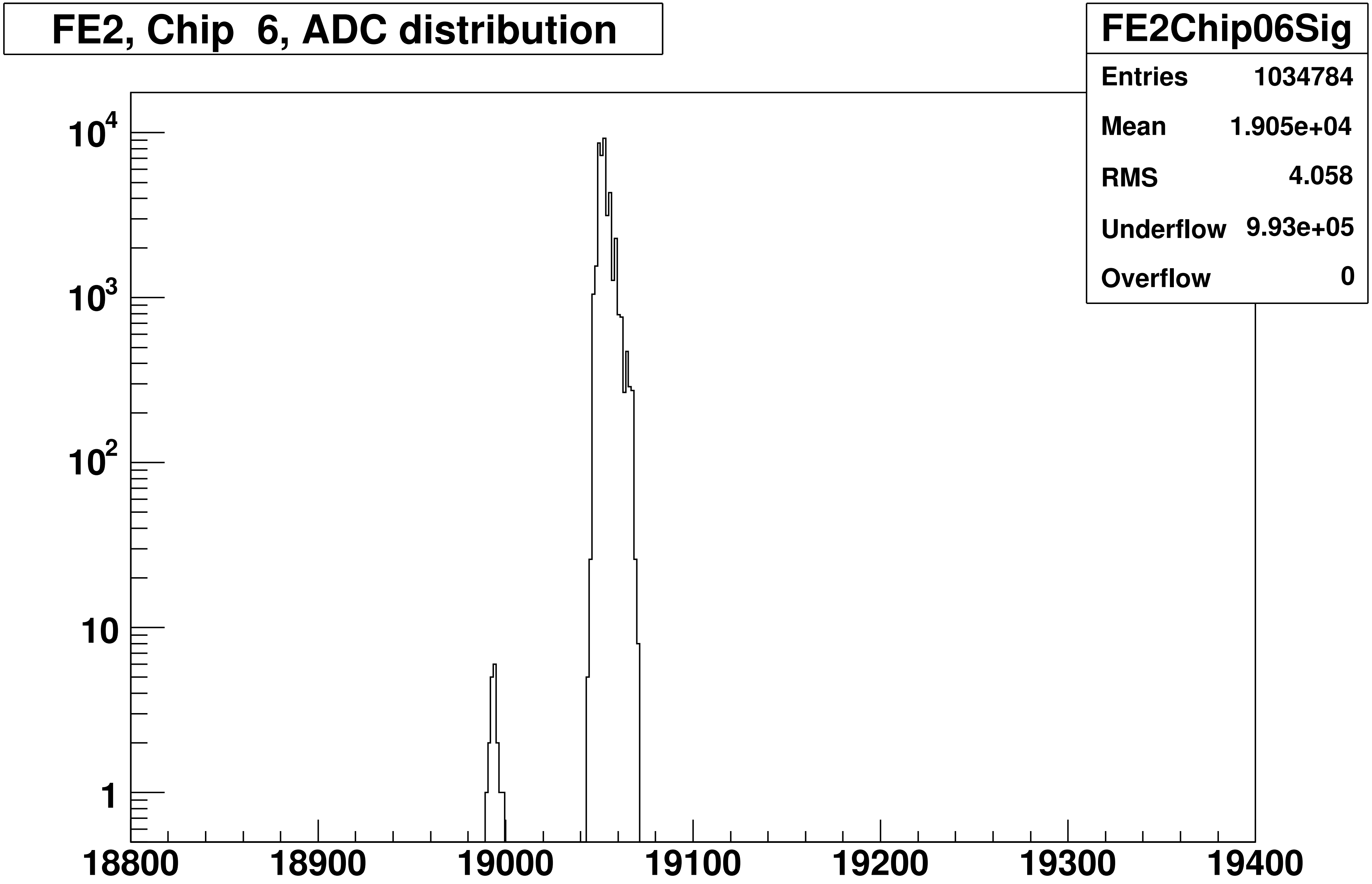
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 1034784
Mean 1.905e+04
RMS 4.058
Underflow 9.93e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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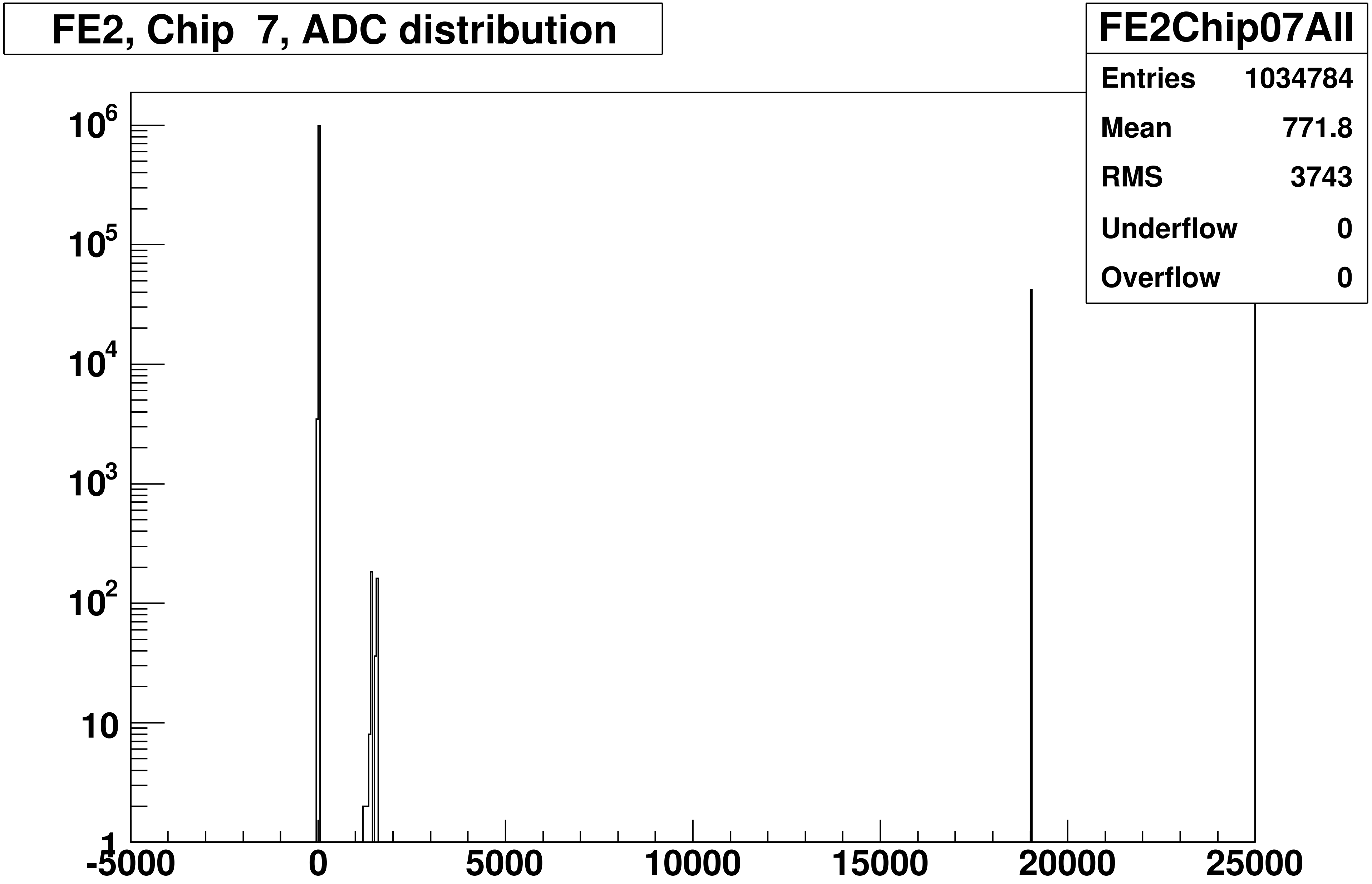
19200
19300
19400


FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 1034784
Mean 771.8
RMS 3743
Underflow 0
Overflow 0
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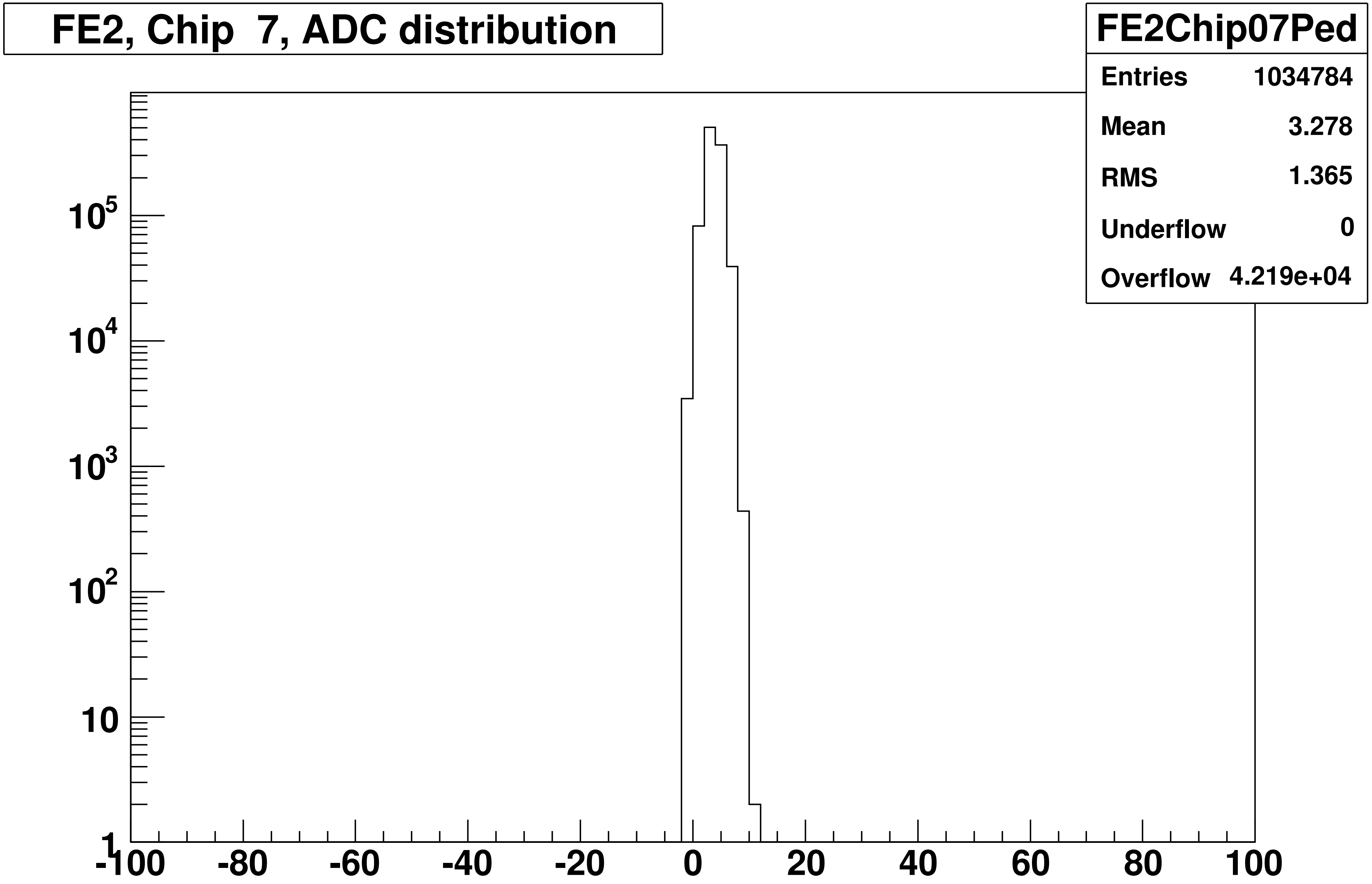
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 1034784
Mean 3.278
RMS 1.365
Underflow 0
Overflow 4.219e+04
10^5
10^4
10^3
10^2
10
1
-100
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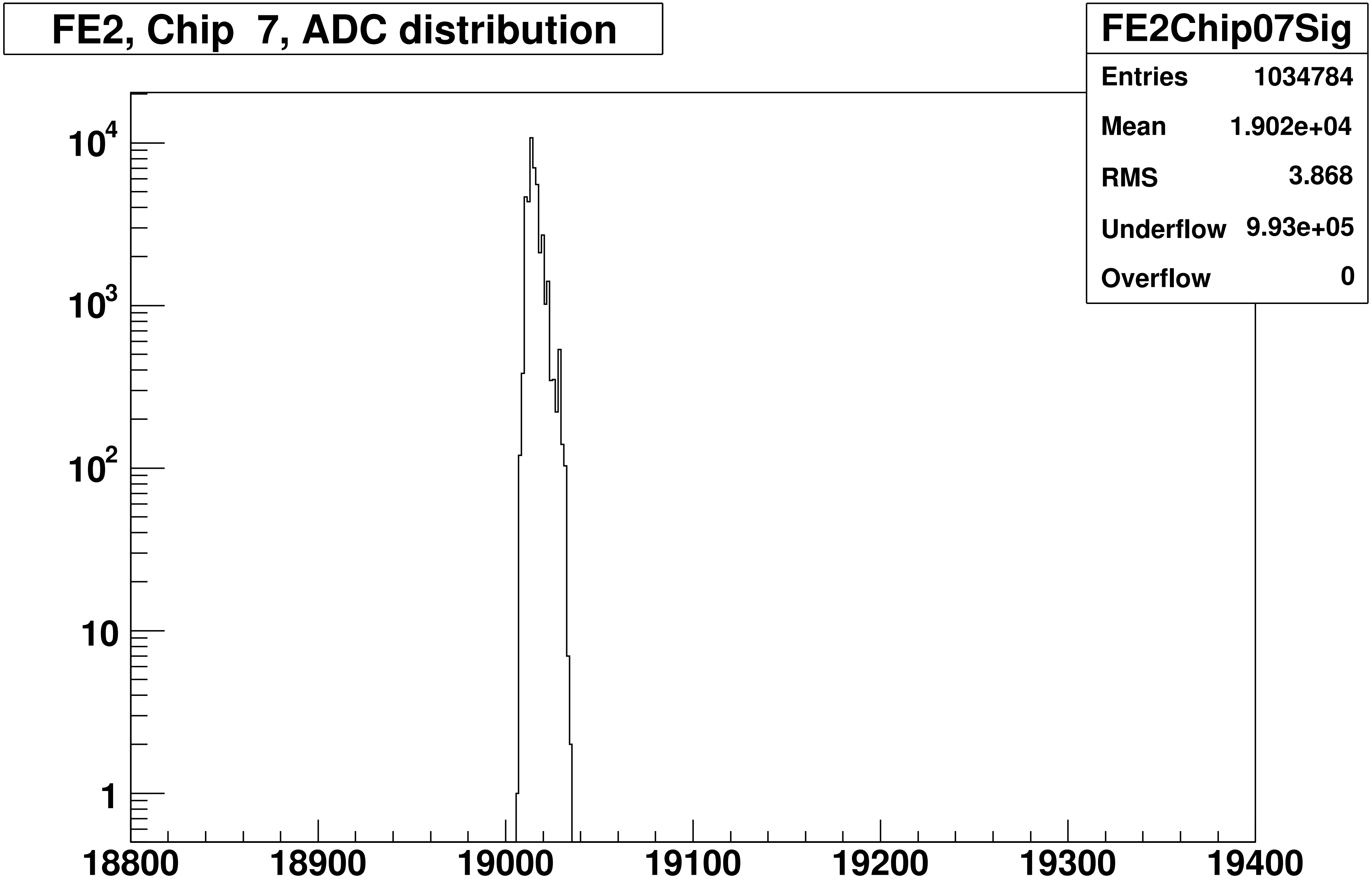
-40
-20
0
20
40
60
80
100


FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 1034784
Mean 1.902e+04
RMS 3.868
Underflow 9.93e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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19100
19200
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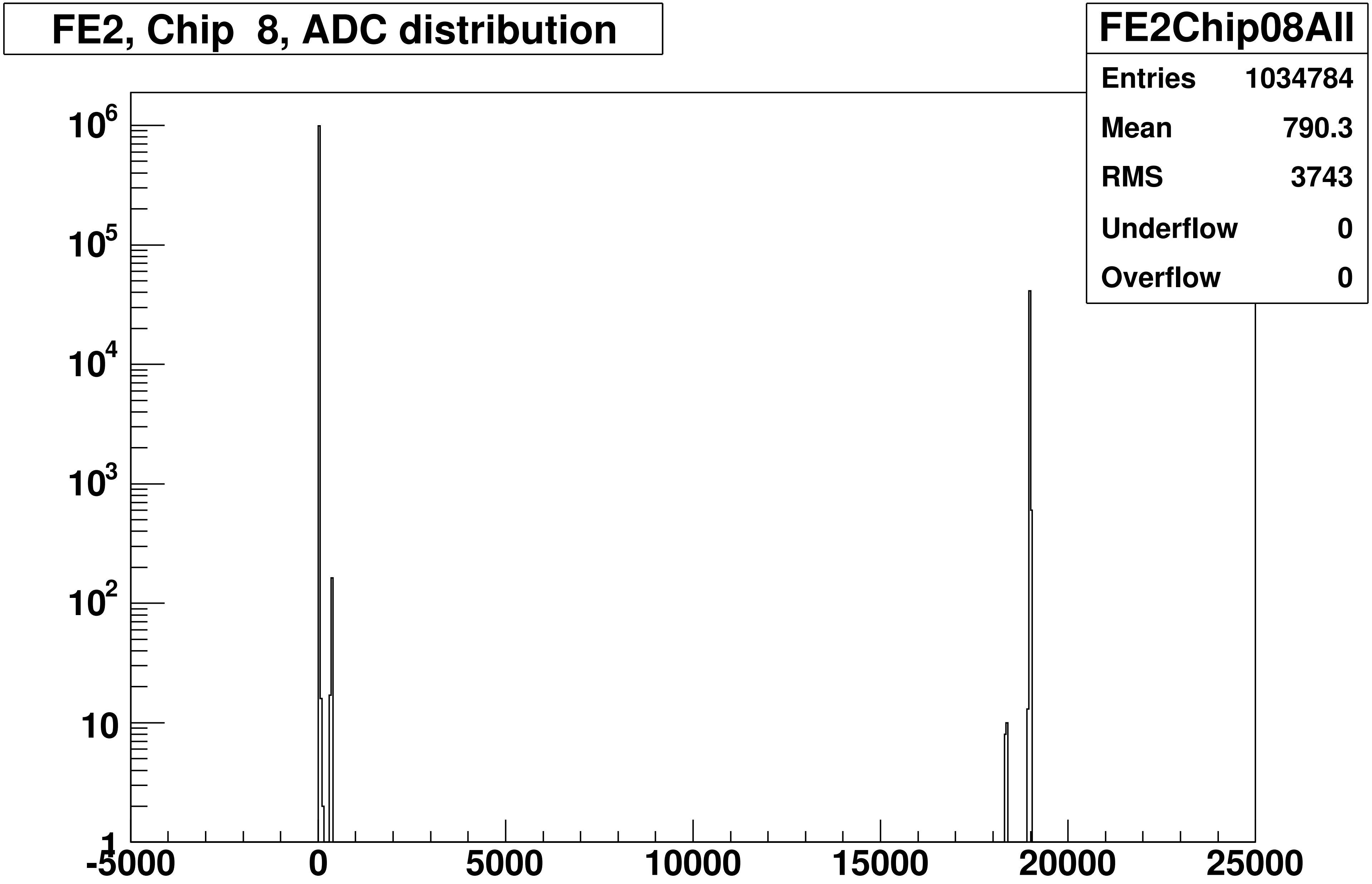
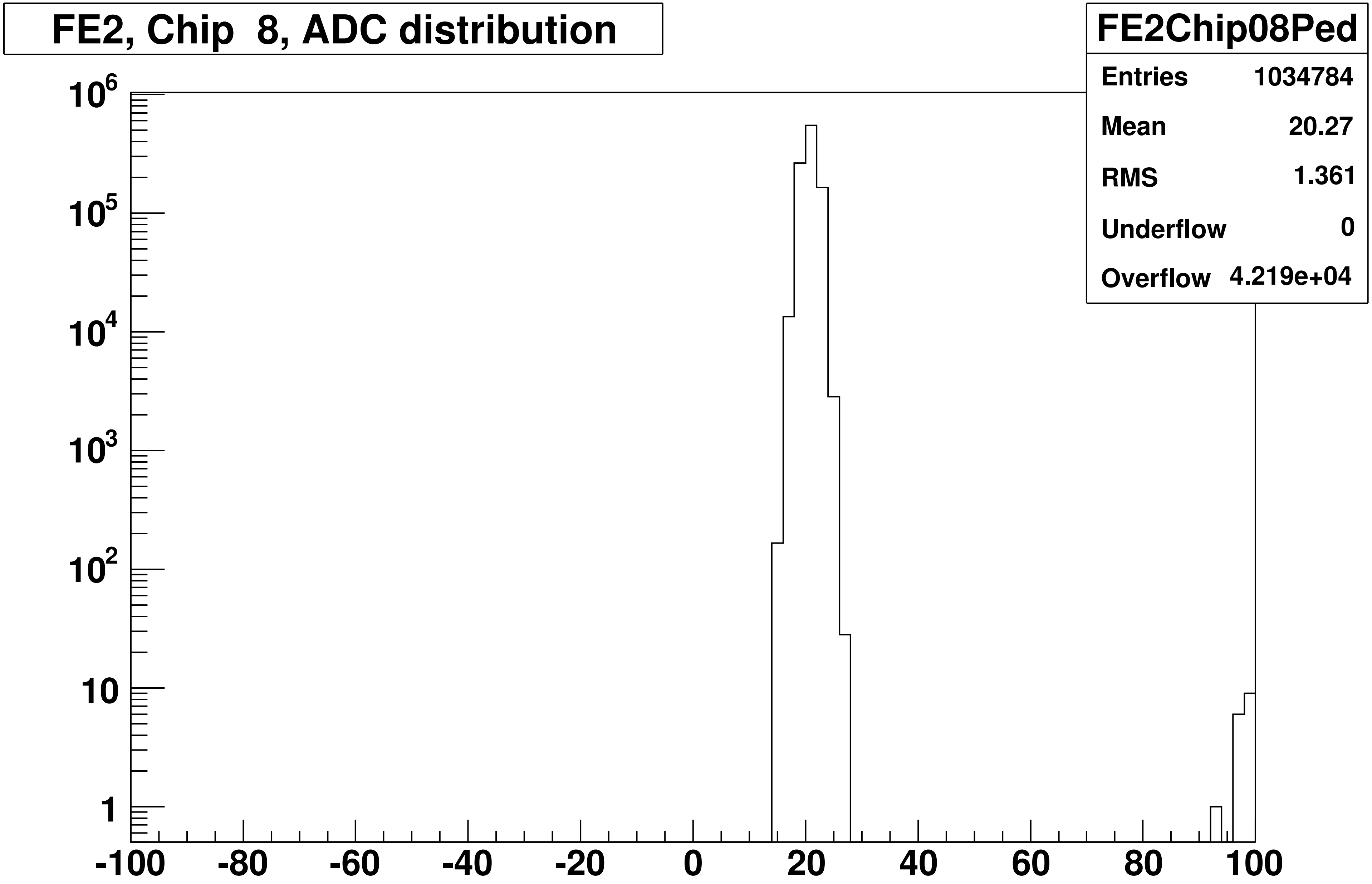


FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 1034784
Mean 790.3
RMS 3743
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 1034784
Mean 20.27
RMS 1.361
Underflow 0
Overflow 4.219e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
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-40
-20
0
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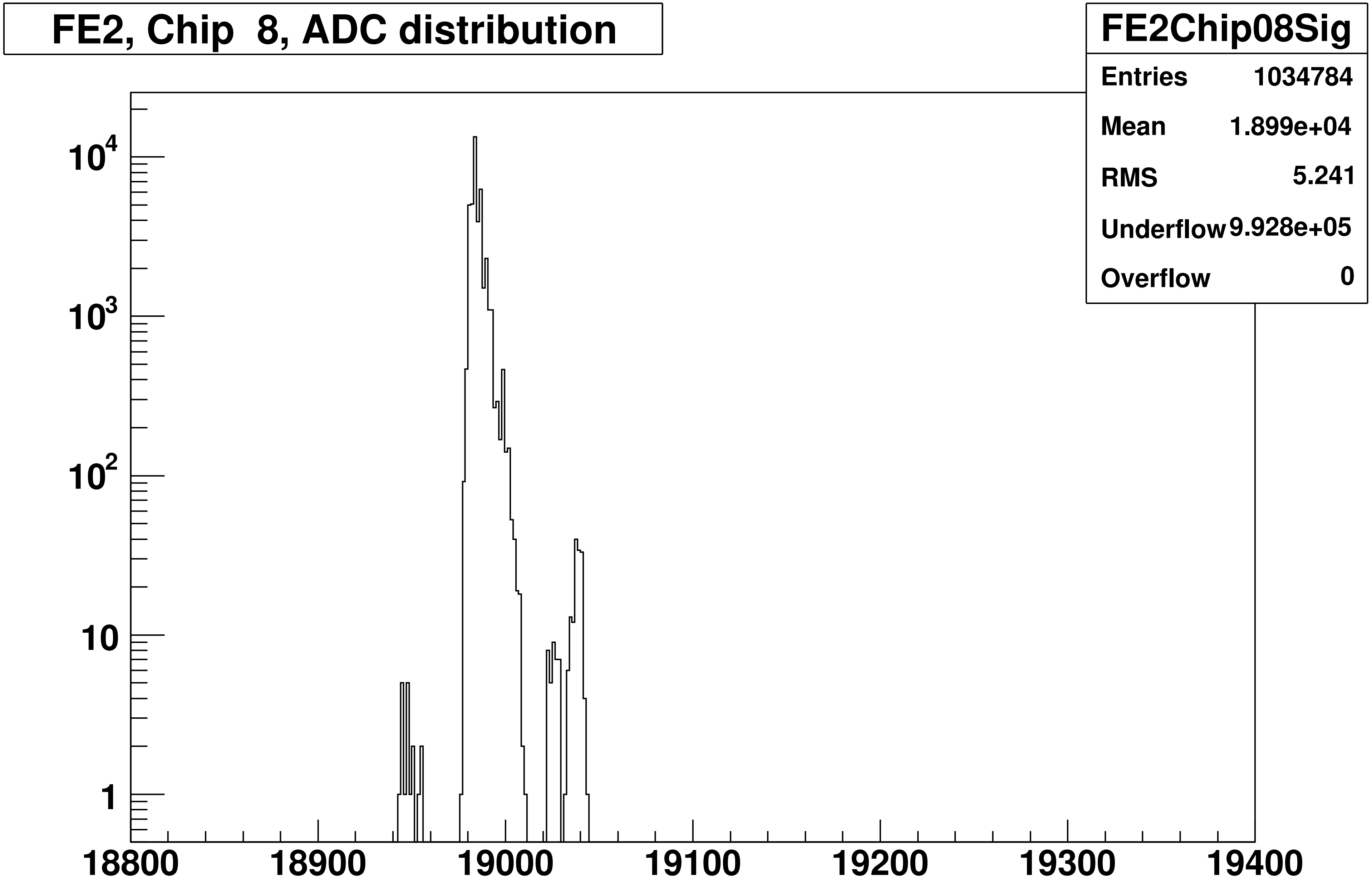
60
80
100



FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 1034784
Mean 1.899e+04
RMS 5.241
Underflow 9.928e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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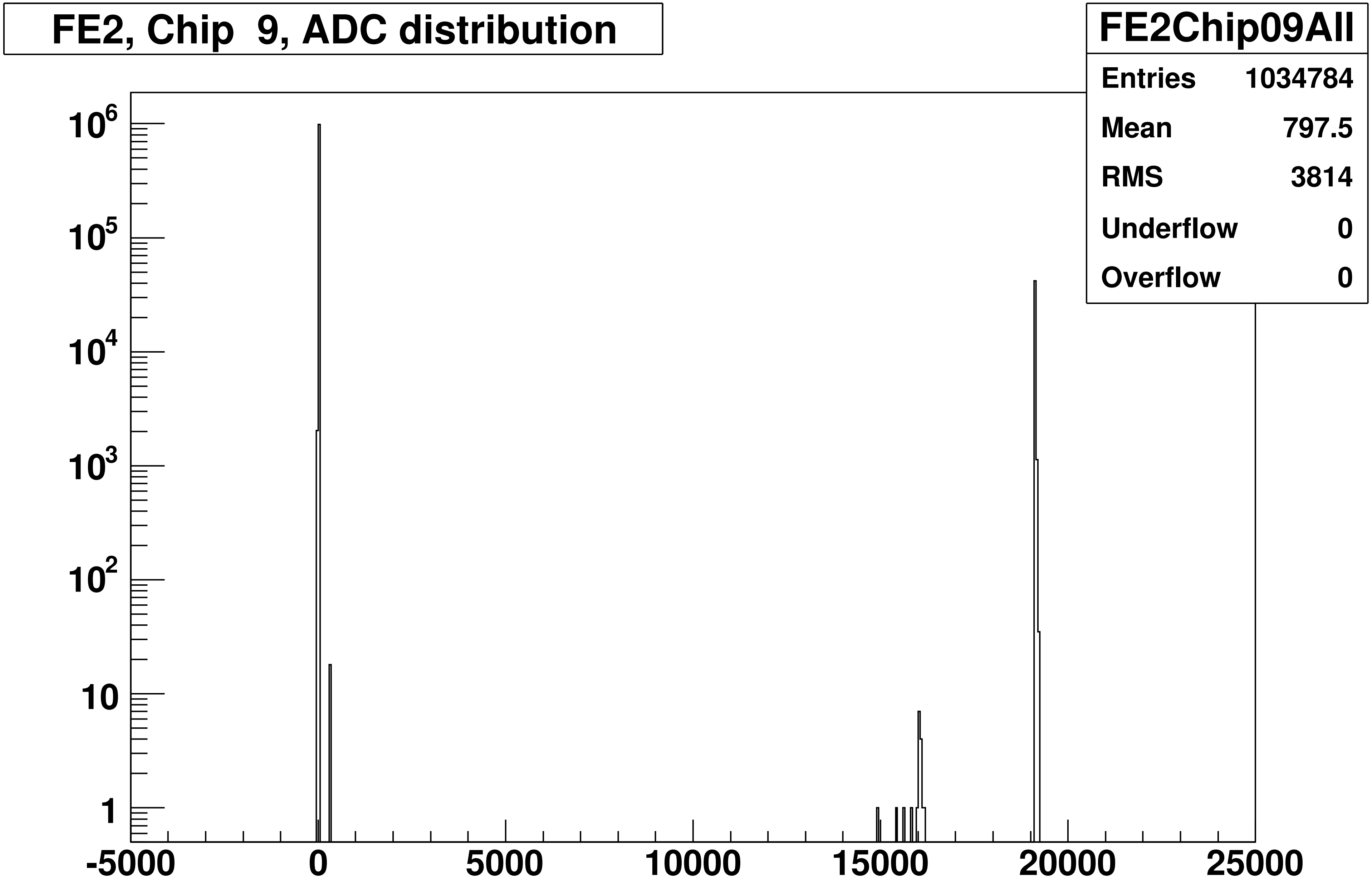
FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 1034784
Mean 797.5
RMS 3814
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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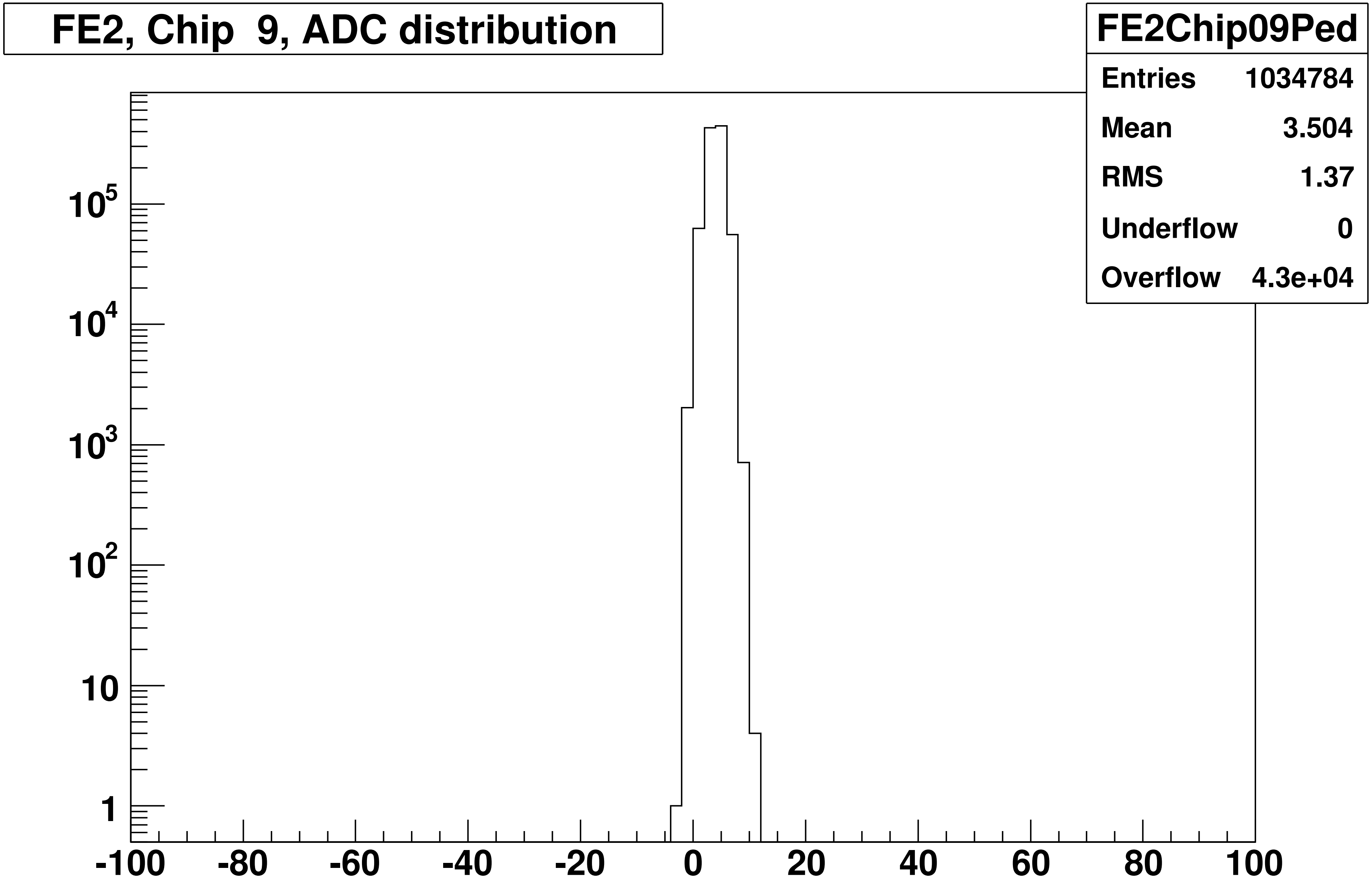
10000
15000
20000
25000


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 1034784
Mean 3.504
RMS 1.37
Underflow 0
Overflow 4.3e+04
10^5
10^4
10^3
10^2
10
1
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80
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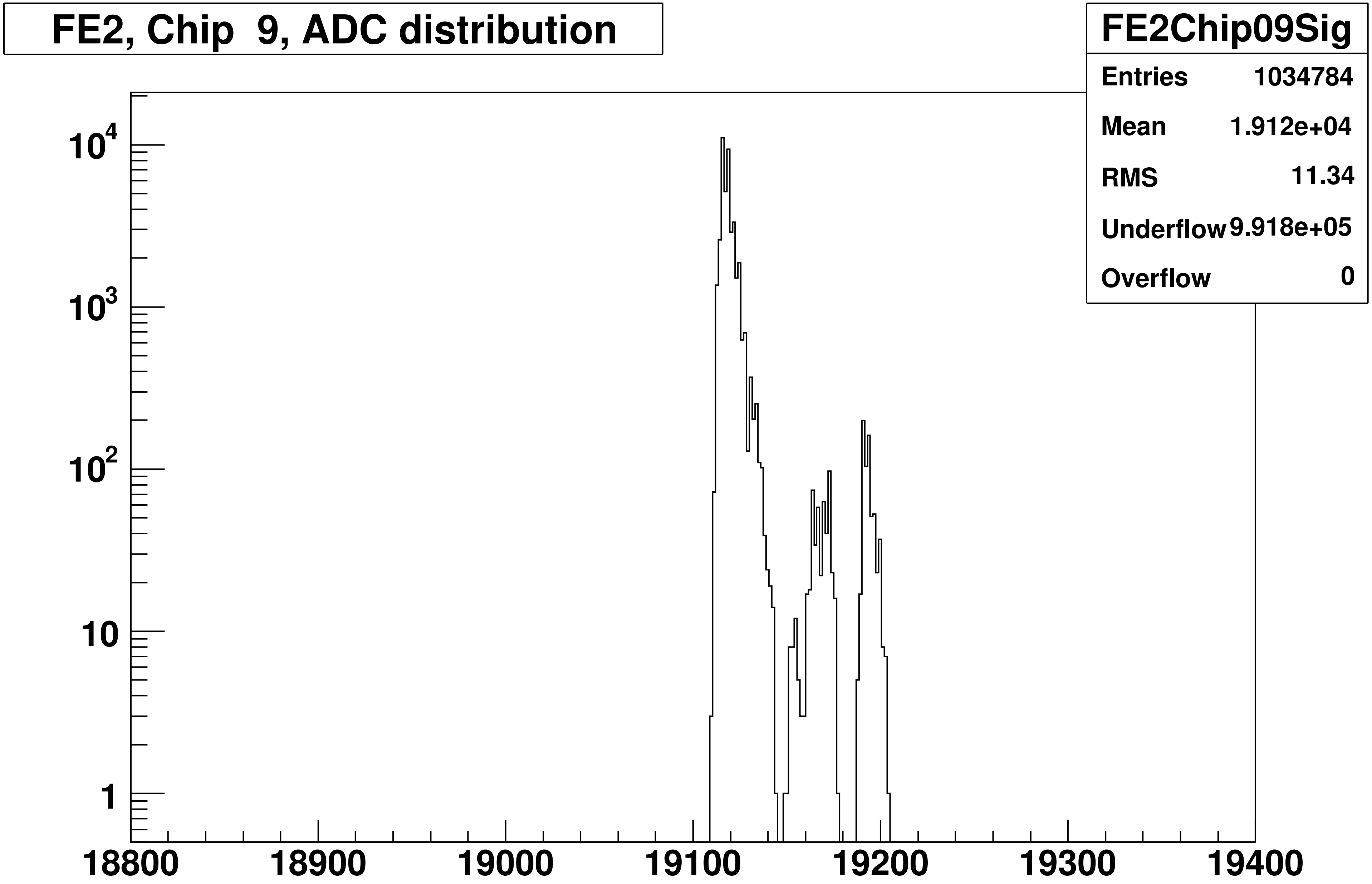
FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 1034784
Mean 1.912e+04
RMS 11.34
Underflow 9.918e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
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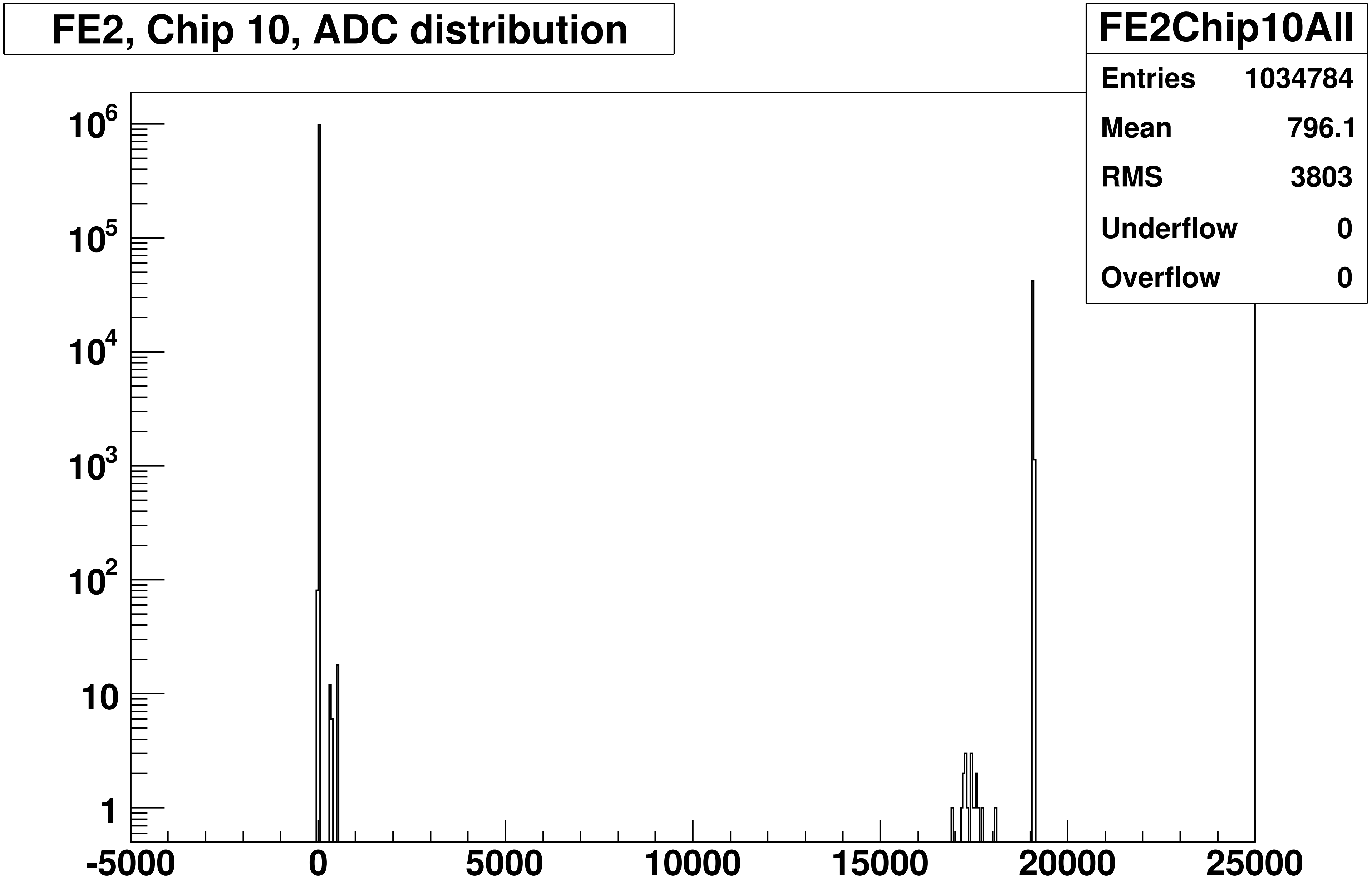
FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 1034784
Mean 796.1
RMS 3803
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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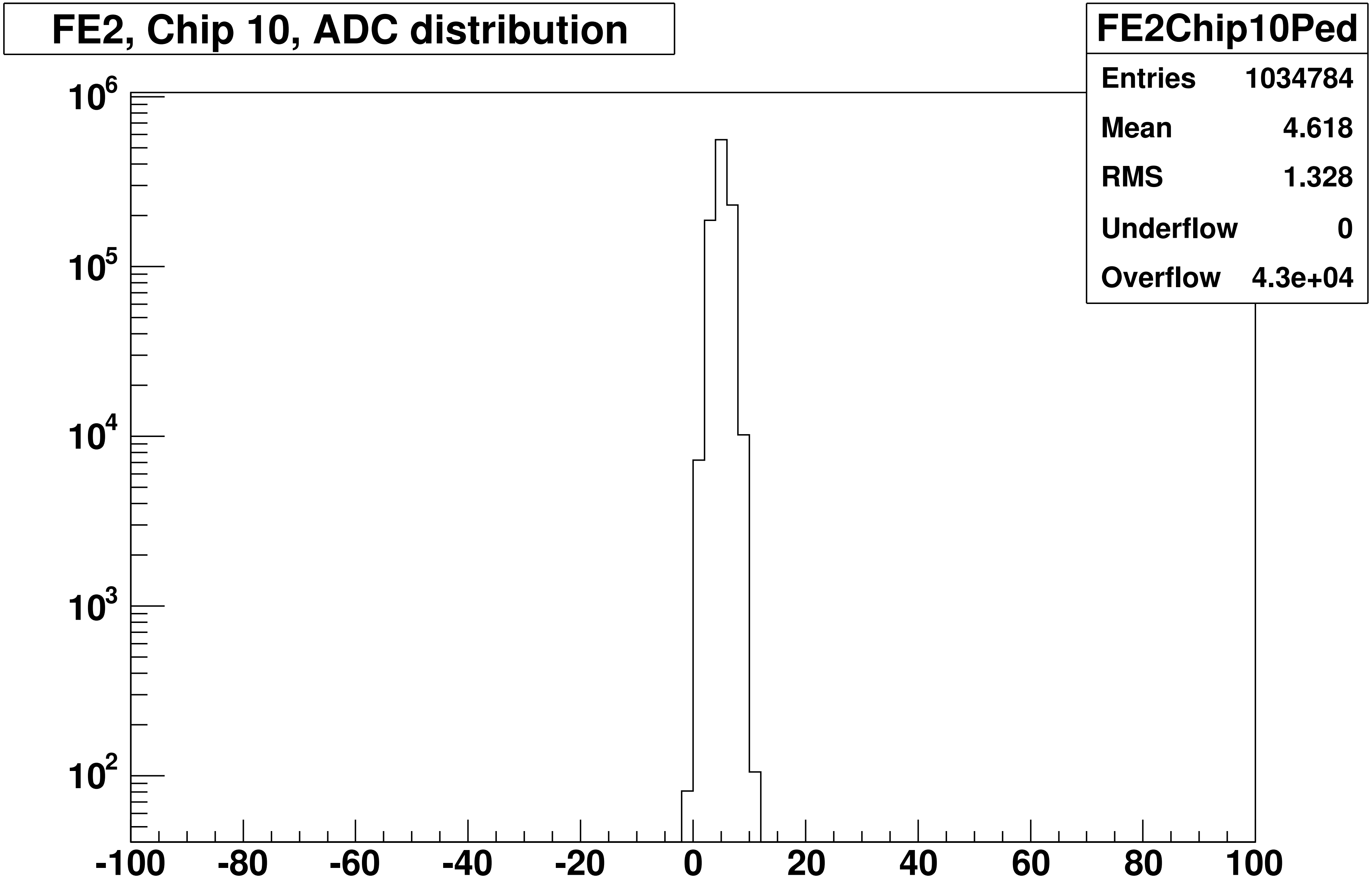
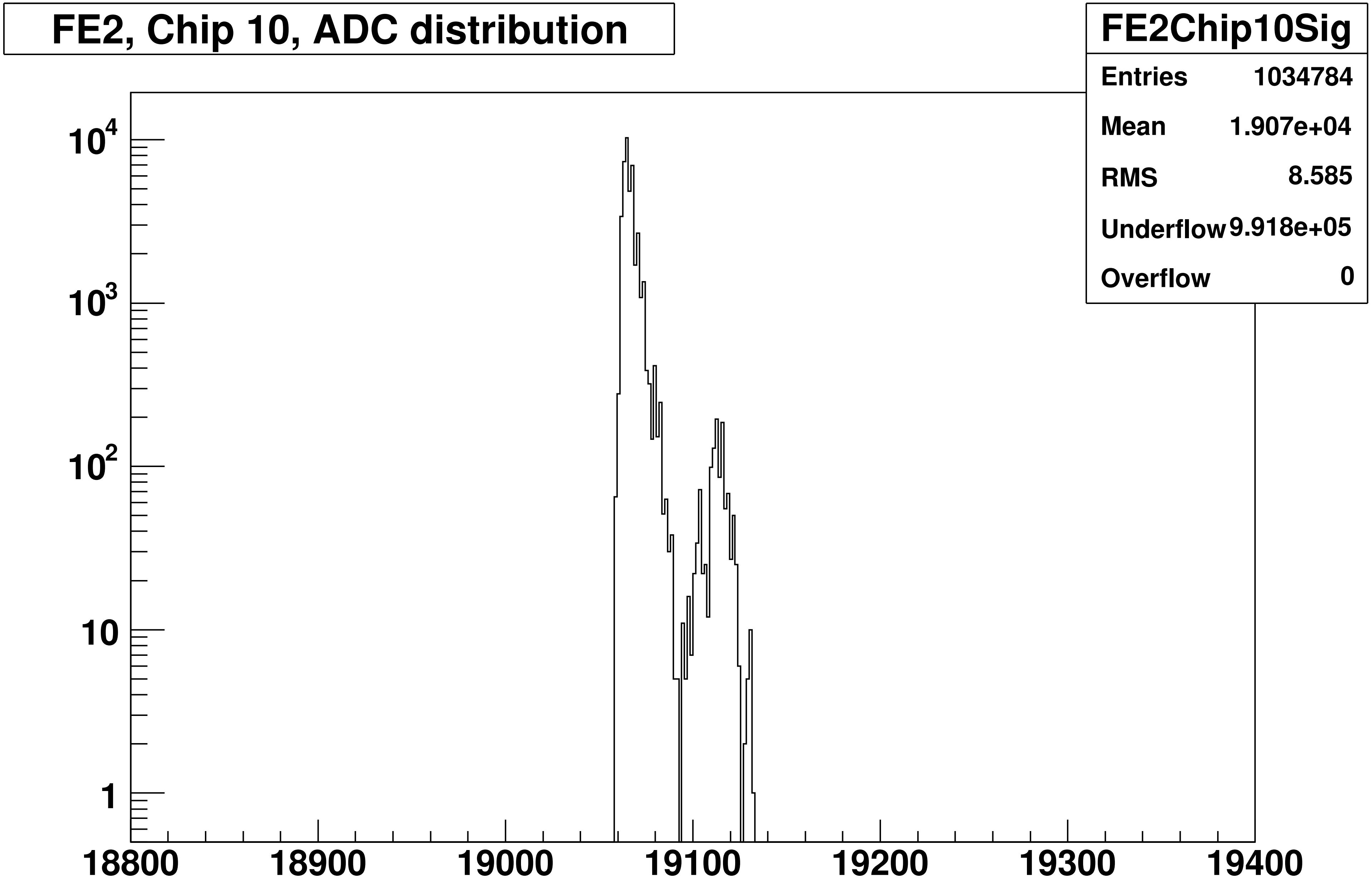
20000
25000



FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 1034784
Mean 4.618
RMS 1.328
Underflow 0
Overflow 4.3e+04
10^6
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 1034784
Mean 1.907e+04
RMS 8.585
Underflow 9.918e+05
Overflow 0
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10^3
10^2
10
1
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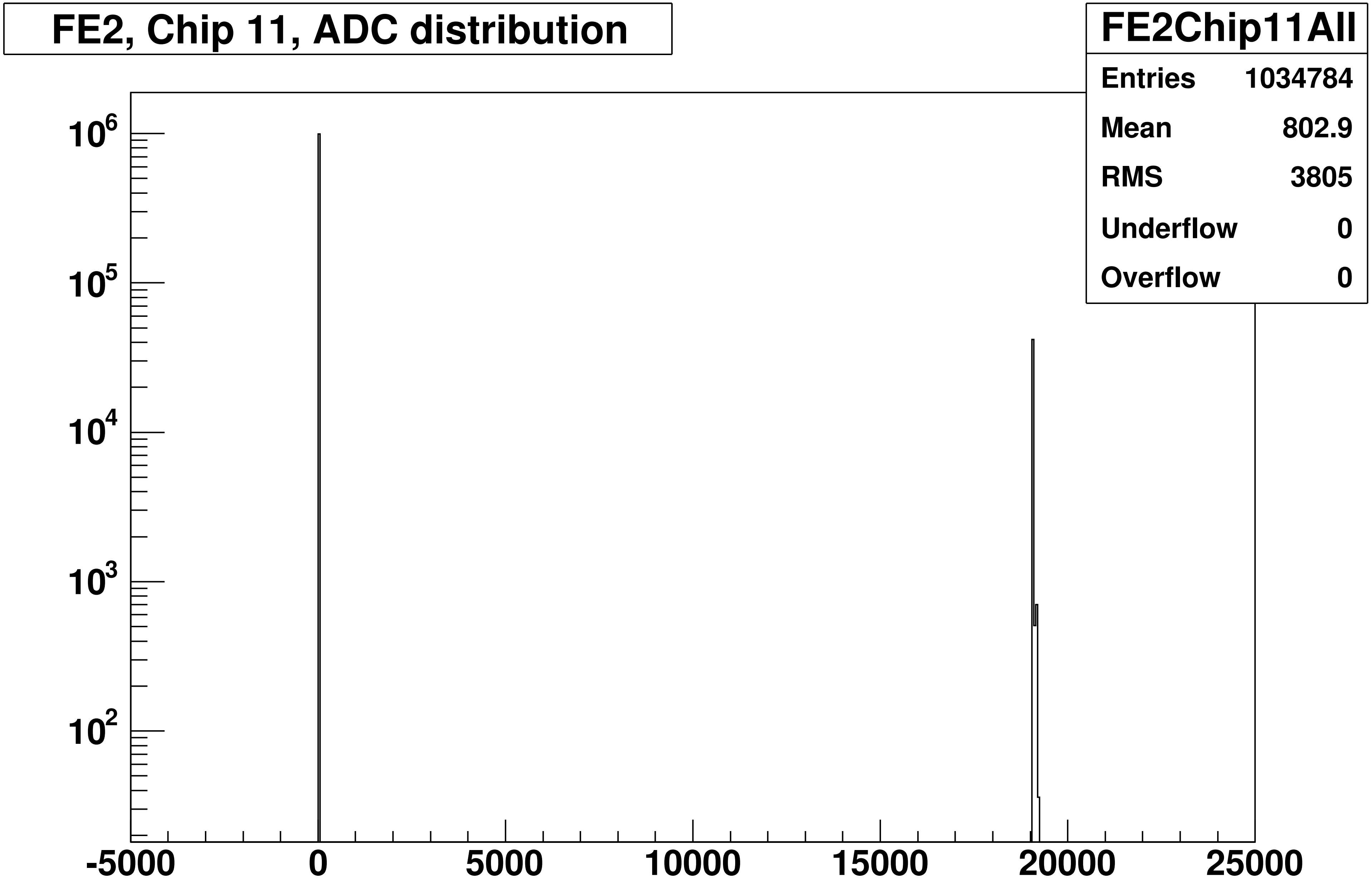
19400



FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 1034784
Mean 802.9
RMS 3805
Underflow 0
Overflow 0
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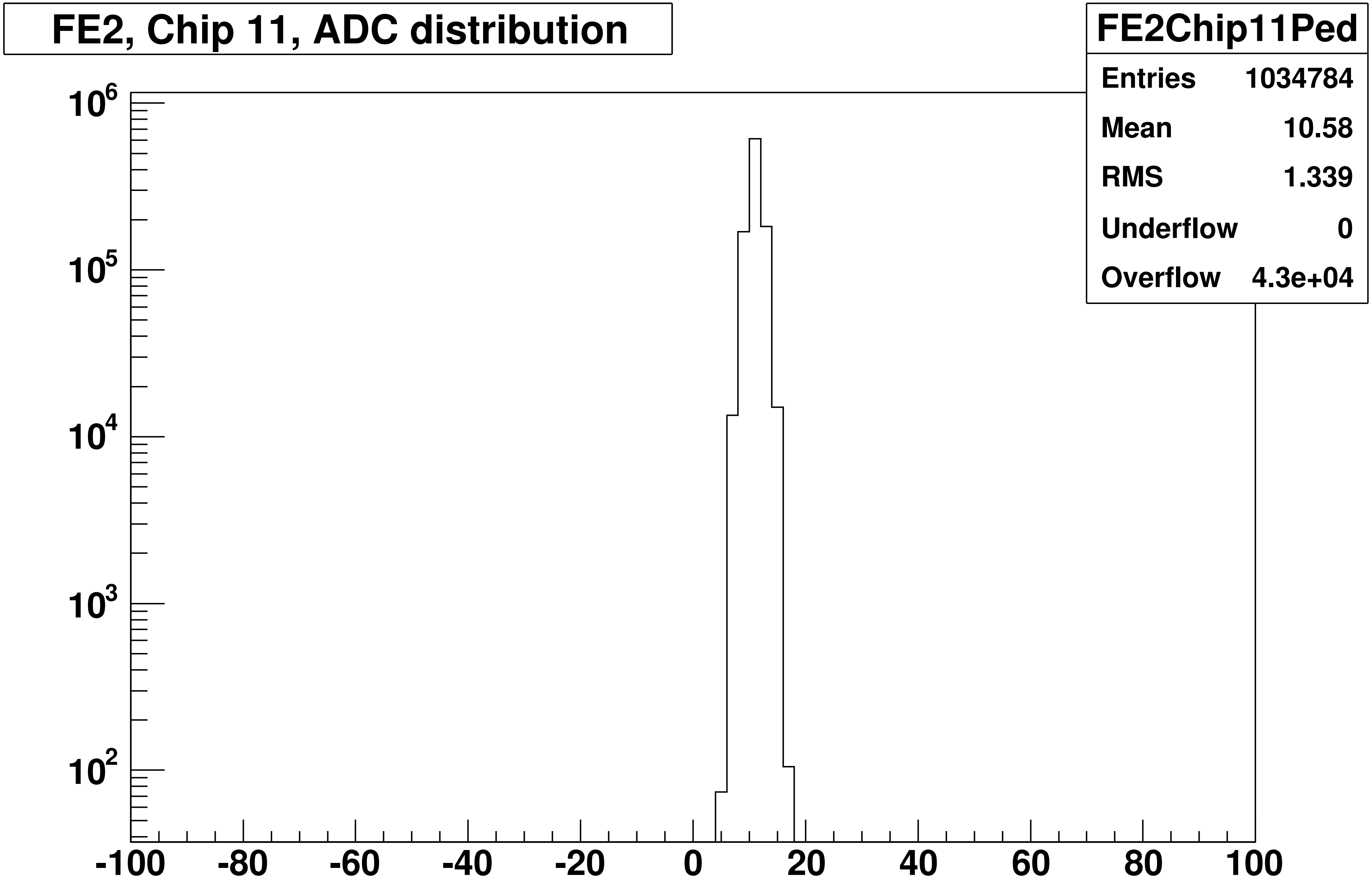
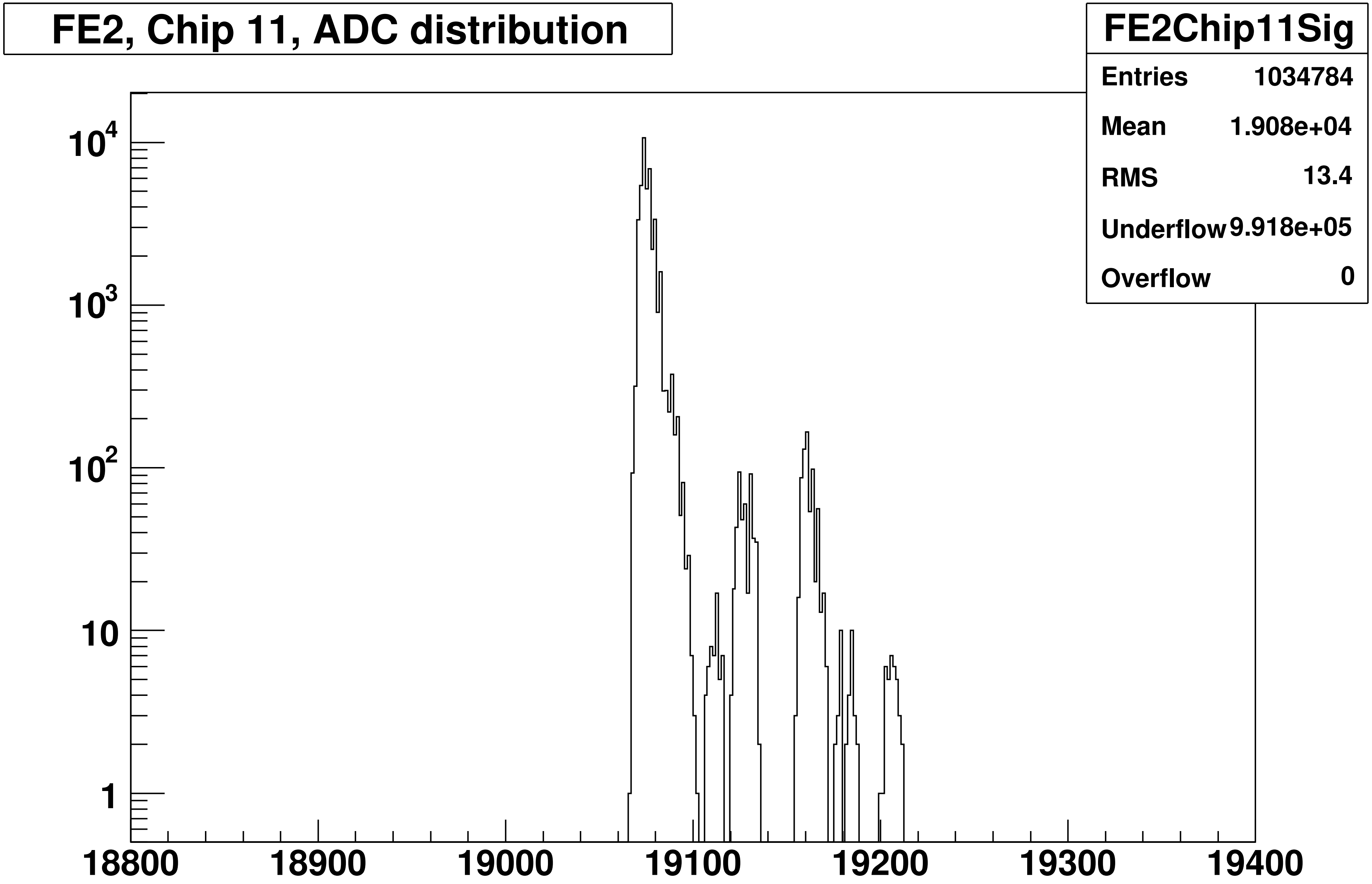
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 1034784
Mean 10.58
RMS 1.339
Underflow 0
Overflow 4.3e+04
10^6
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 1034784
Mean 1.908e+04
RMS 13.4
Underflow 9.918e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
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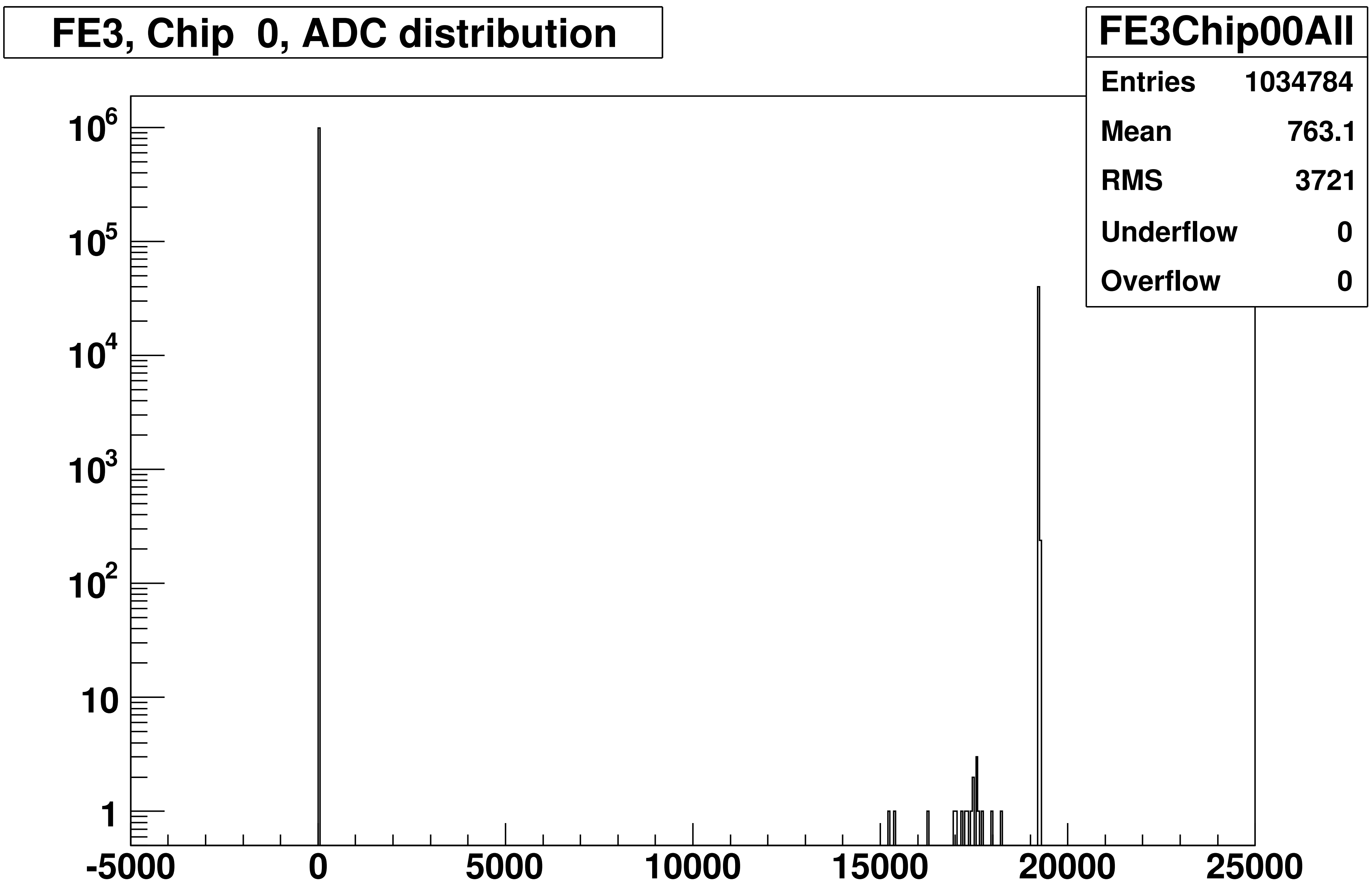
19400



FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 1034784
Mean 763.1
RMS 3721
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
5000
10000
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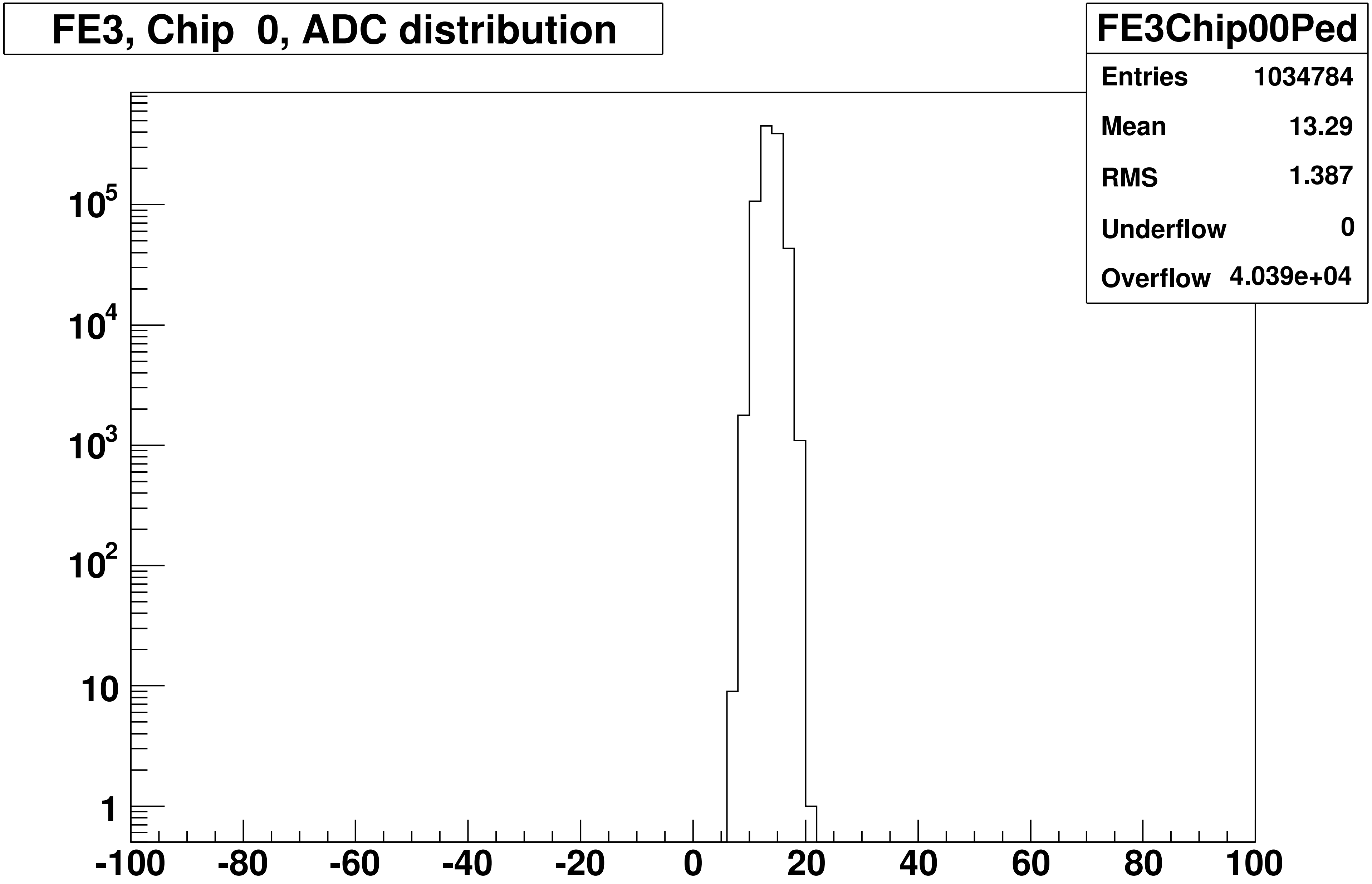
FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 1034784
Mean 13.29
RMS 1.387
Underflow 0
Overflow 4.039e+04
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10^4
10^3
10^2
10
1
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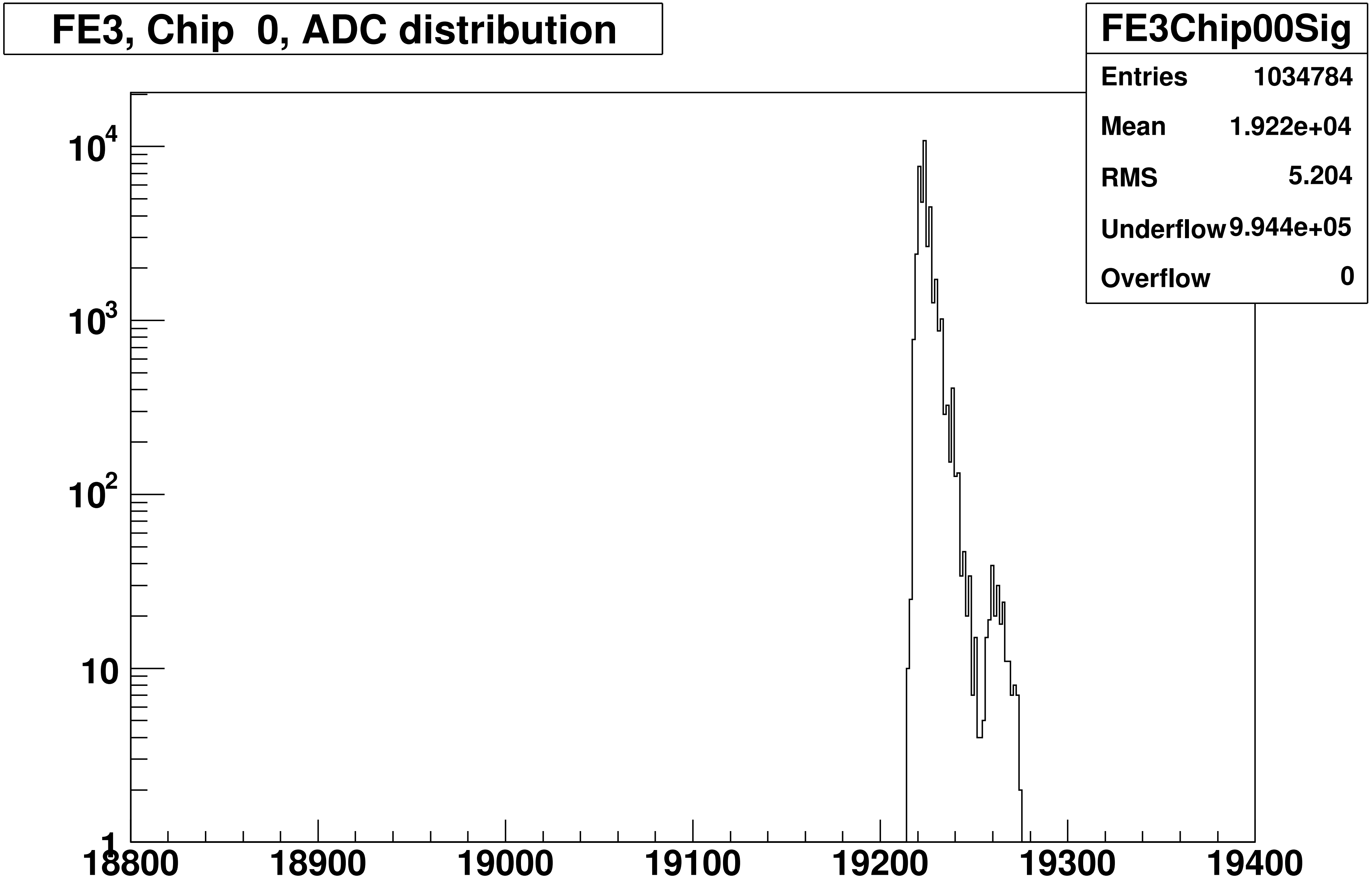

FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 1034784
Mean 1.922e+04
RMS 5.204
Underflow 9.944e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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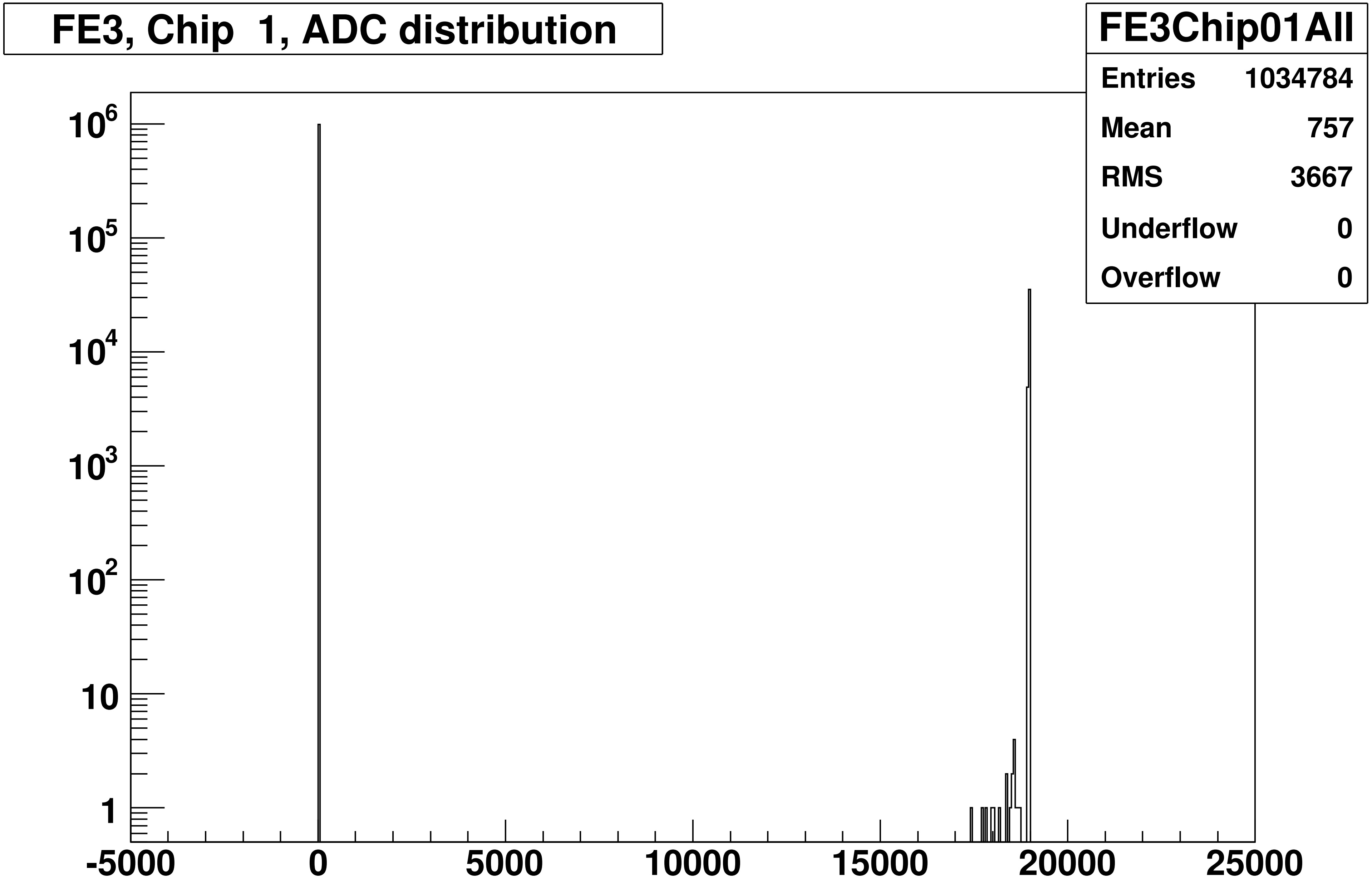
19400


FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 1034784
Mean 757
RMS 3667
Underflow 0
Overflow 0
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10^4
10^3
10^2
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1
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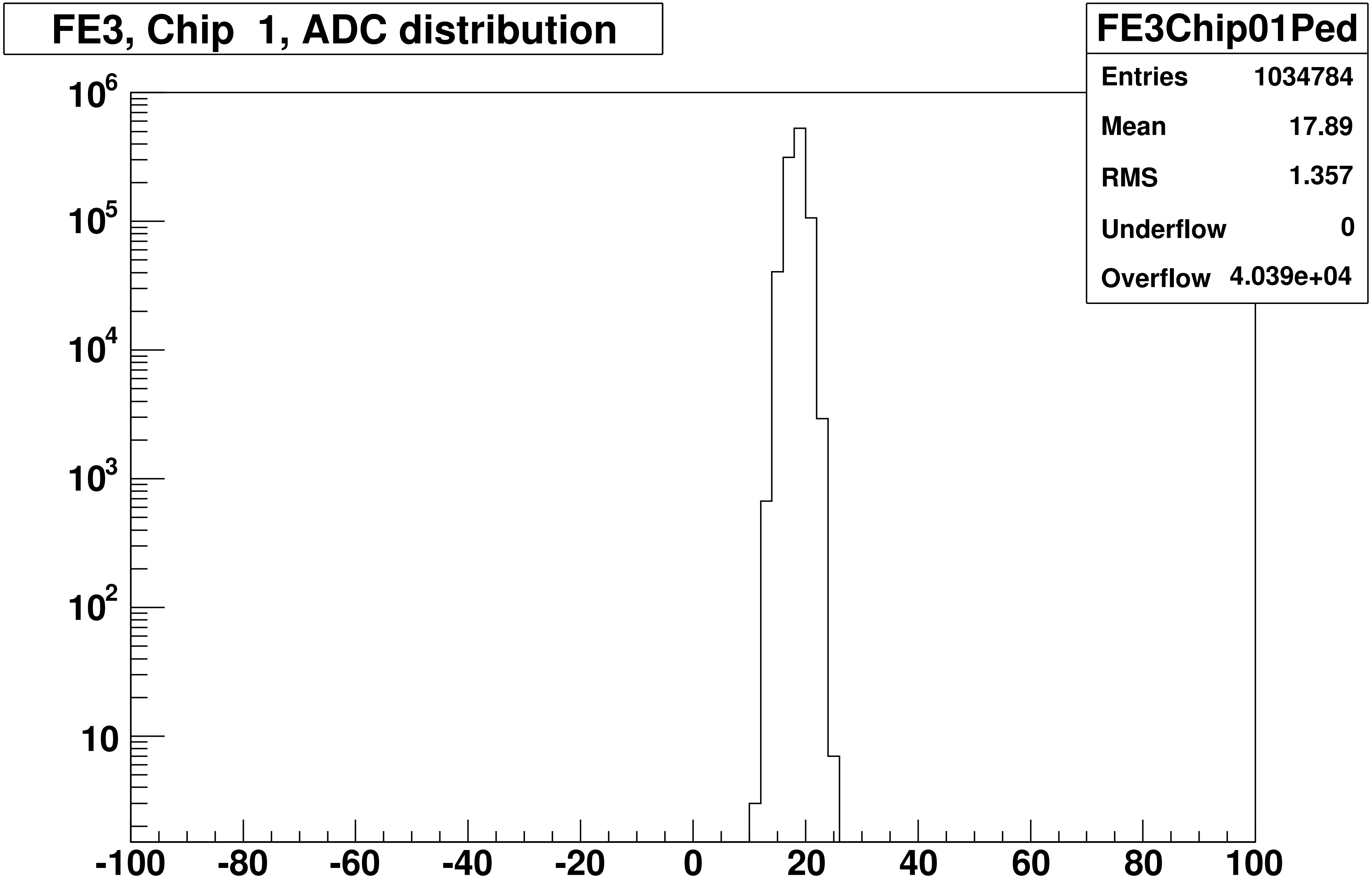

FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 1034784
Mean 17.89
RMS 1.357
Underflow 0
Overflow 4.039e+04
10^6
10^5
10^4
10^3
10^2
10
-100
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-20
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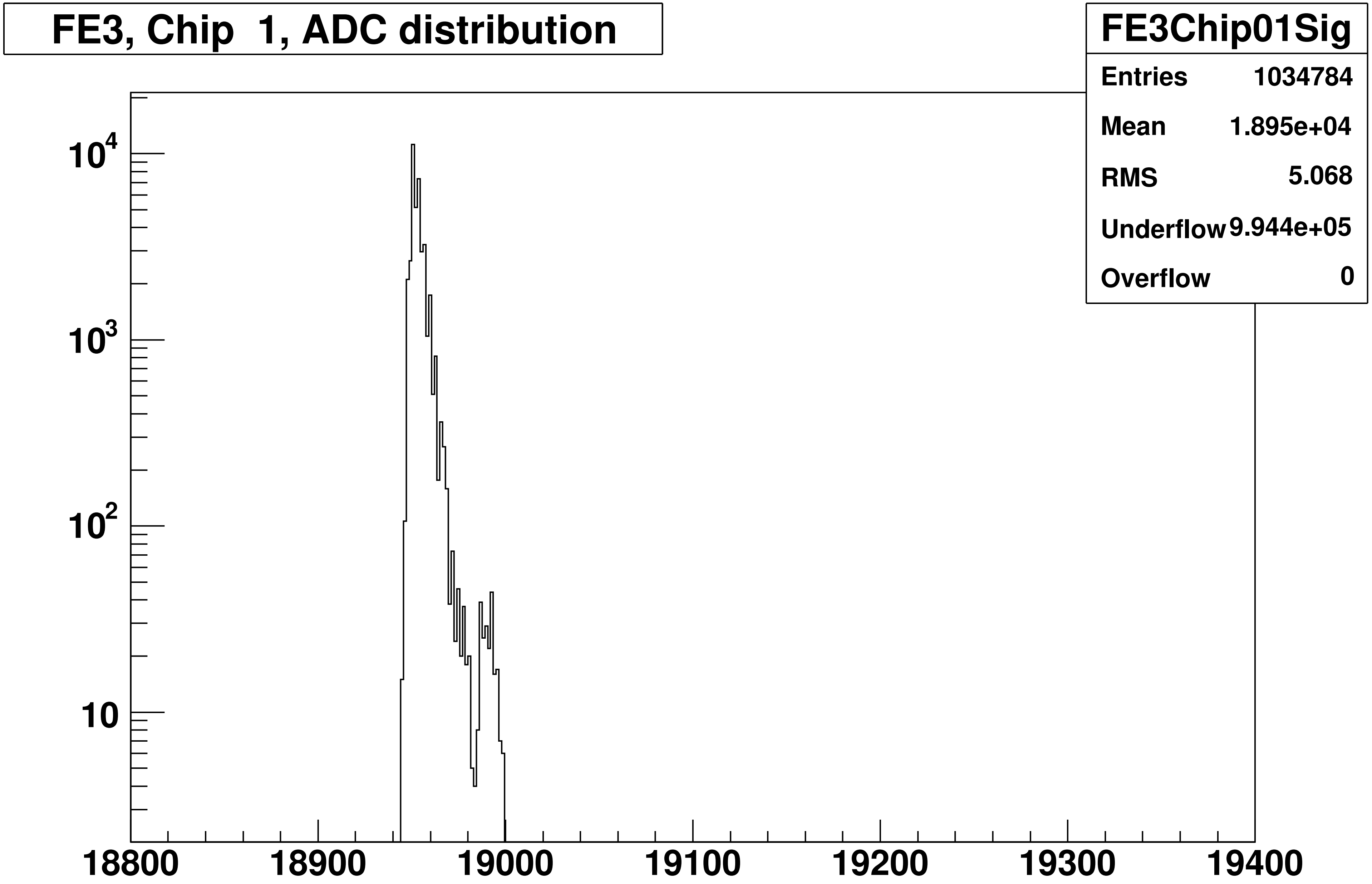
80
100



FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 1034784
Mean 1.895e+04
RMS 5.068
Underflow 9.944e+05
Overflow 0
10
10^2
10^3
10^4
18800
18900
19000
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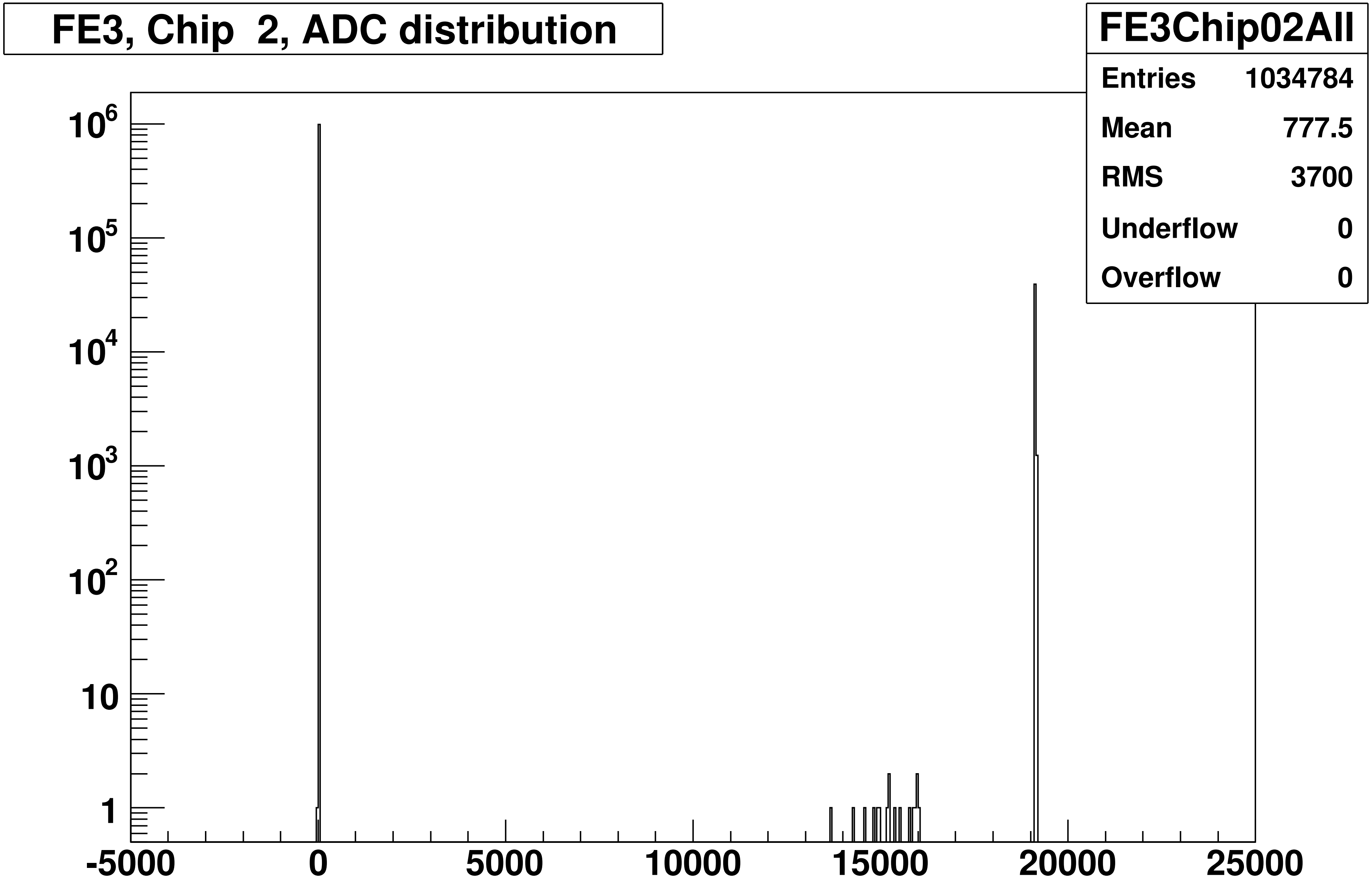
19400



FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 1034784
Mean 777.5
RMS 3700
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
5000
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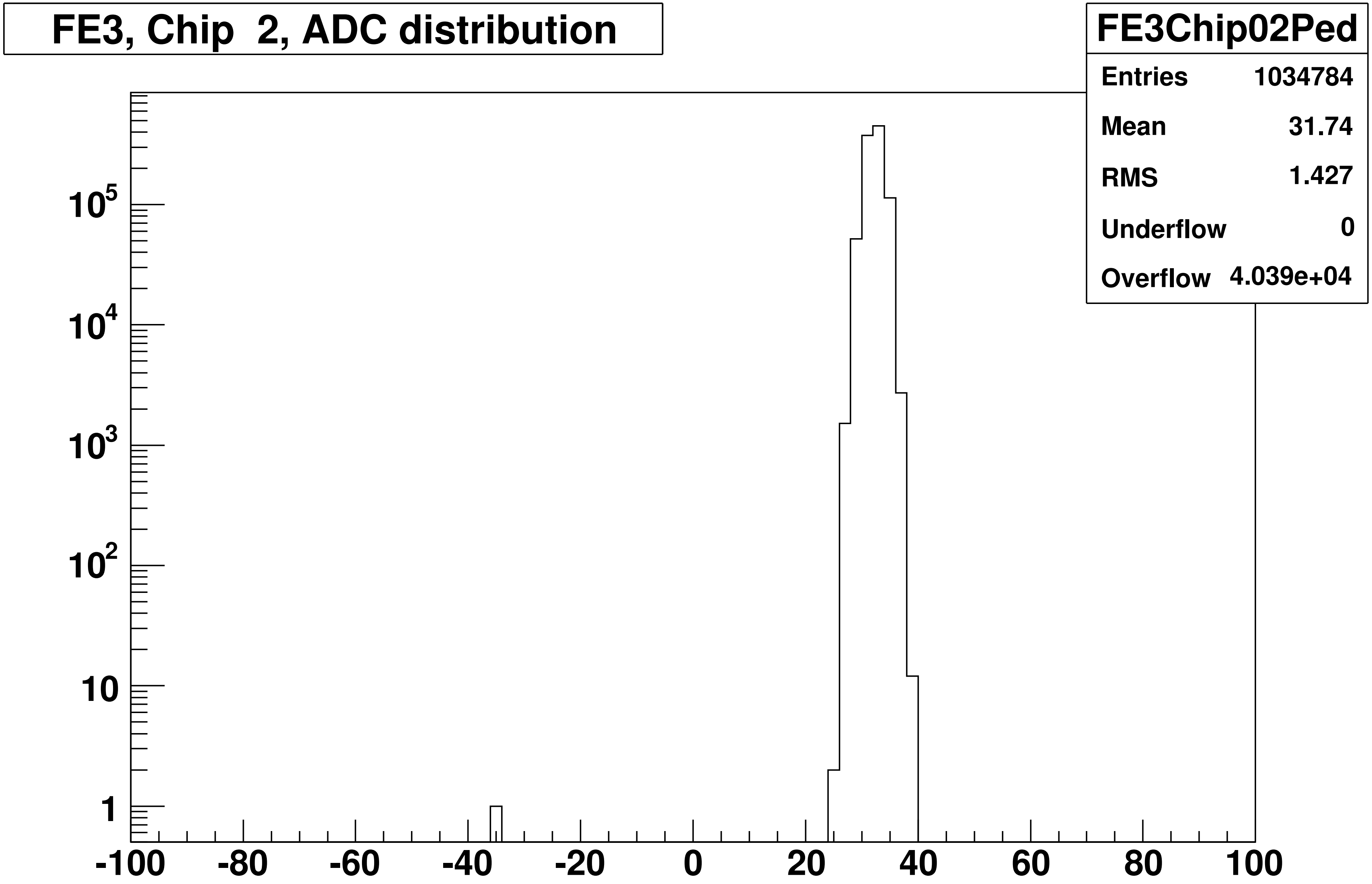
25000



FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 1034784
Mean 31.74
RMS 1.427
Underflow 0
Overflow 4.039e+04
10^5
10^4
10^3
10^2
10
1
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-40
-20
0
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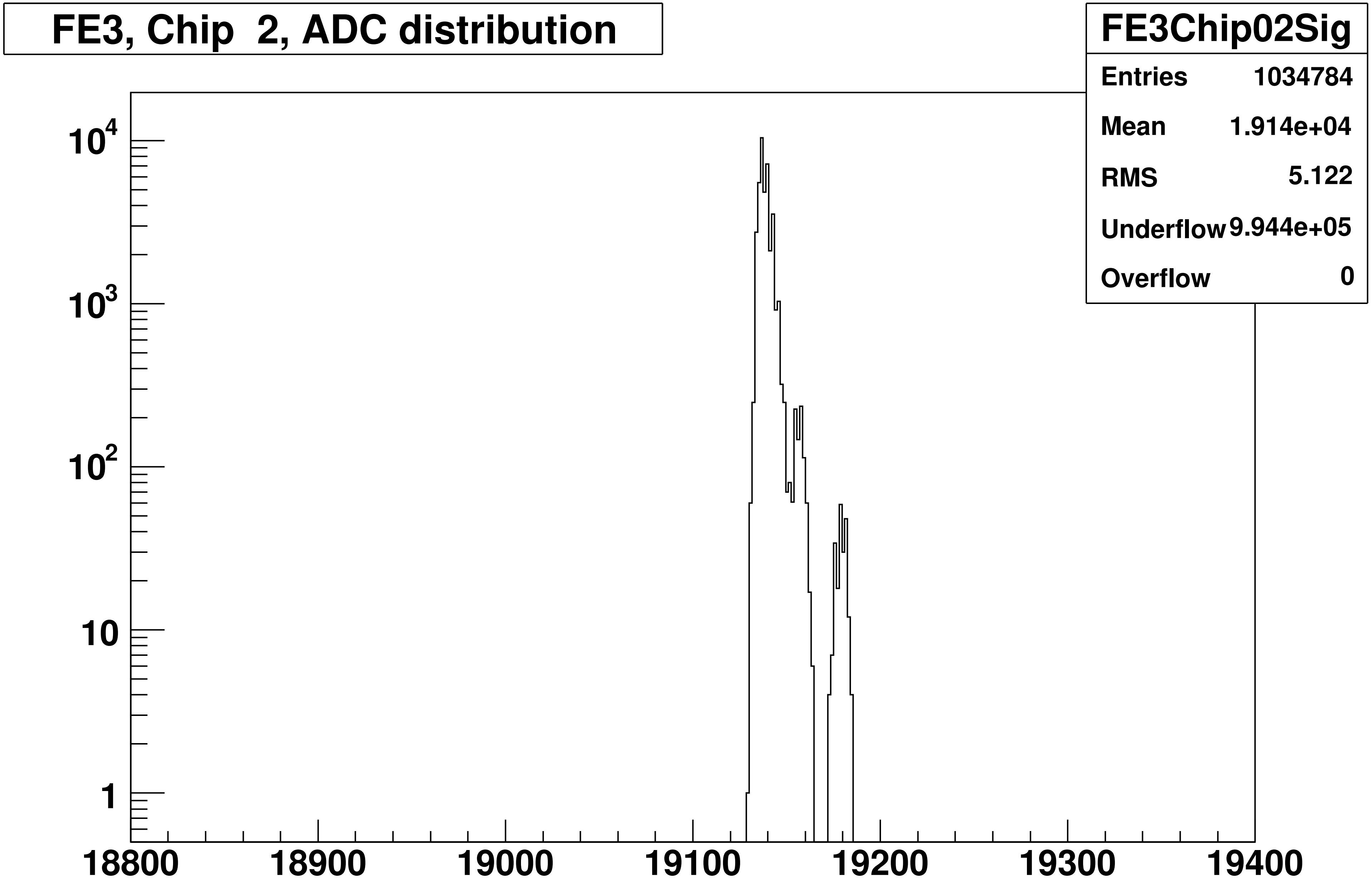
60
80
100


FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 1034784
Mean 1.914e+04
RMS 5.122
Underflow 9.944e+05
Overflow 0
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10^3
10^2
10
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18800
18900
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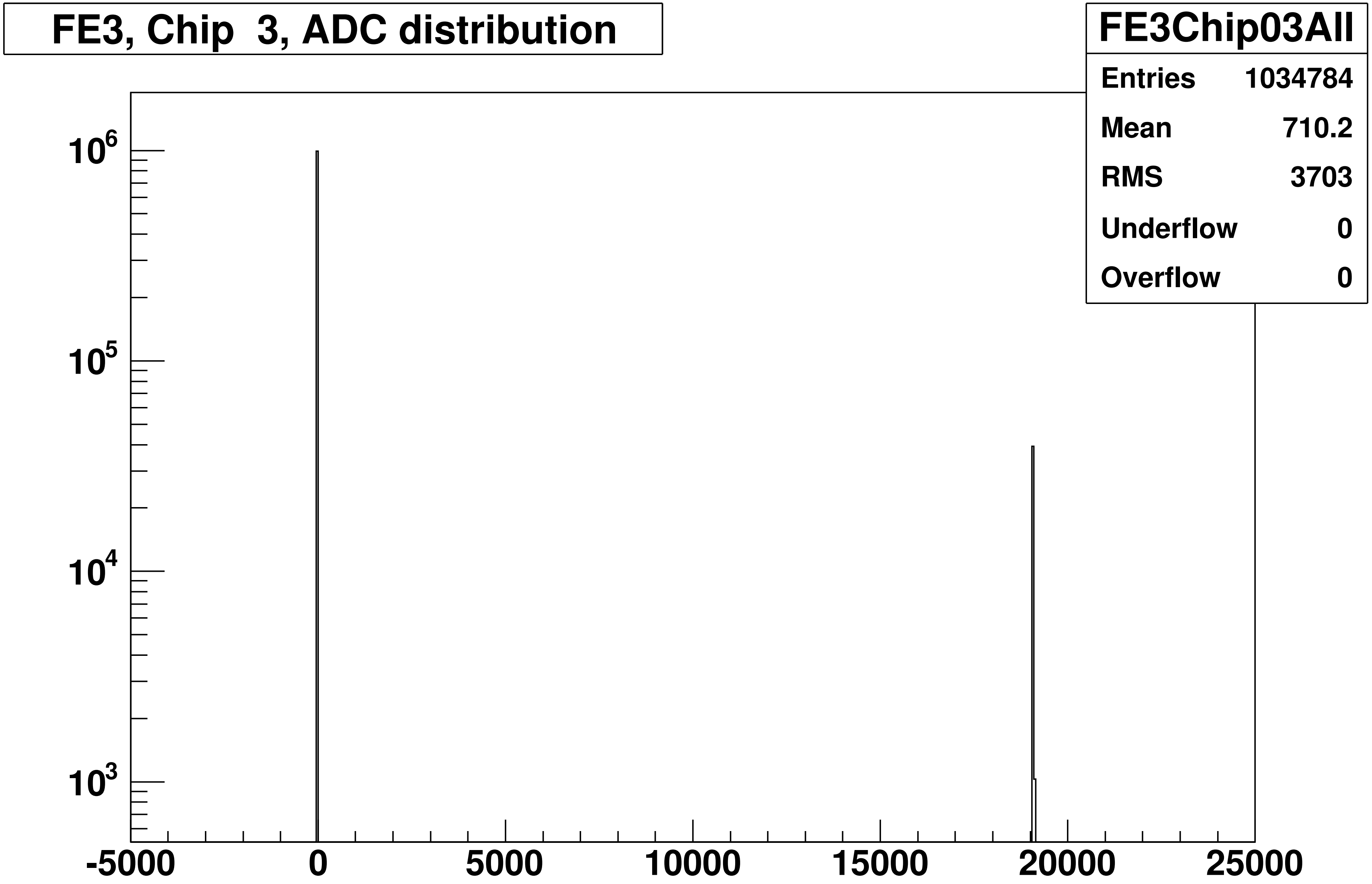
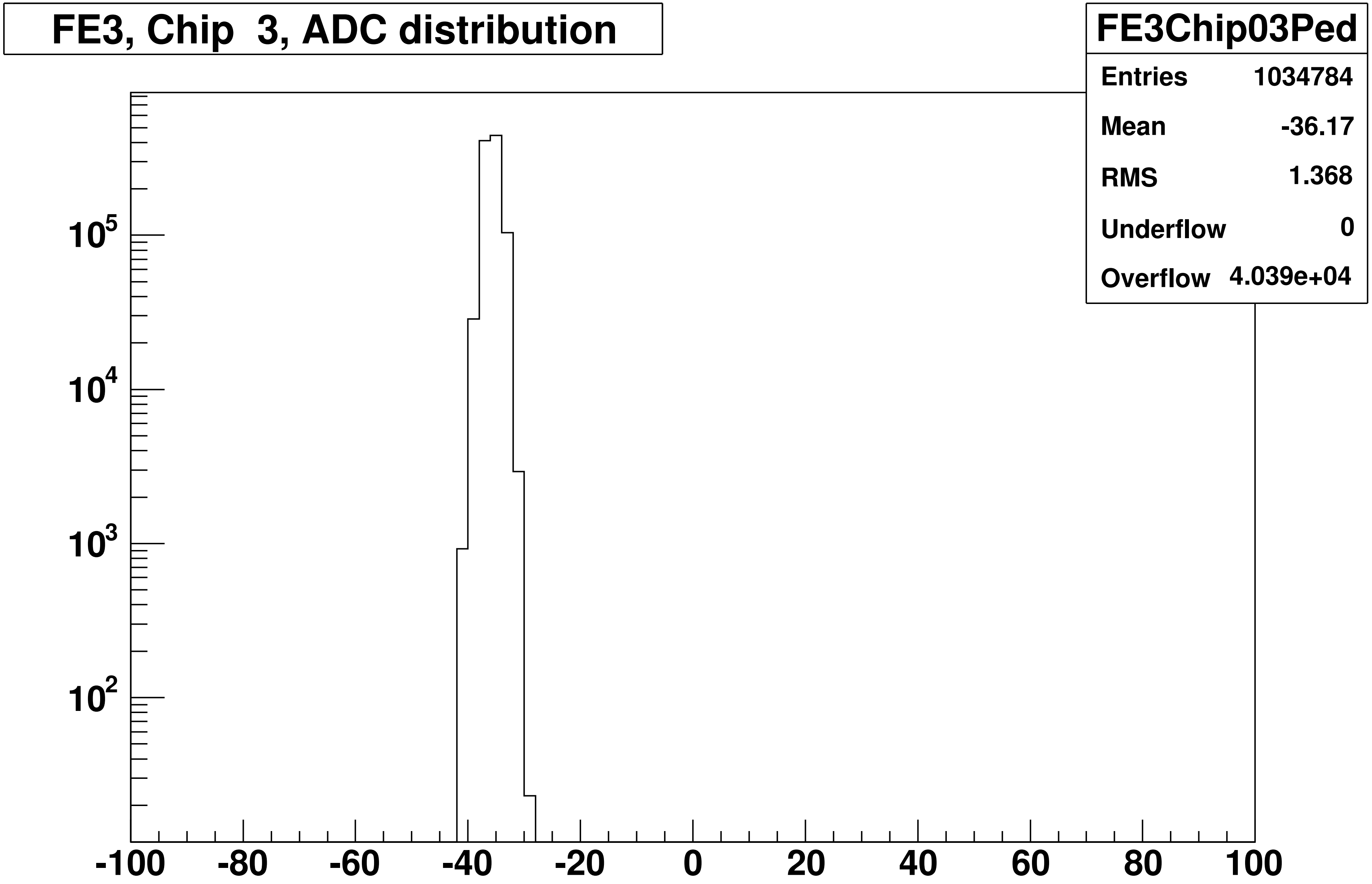


FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 1034784
Mean 710.2
RMS 3703
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 1034784
Mean -36.17
RMS 1.368
Underflow 0
Overflow 4.039e+04
10^5
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10^3
10^2
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-60
-40
-20
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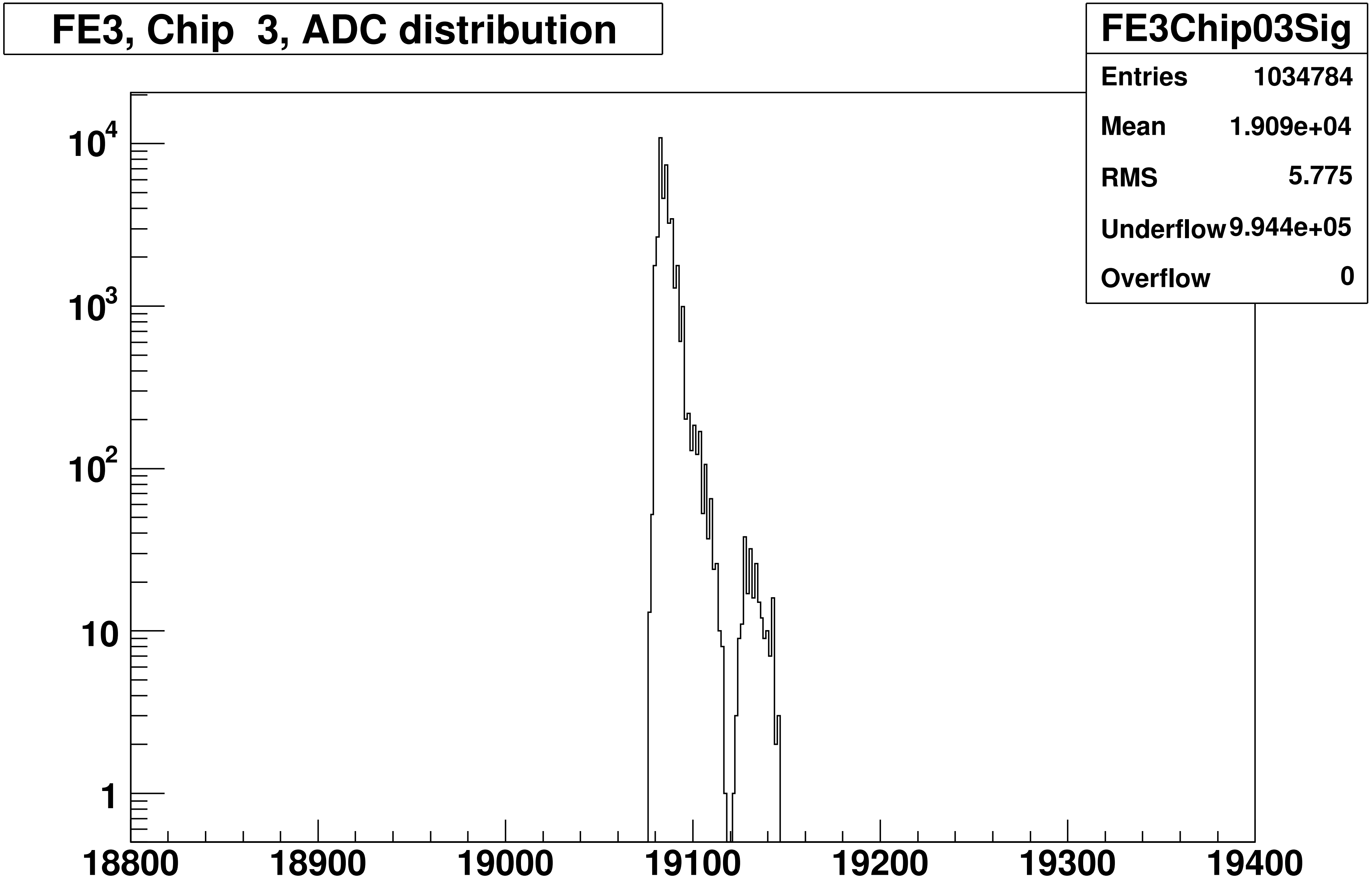
60
80
100



FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 1034784
Mean 1.909e+04
RMS 5.775
Underflow 9.944e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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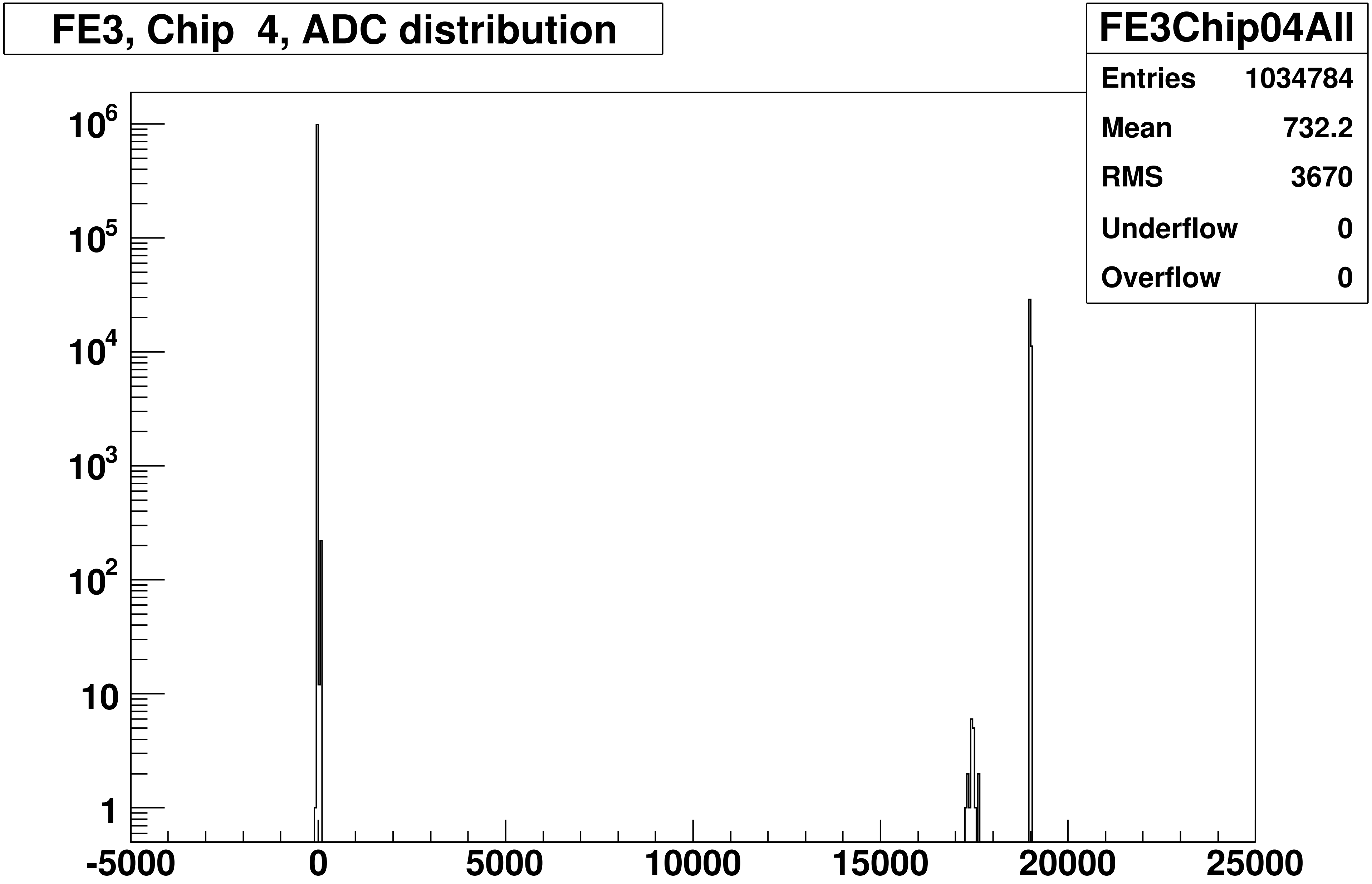
FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 1034784
Mean 732.2
RMS 3670
Underflow 0
Overflow 0
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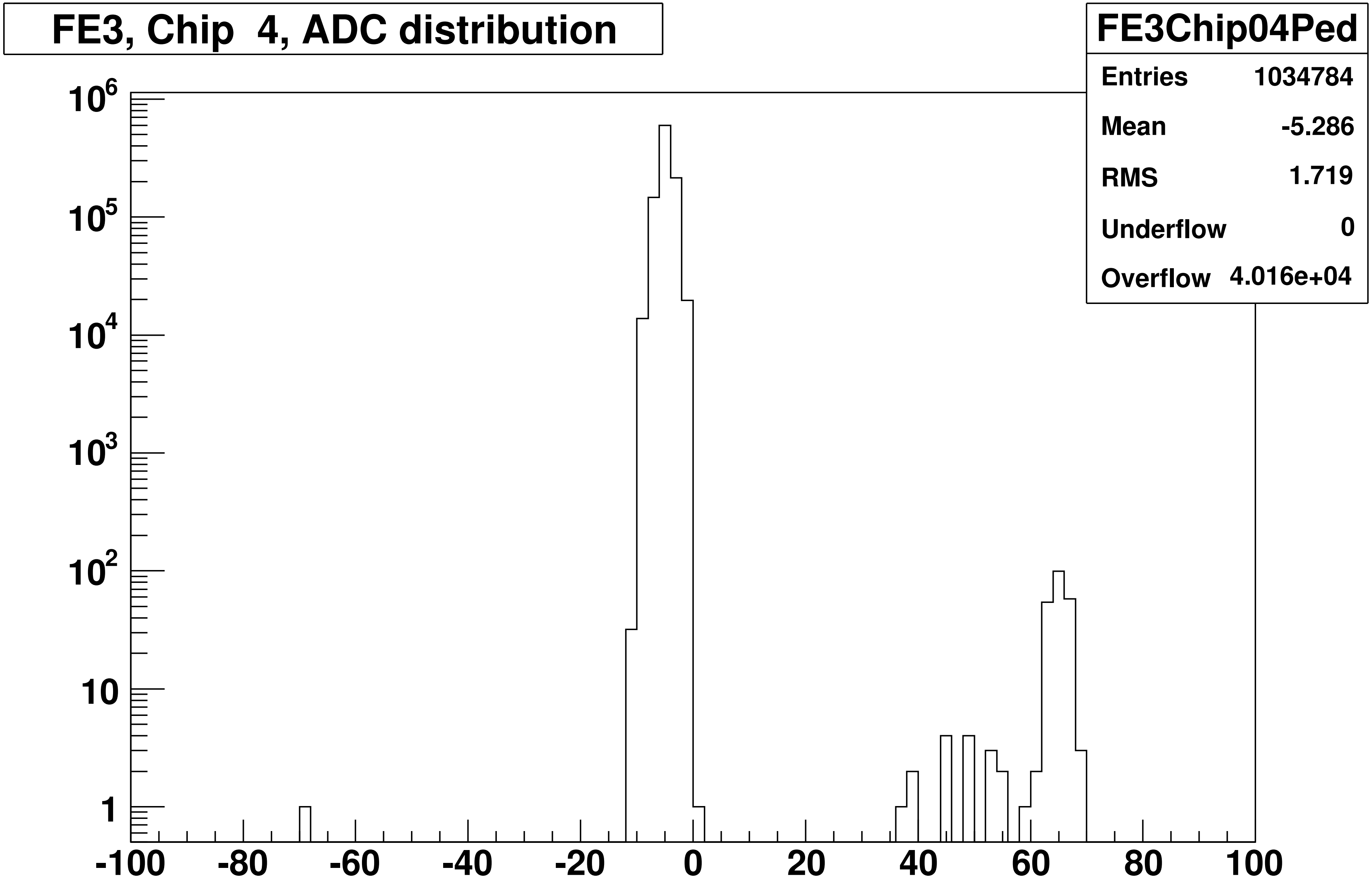
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 1034784
Mean -5.286
RMS 1.719
Underflow 0
Overflow 4.016e+04
10^6
10^5
10^4
10^3
10^2
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1
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-20
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80
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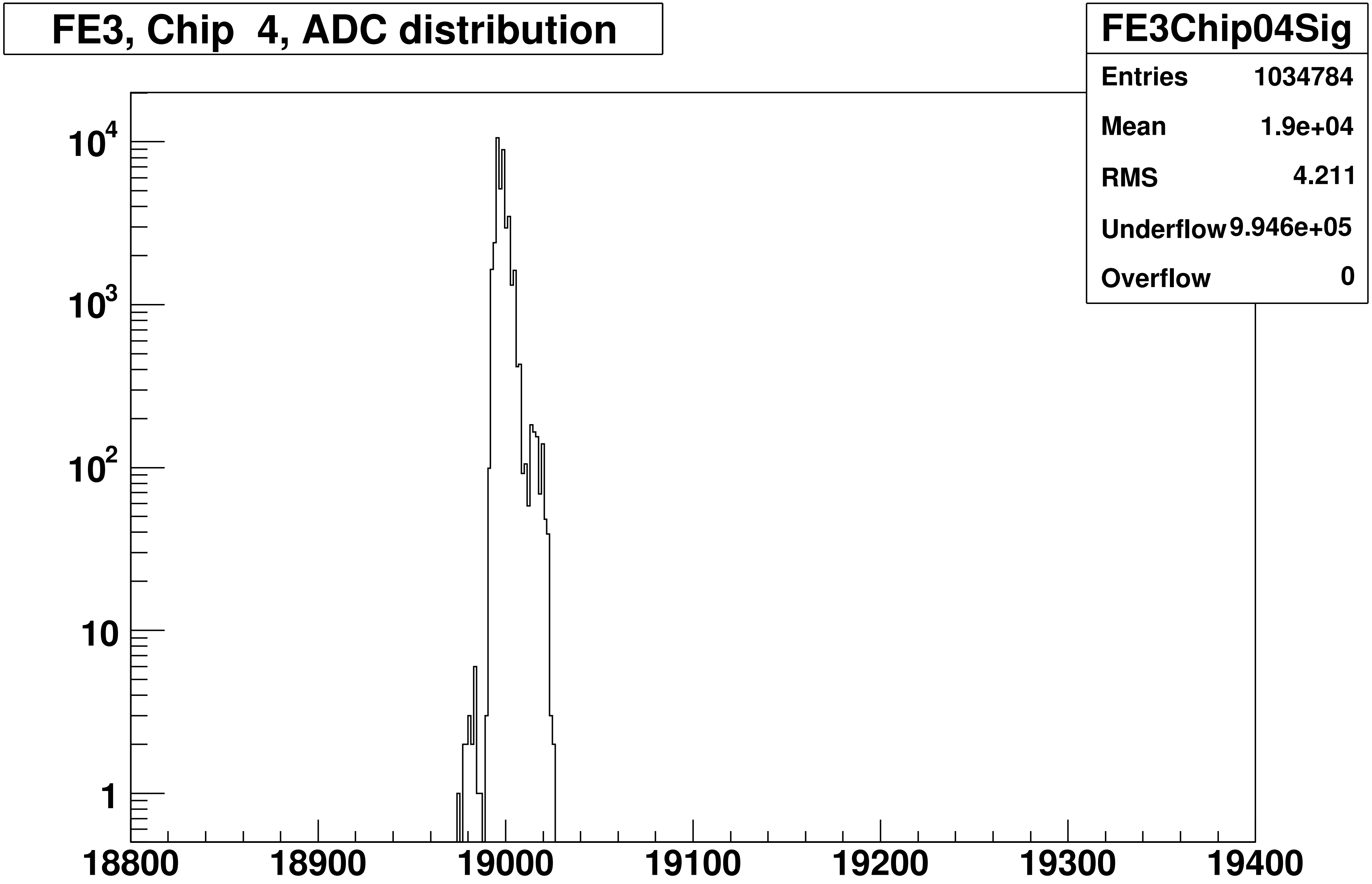

FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 1034784
Mean 1.9e+04
RMS 4.211
Underflow 9.946e+05
Overflow 0
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10^3
10^2
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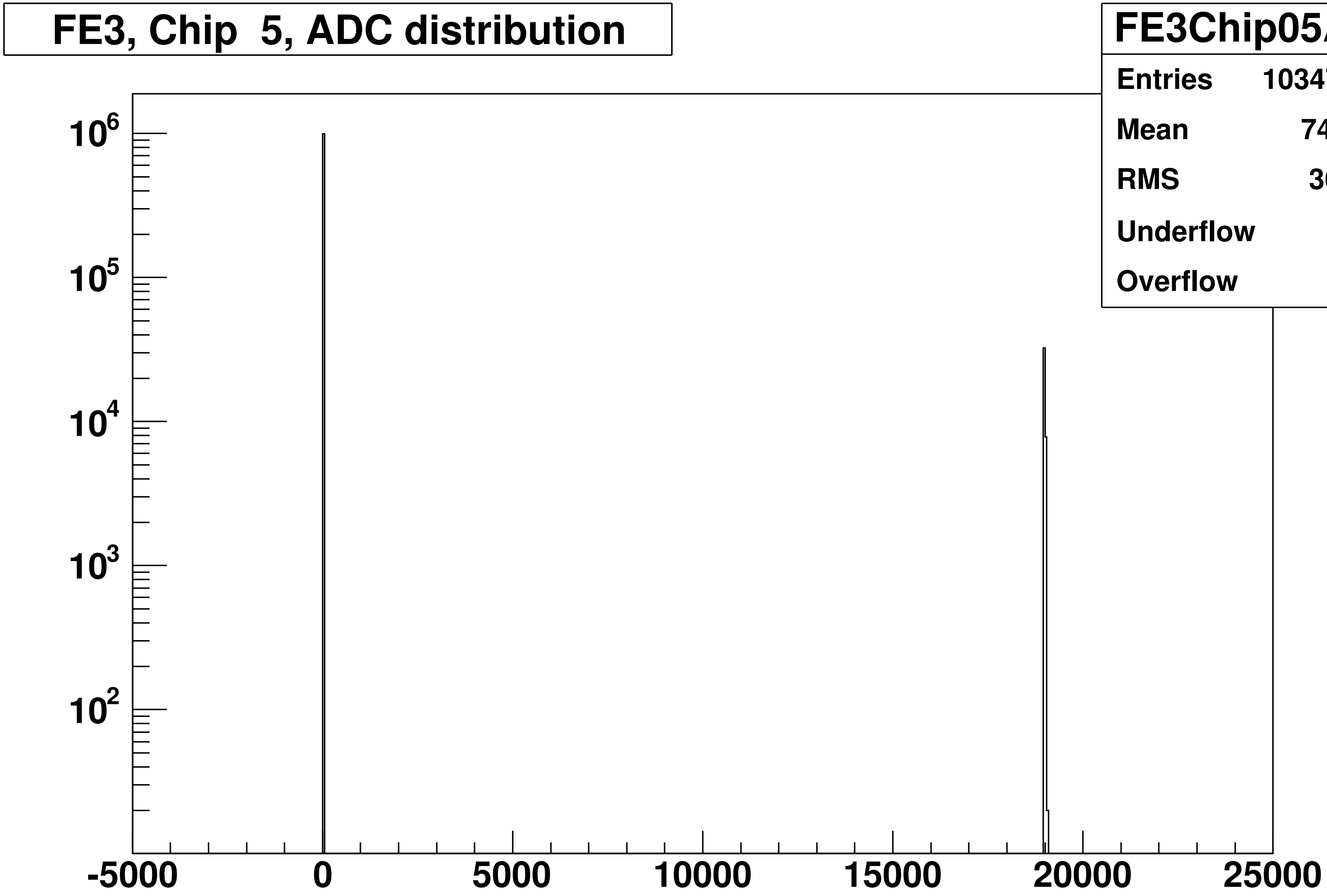

FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 1034784
Mean 747.8
RMS 3678
Underflow 0
Overflow 0
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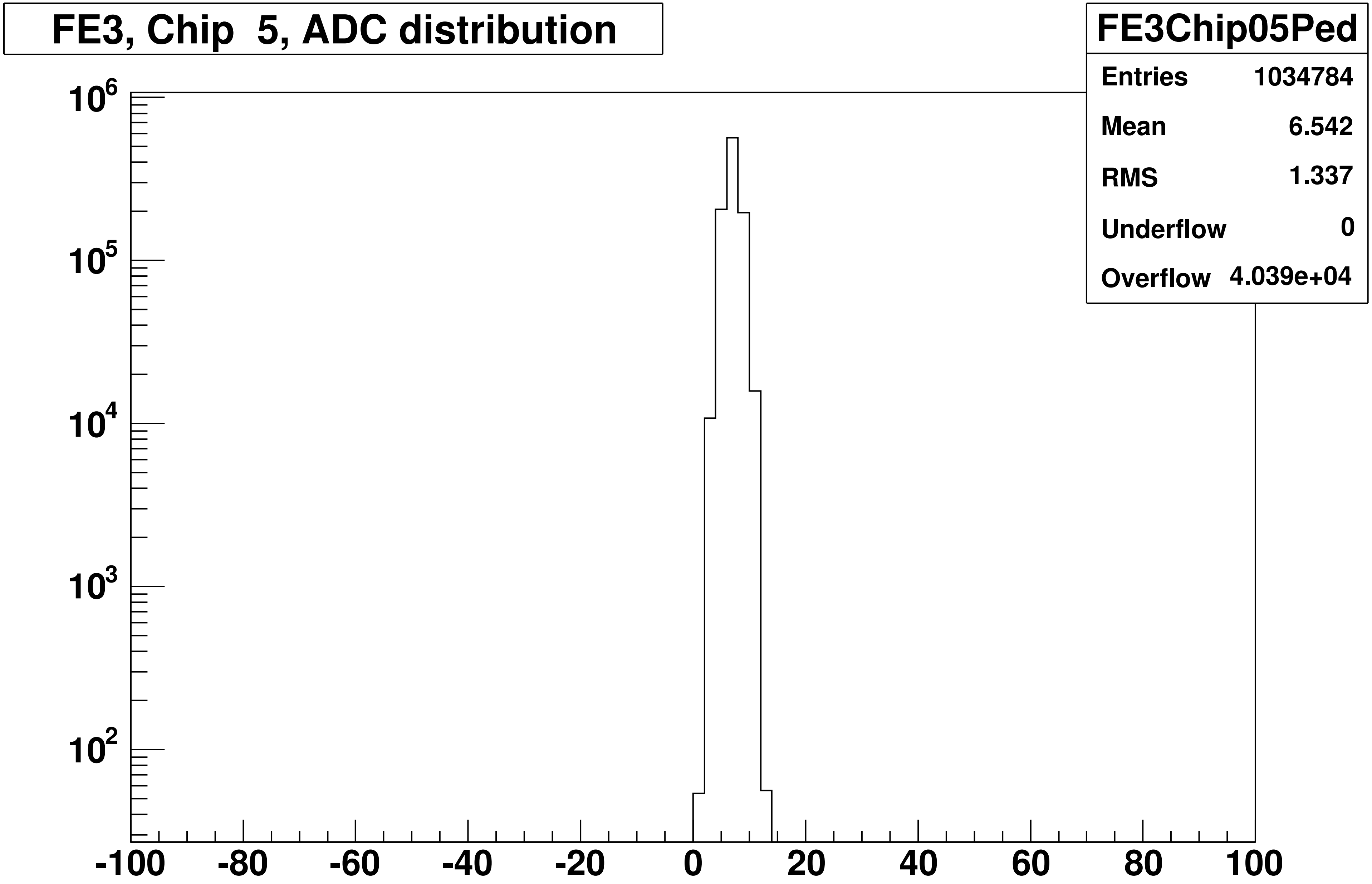
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 1034784
Mean 6.542
RMS 1.337
Underflow 0
Overflow 4.039e+04
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10^5
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10^3
10^2
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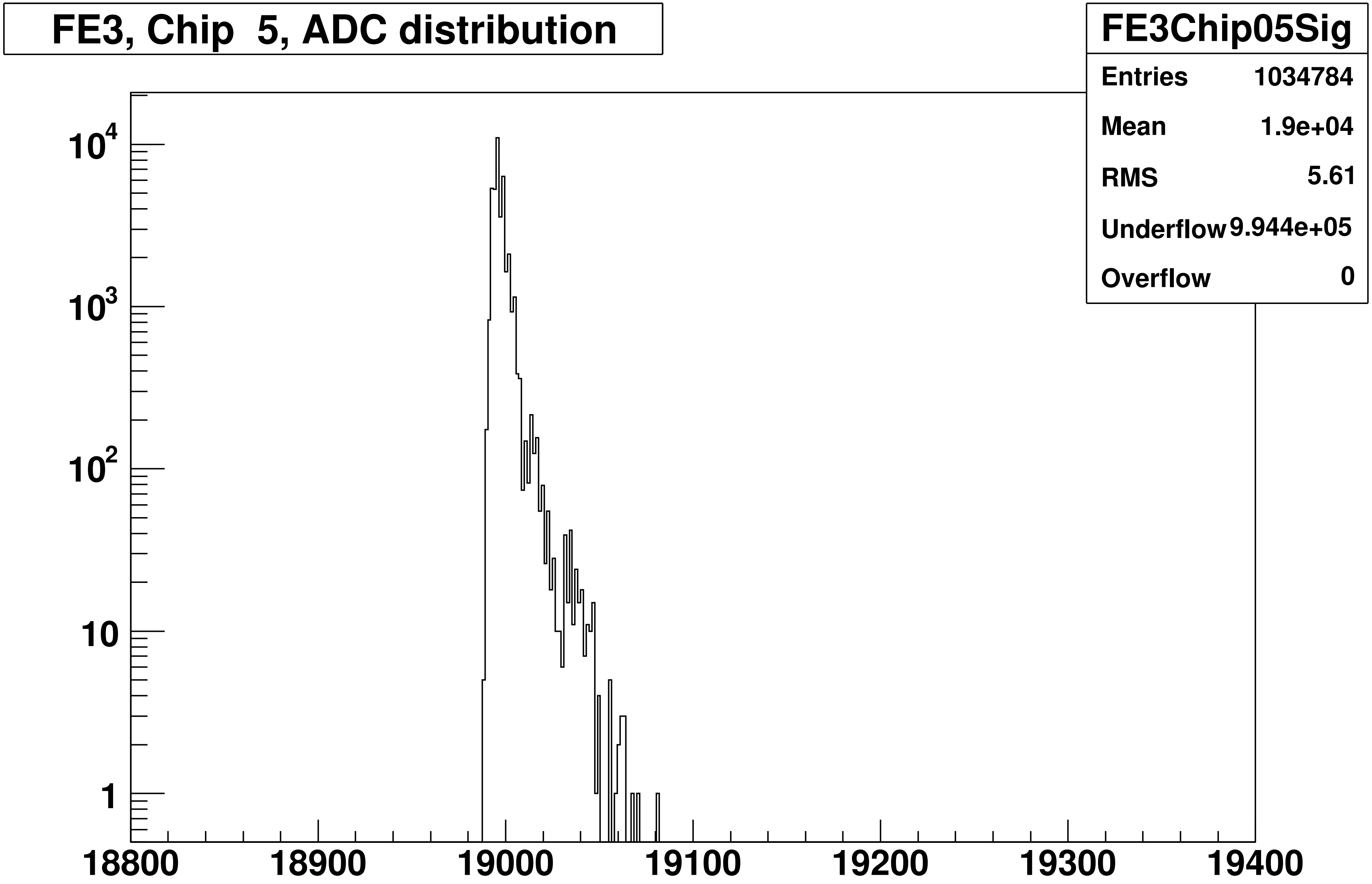

FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 1034784
Mean 1.9e+04
RMS 5.61
Underflow 9.944e+05
Overflow 0
10^4
10^3
10^2
10
1
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18900
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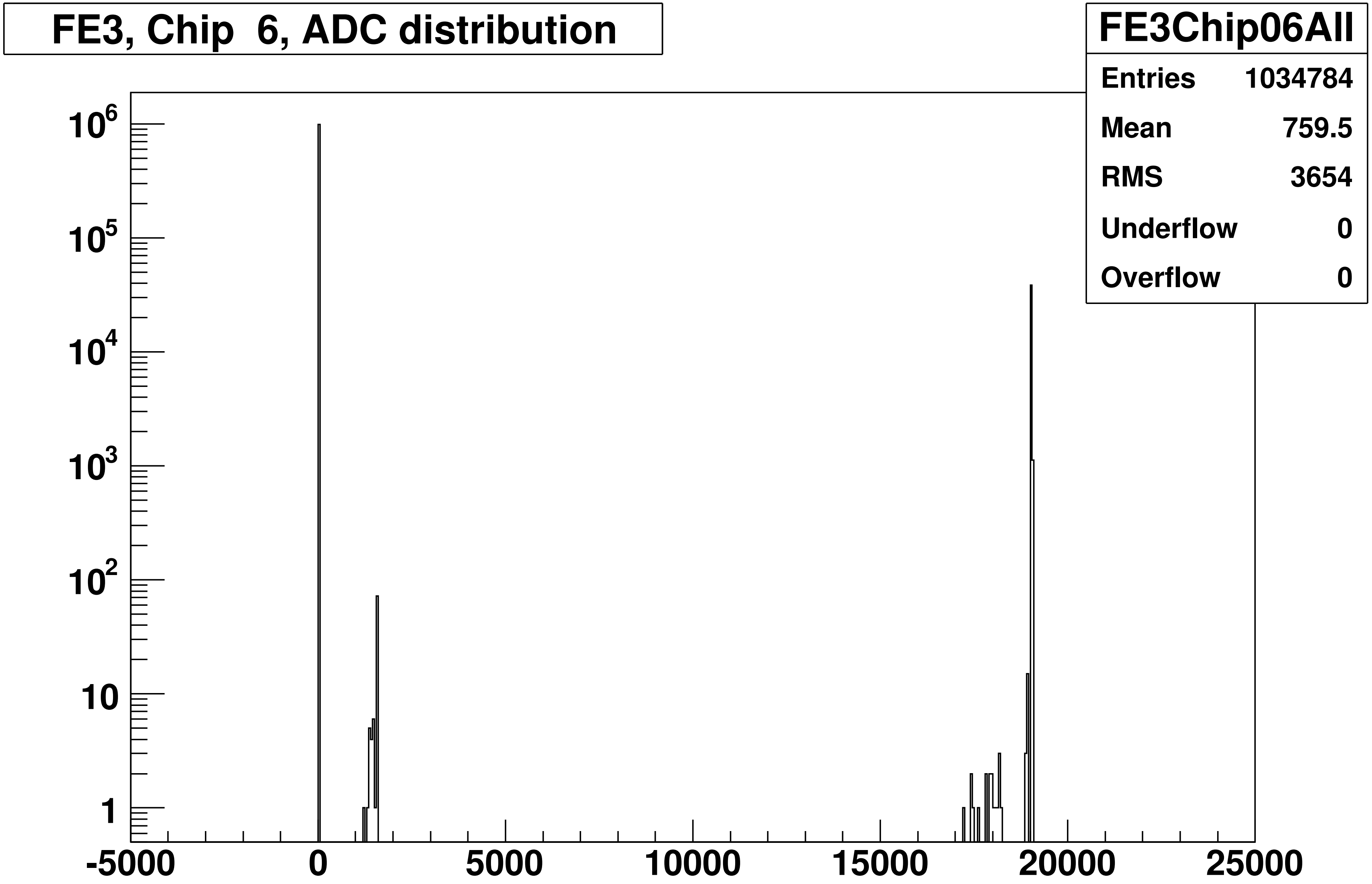
19400


FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 1034784
Mean 759.5
RMS 3654
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
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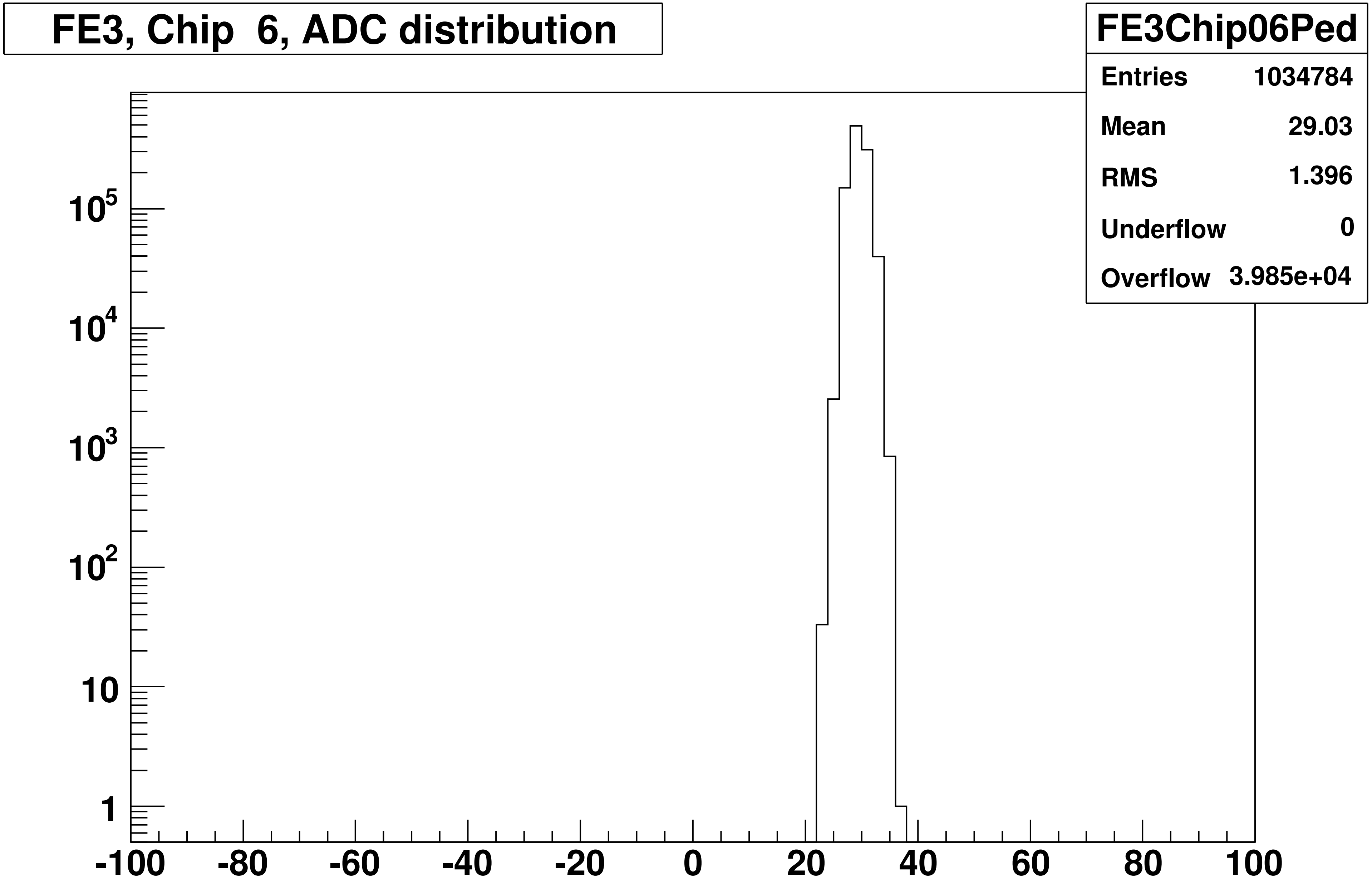
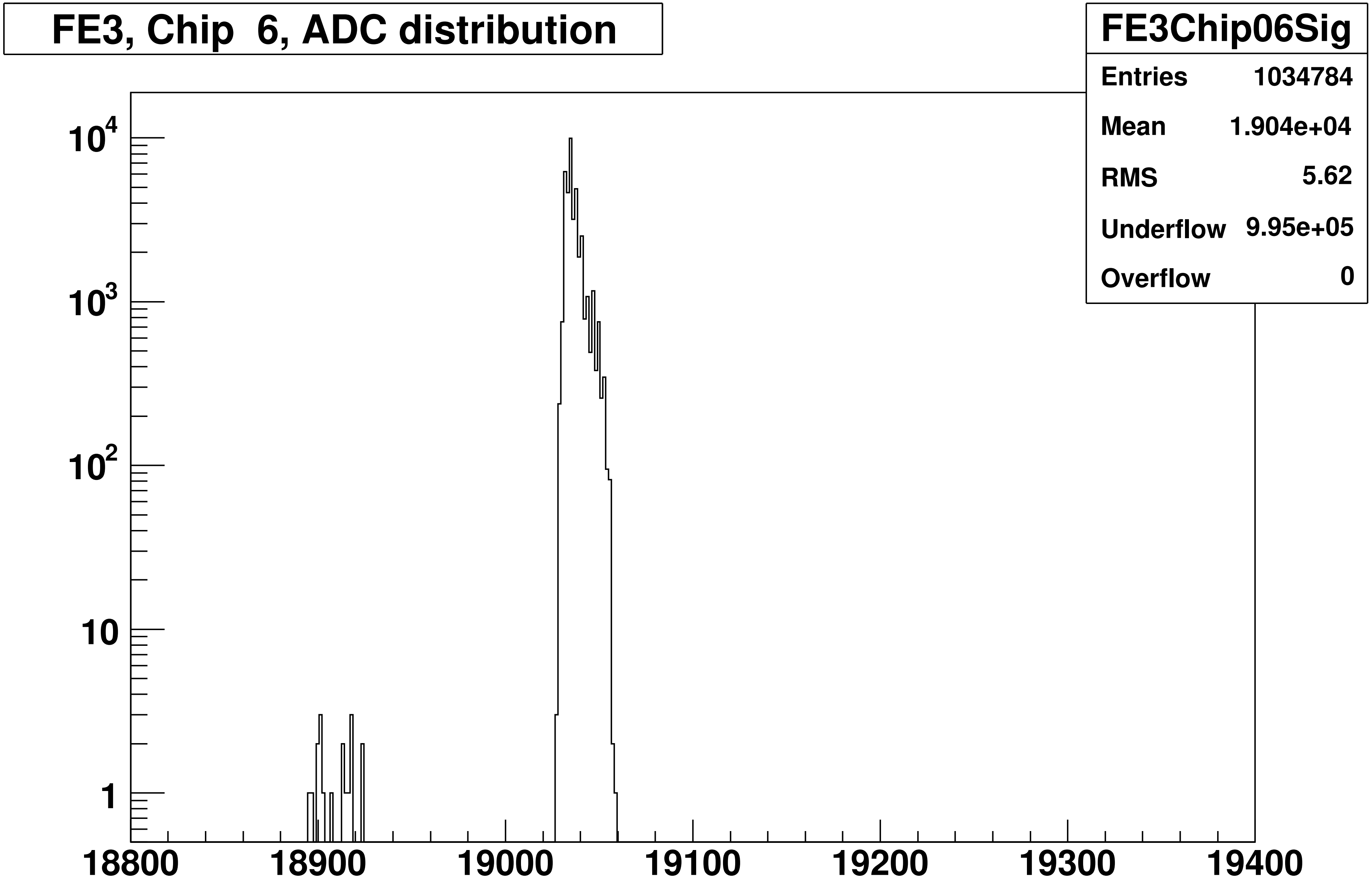
10000
15000
20000
25000



FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 1034784
Mean 29.03
RMS 1.396
Underflow 0
Overflow 3.985e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 1034784
Mean 1.904e+04
RMS 5.62
Underflow 9.95e+05
Overflow 0
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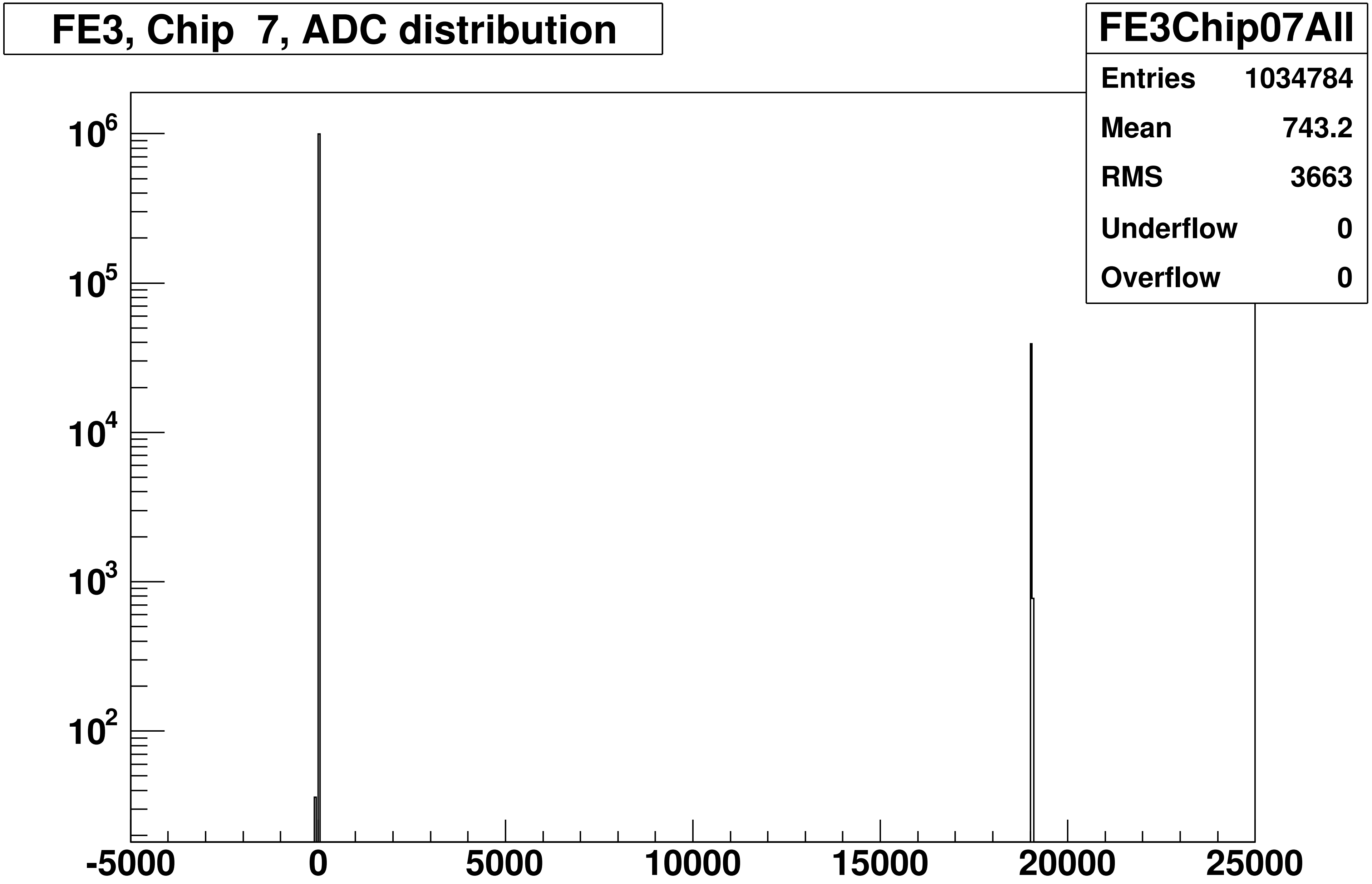
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 1034784
Mean 743.2
RMS 3663
Underflow 0
Overflow 0
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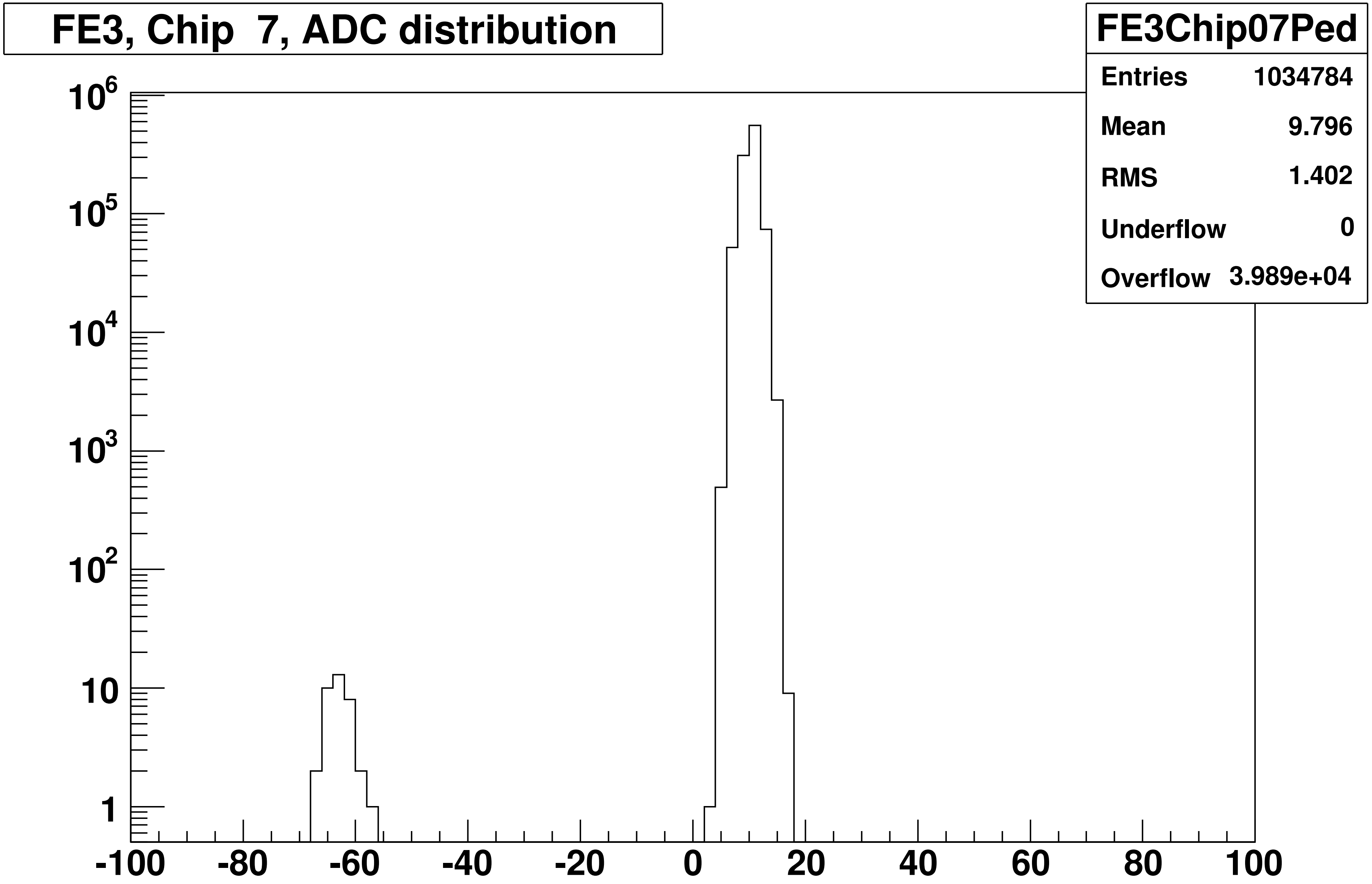
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 1034784
Mean 9.796
RMS 1.402
Underflow 0
Overflow 3.989e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
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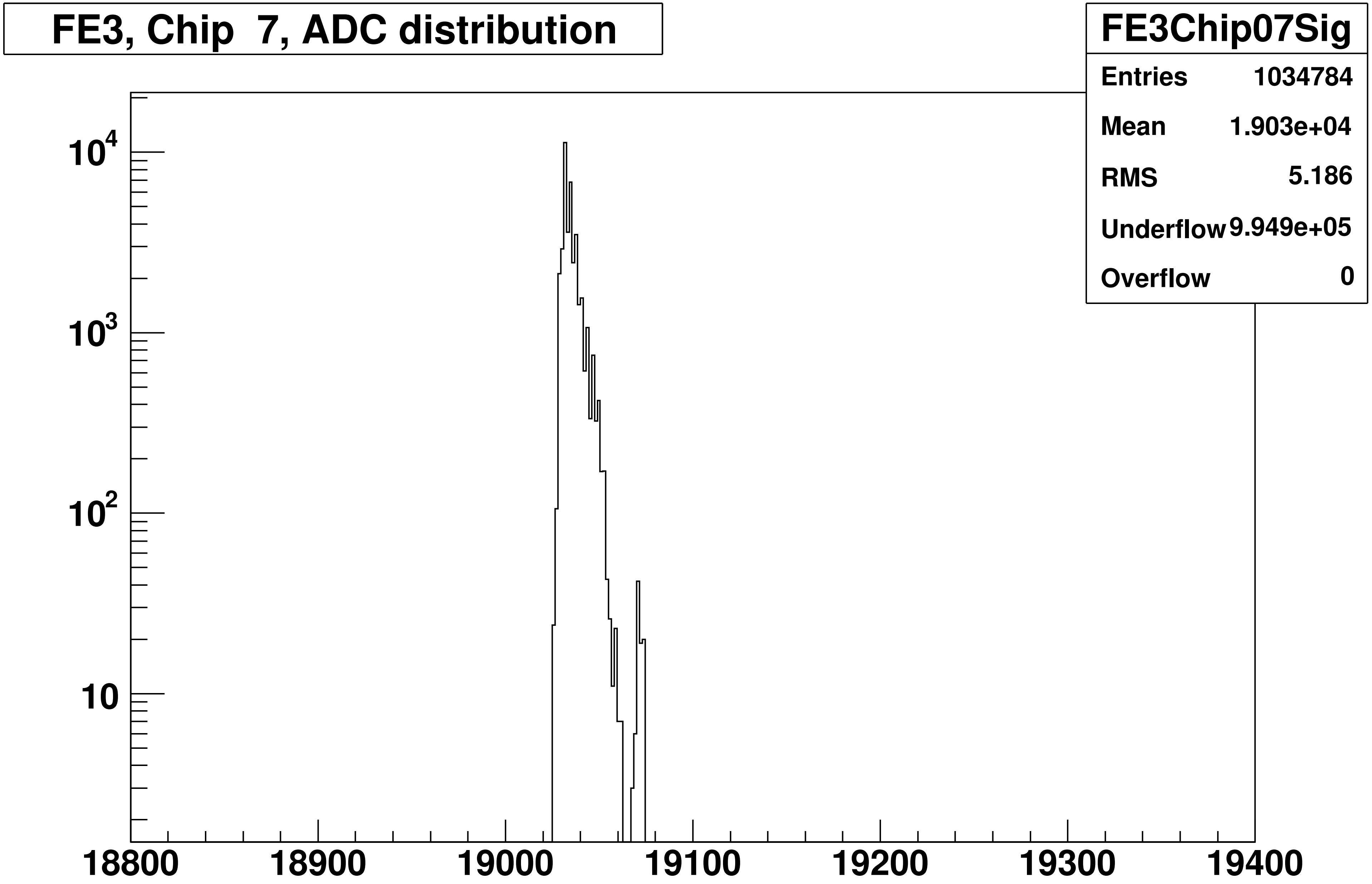
40
60
80
100



FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 1034784
Mean 1.903e+04
RMS 5.186
Underflow 9.949e+05
Overflow 0
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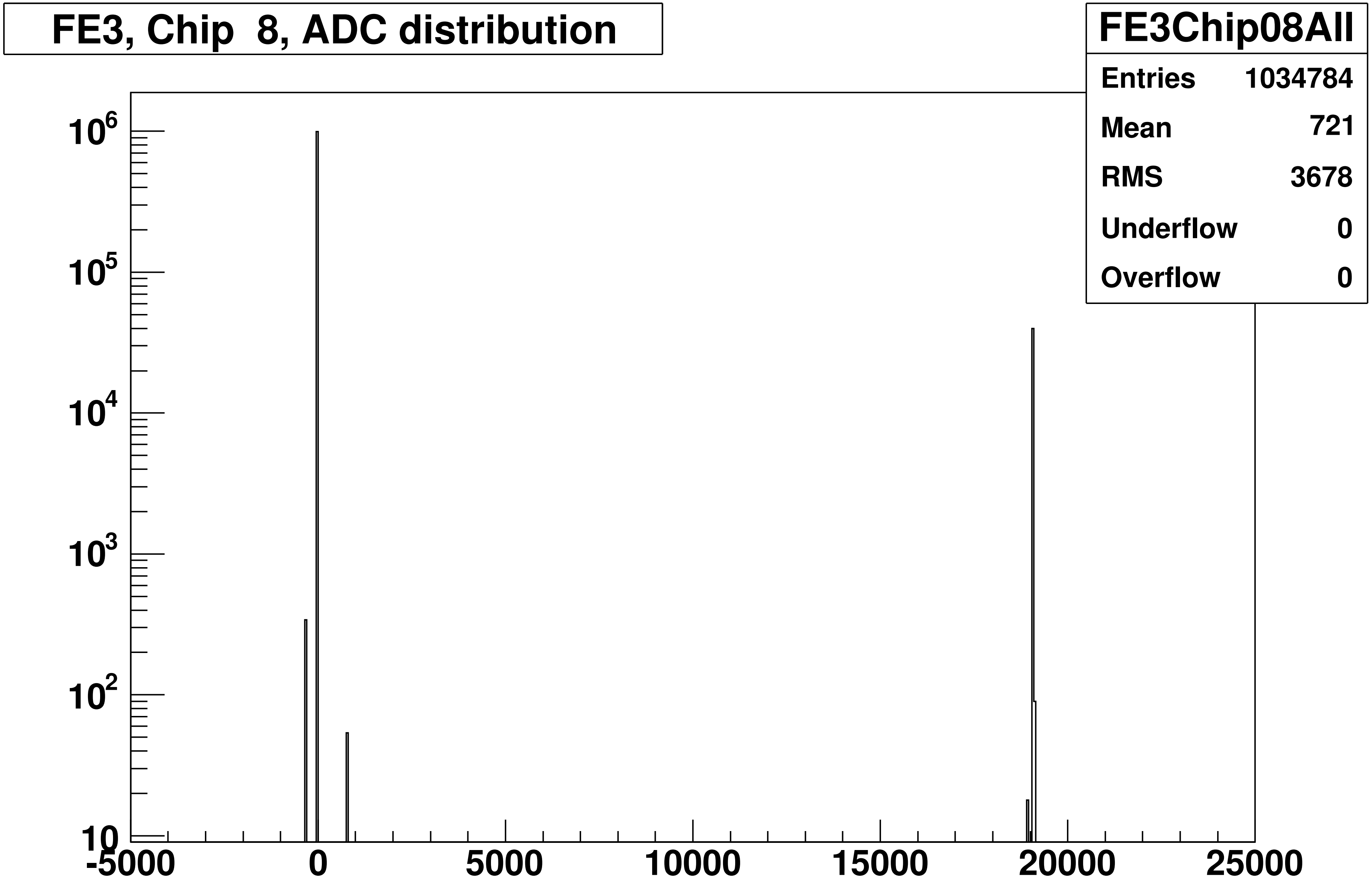
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400



FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 1034784
Mean 721
RMS 3678
Underflow 0
Overflow 0
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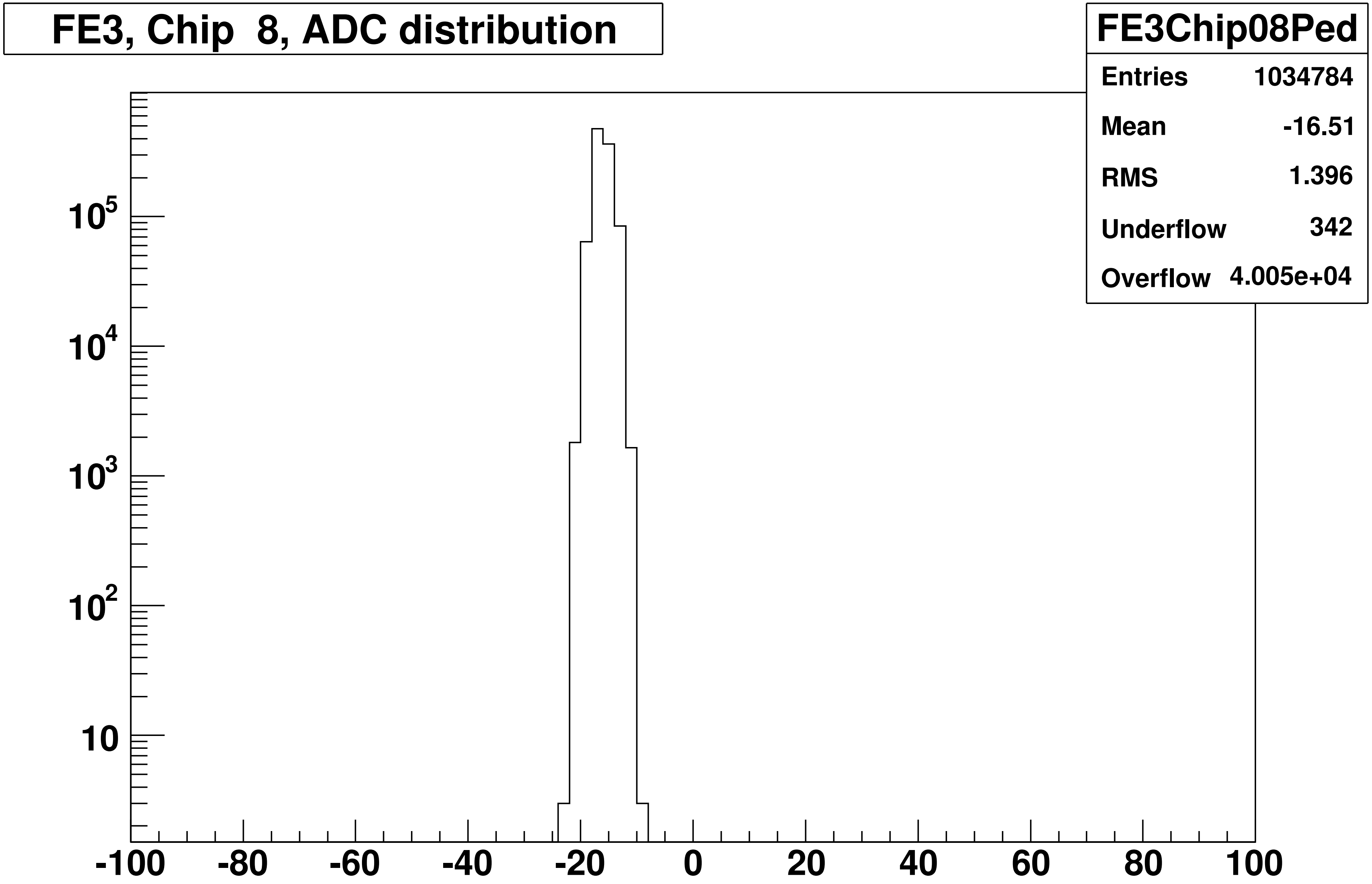
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 1034784
Mean -16.51
RMS 1.396
Underflow 342
Overflow 4.005e+04
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10^4
10^3
10^2
10
-100
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-60
-40
-20
0
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40
60
80
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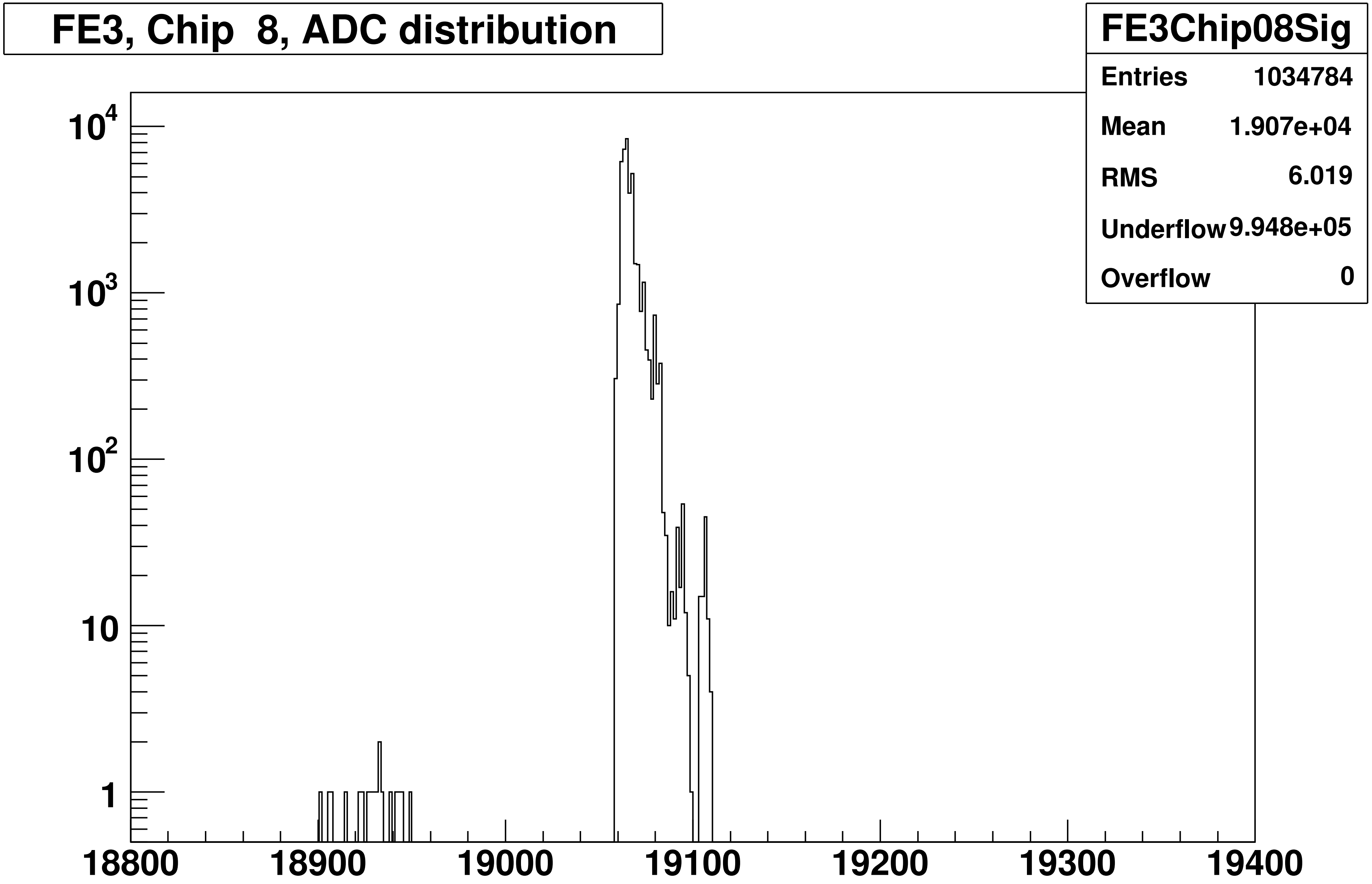

FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 1034784
Mean 1.907e+04
RMS 6.019
Underflow 9.948e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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19200
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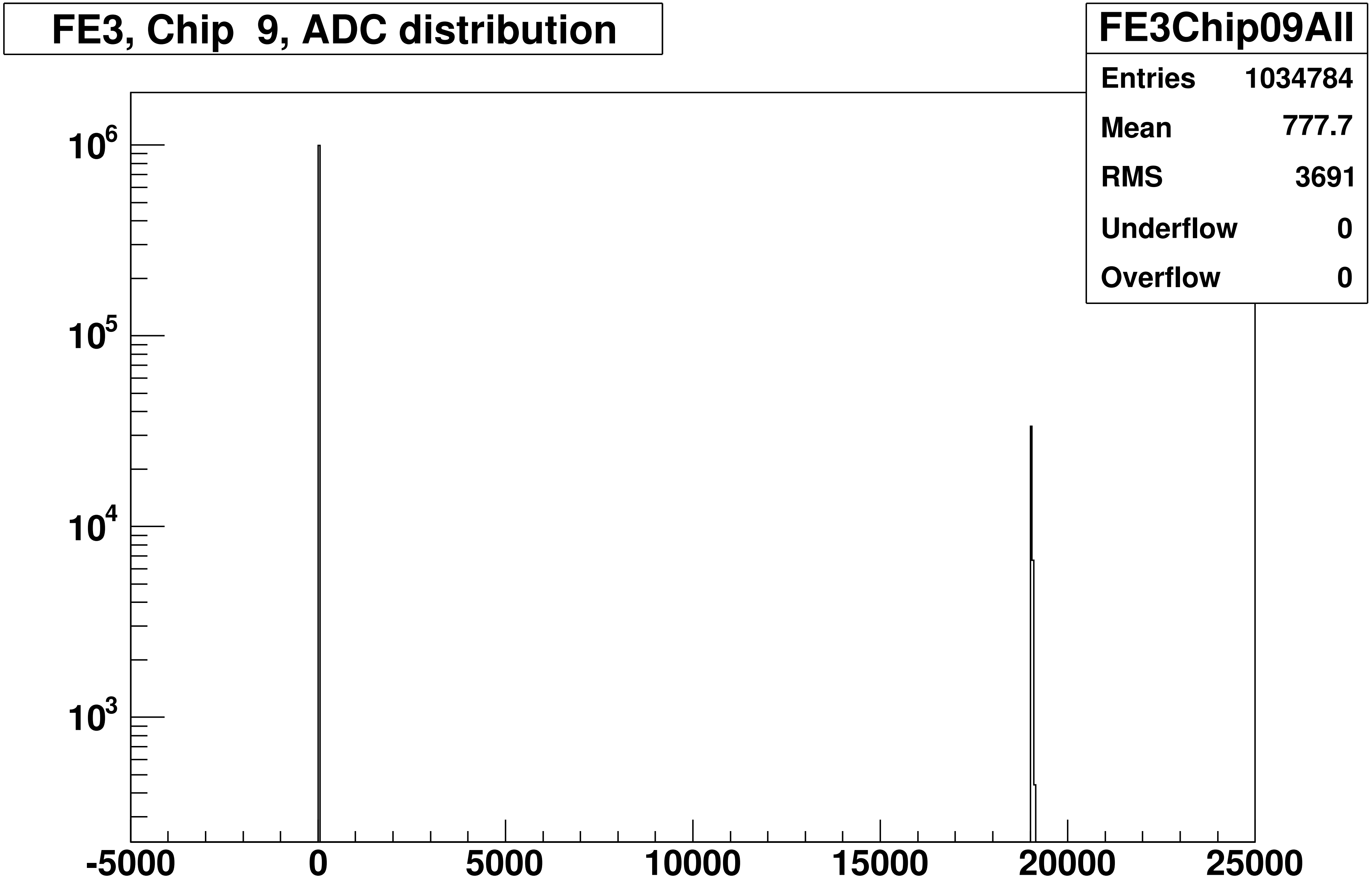
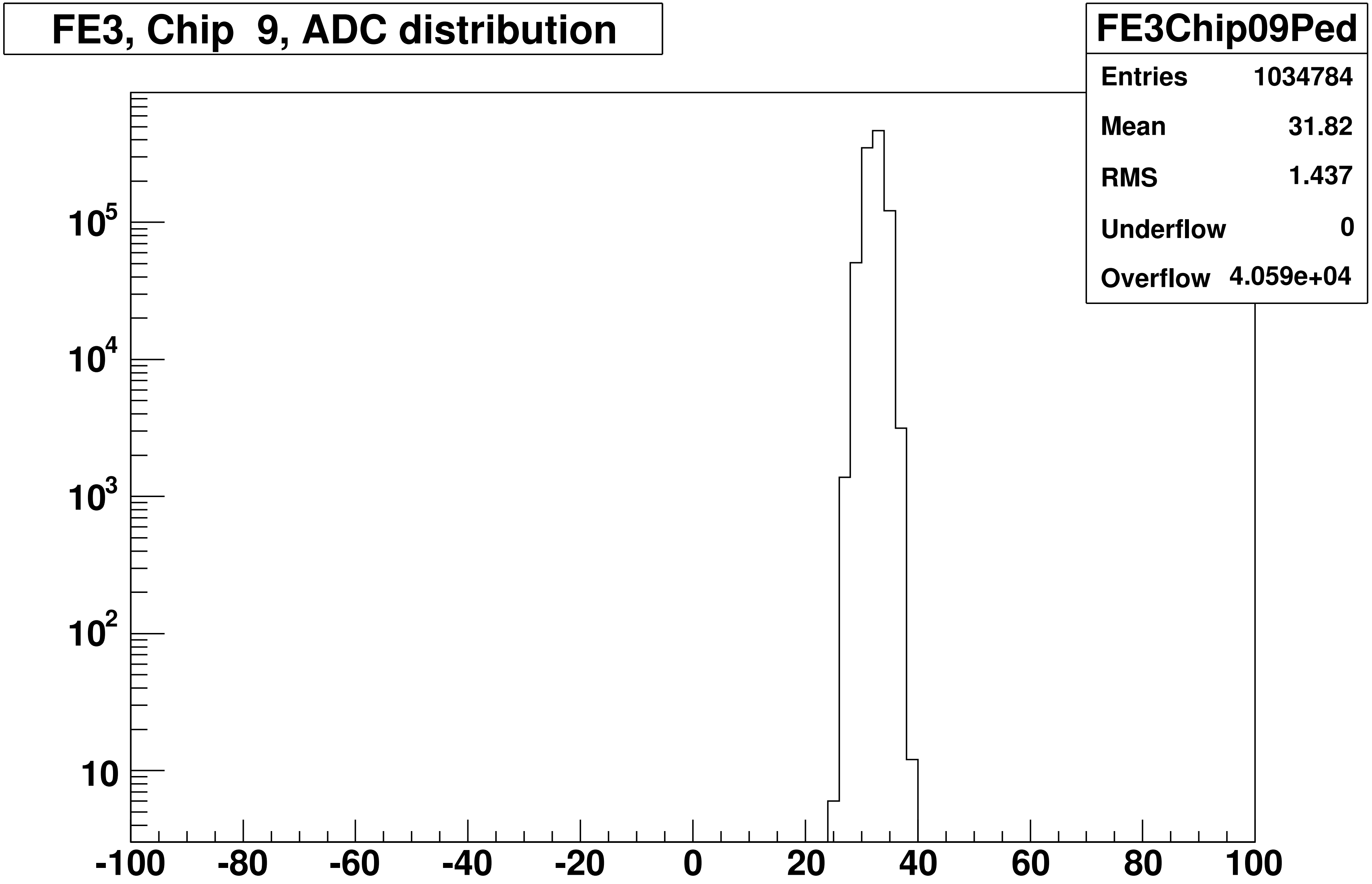
19400



FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 1034784
Mean 777.7
RMS 3691
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 1034784
Mean 31.82
RMS 1.437
Underflow 0
Overflow 4.059e+04
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10^4
10^3
10^2
10
-100
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-60
-40
-20
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40
60
80
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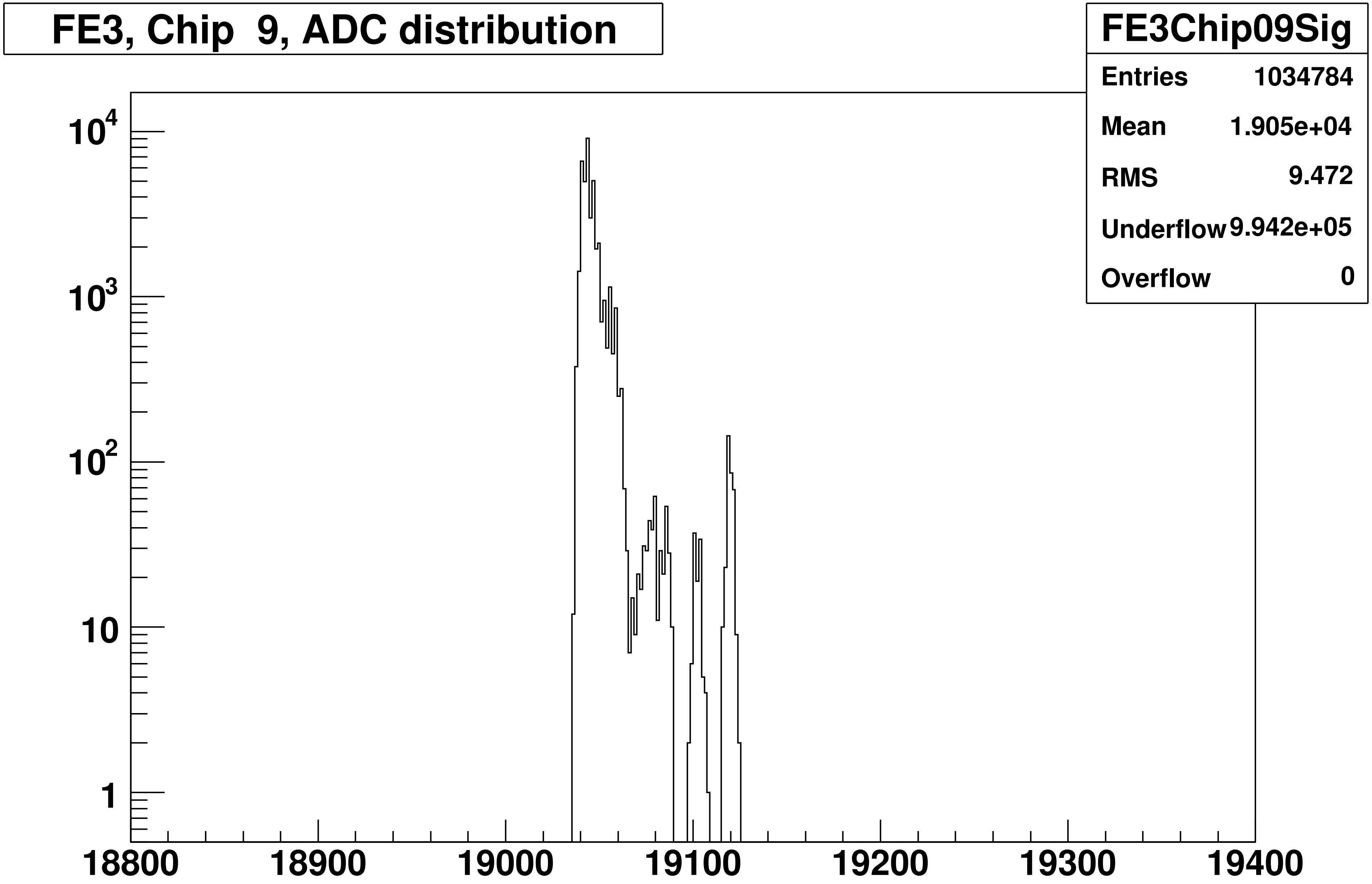
FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 1034784
Mean 1.905e+04
RMS 9.472
Underflow 9.942e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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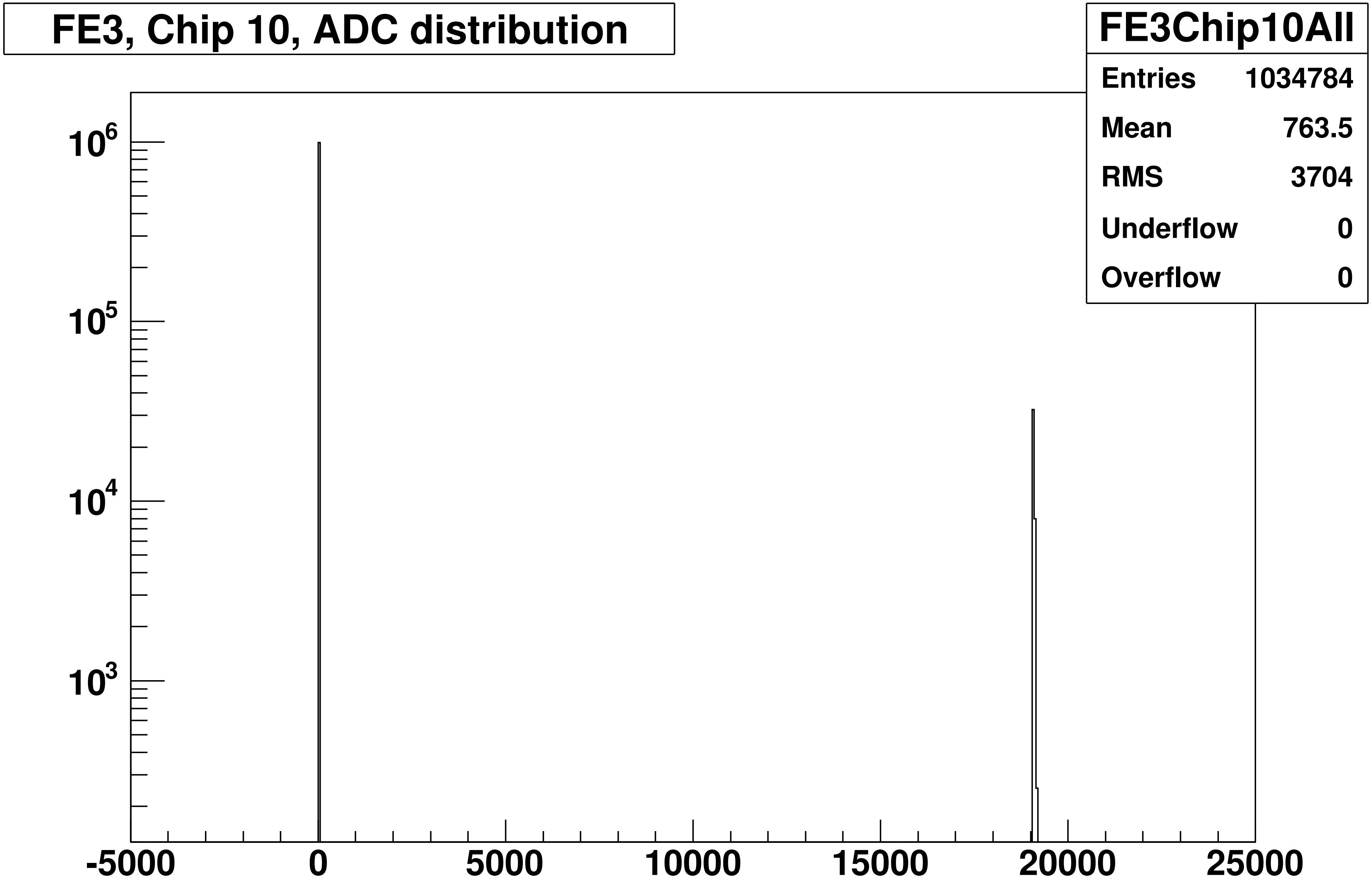
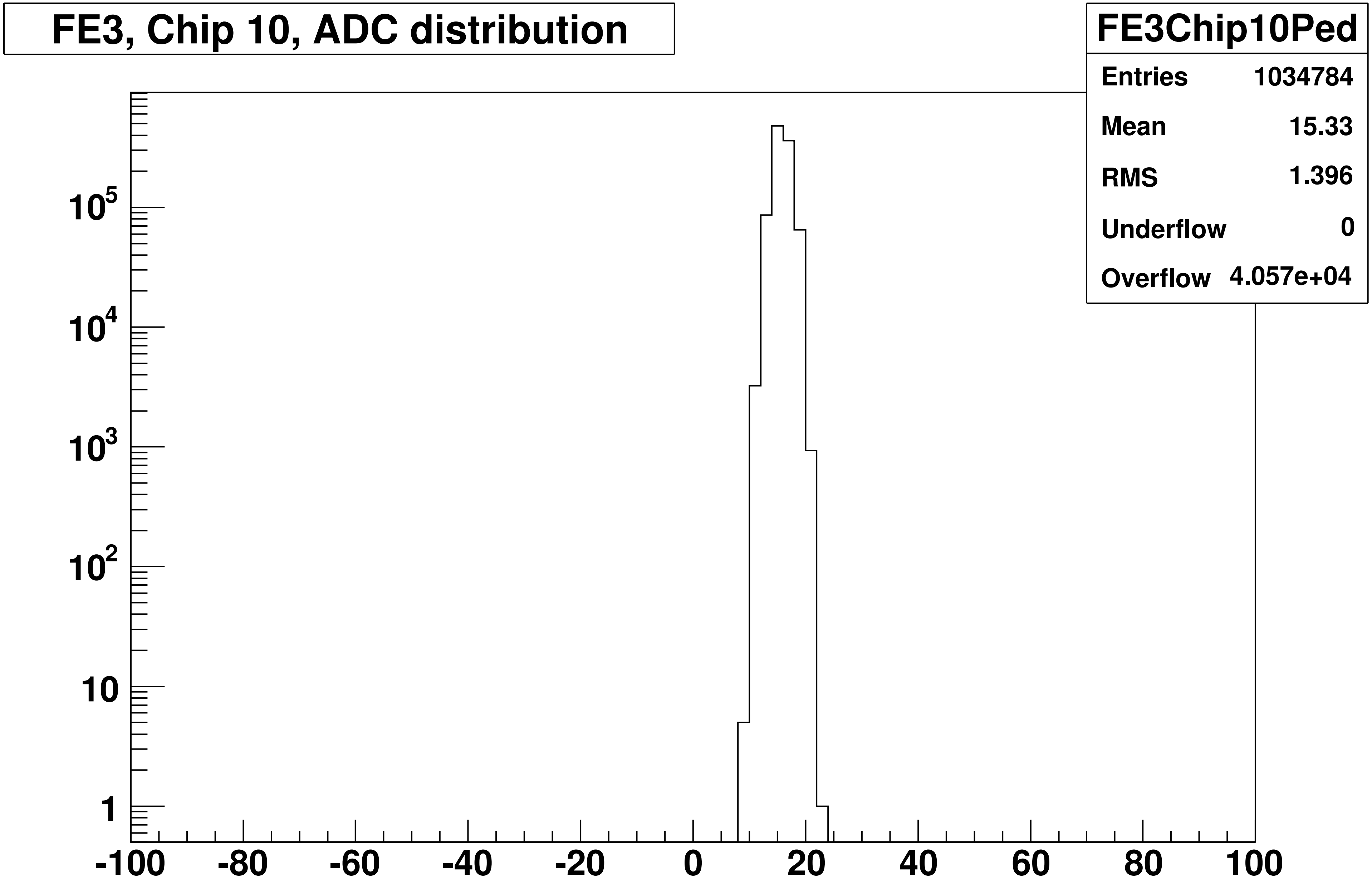
19400


FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 1034784
Mean 763.5
RMS 3704
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 1034784
Mean 15.33
RMS 1.396
Underflow 0
Overflow 4.057e+04
10^5
10^4
10^3
10^2
10
1
-100
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-60
-40
-20
0
20
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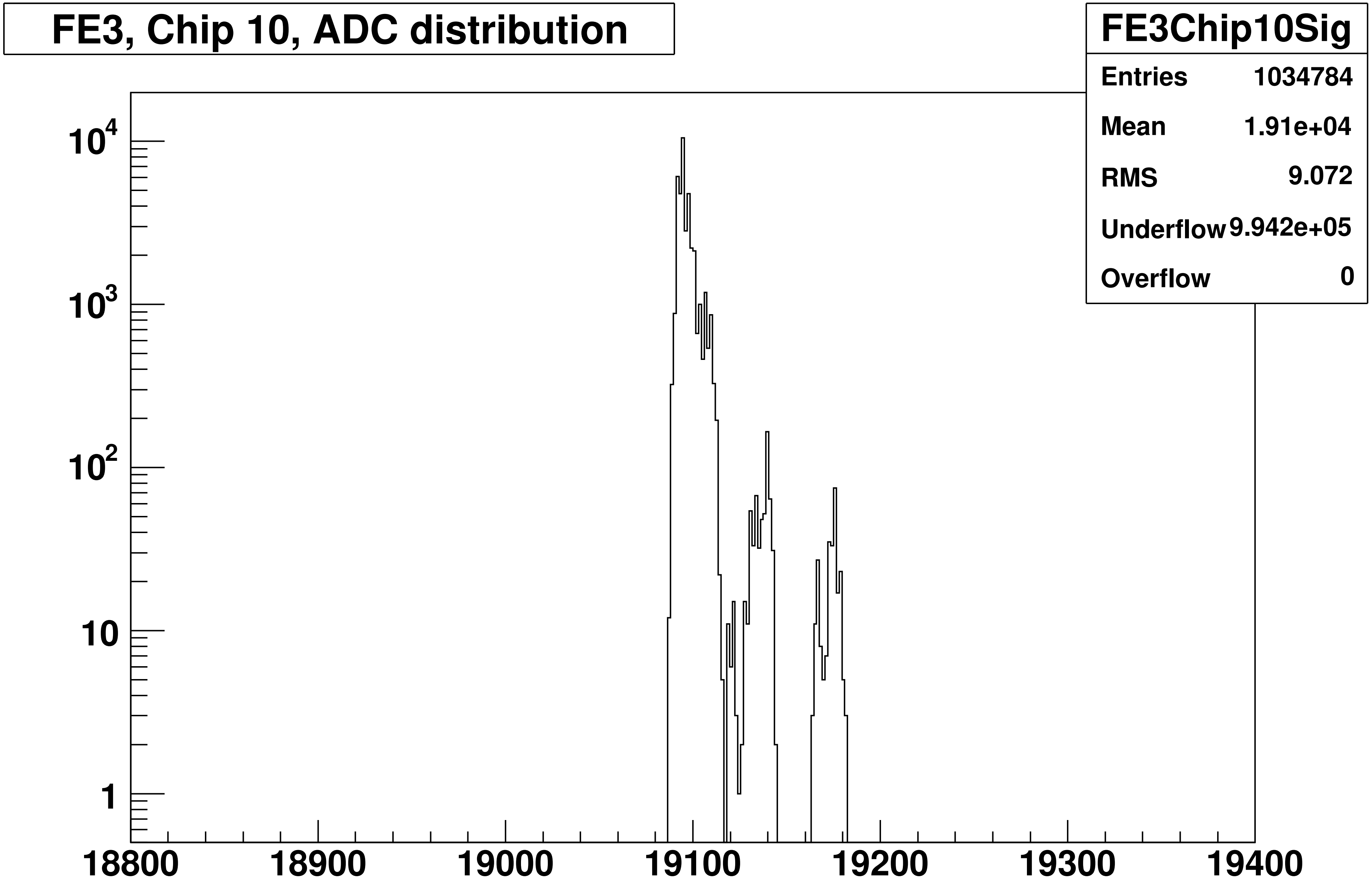
60
80
100


FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 1034784
Mean 1.91e+04
RMS 9.072
Underflow 9.942e+05
Overflow 0
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10^3
10^2
10
1
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18900
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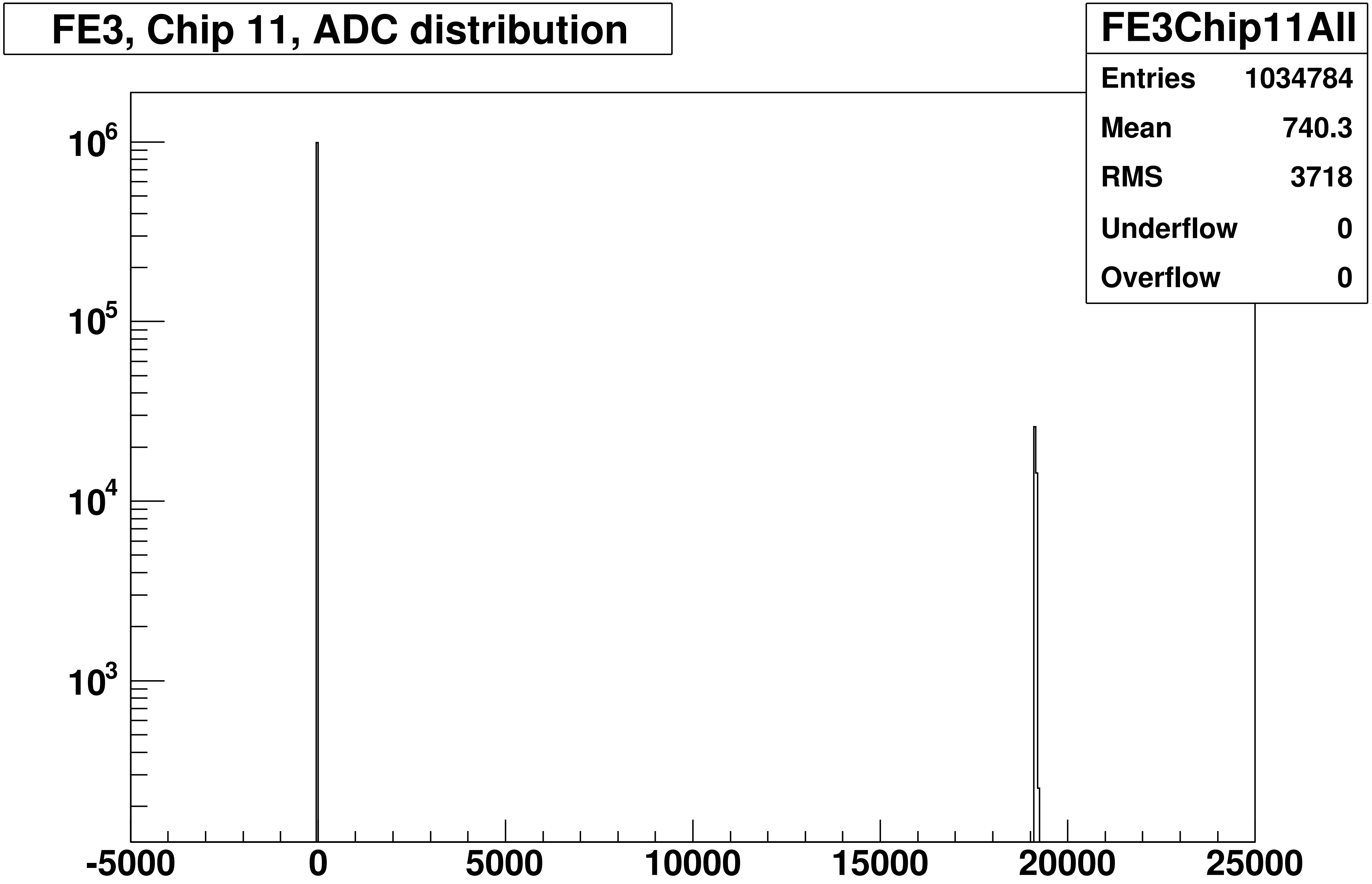
19400


FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 1034784
Mean 740.3
RMS 3718
Underflow 0
Overflow 0
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10^5
10^4
10^3
-5000
0
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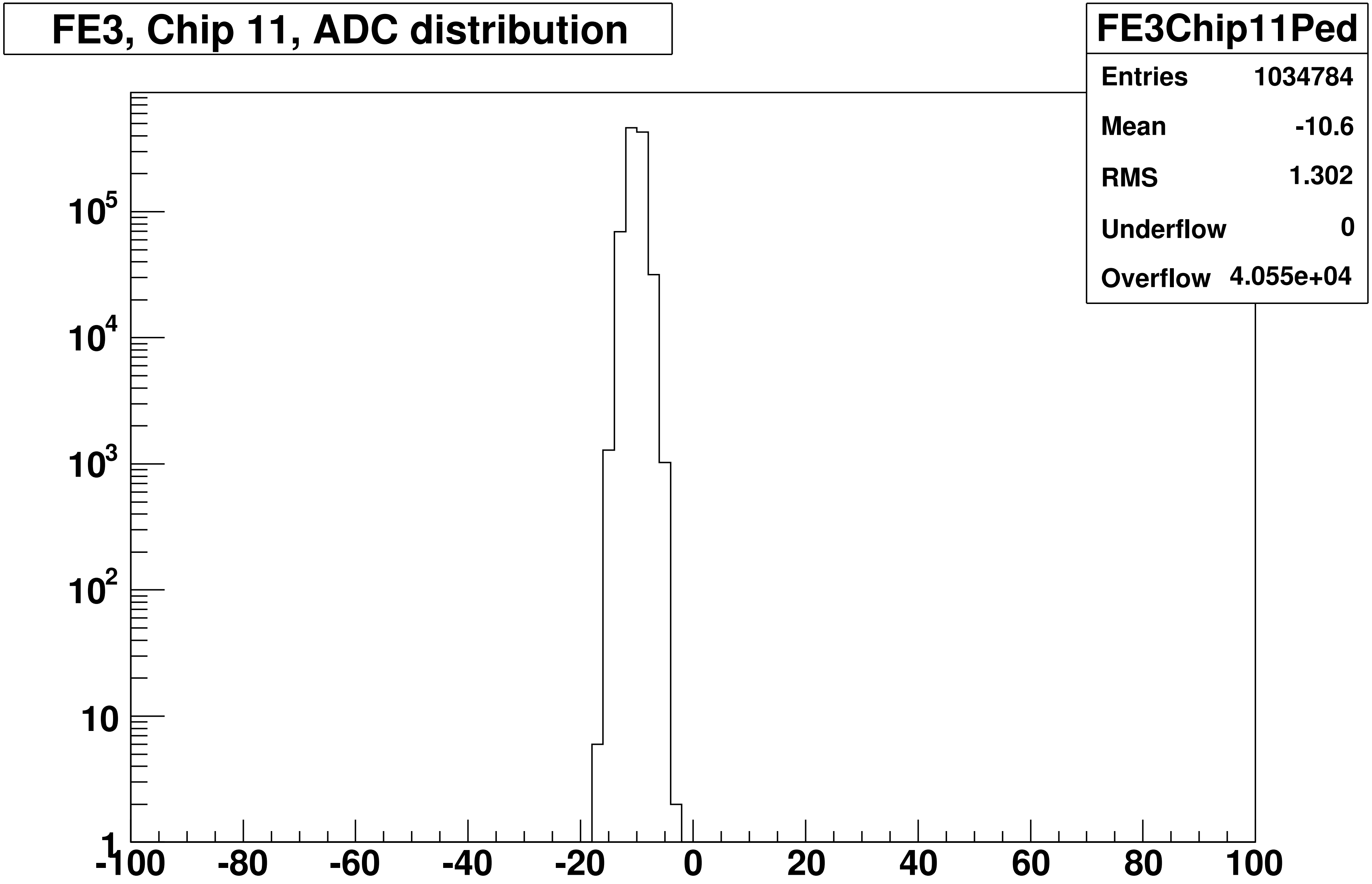
10000
15000
20000
25000


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 1034784
Mean -10.6
RMS 1.302
Underflow 0
Overflow 4.055e+04
10^5
10^4
10^3
10^2
10
1
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-20
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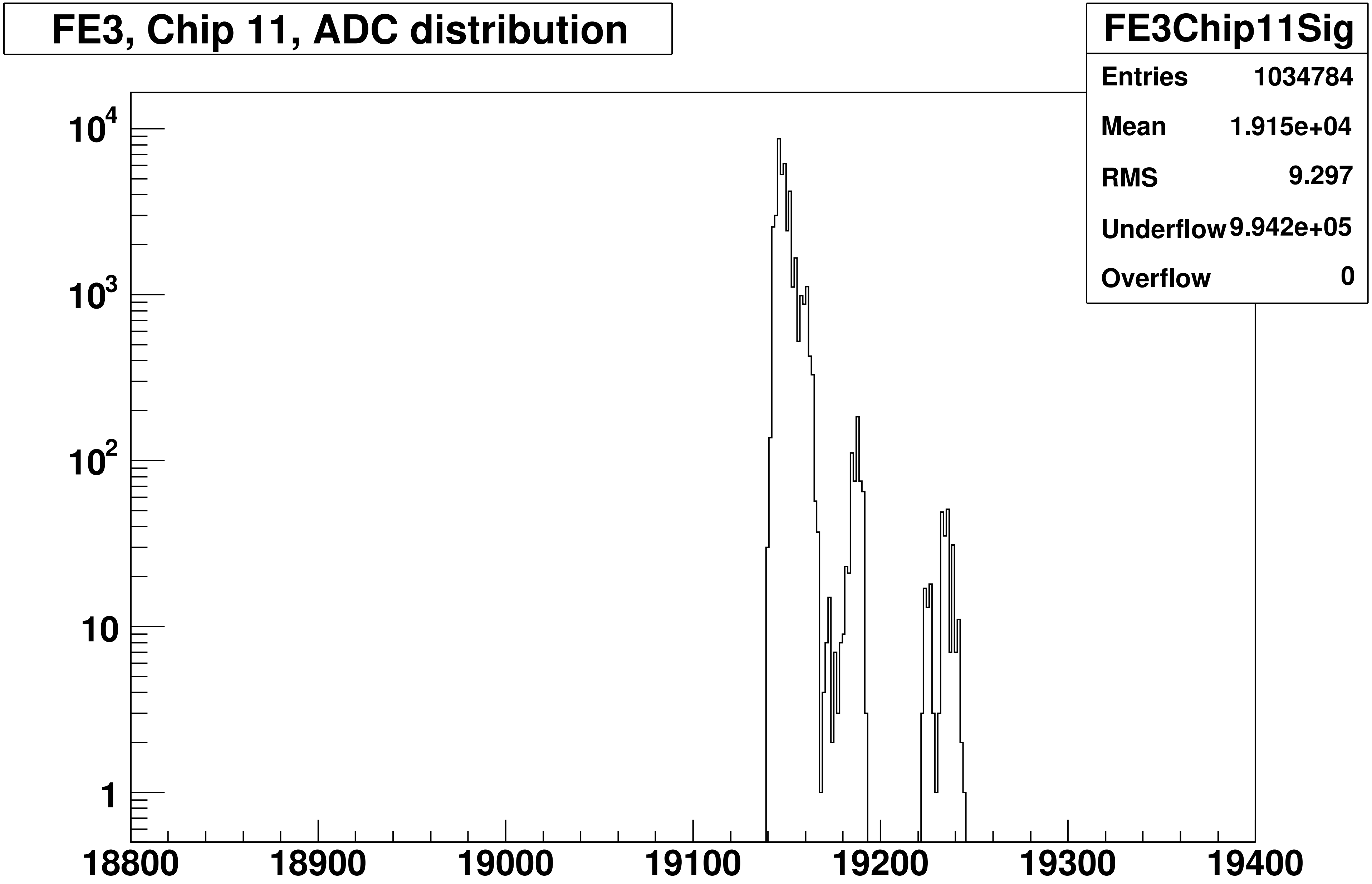

FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 1034784
Mean 1.915e+04
RMS 9.297
Underflow 9.942e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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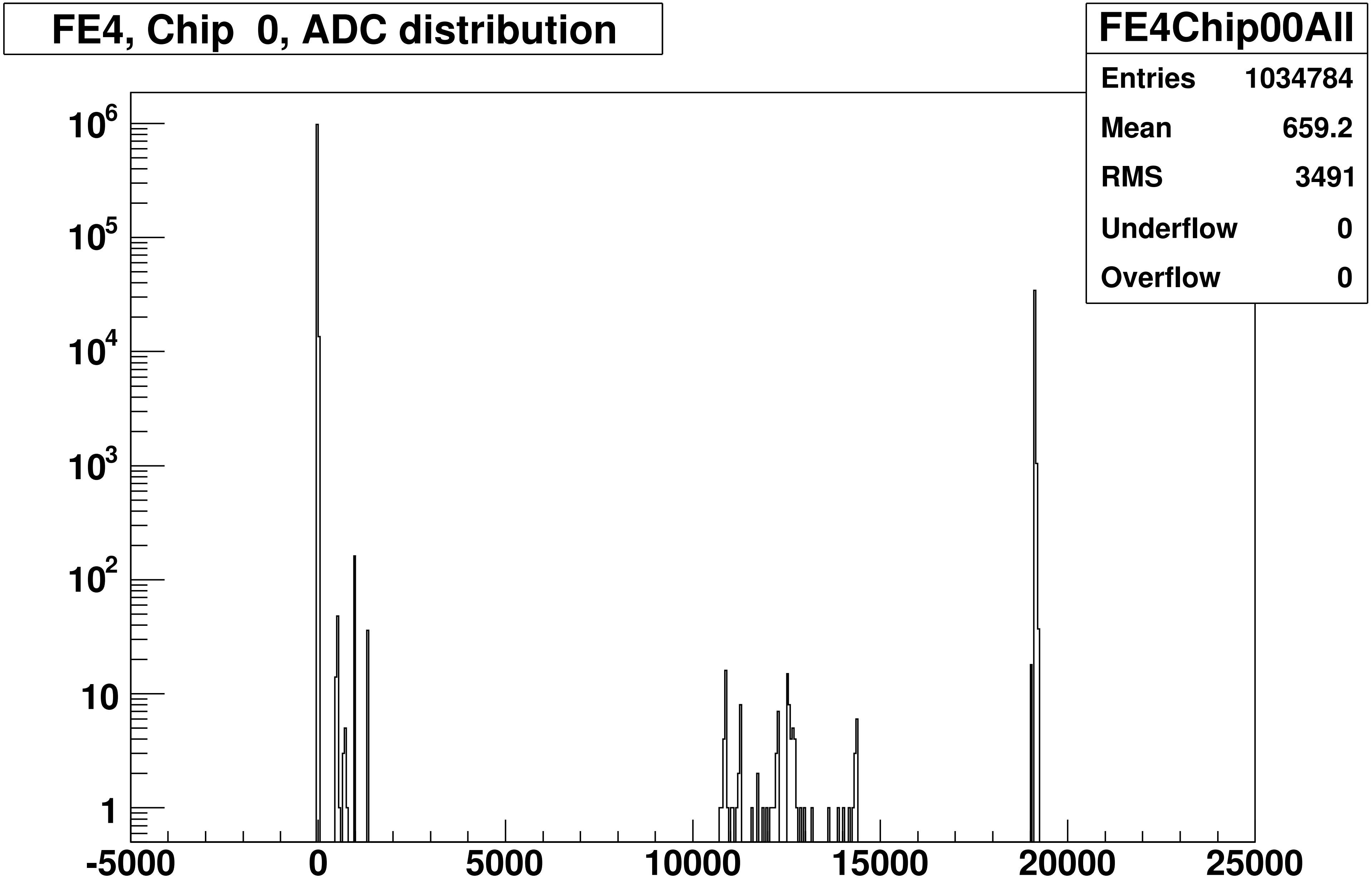

FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 1034784
Mean 659.2
RMS 3491
Underflow 0
Overflow 0
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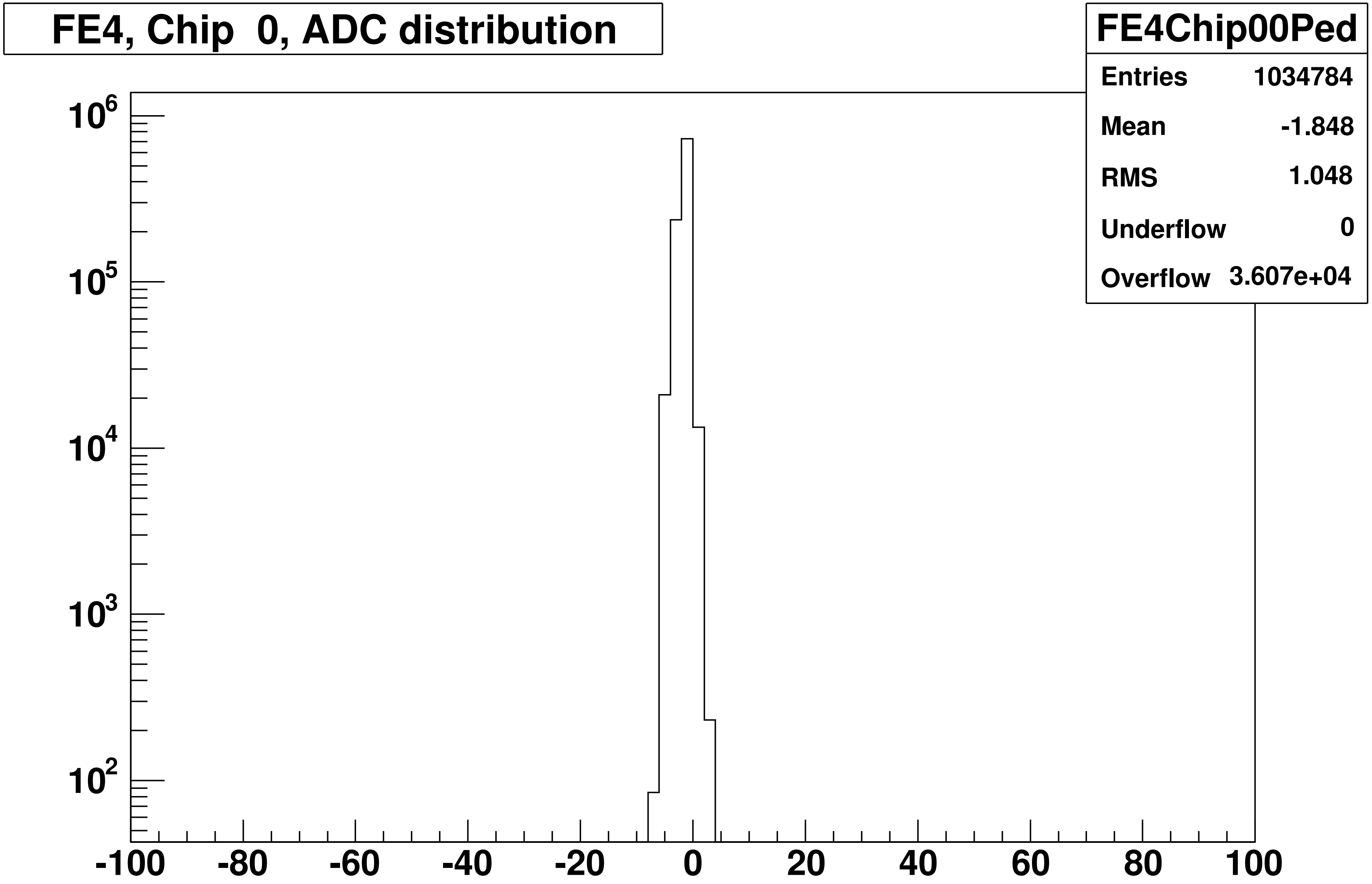
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 1034784
Mean -1.848
RMS 1.048
Underflow 0
Overflow 3.607e+04
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10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
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60
80
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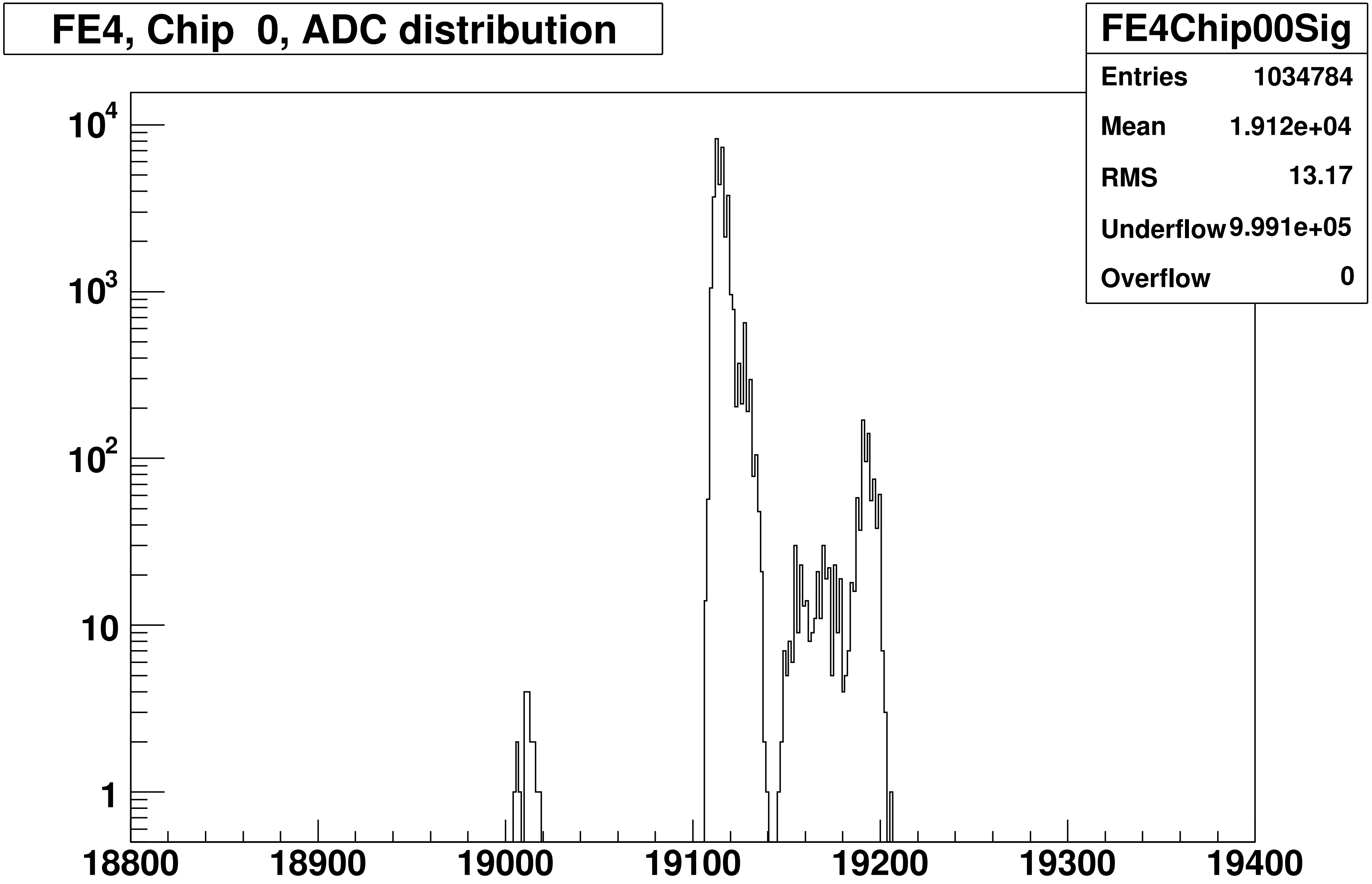

FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 1034784
Mean 1.912e+04
RMS 13.17
Underflow 9.991e+05
Overflow 0
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10^3
10^2
10
1
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18900
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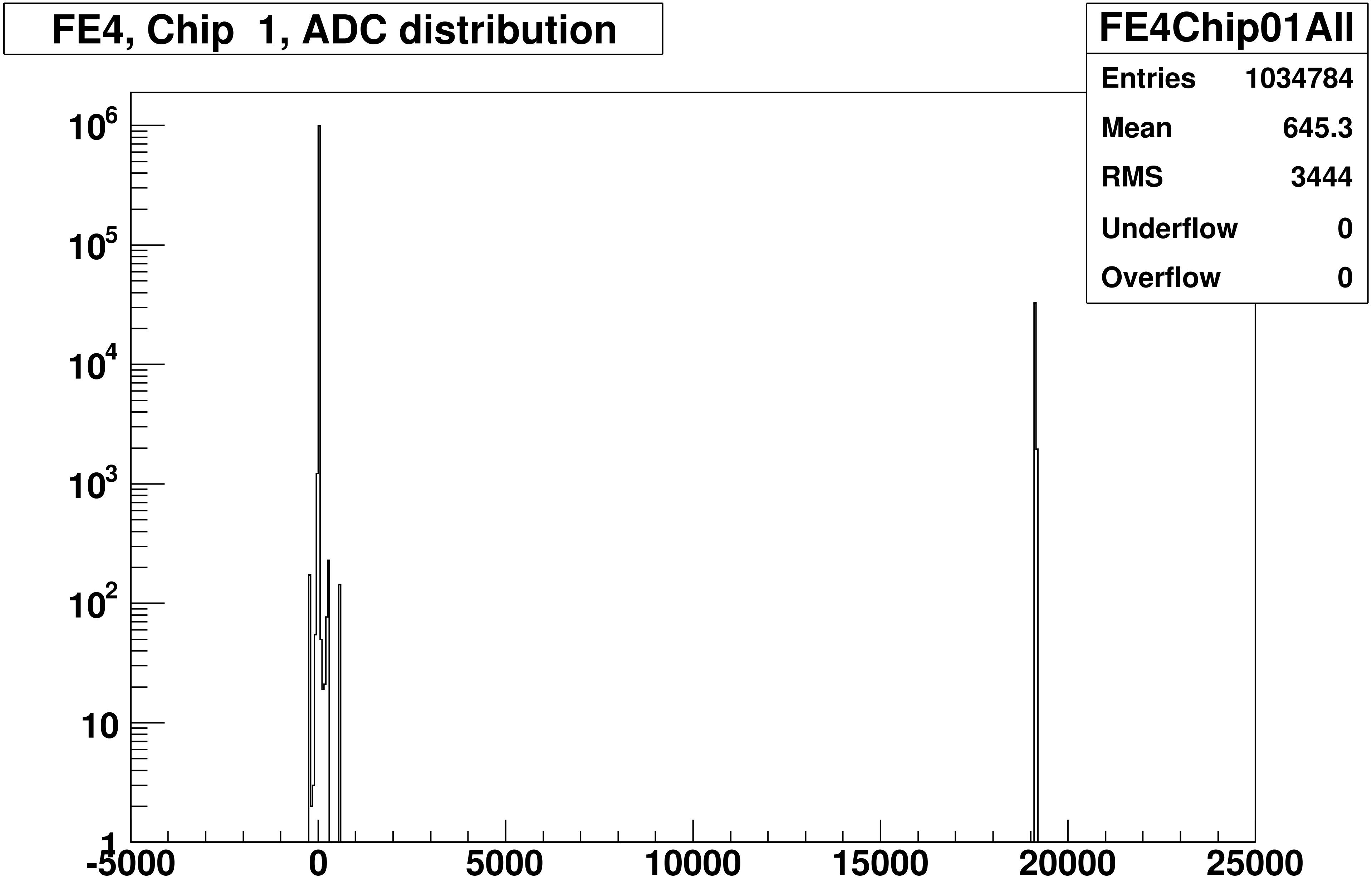
19400


FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 1034784
Mean 645.3
RMS 3444
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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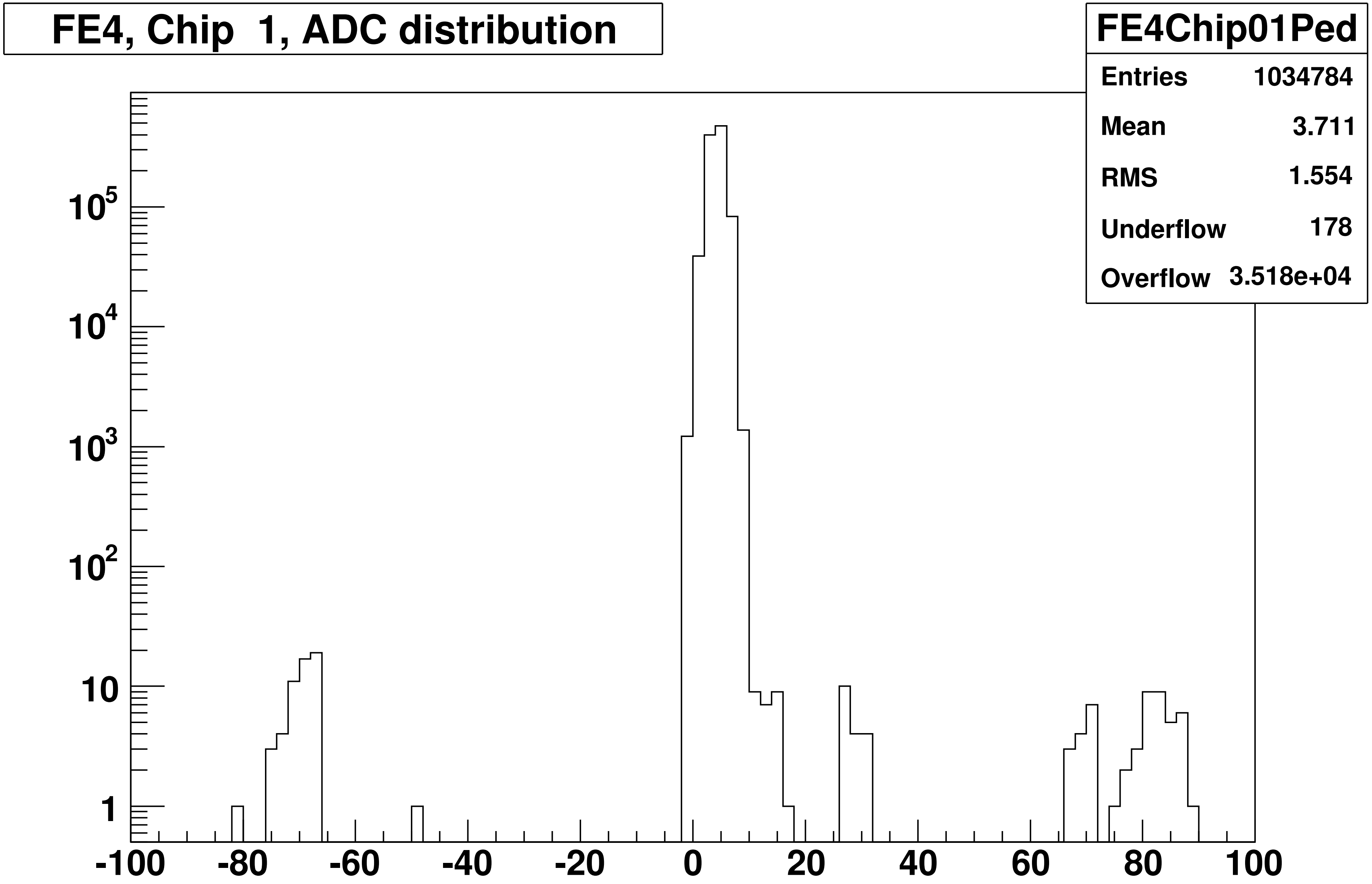
25000


FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 1034784
Mean 3.711
RMS 1.554
Underflow 178
Overflow 3.518e+04
10^5
10^4
10^3
10^2
10
1
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-20
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80
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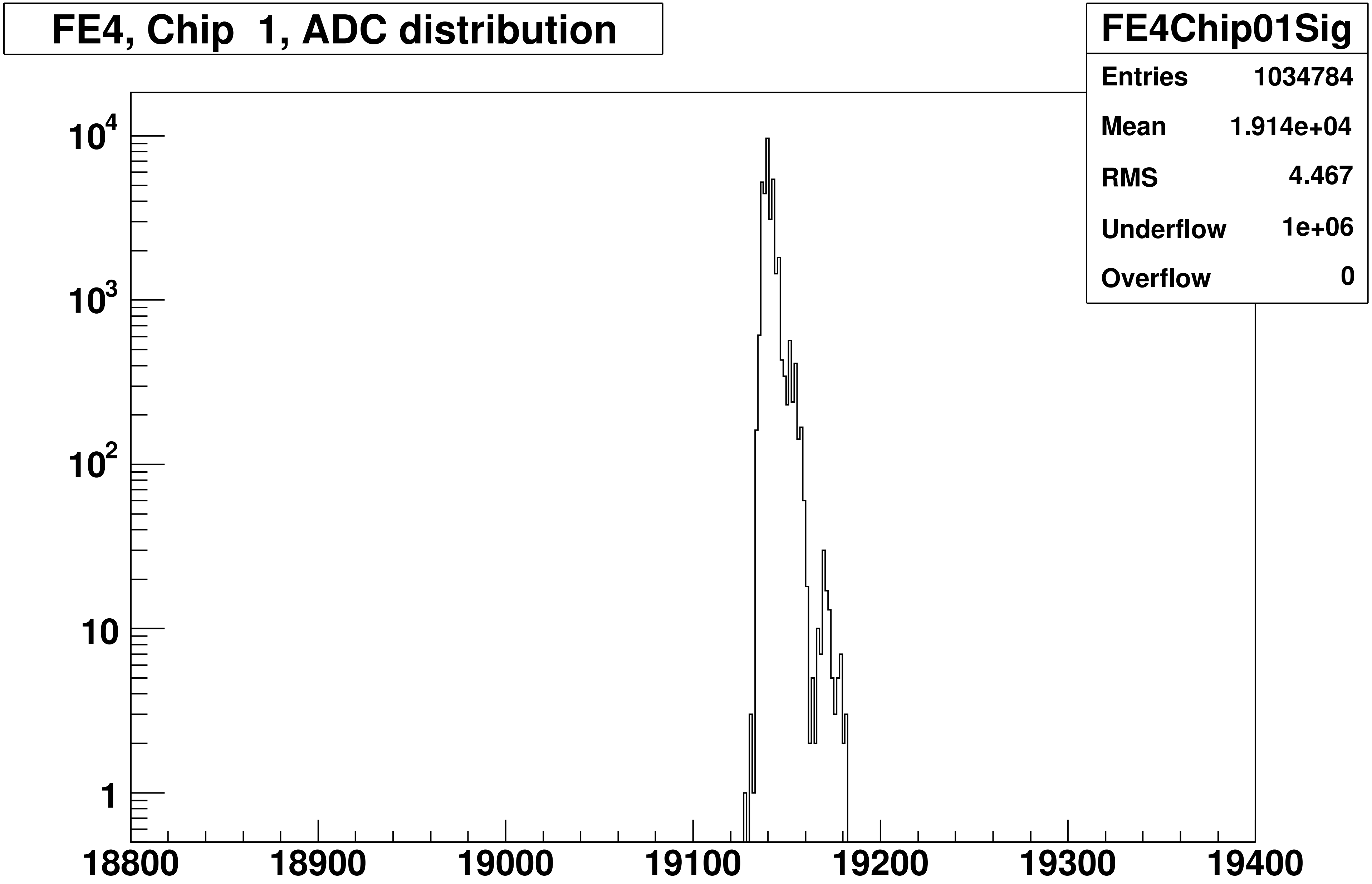

FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 1034784
Mean 1.914e+04
RMS 4.467
Underflow 1e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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19200
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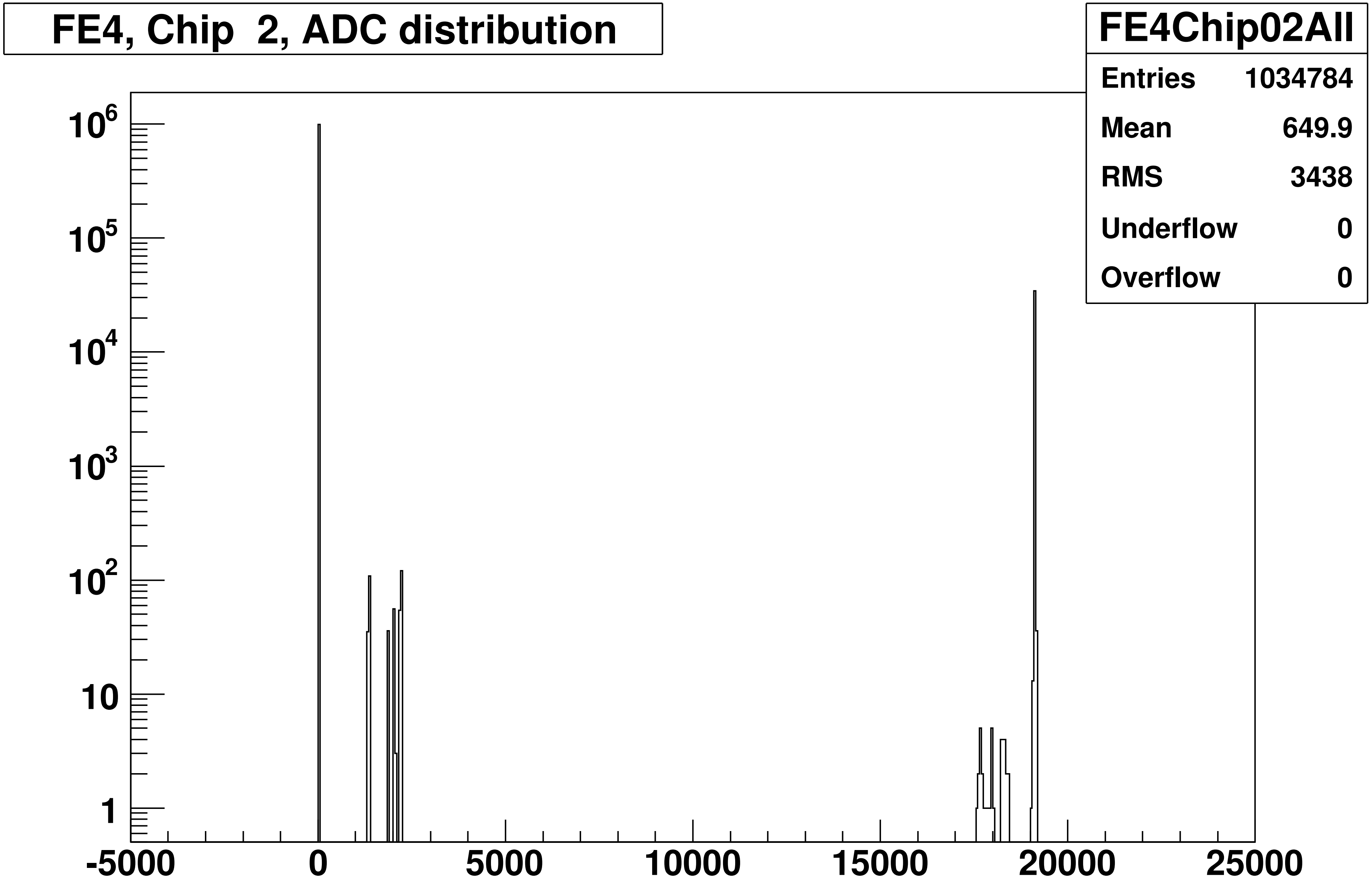
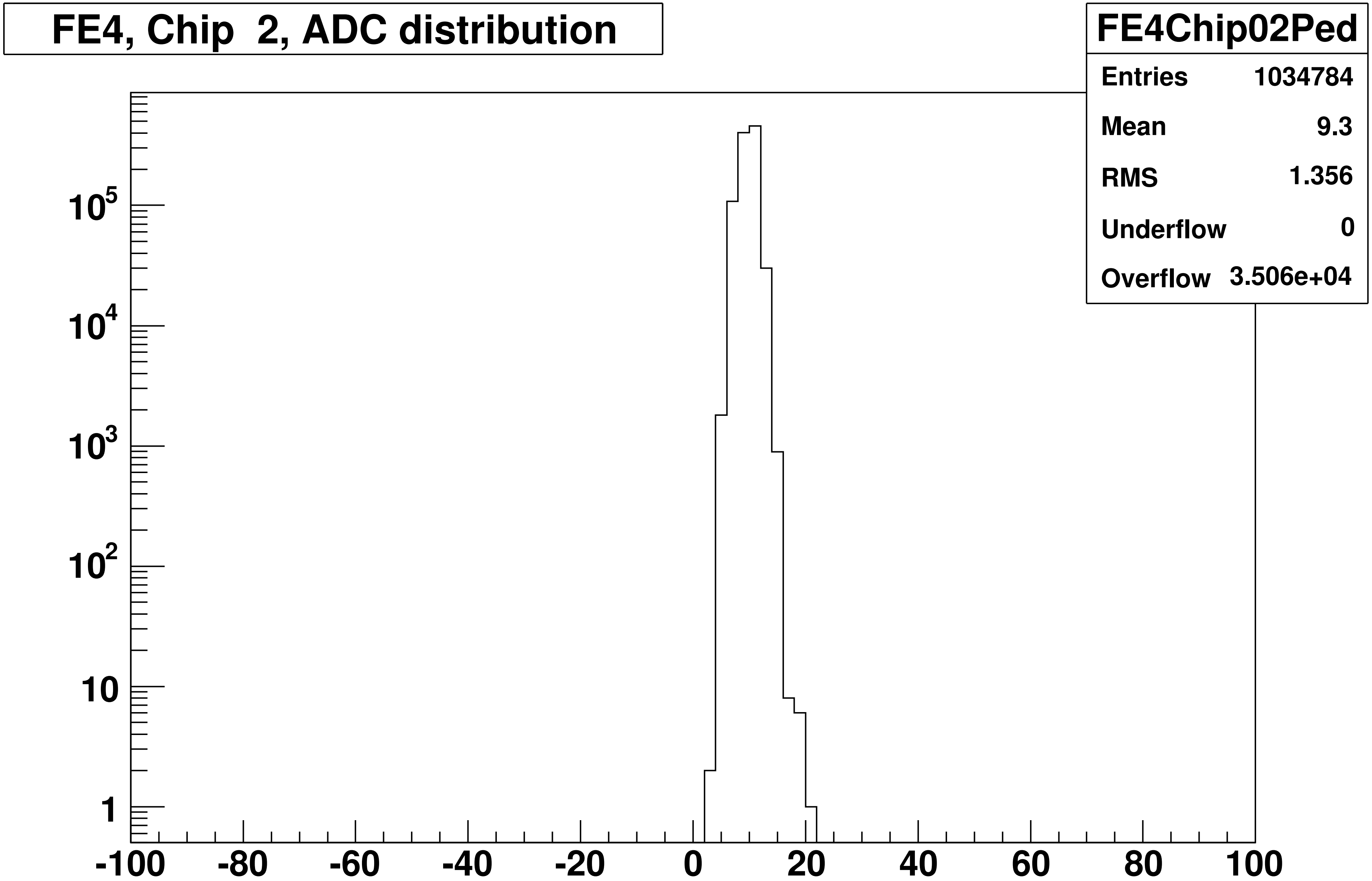
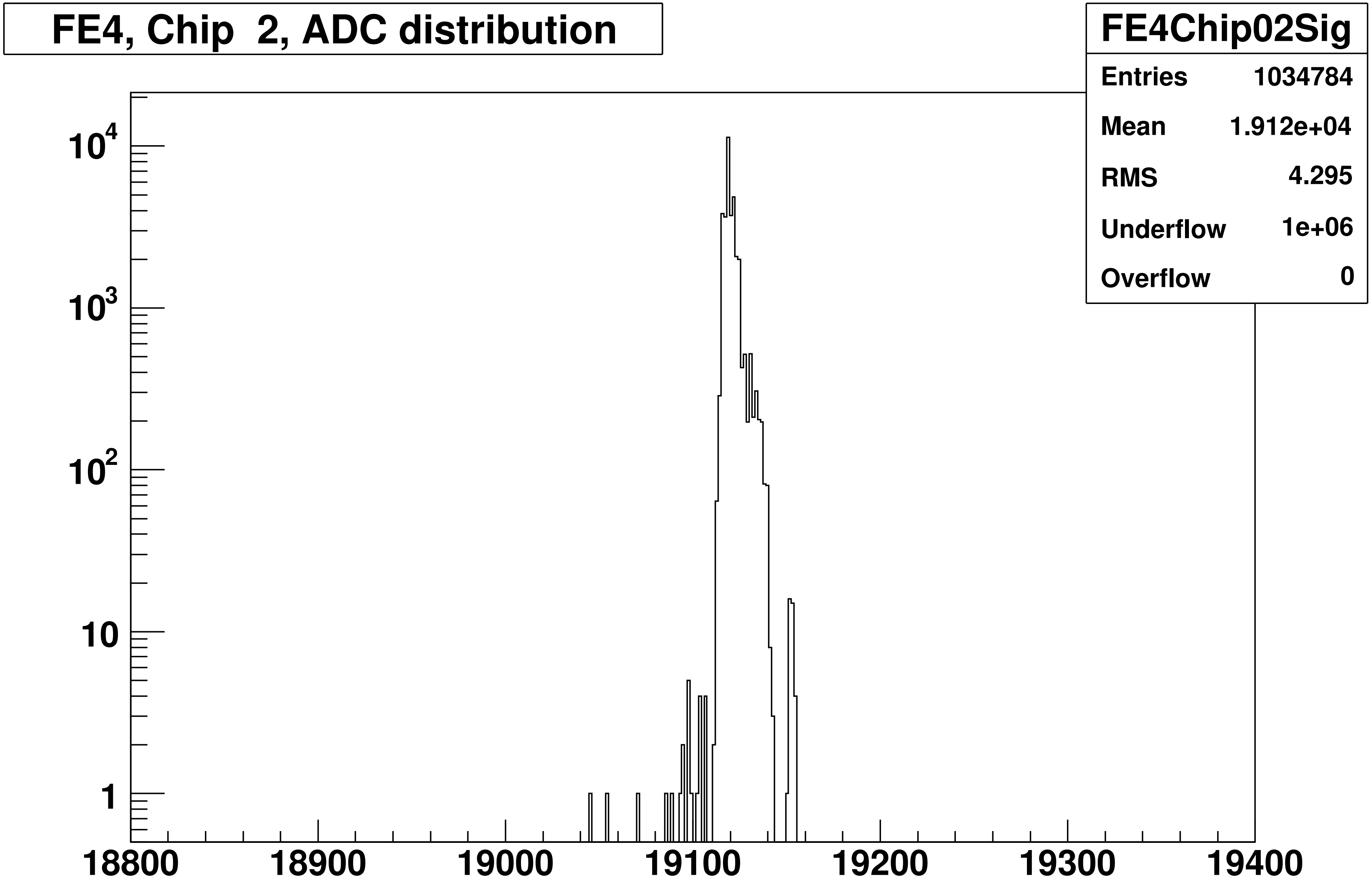
19400



FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 1034784
Mean 649.9
RMS 3438
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 1034784
Mean 9.3
RMS 1.356
Underflow 0
Overflow 3.506e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 1034784
Mean 1.912e+04
RMS 4.295
Underflow 1e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
19100
19200
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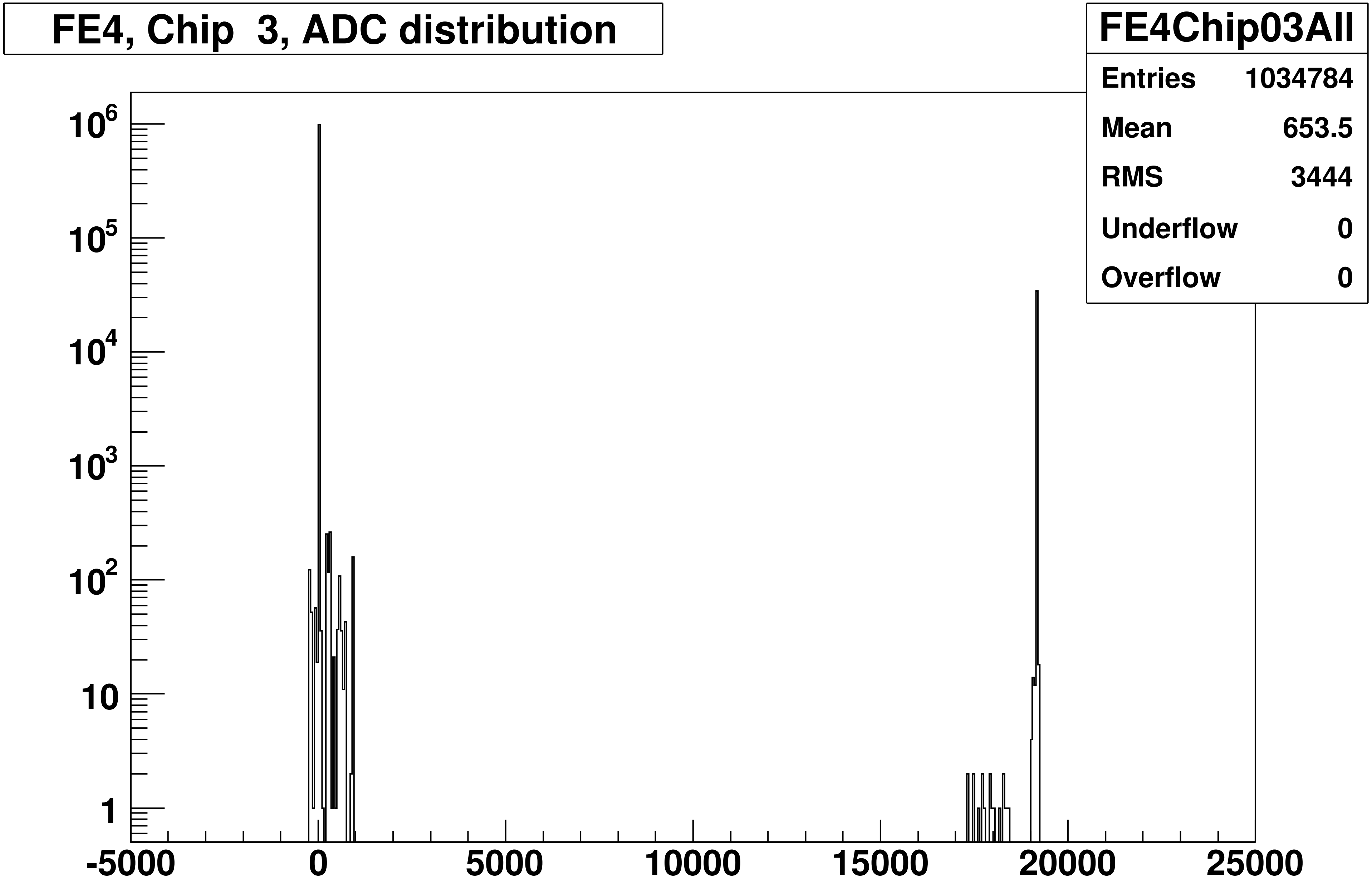
19400


FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 1034784
Mean 653.5
RMS 3444
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
5000
10000
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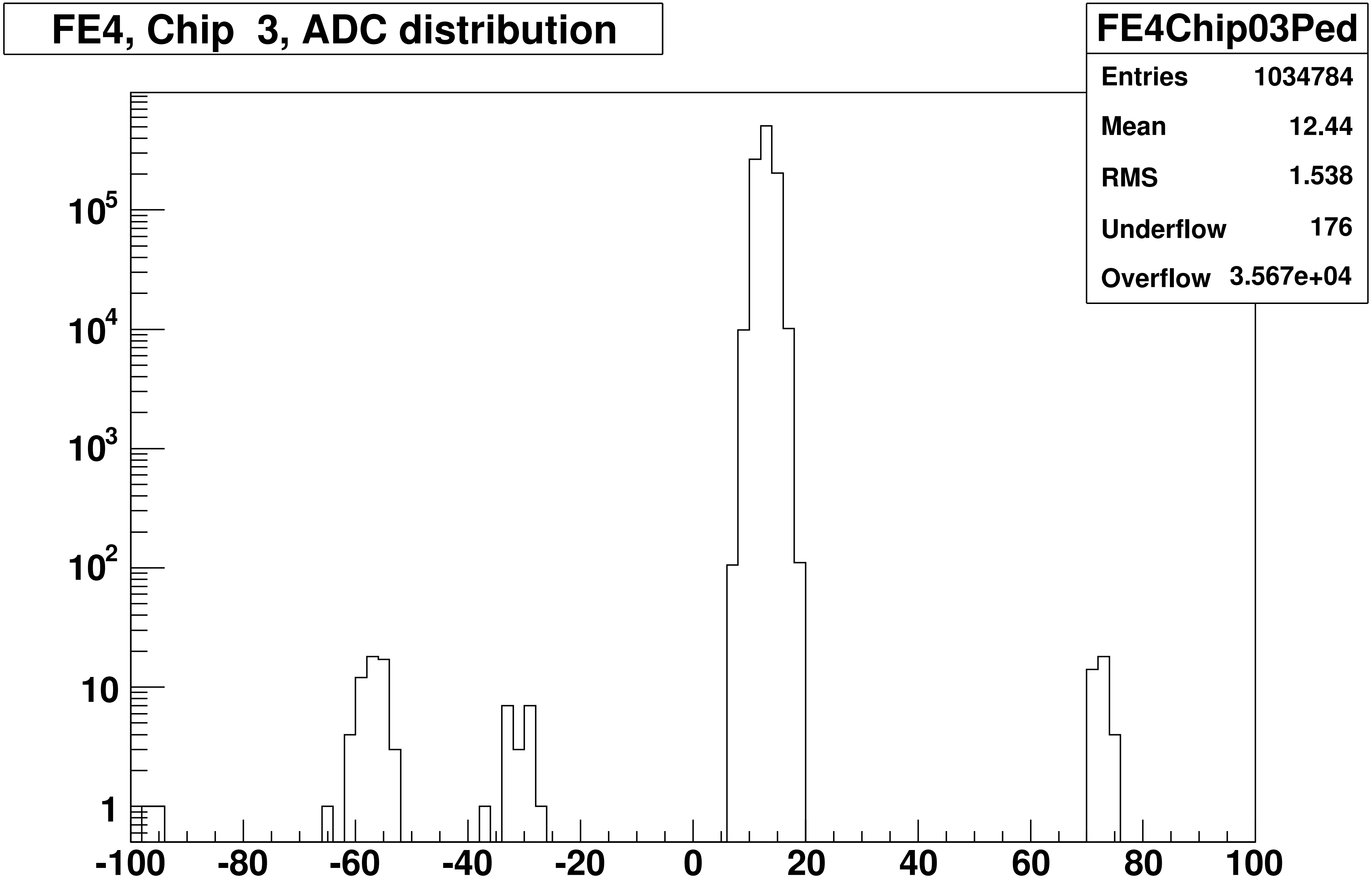
25000


FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 1034784
Mean 12.44
RMS 1.538
Underflow 176
Overflow 3.567e+04
10^5
10^4
10^3
10^2
10
1
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-80
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-20
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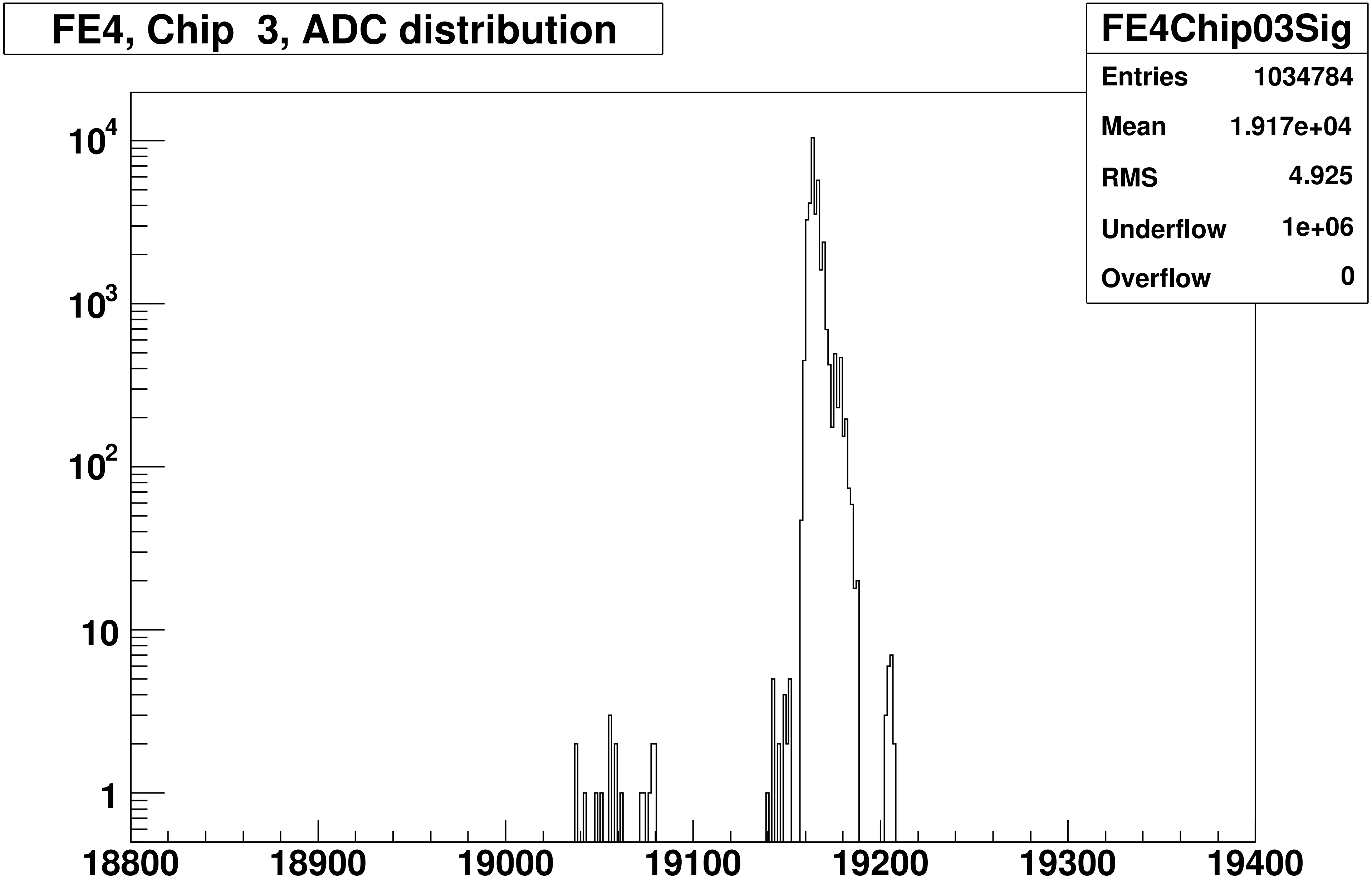
80
100



FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 1034784
Mean 1.917e+04
RMS 4.925
Underflow 1e+06
Overflow 0
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10^3
10^2
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18800
18900
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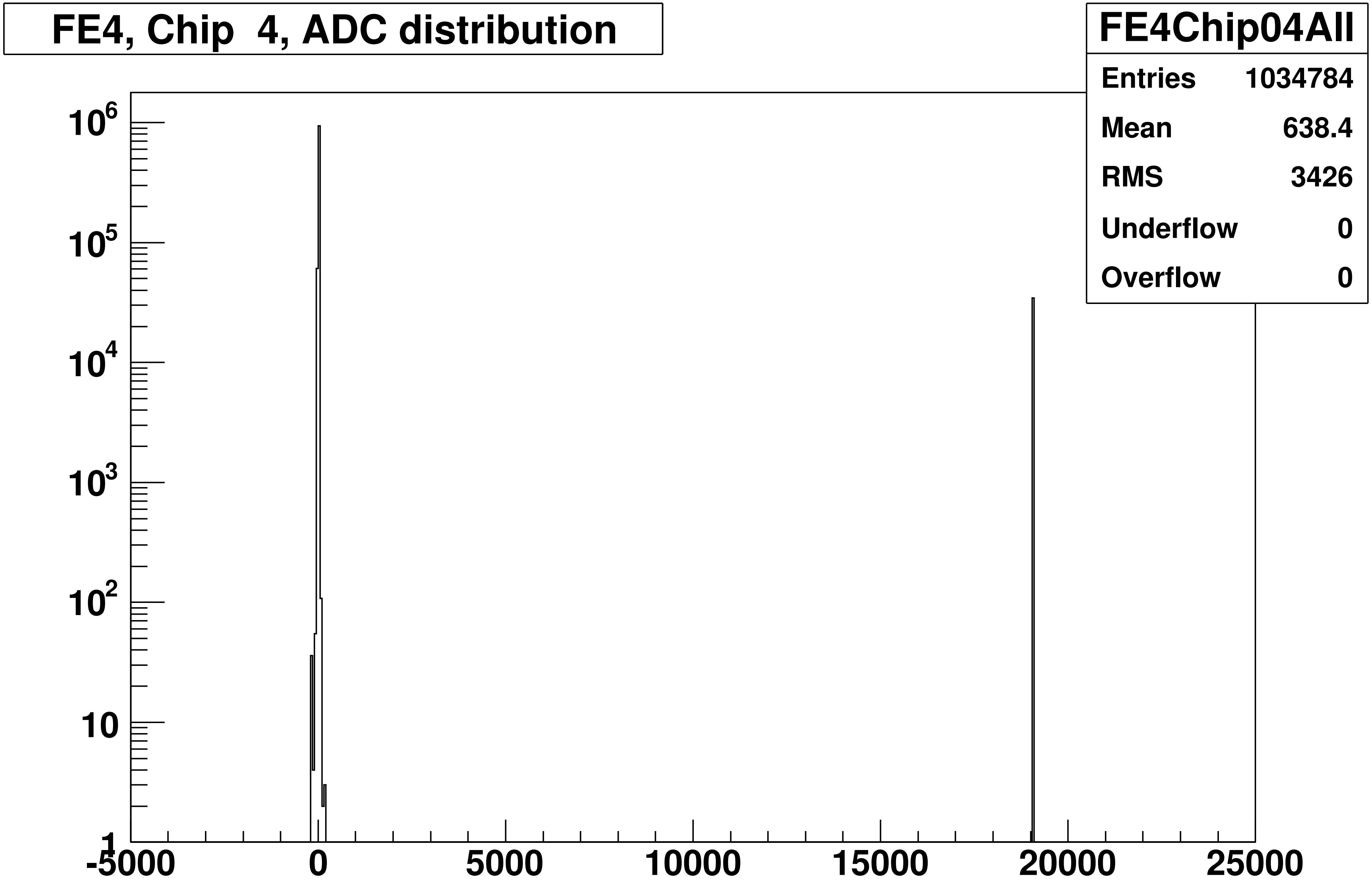
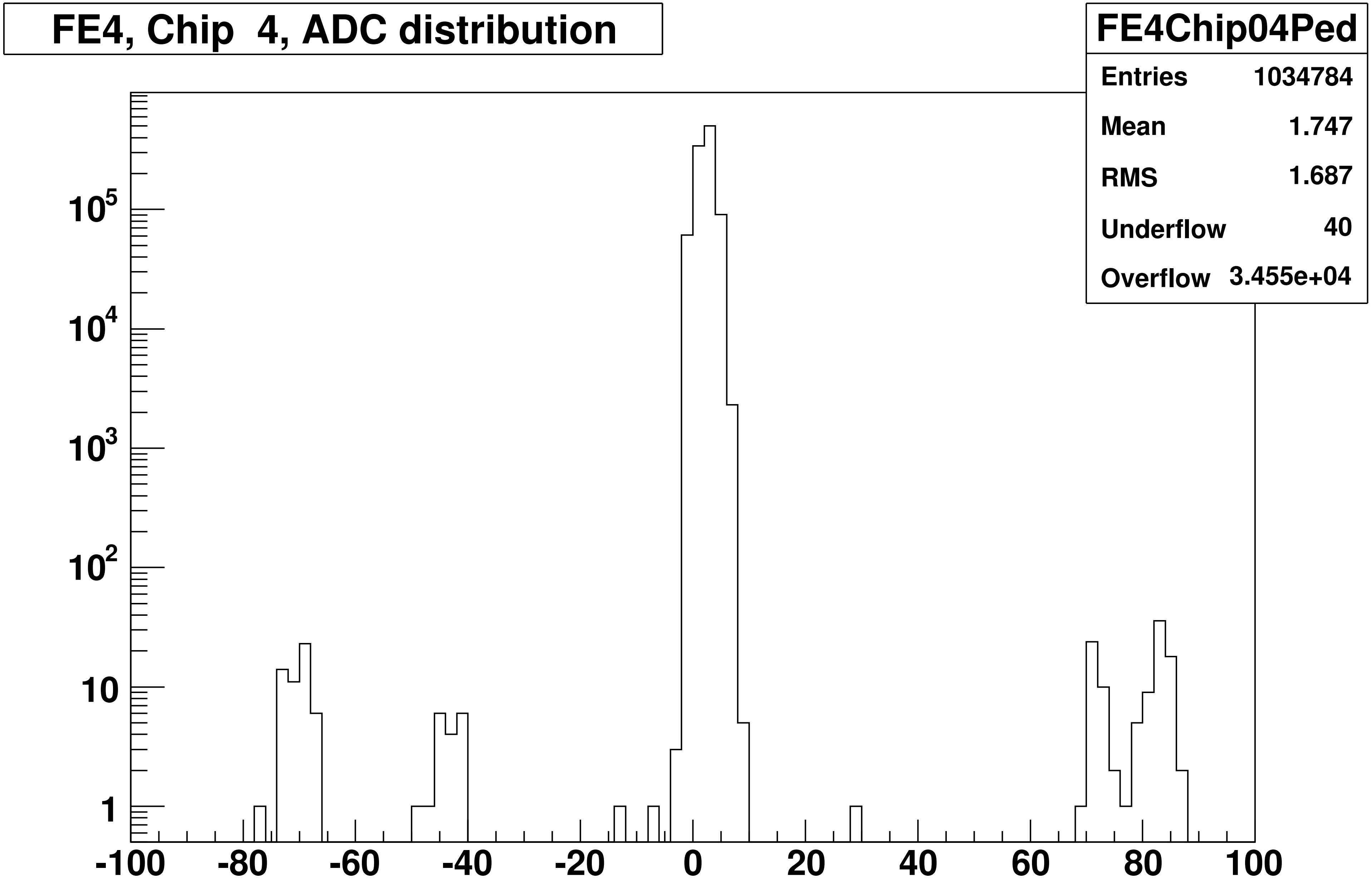
19400


FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 1034784
Mean 638.4
RMS 3426
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 1034784
Mean 1.747
RMS 1.687
Underflow 40
Overflow 3.455e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
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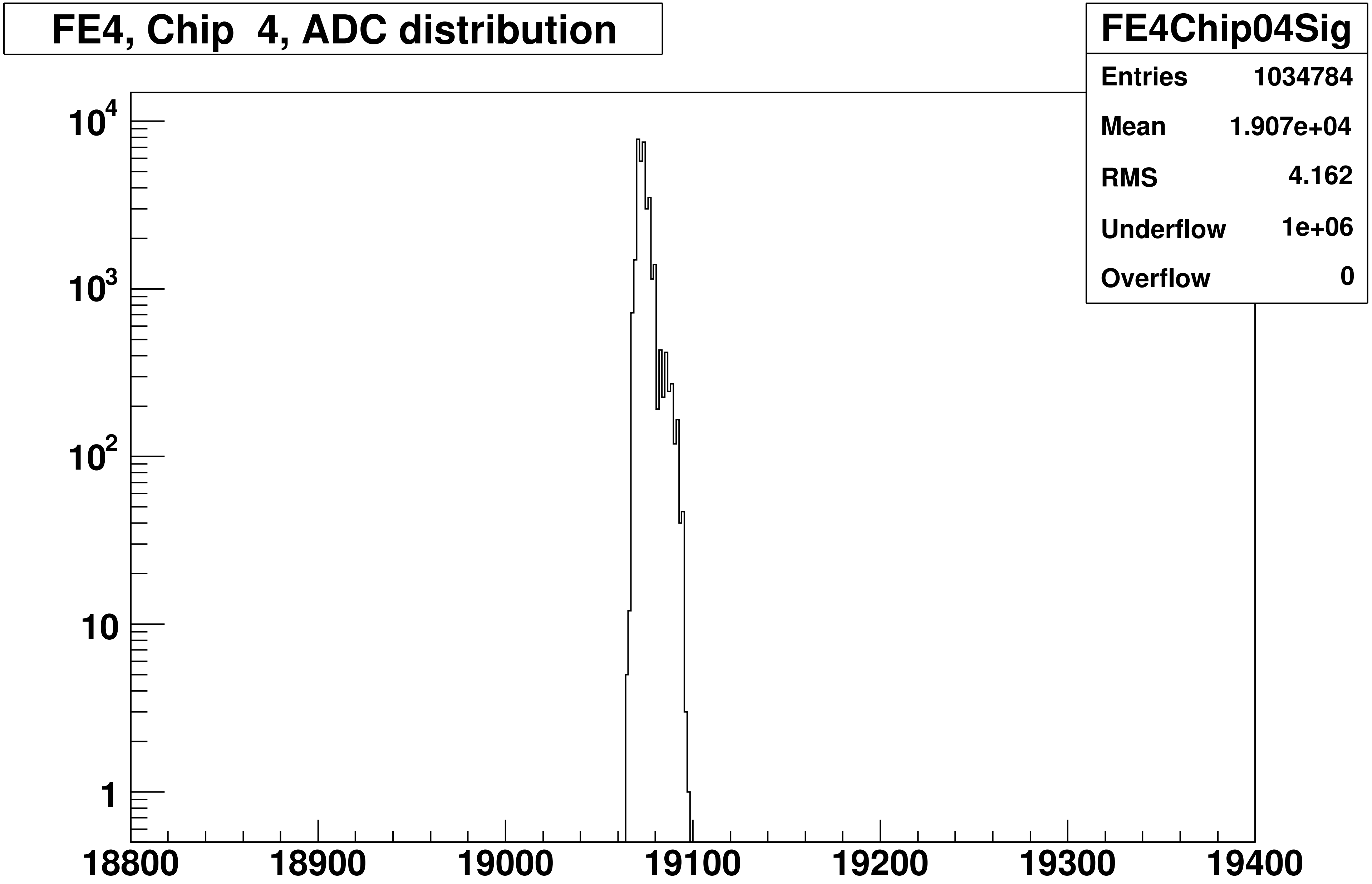
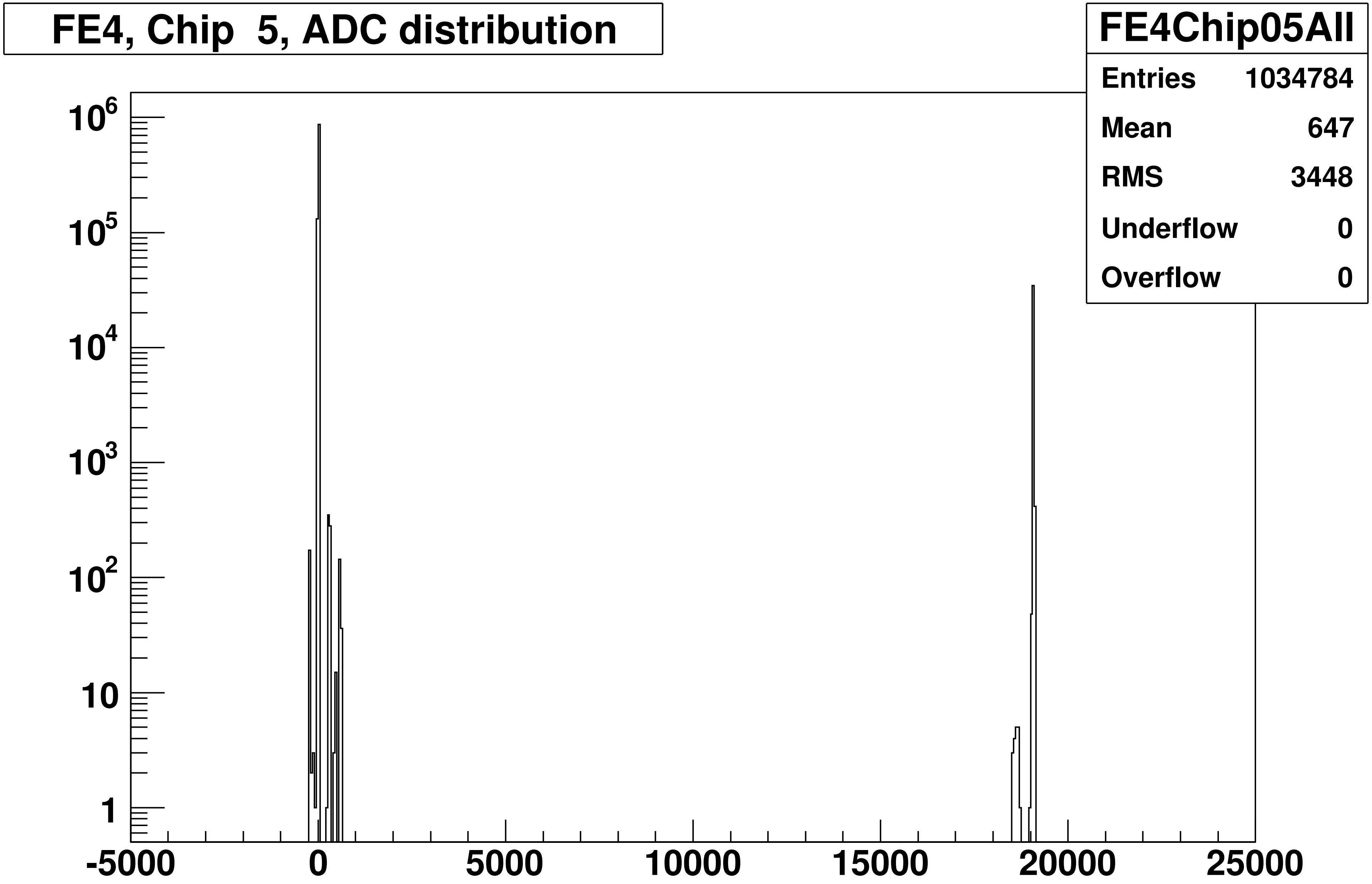
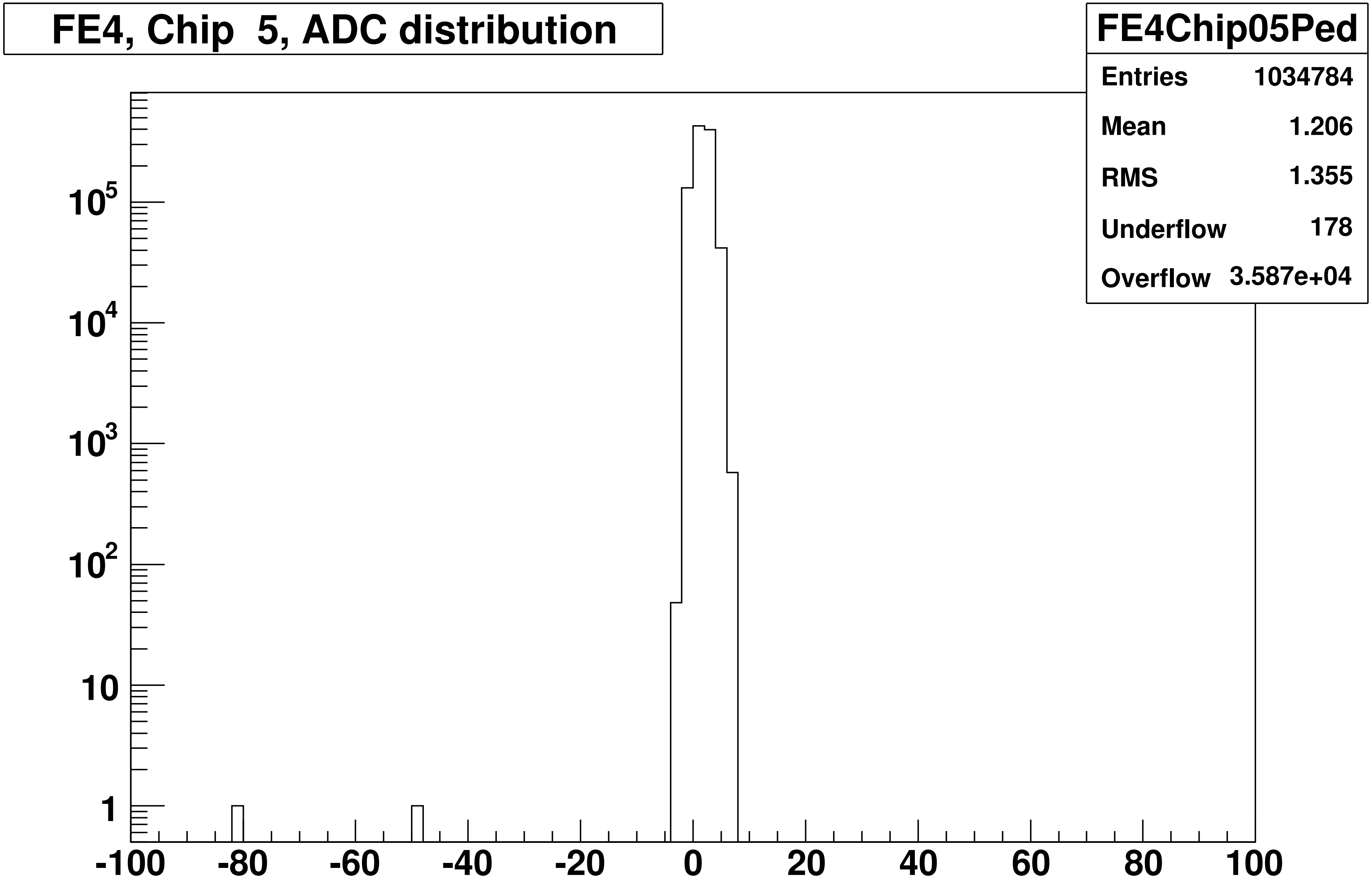
60
80
100



FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 1034784
Mean 1.907e+04
RMS 4.162
Underflow 1e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 1034784
Mean 647
RMS 3448
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 1034784
Mean 1.206
RMS 1.355
Underflow 178
Overflow 3.587e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
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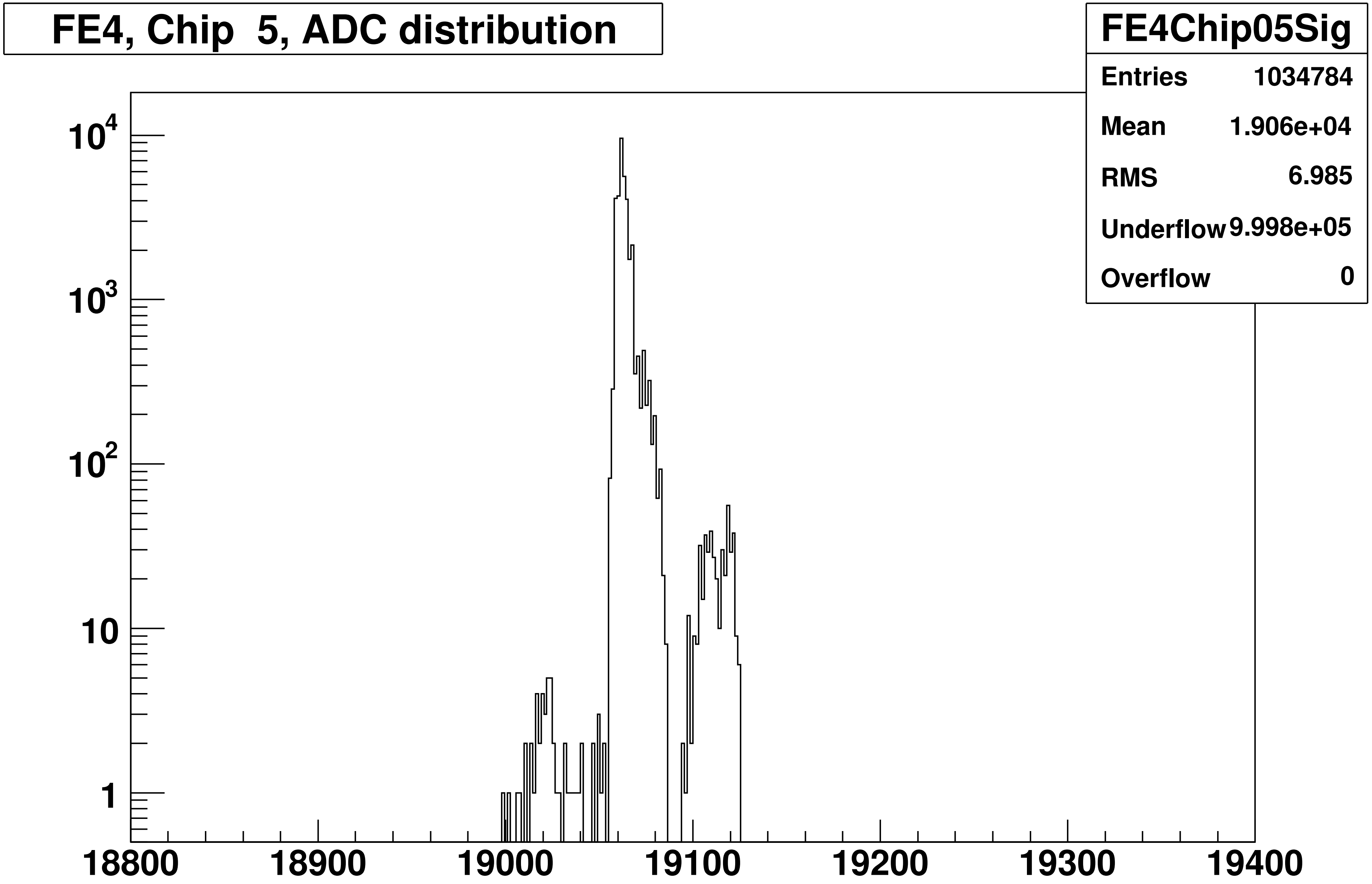
60
80
100


FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 1034784
Mean 1.906e+04
RMS 6.985
Underflow 9.998e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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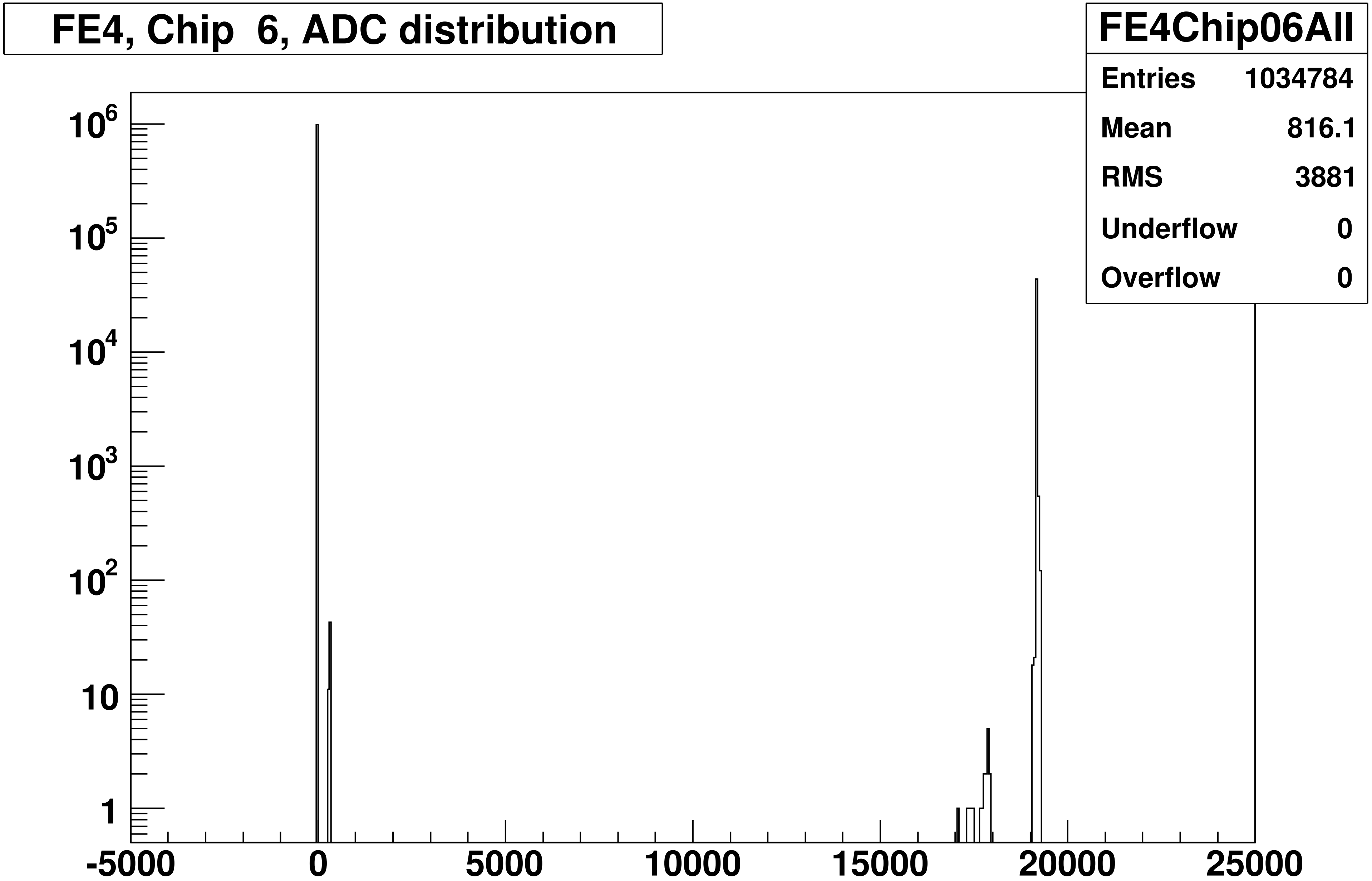
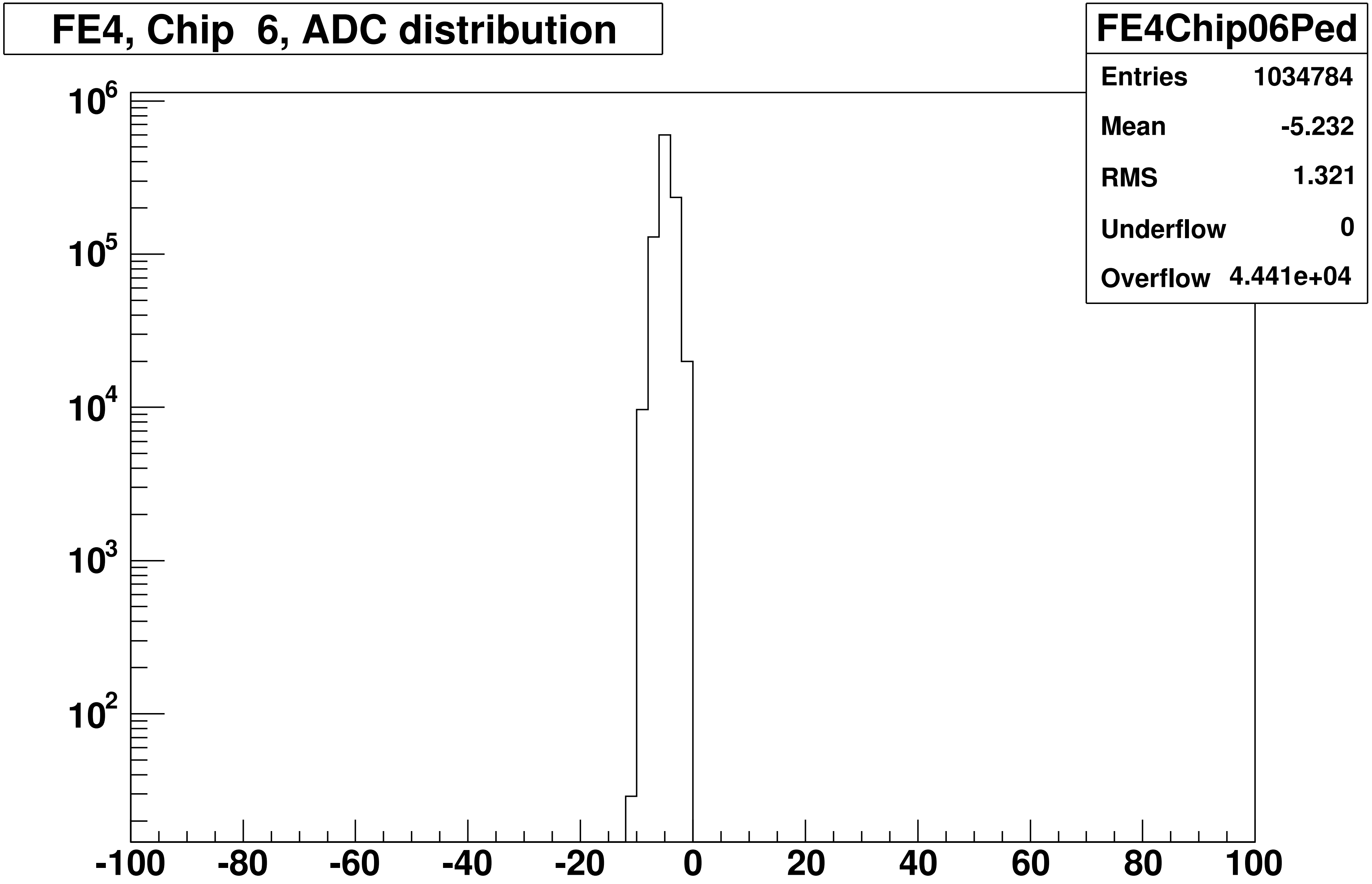
19400



FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 1034784
Mean 816.1
RMS 3881
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 1034784
Mean -5.232
RMS 1.321
Underflow 0
Overflow 4.441e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
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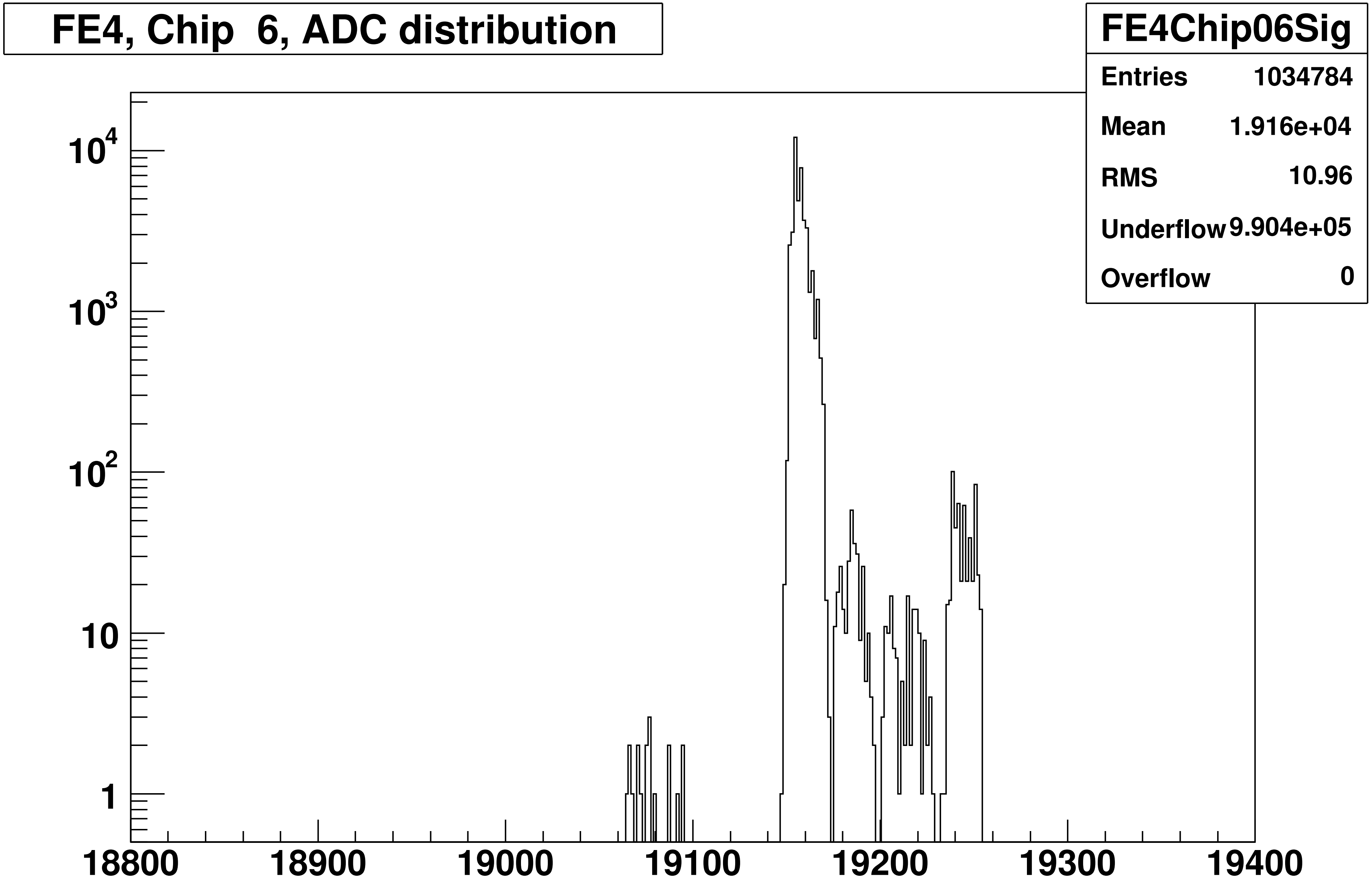
60
80
100



FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 1034784
Mean 1.916e+04
RMS 10.96
Underflow 9.904e+05
Overflow 0
10^4
10^3
10^2
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1
18800
18900
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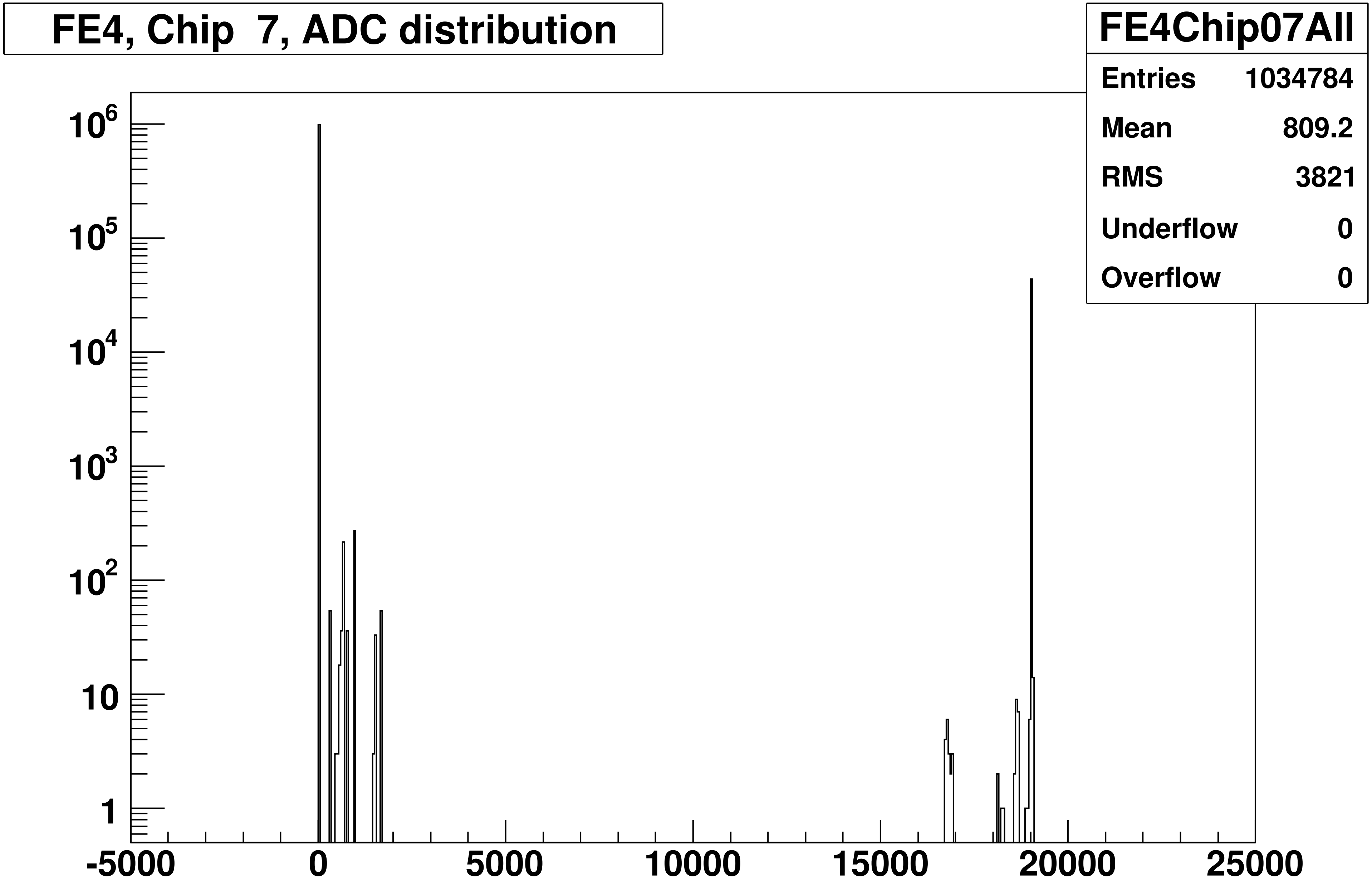

FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 1034784
Mean 809.2
RMS 3821
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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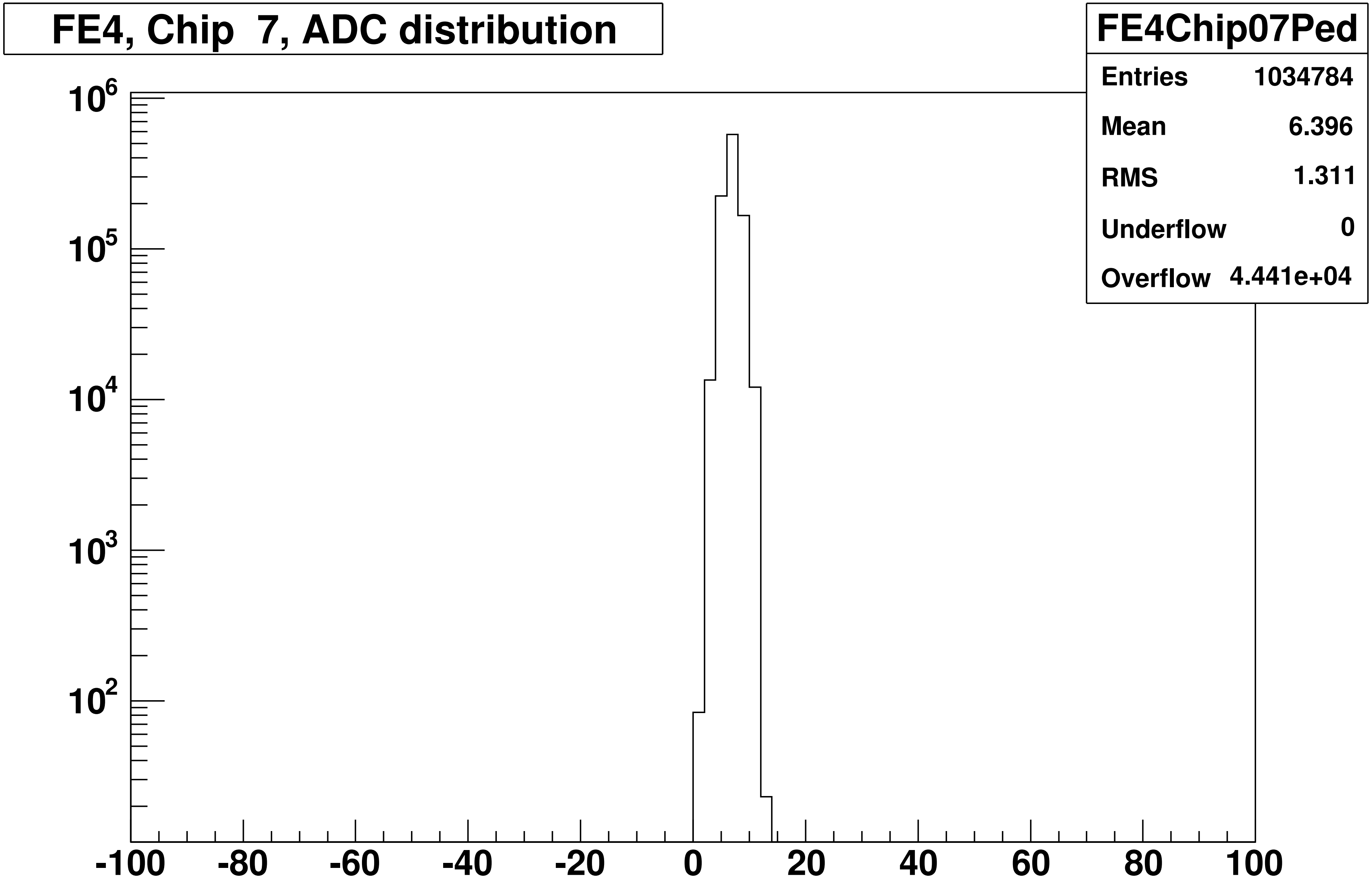
FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 1034784
Mean 6.396
RMS 1.311
Underflow 0
Overflow 4.441e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
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-20
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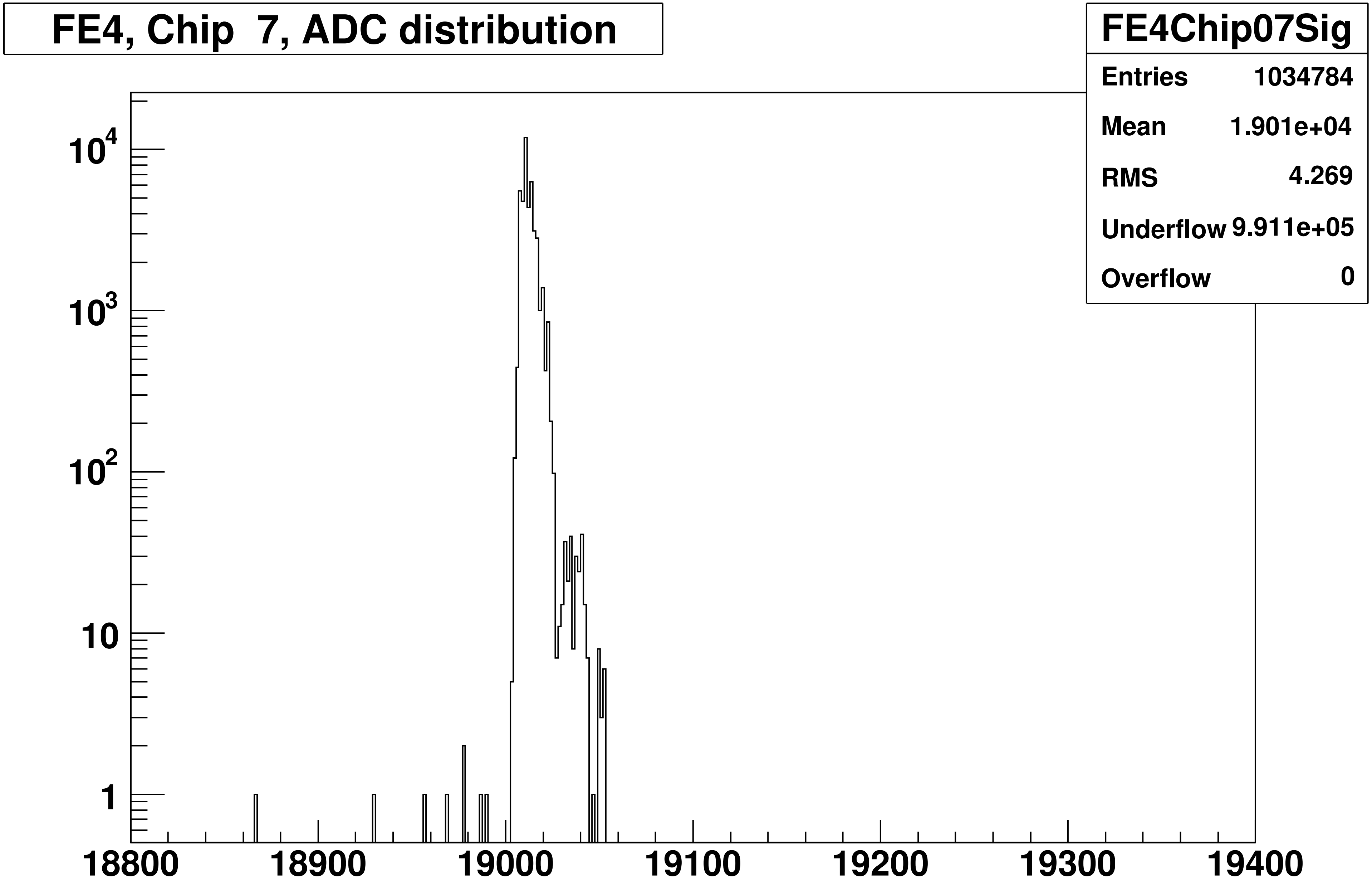
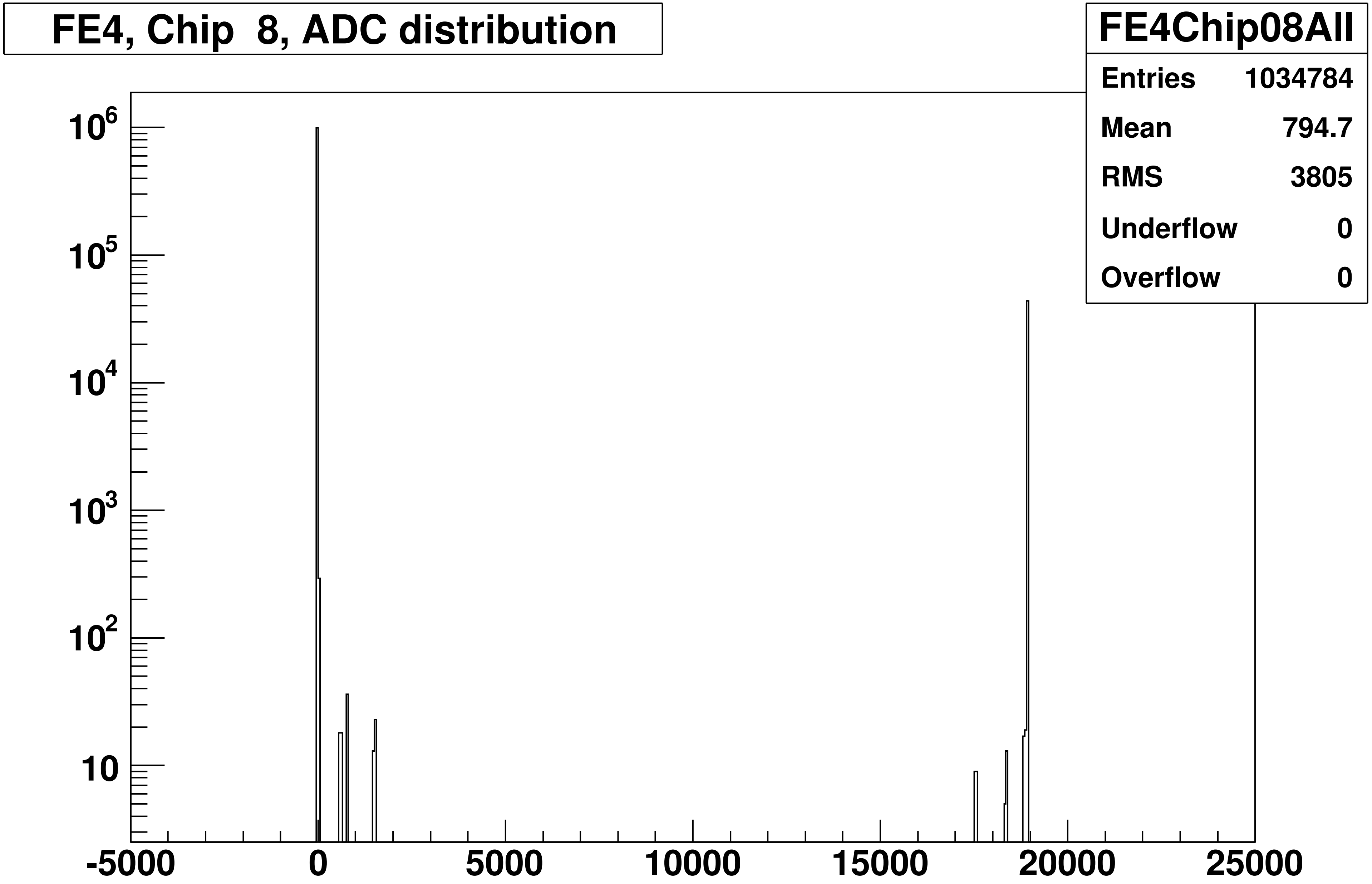
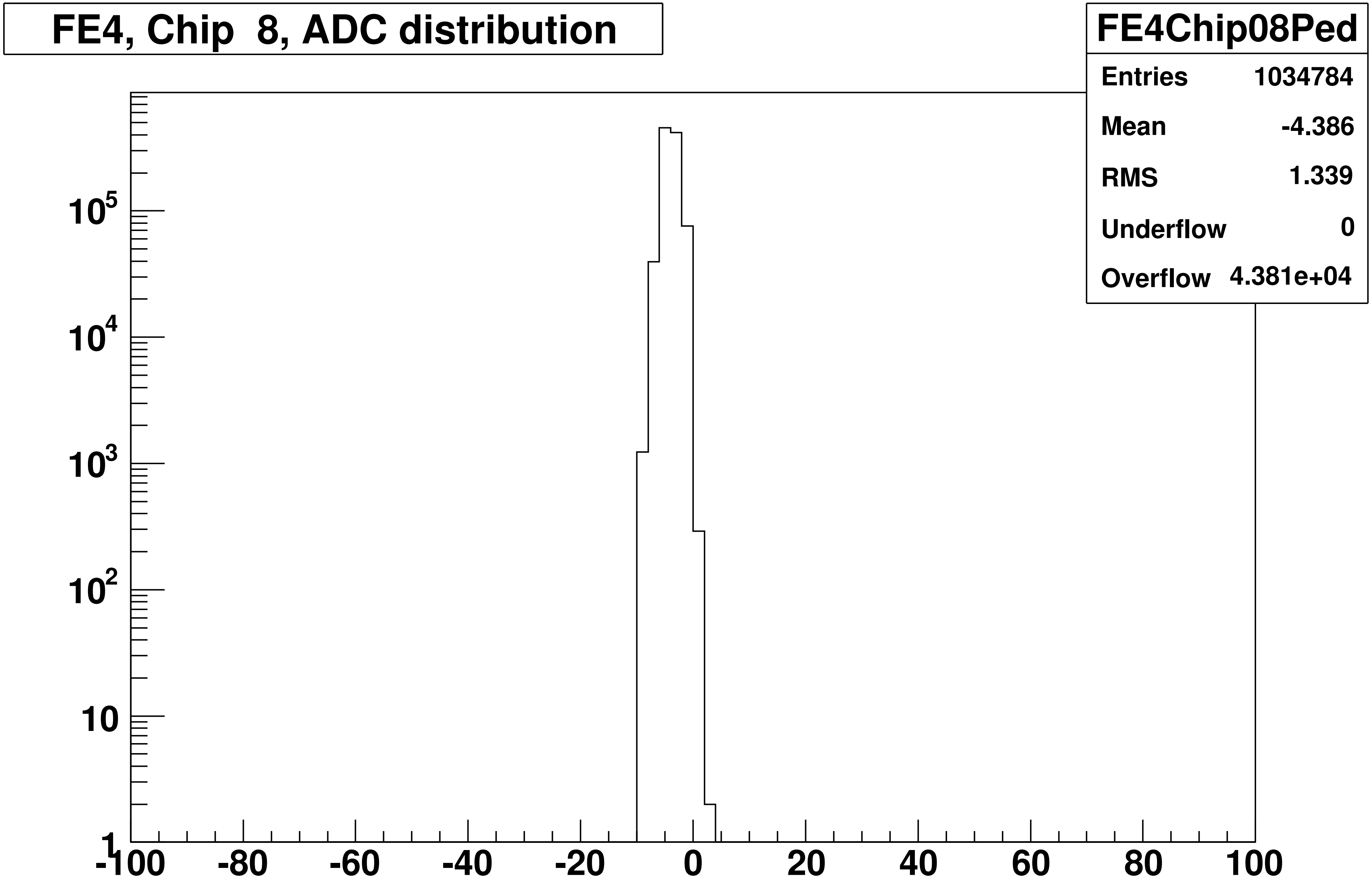
80
100



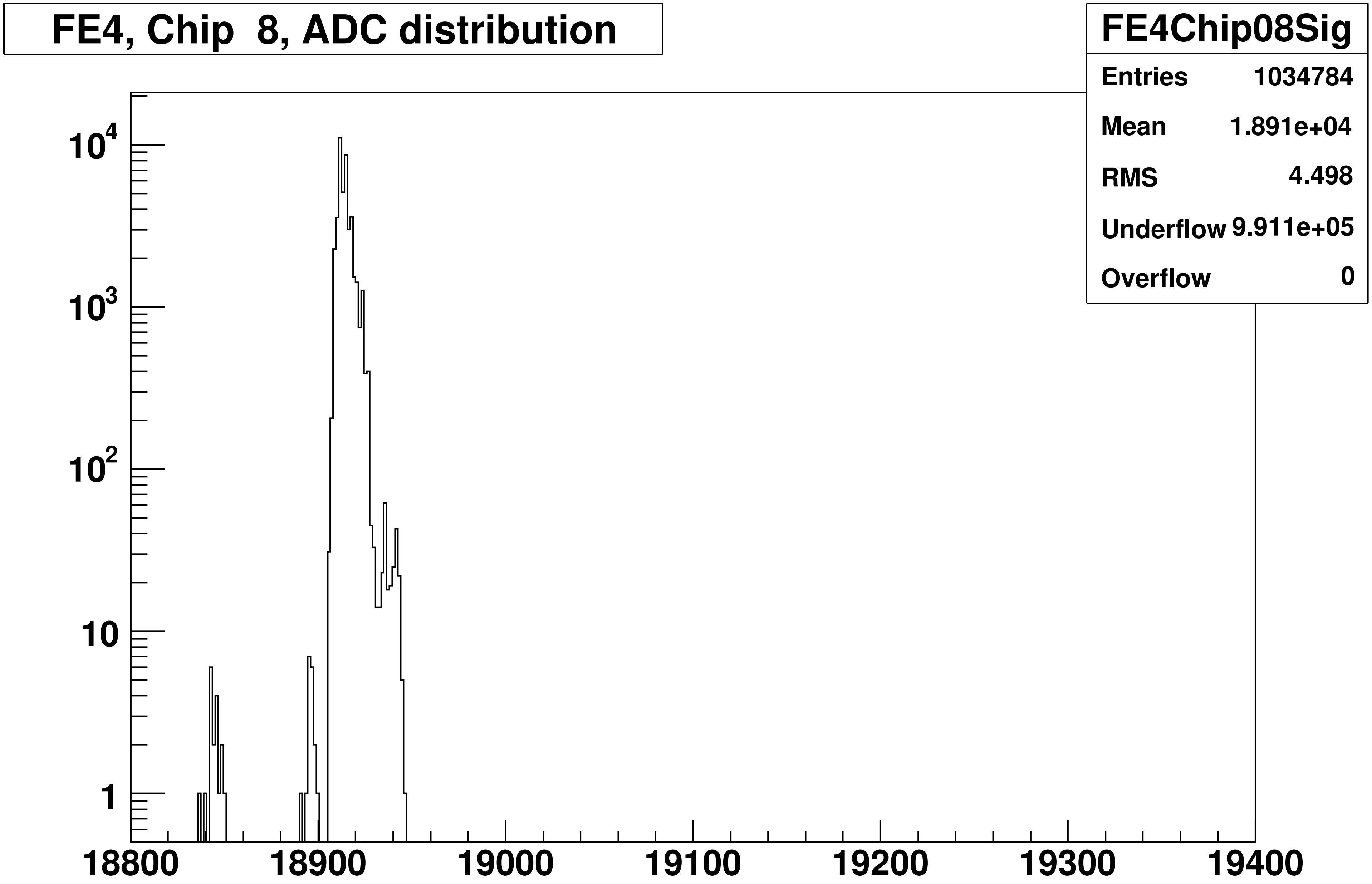
FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 1034784
Mean 1.901e+04
RMS 4.269
Underflow 9.911e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 1034784
Mean 794.7
RMS 3805
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 1034784
Mean -4.386
RMS 1.339
Underflow 0
Overflow 4.381e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 1034784
Mean 1.891e+04
RMS 4.498
Underflow 9.911e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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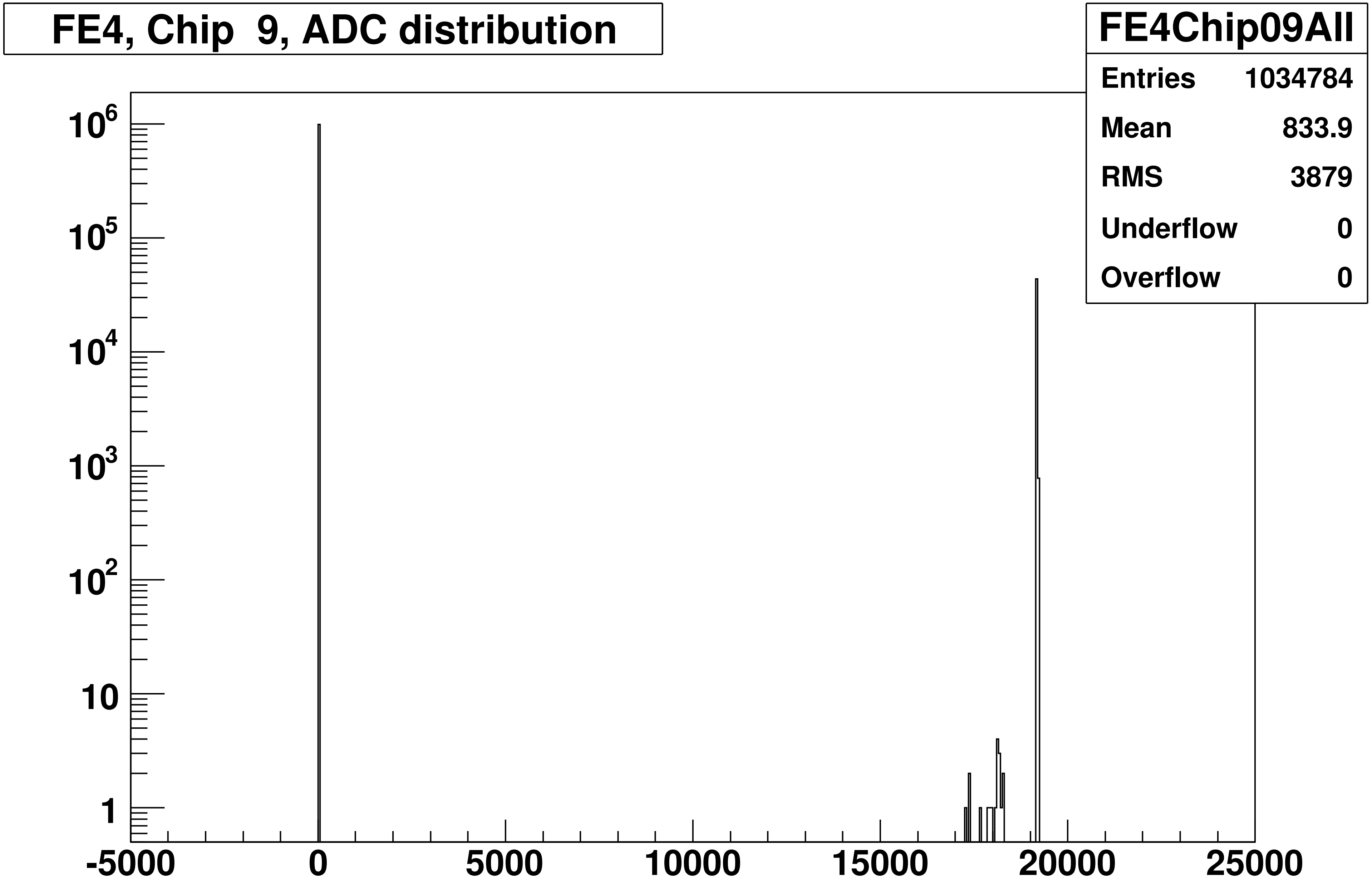
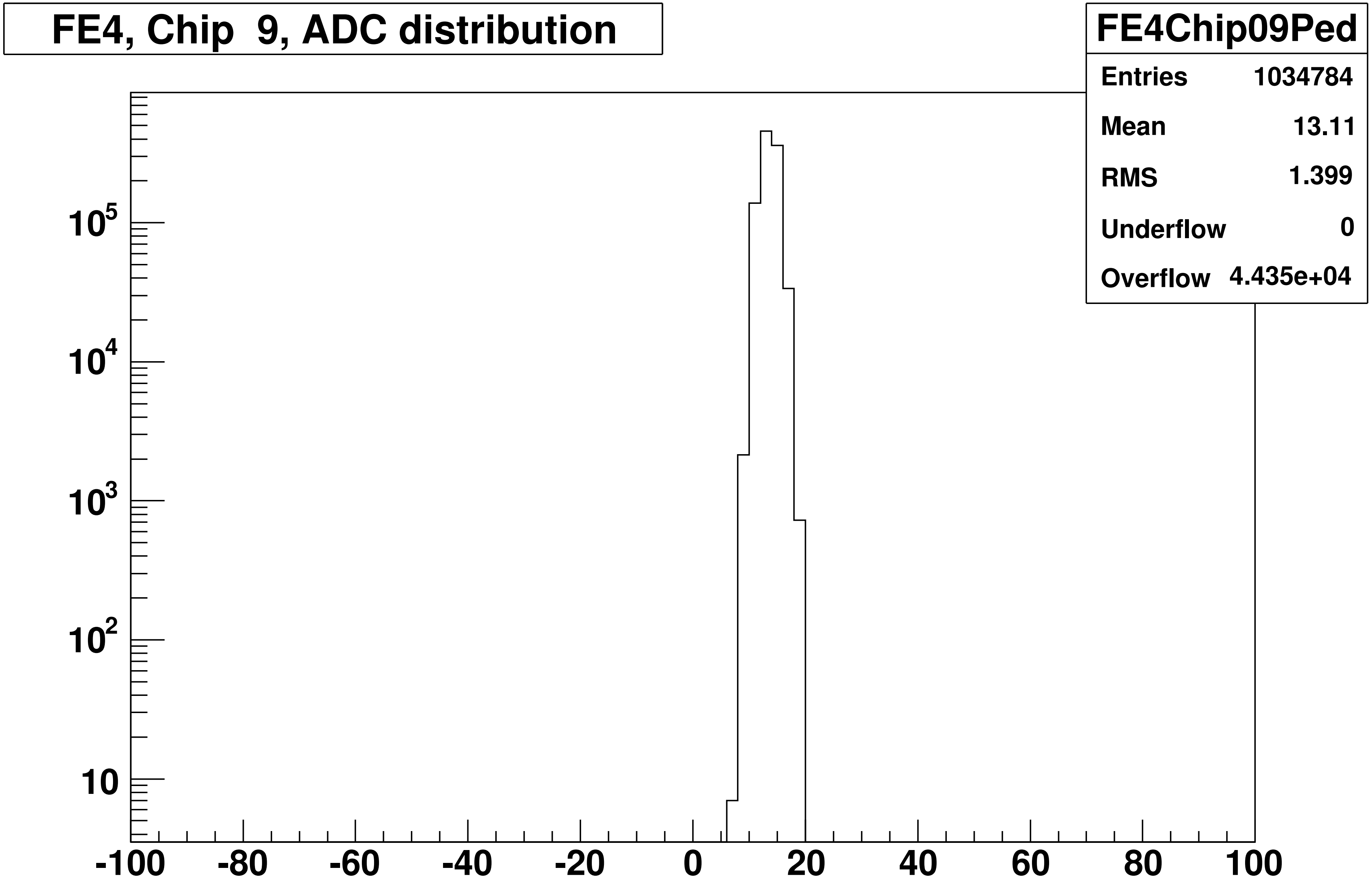
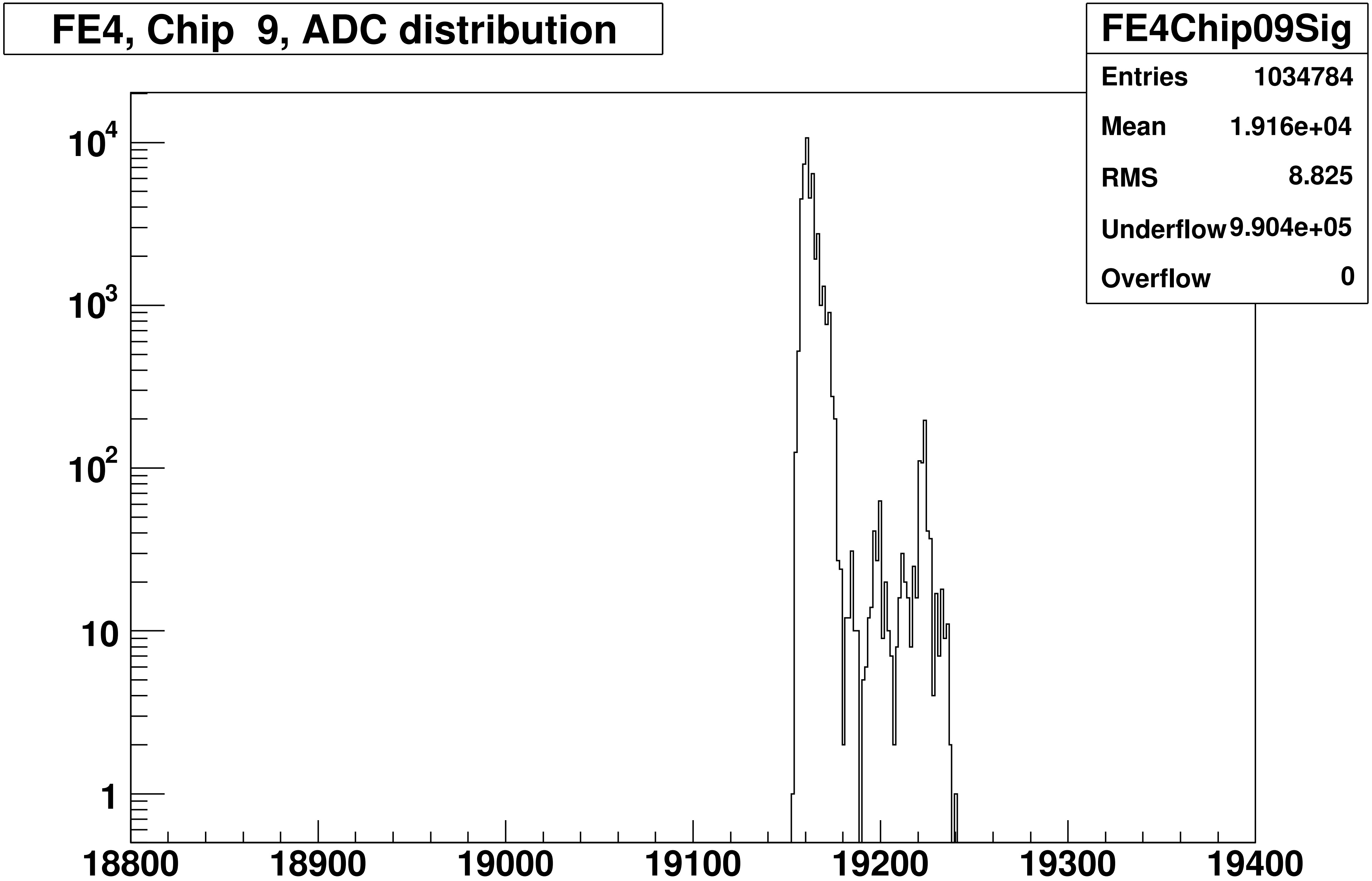
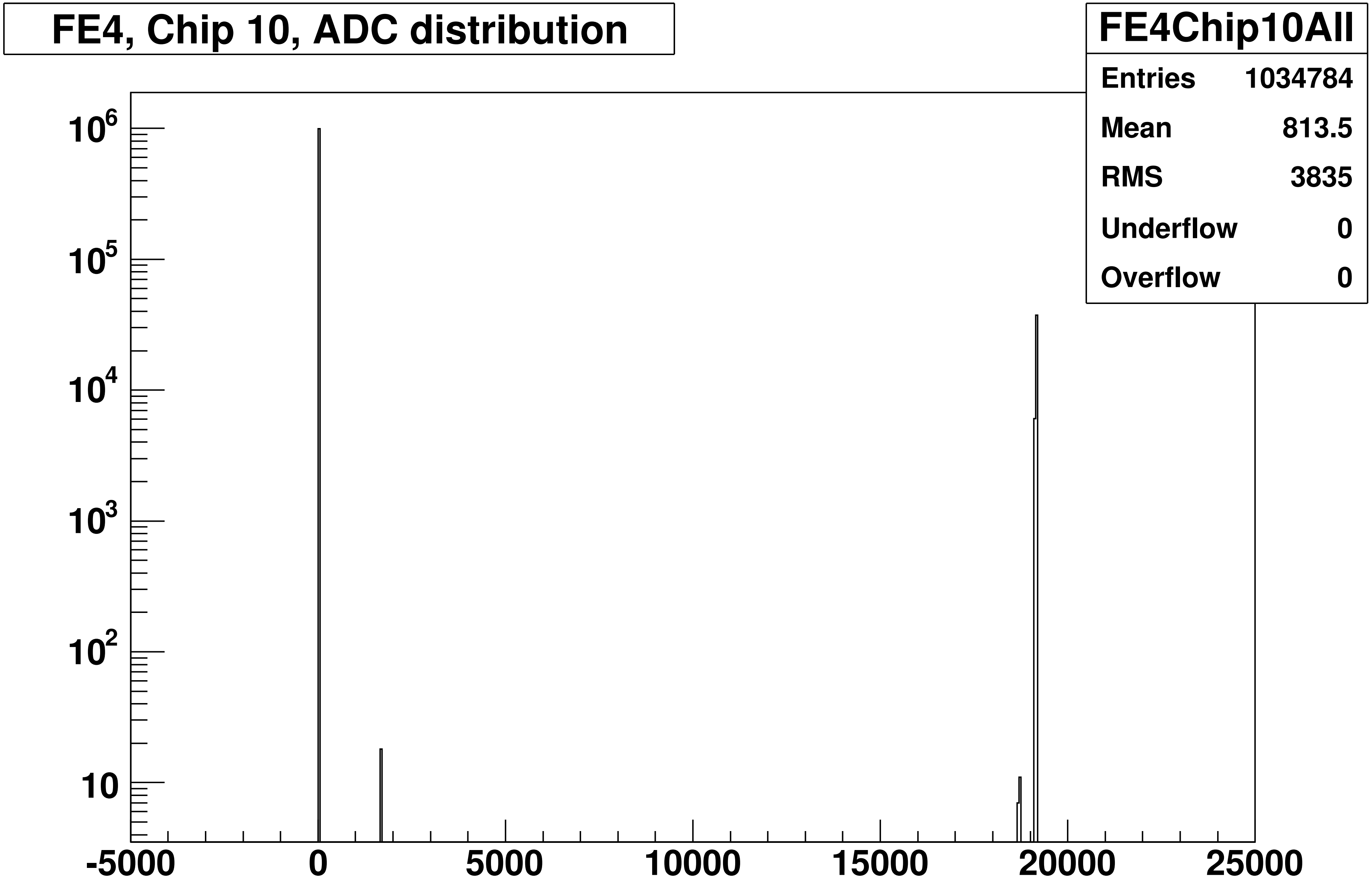
19200
19300
19400


FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 1034784
Mean 833.9
RMS 3879
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 1034784
Mean 13.11
RMS 1.399
Underflow 0
Overflow 4.435e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 1034784
Mean 1.916e+04
RMS 8.825
Underflow 9.904e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 1034784
Mean 813.5
RMS 3835
Underflow 0
Overflow 0
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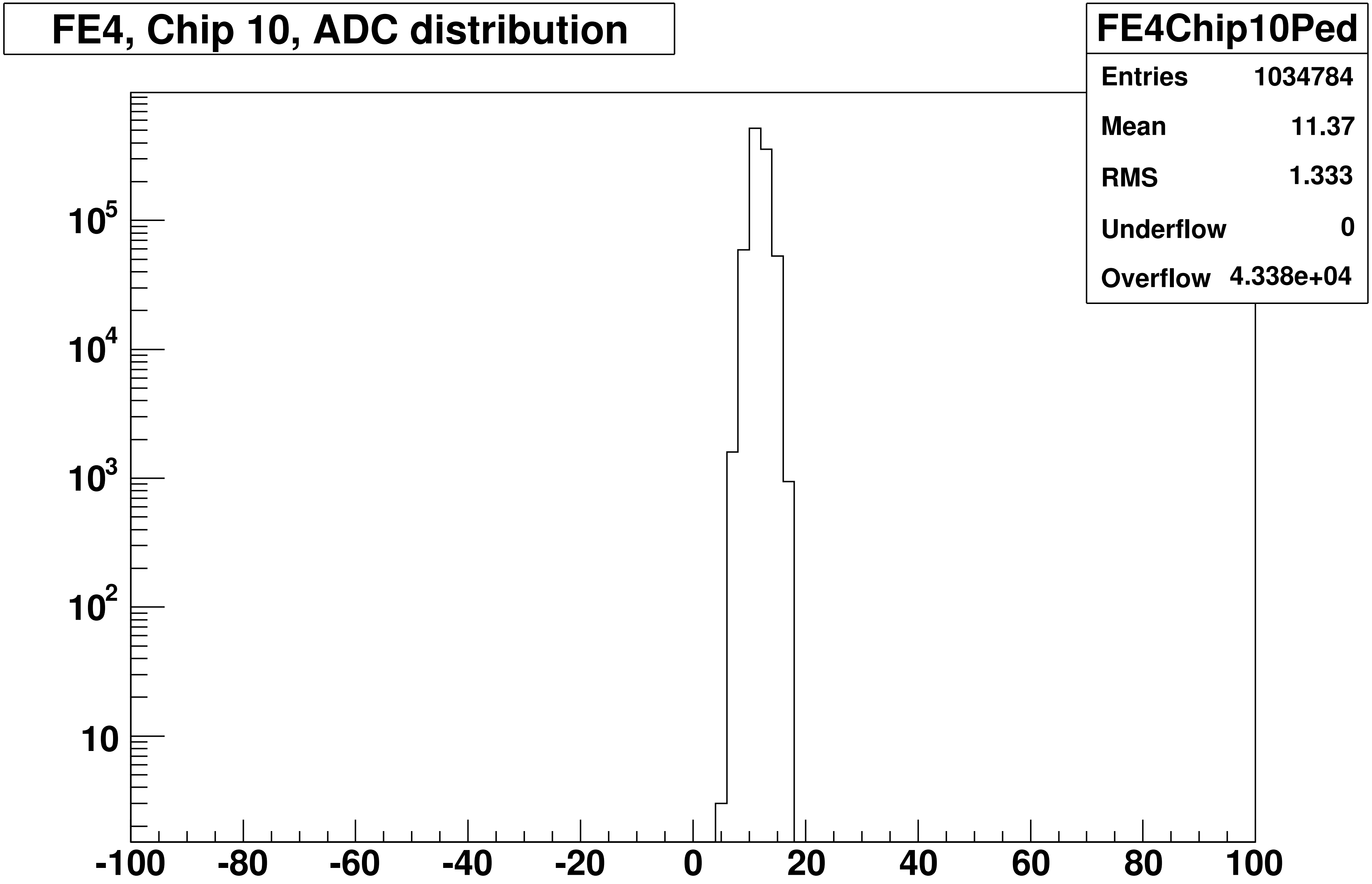
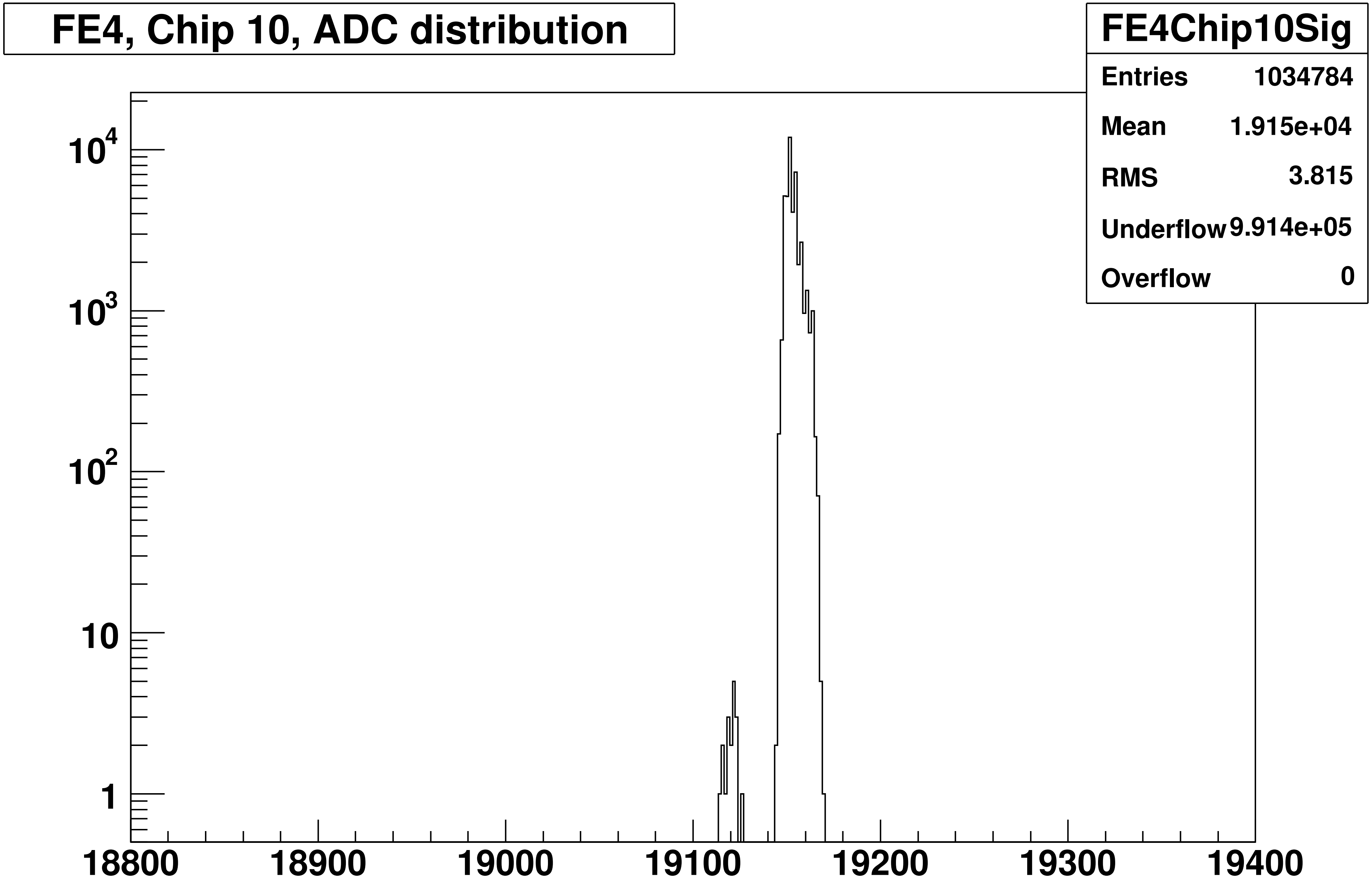
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 1034784
Mean 11.37
RMS 1.333
Underflow 0
Overflow 4.338e+04



FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 1034784
Mean 1.915e+04
RMS 3.815
Underflow 9.914e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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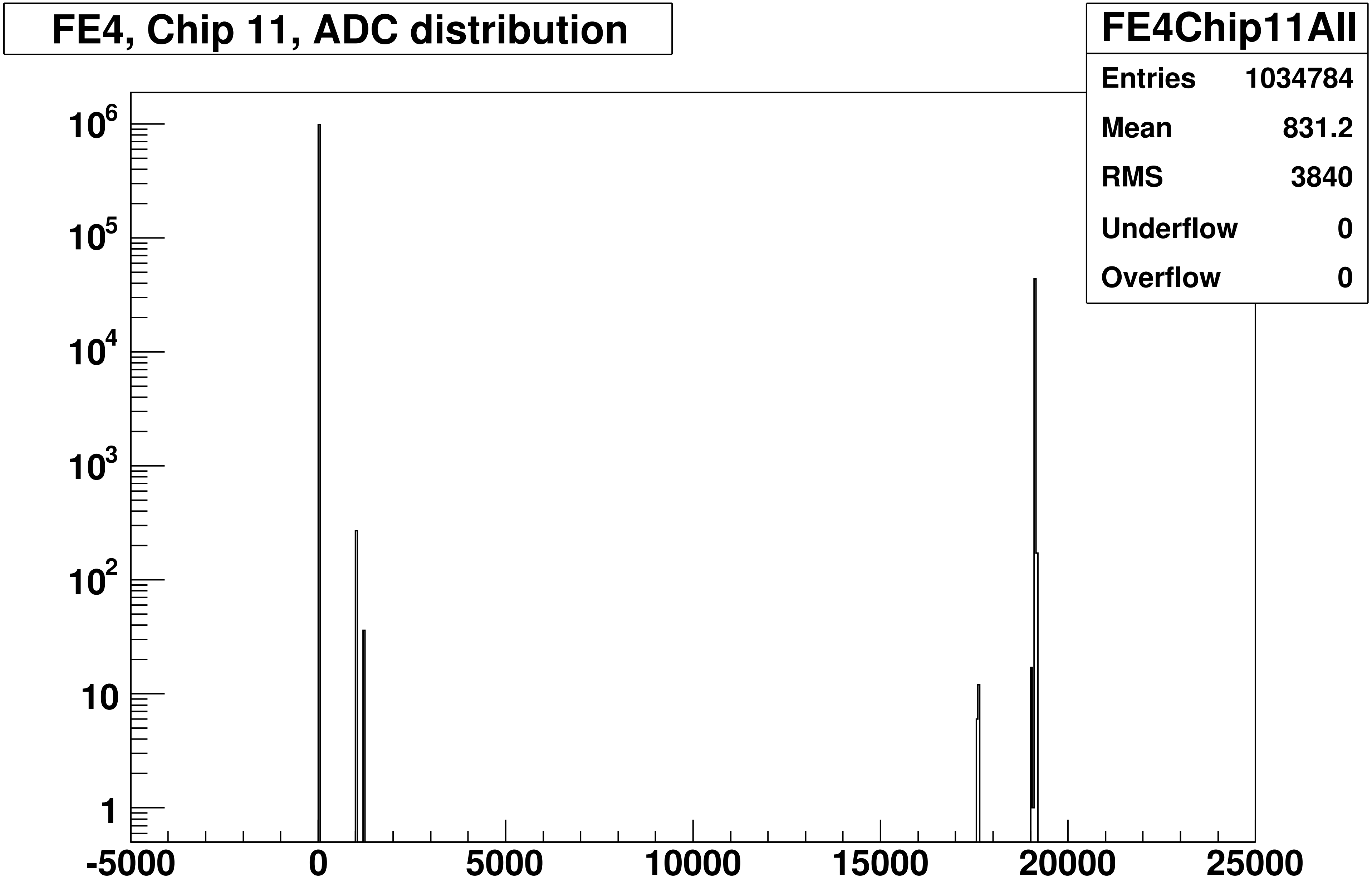
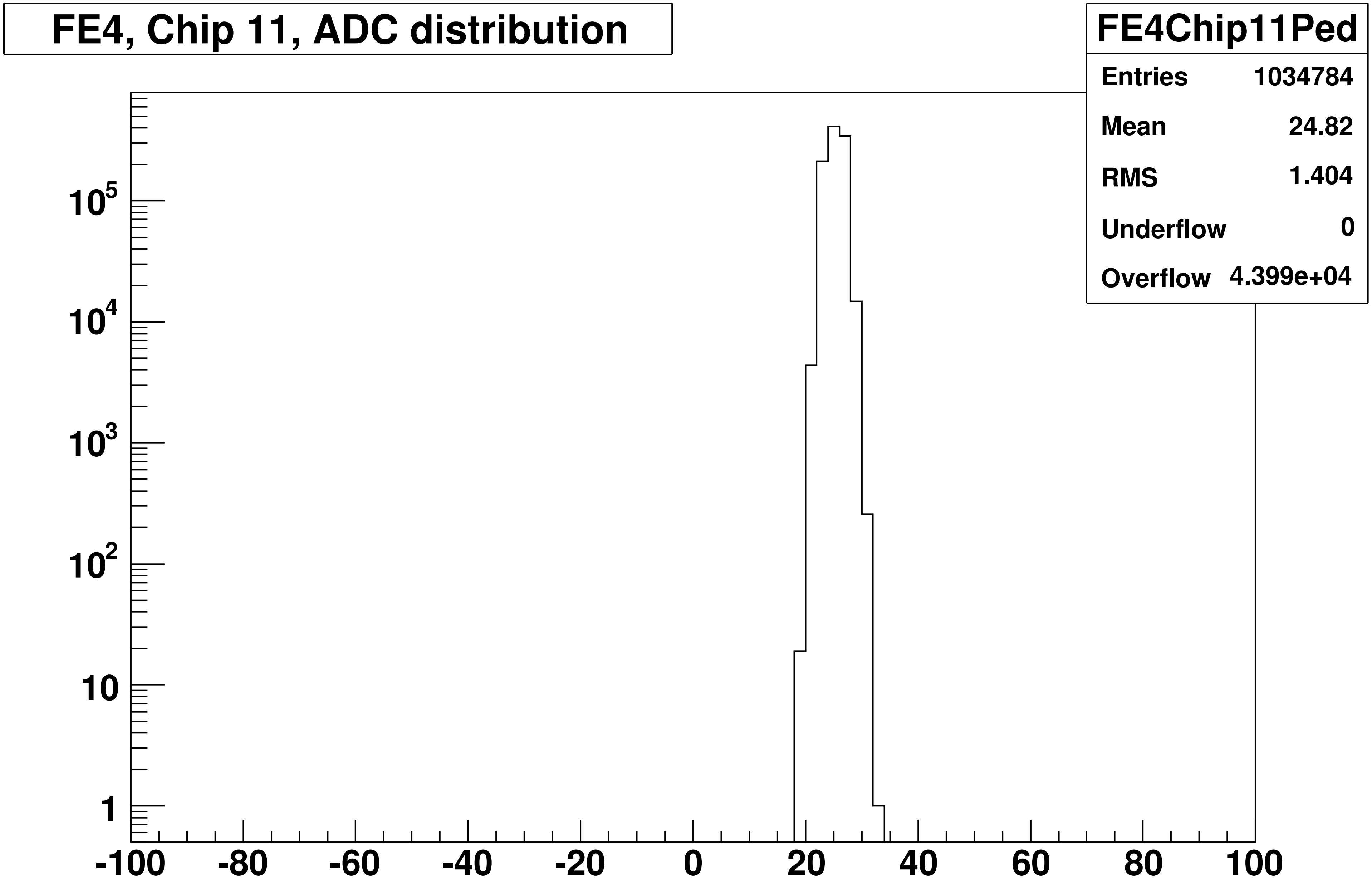
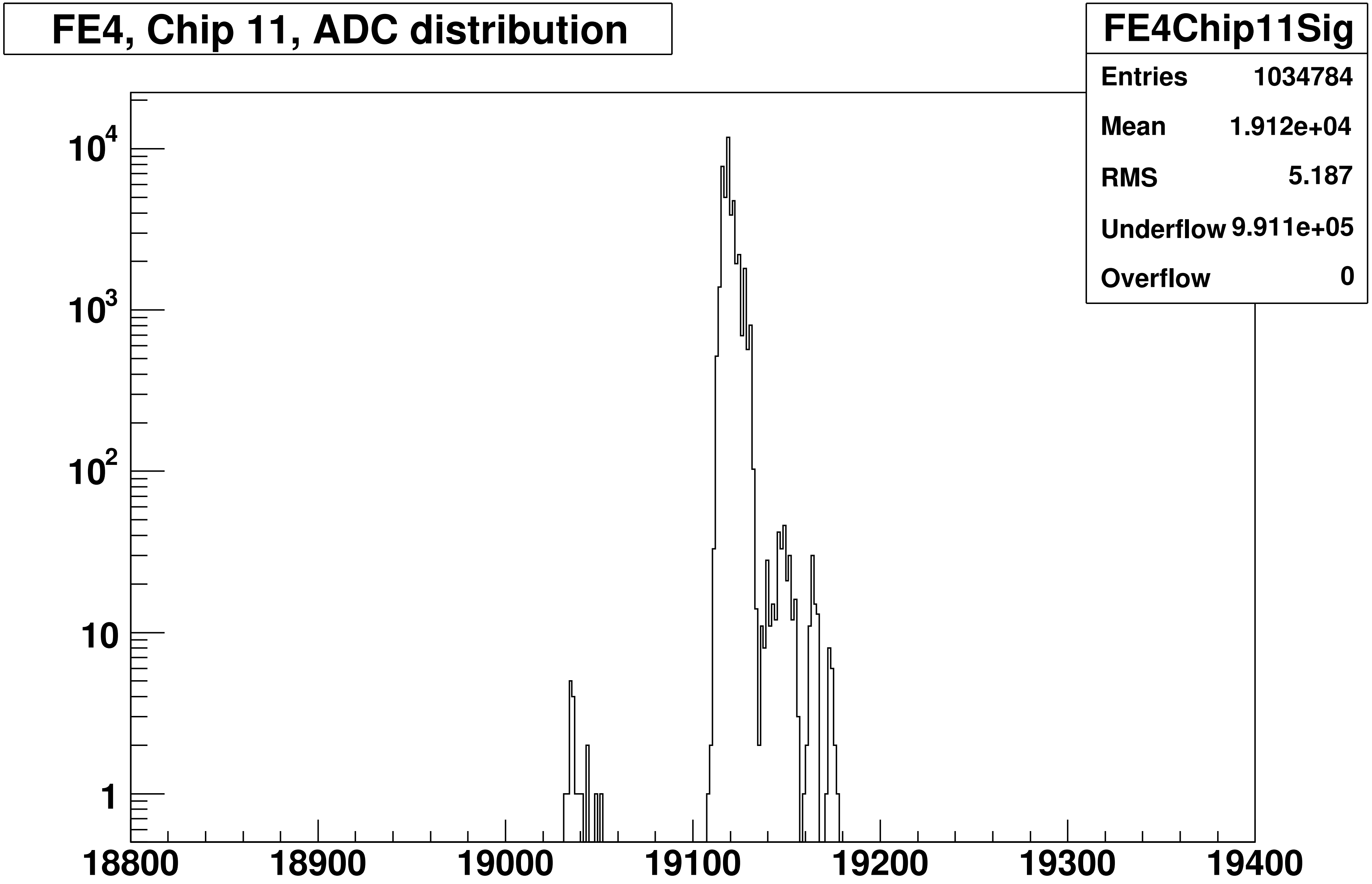
19100
19200
19300
19400


FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 1034784
Mean 831.2
RMS 3840
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 1034784
Mean 24.82
RMS 1.404
Underflow 0
Overflow 4.399e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 1034784
Mean 1.912e+04
RMS 5.187
Underflow 9.911e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
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19200
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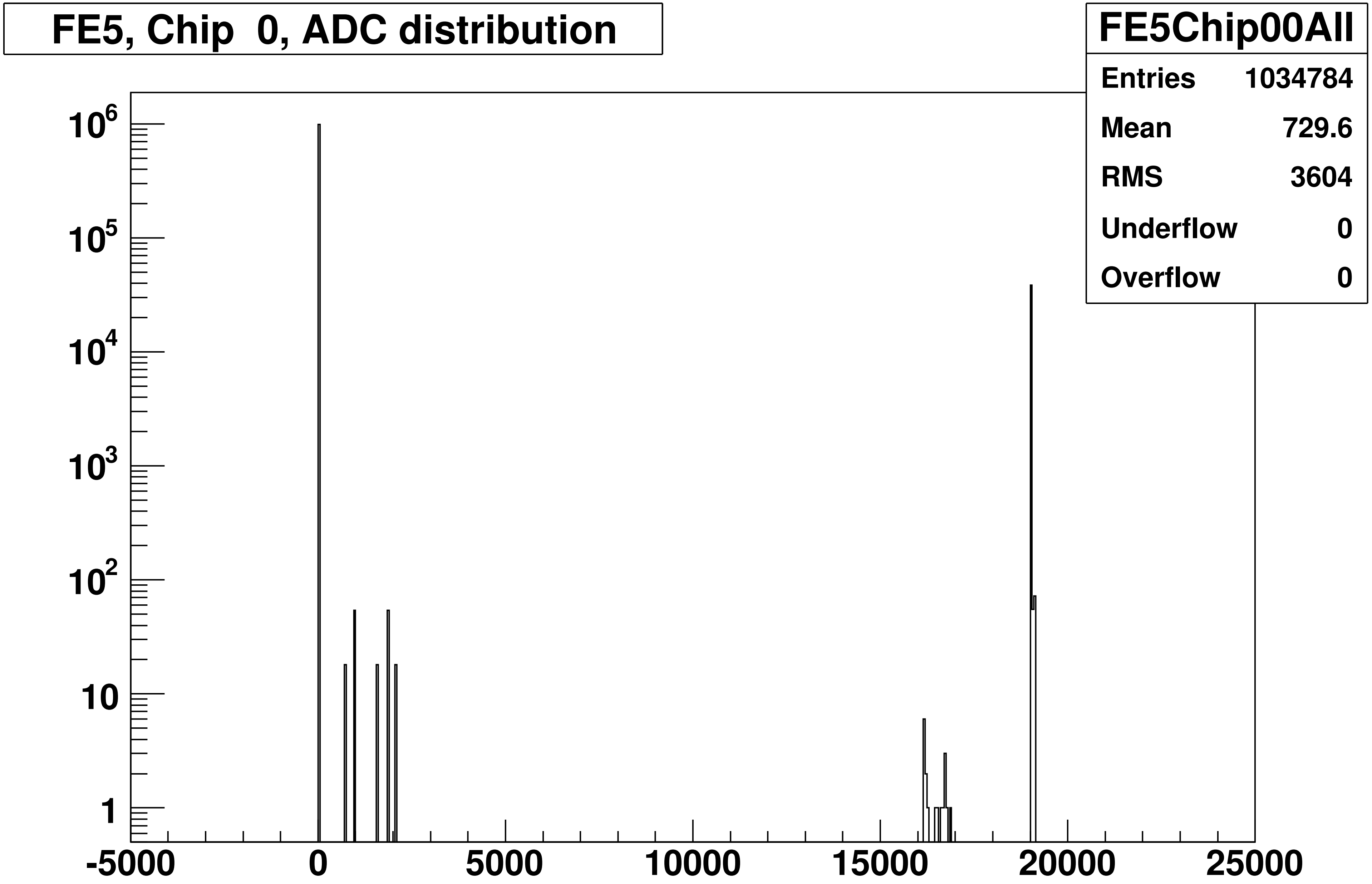
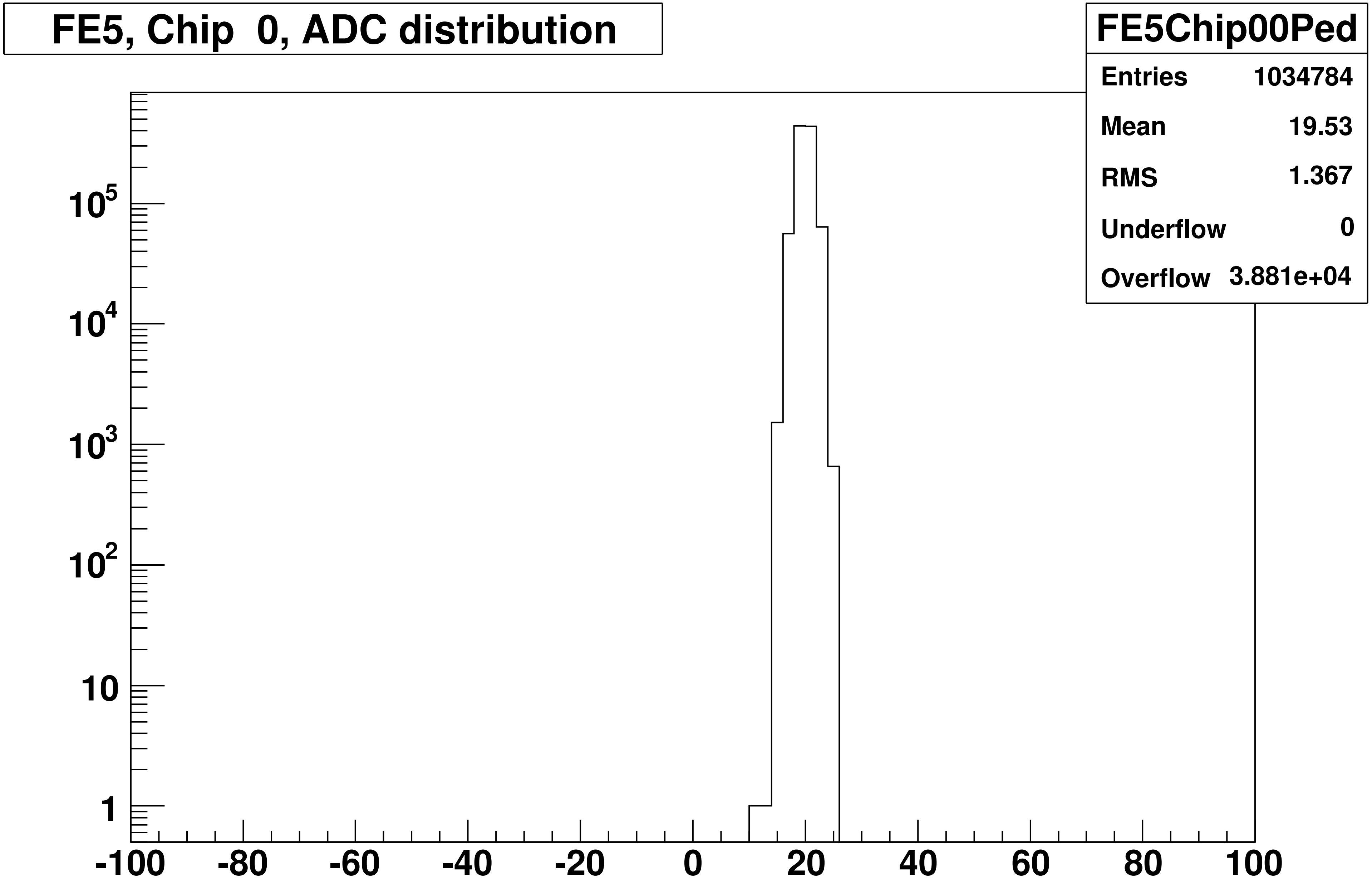
19400



FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 1034784
Mean 729.6
RMS 3604
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 1034784
Mean 19.53
RMS 1.367
Underflow 0
Overflow 3.881e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
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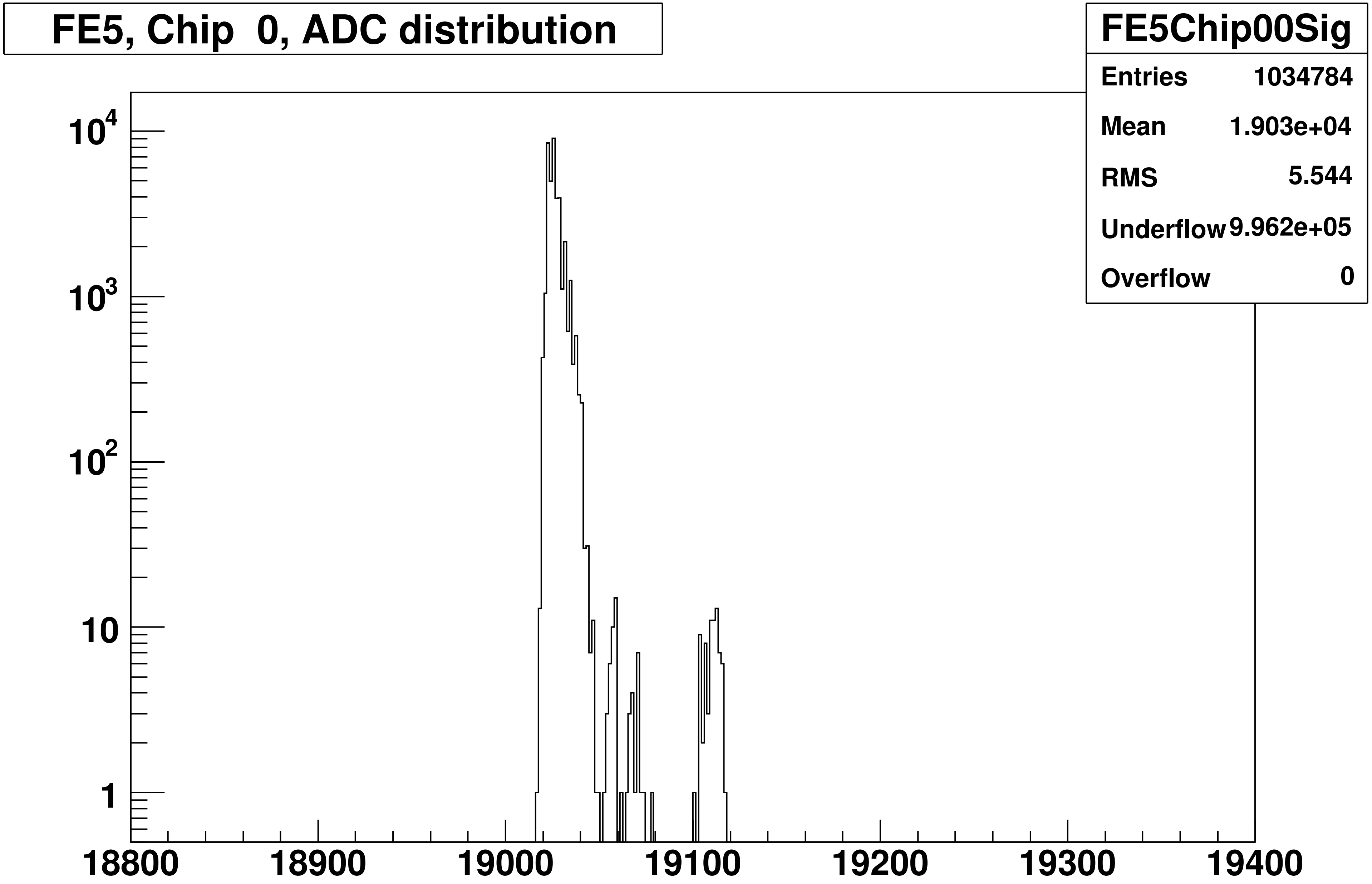
80
100



FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 1034784
Mean 1.903e+04
RMS 5.544
Underflow 9.962e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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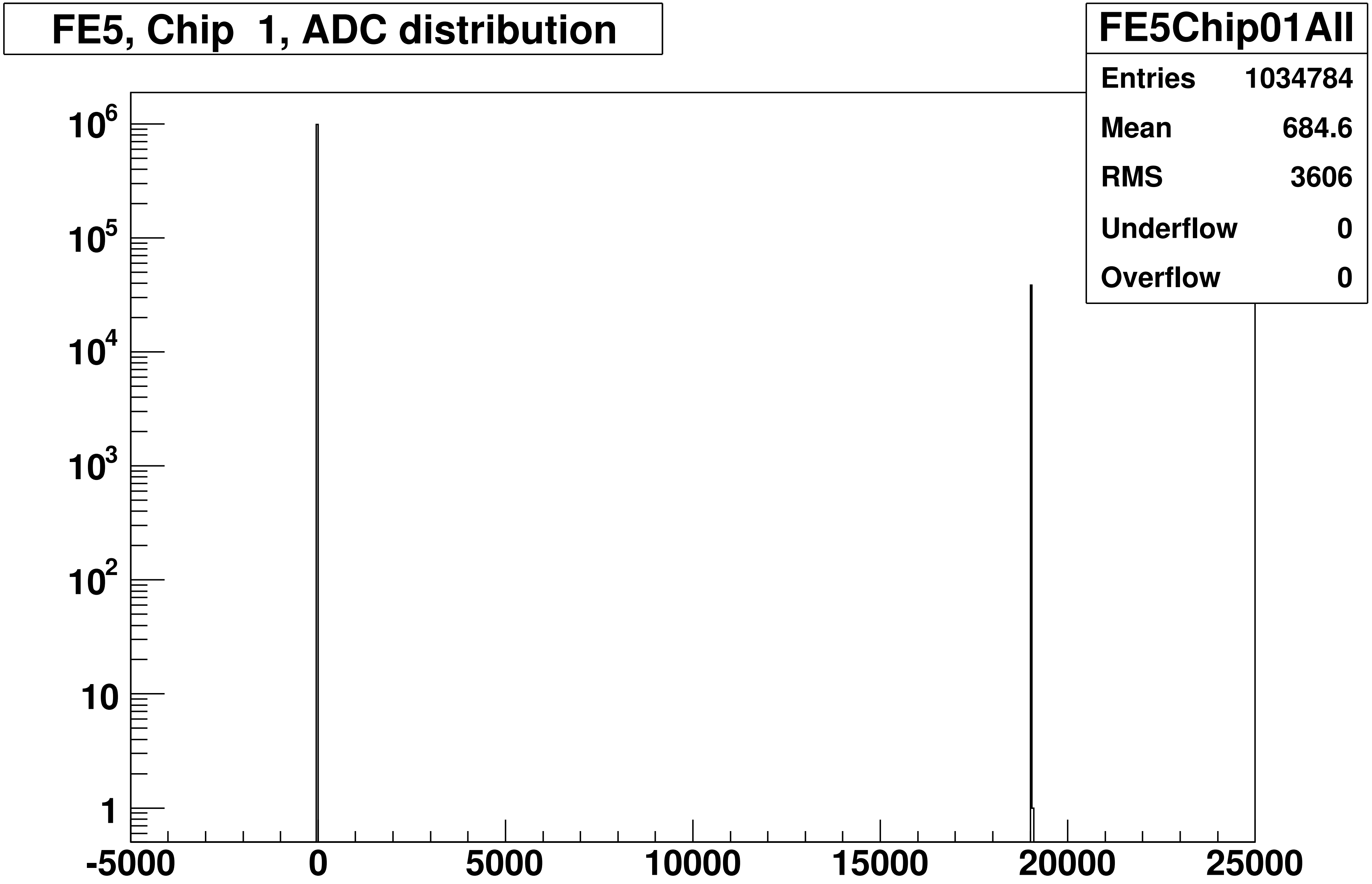
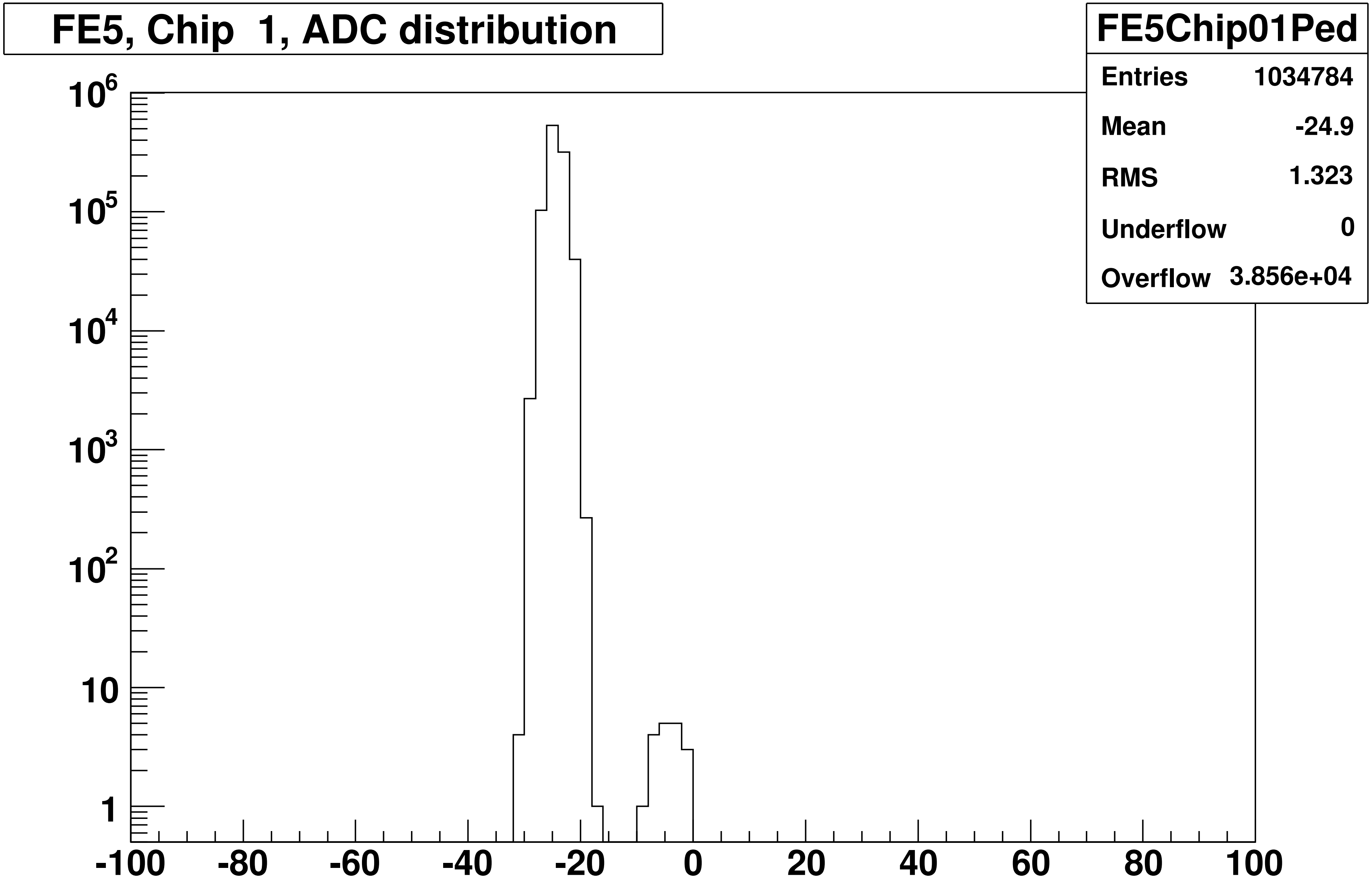
19400


FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 1034784
Mean 684.6
RMS 3606
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 1034784
Mean -24.9
RMS 1.323
Underflow 0
Overflow 3.856e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
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-40
-20
0
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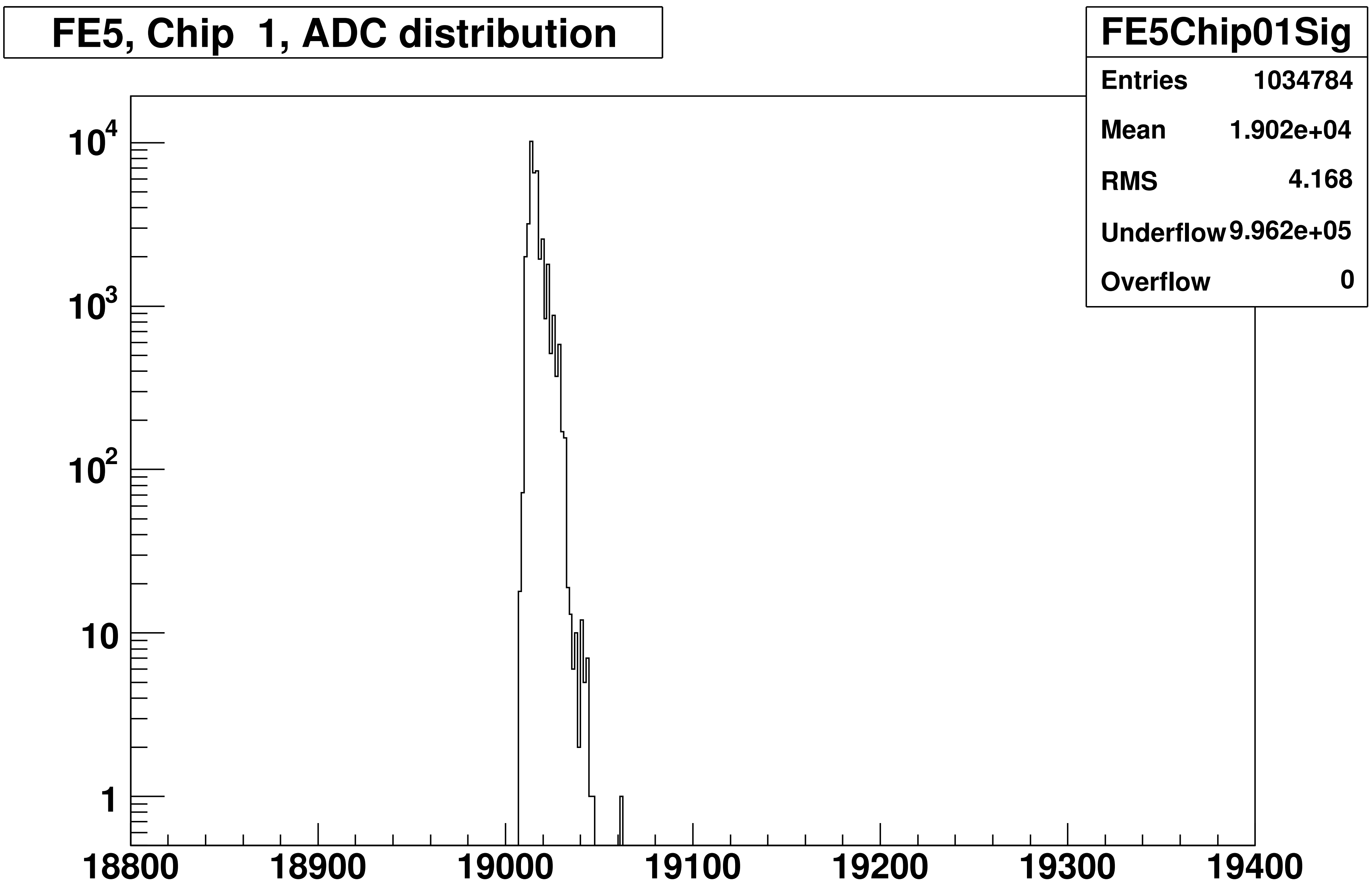
40
60
80
100


FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 1034784
Mean 1.902e+04
RMS 4.168
Underflow 9.962e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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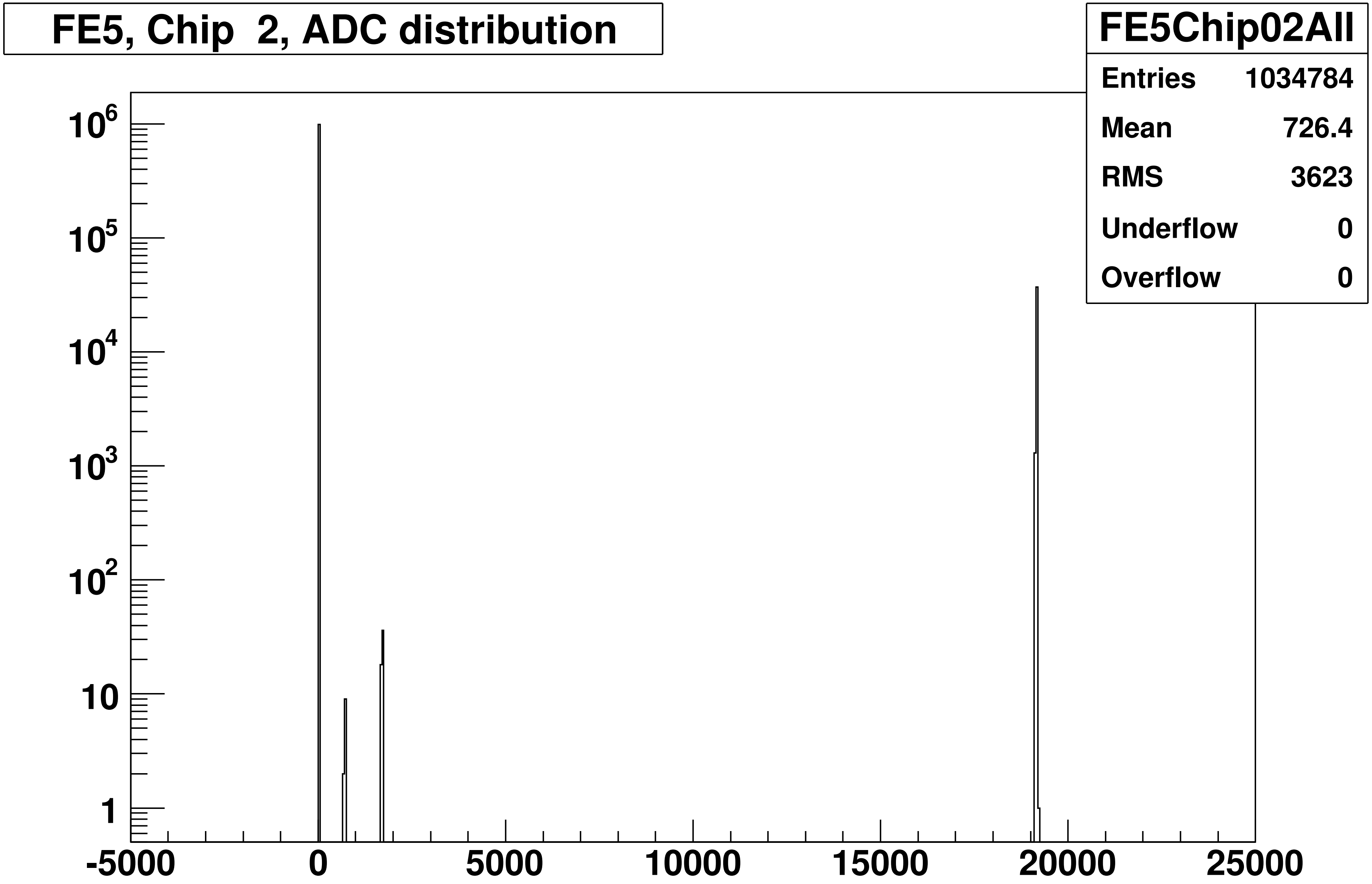
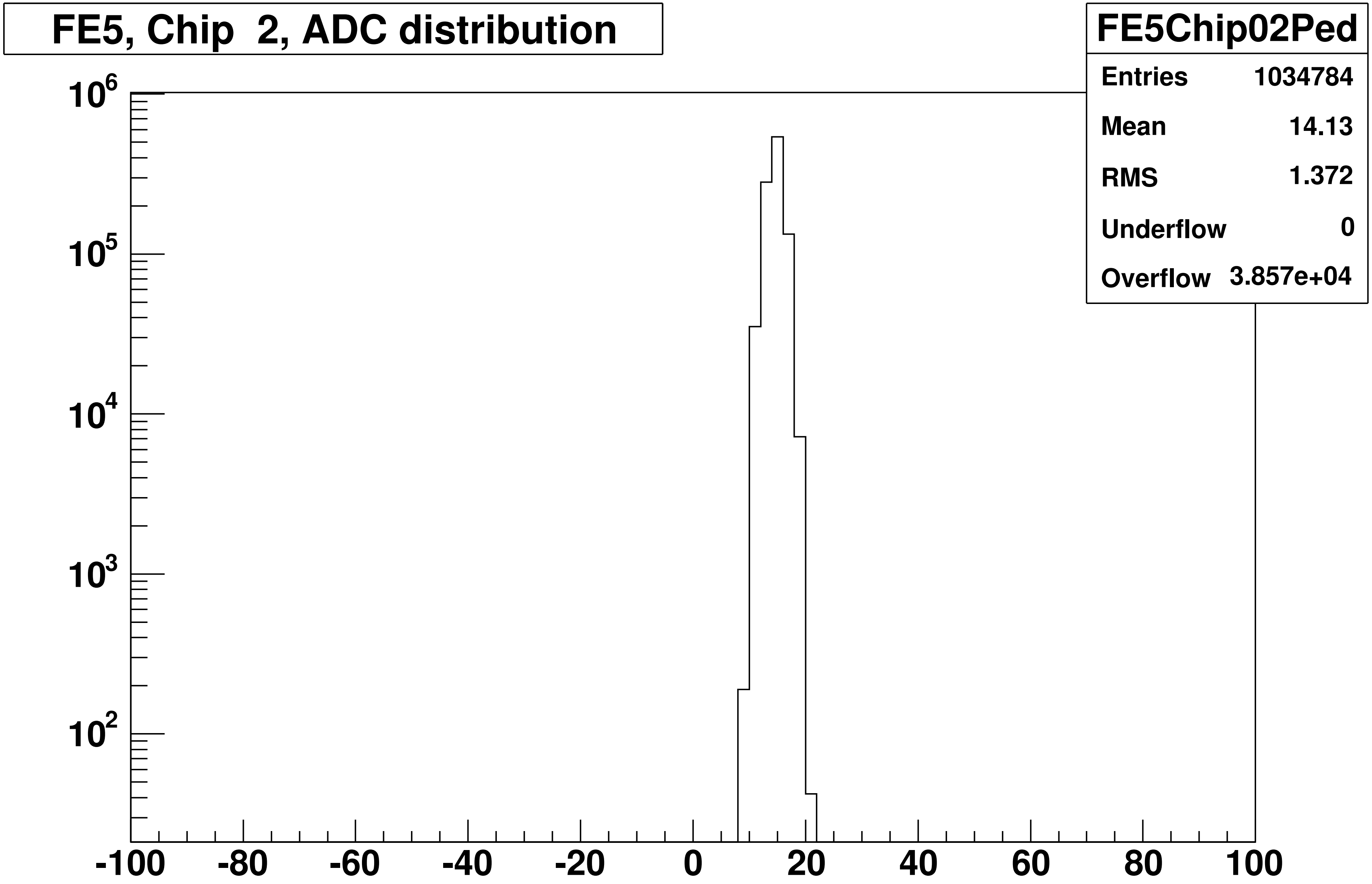
19400


FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 1034784
Mean 726.4
RMS 3623
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 1034784
Mean 14.13
RMS 1.372
Underflow 0
Overflow 3.857e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
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-40
-20
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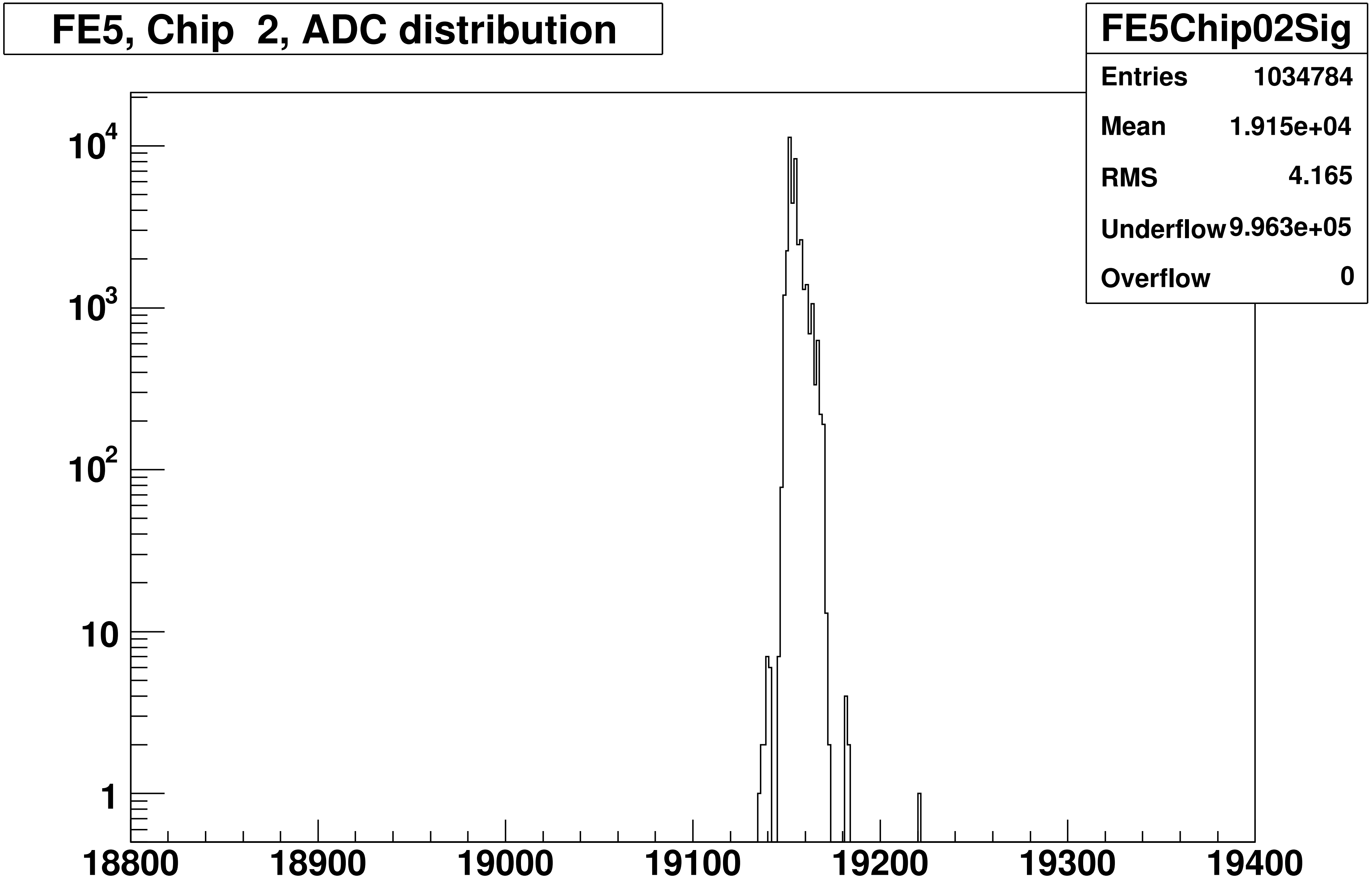
40
60
80
100


FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 1034784
Mean 1.915e+04
RMS 4.165
Underflow 9.963e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
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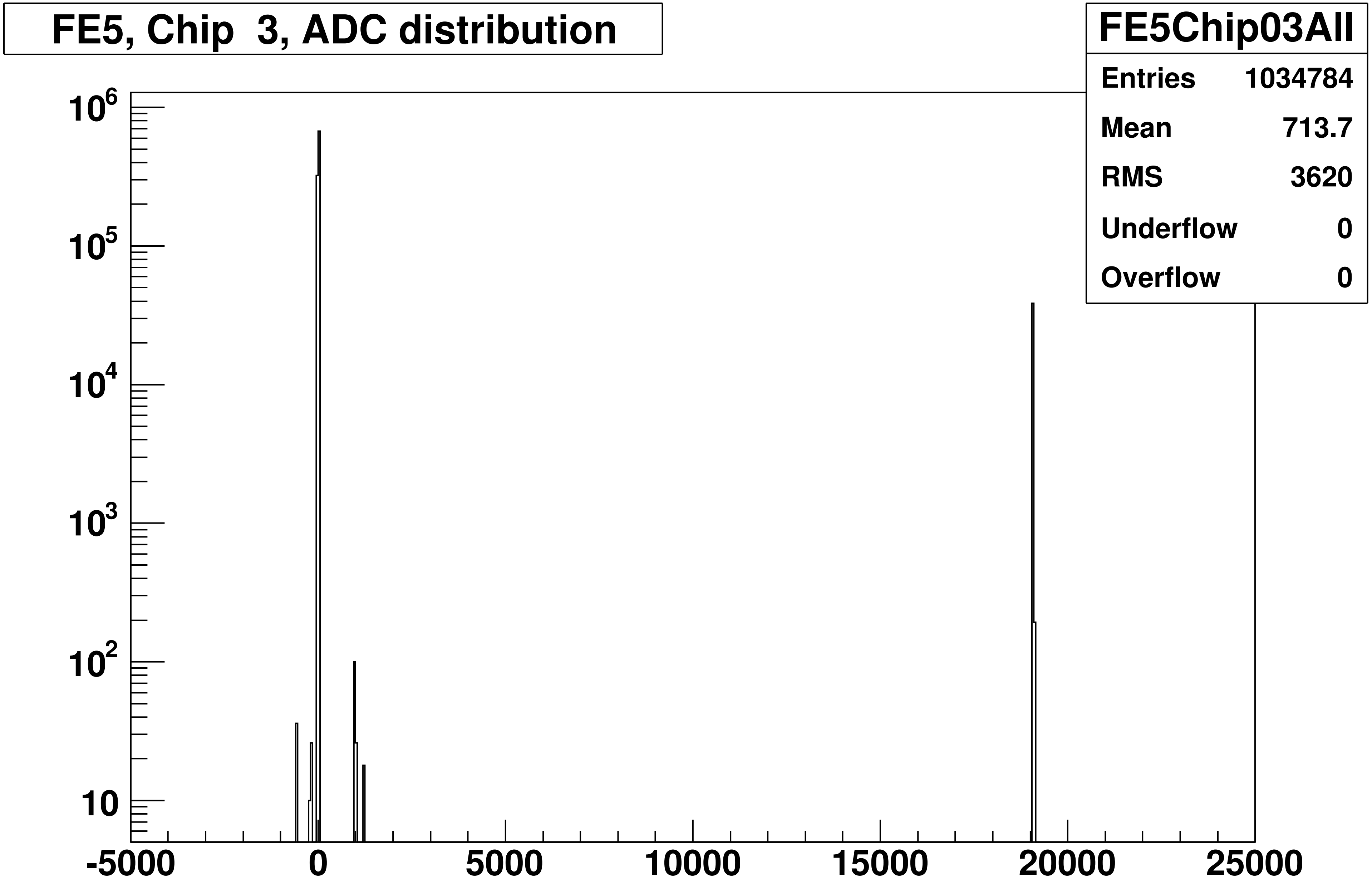
19400


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 1034784
Mean 713.7
RMS 3620
Underflow 0
Overflow 0
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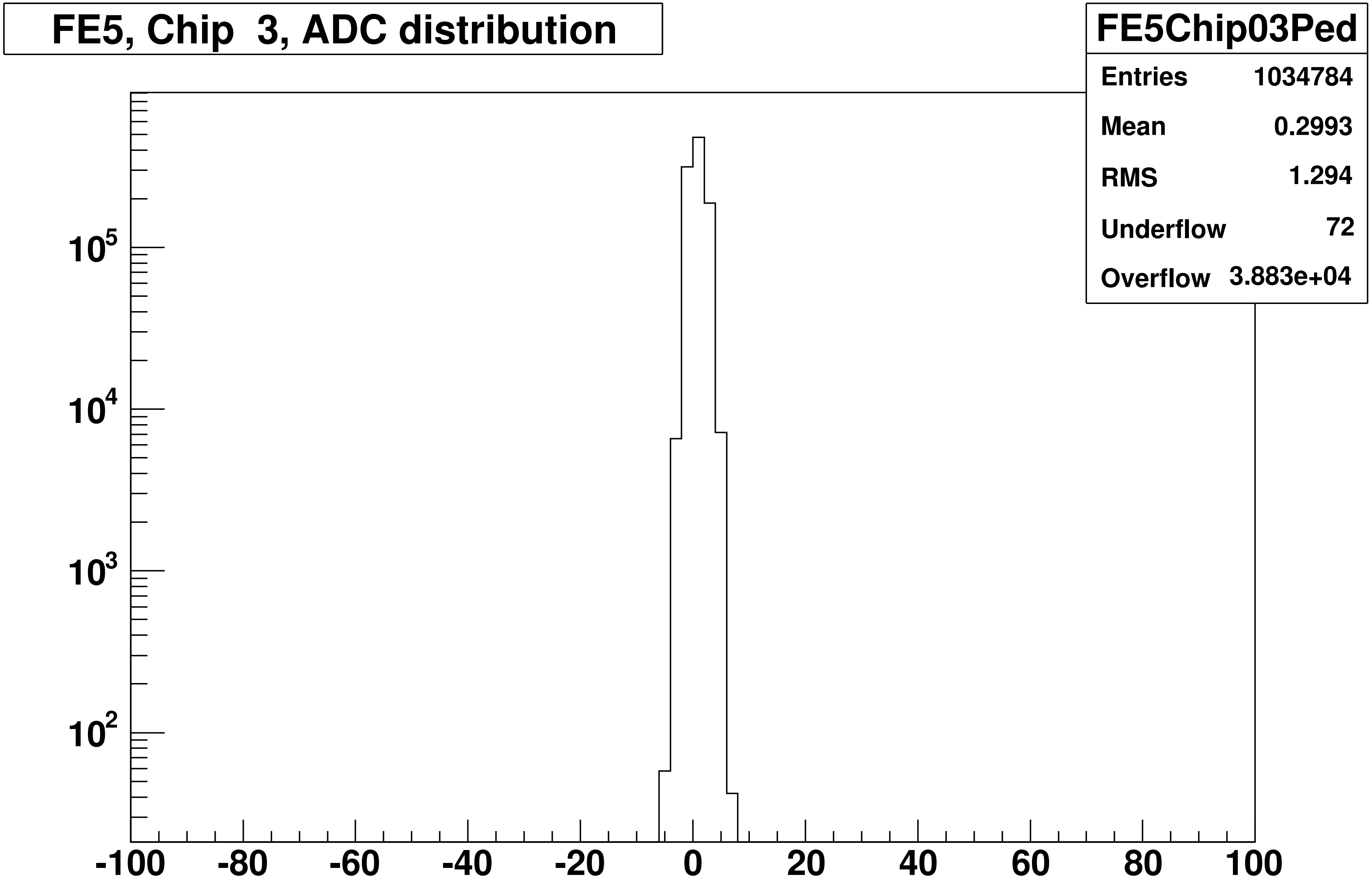
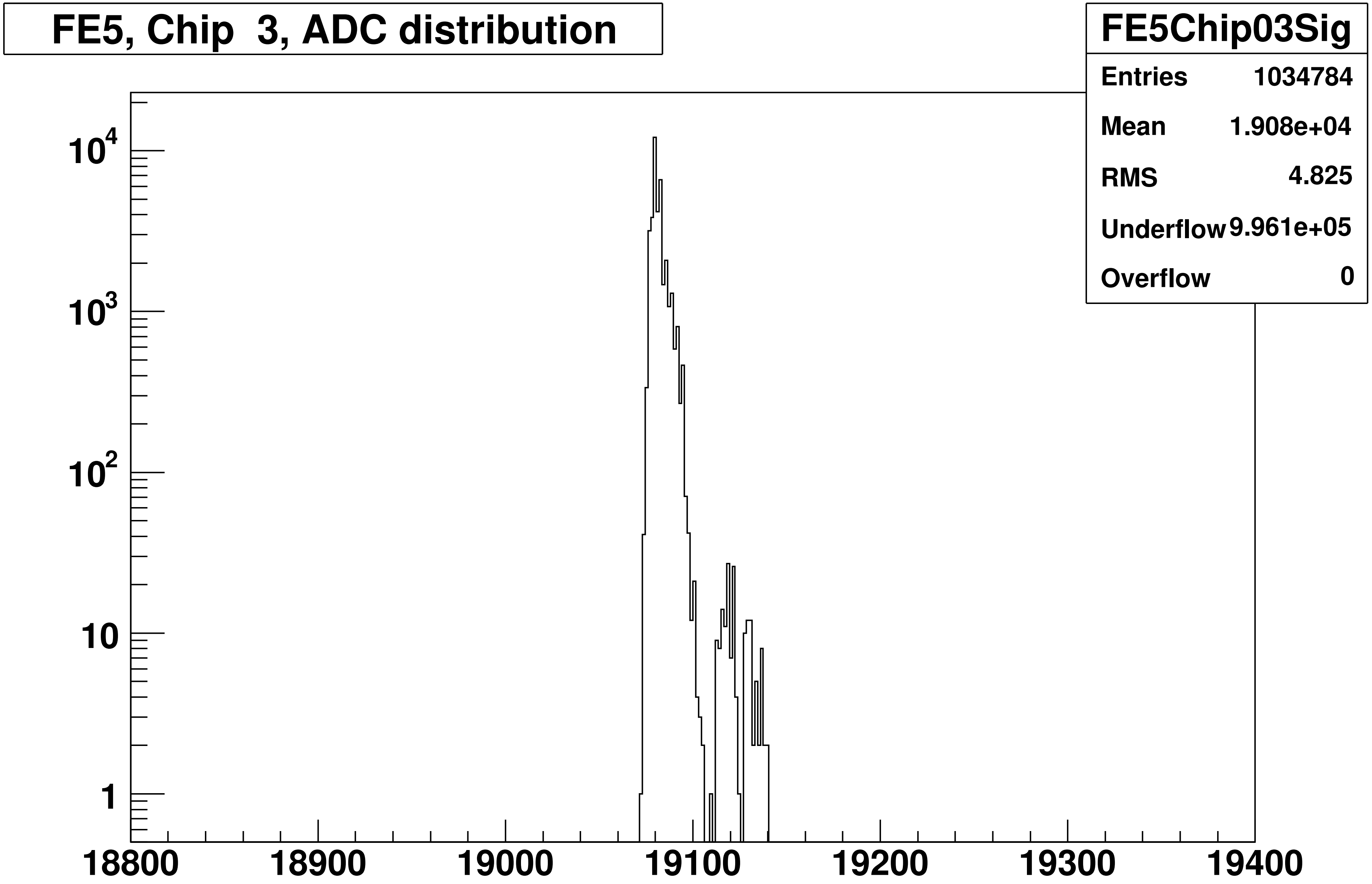
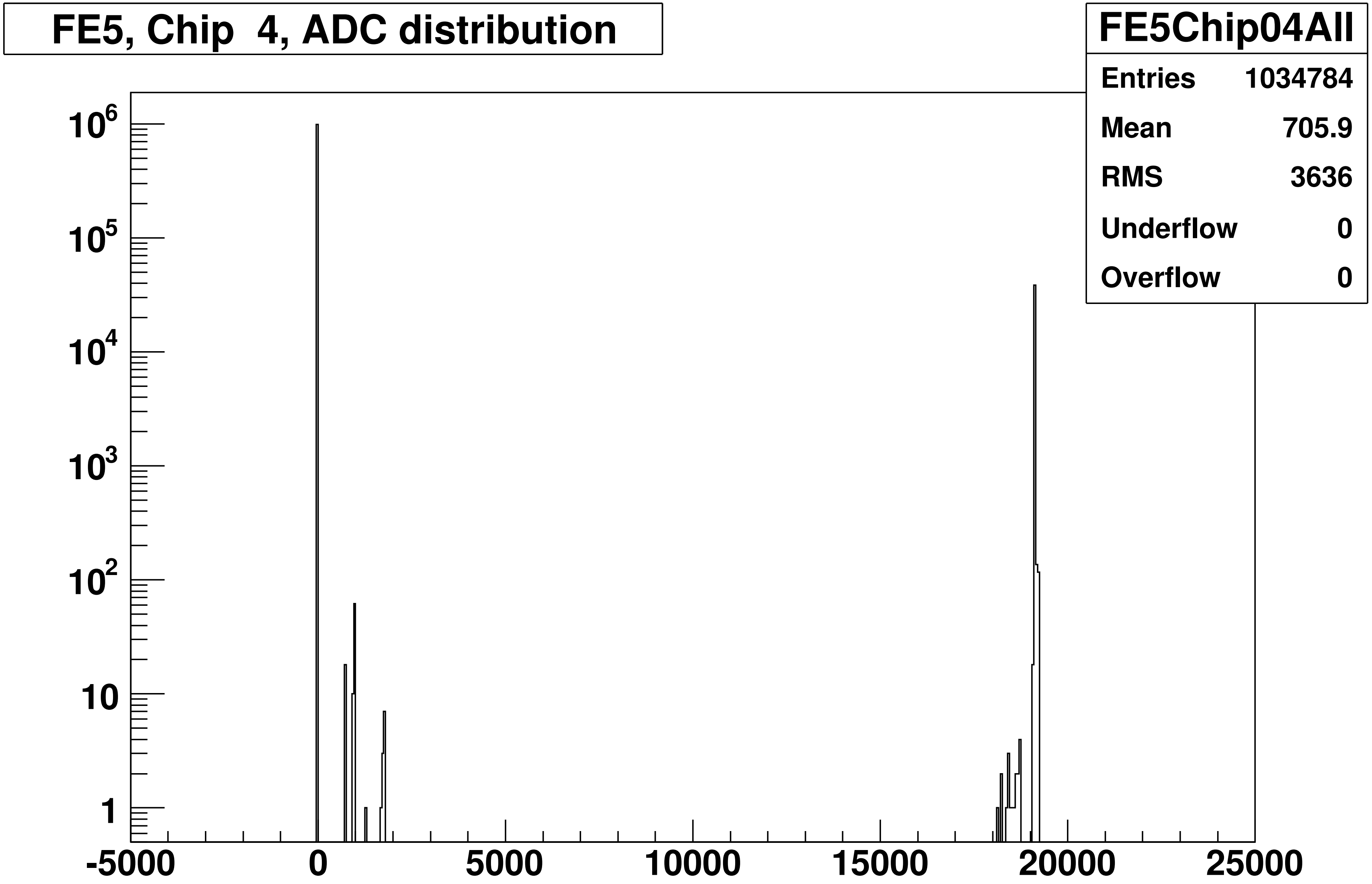
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 1034784
Mean 0.2993
RMS 1.294
Underflow 72
Overflow 3.883e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 1034784
Mean 1.908e+04
RMS 4.825
Underflow 9.961e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 1034784
Mean 705.9
RMS 3636
Underflow 0
Overflow 0
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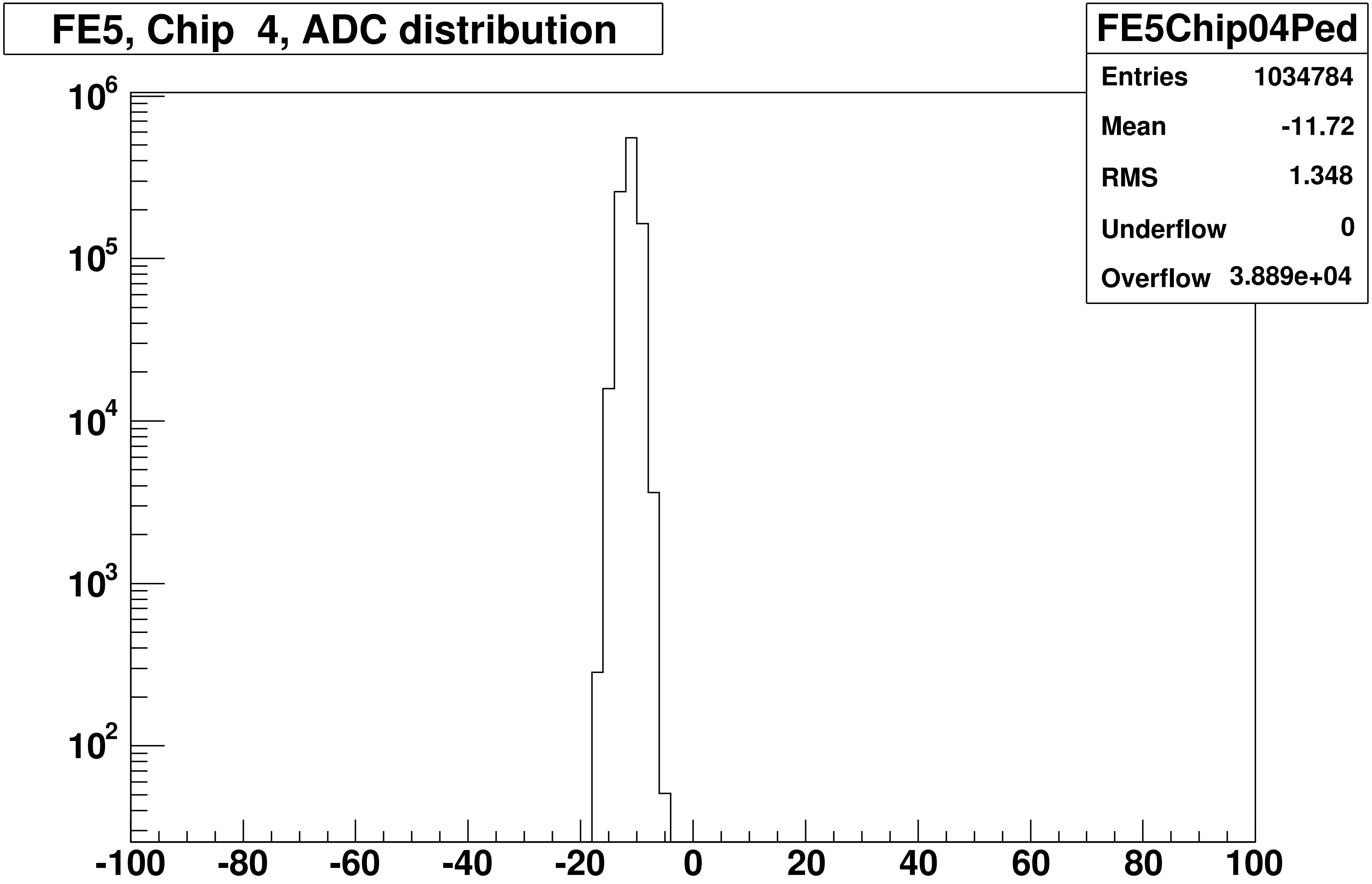
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 1034784
Mean -11.72
RMS 1.348
Underflow 0
Overflow 3.889e+04
10^6
10^5
10^4
10^3
10^2
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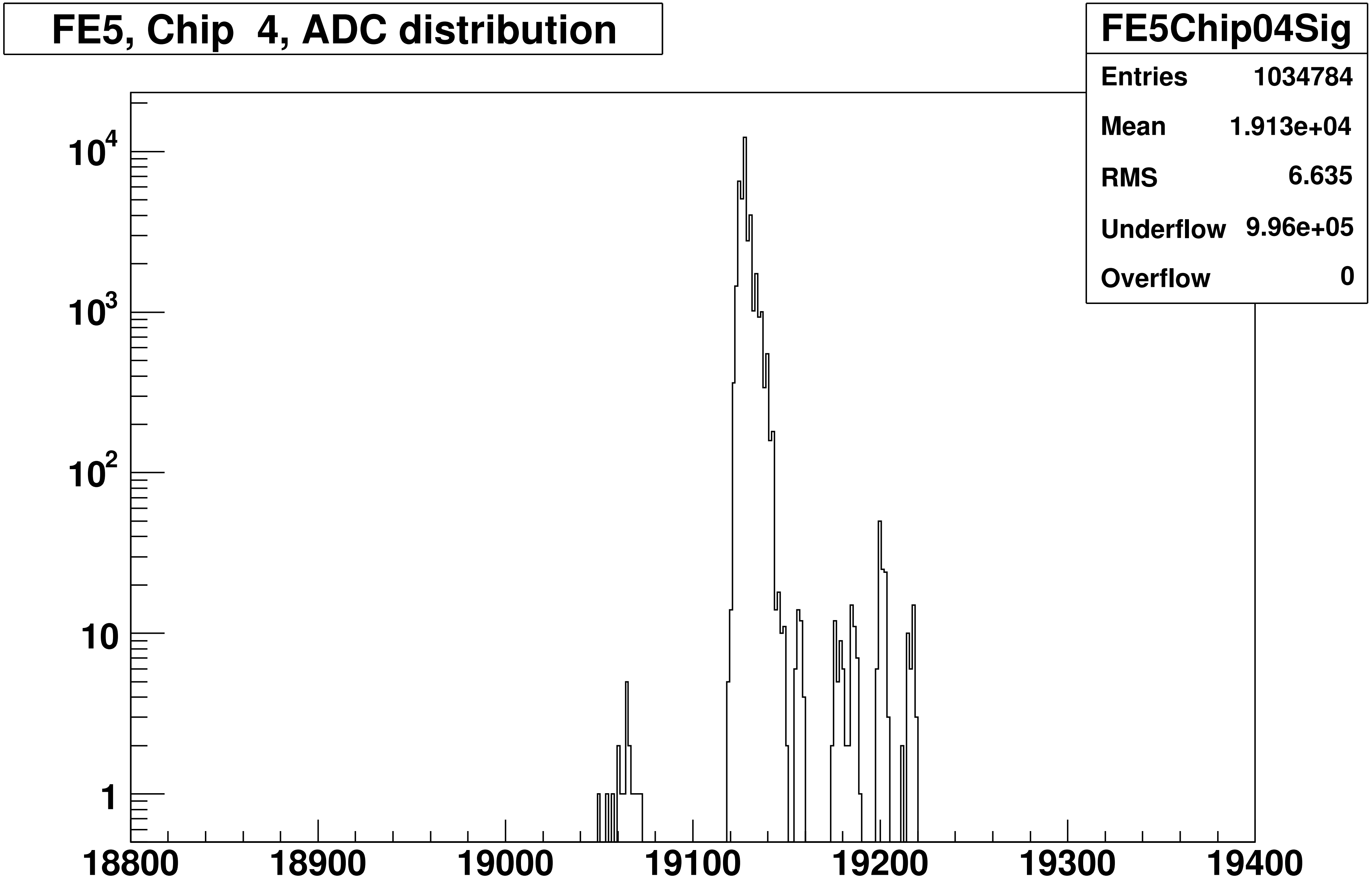
60
80
100


FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 1034784
Mean 1.913e+04
RMS 6.635
Underflow 9.96e+05
Overflow 0
10^4
10^3
10^2
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1
18800
18900
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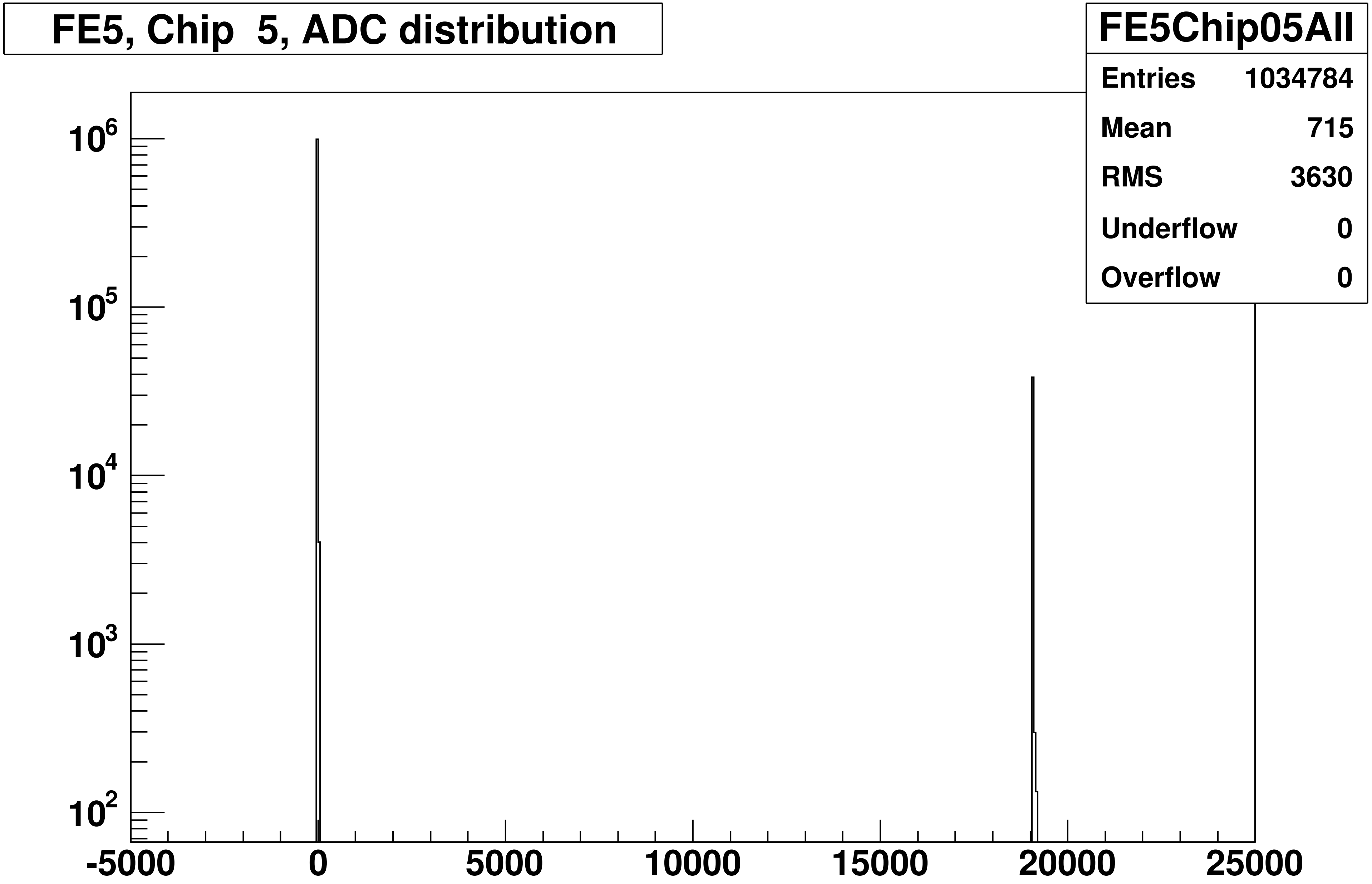

FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 1034784
Mean 715
RMS 3630
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
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0
5000
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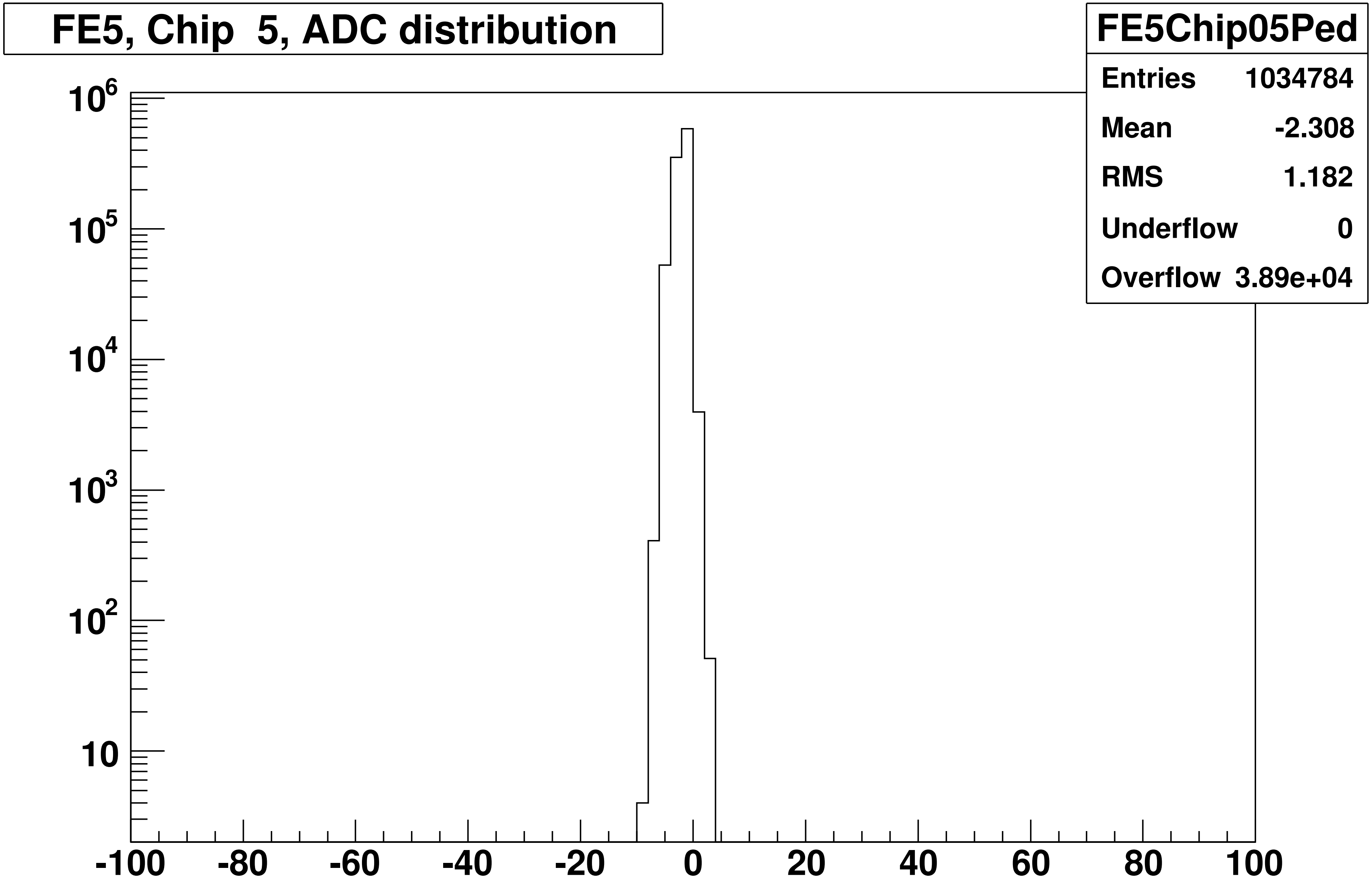
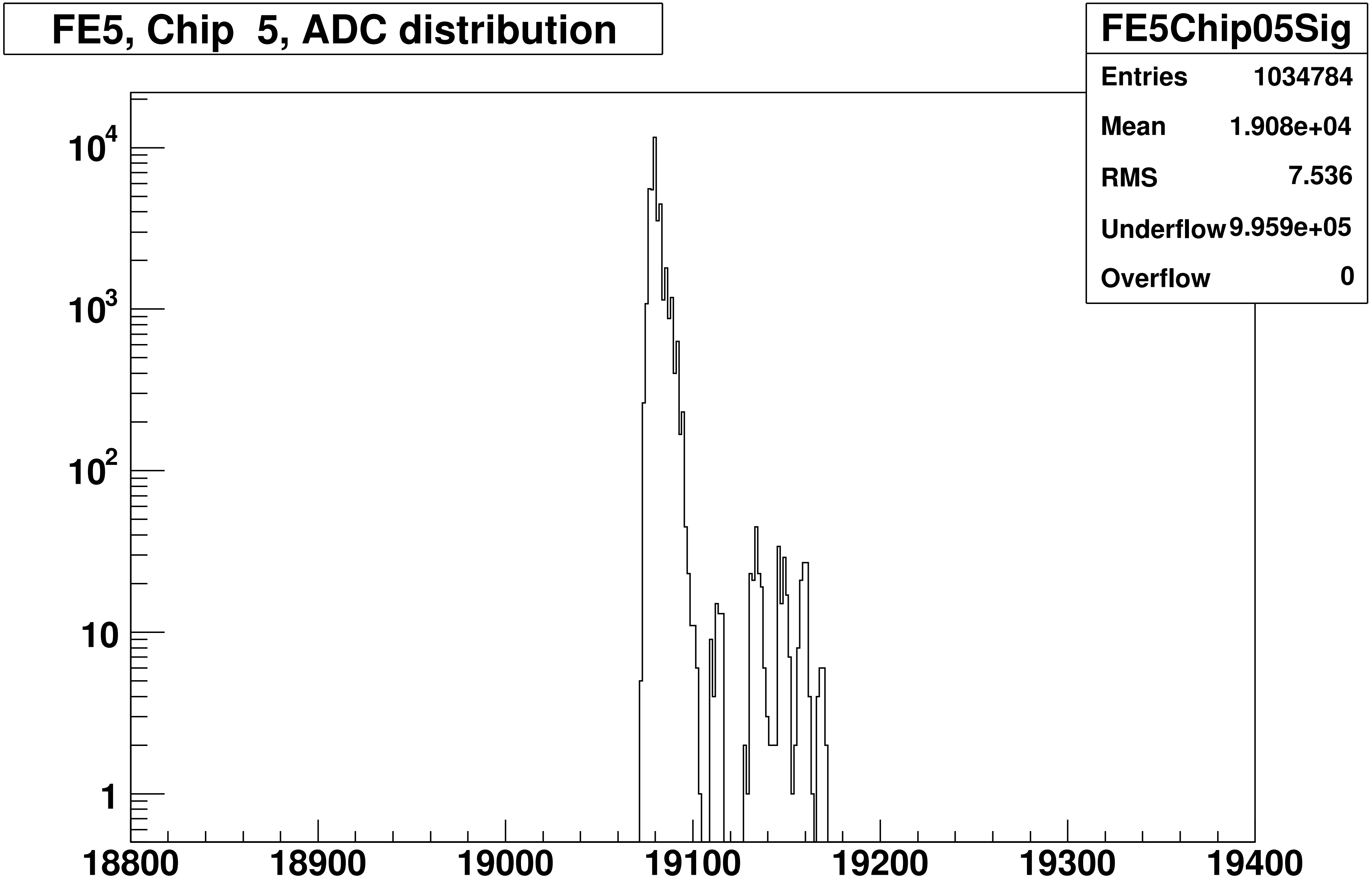
25000



FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 1034784
Mean -2.308
RMS 1.182
Underflow 0
Overflow 3.89e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 1034784
Mean 1.908e+04
RMS 7.536
Underflow 9.959e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
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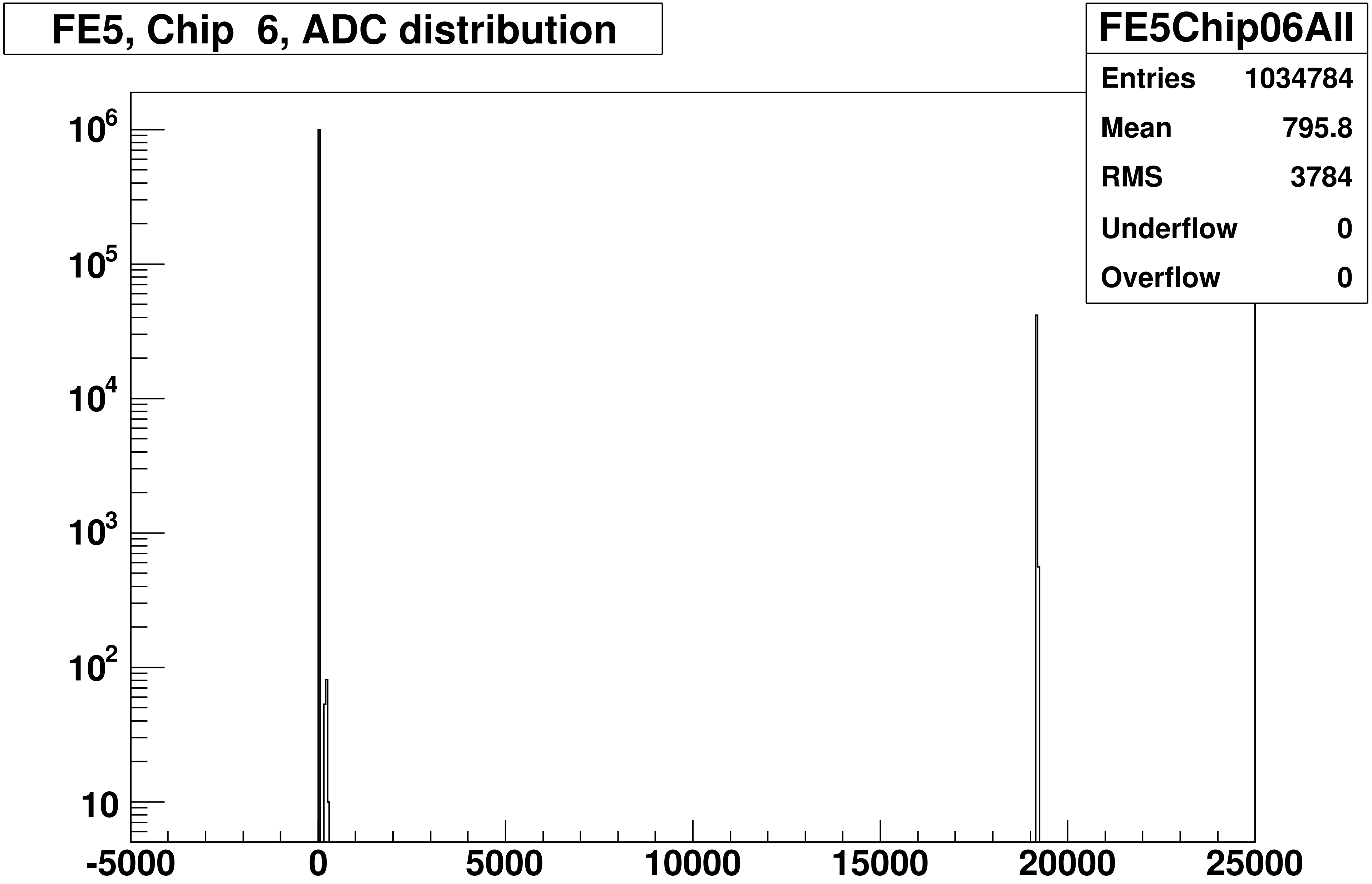
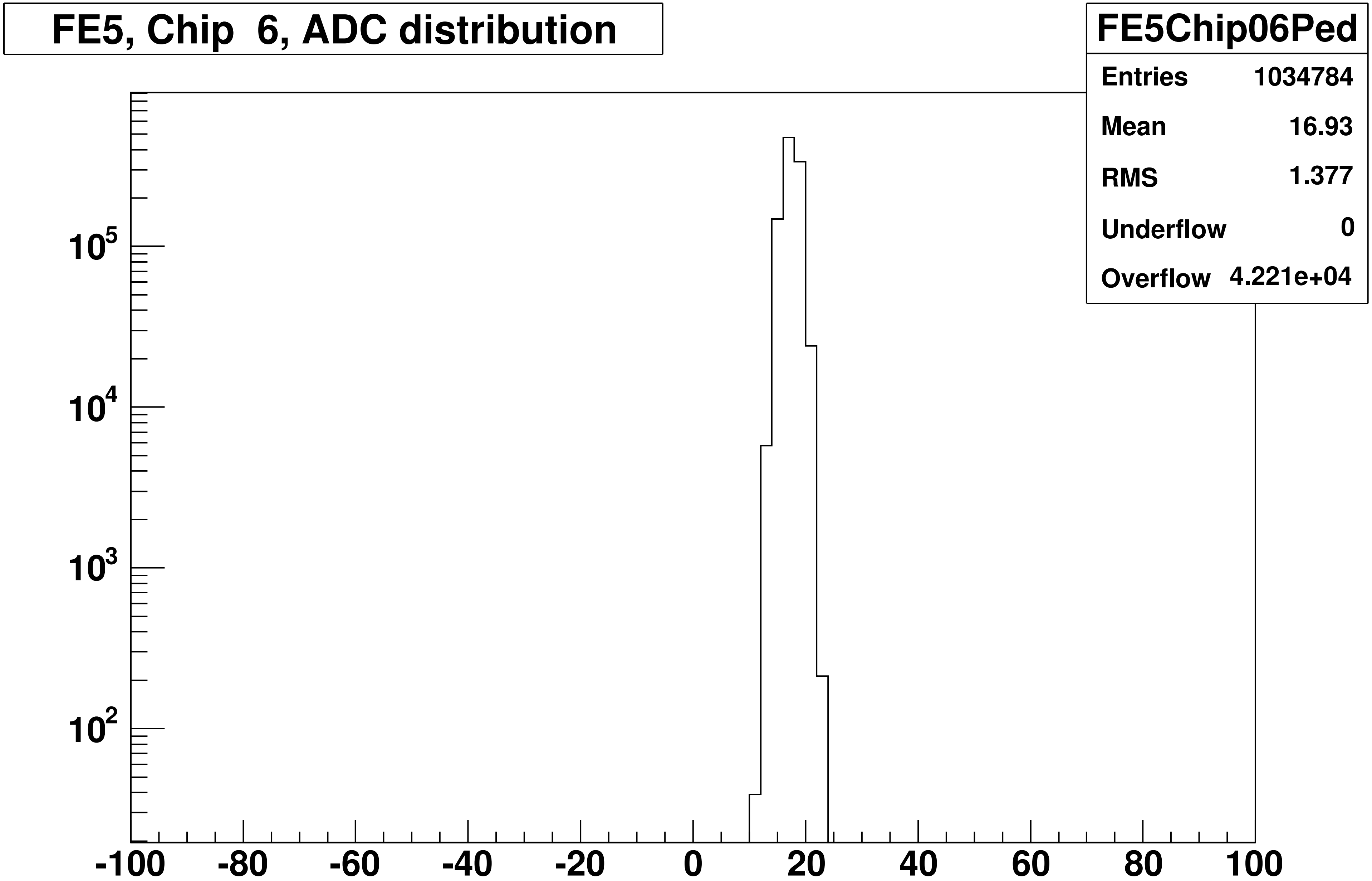

FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 1034784
Mean 795.8
RMS 3784
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 1034784
Mean 16.93
RMS 1.377
Underflow 0
Overflow 4.221e+04
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10^4
10^3
10^2
-100
-80
-60
-40
-20
0
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40
60
80
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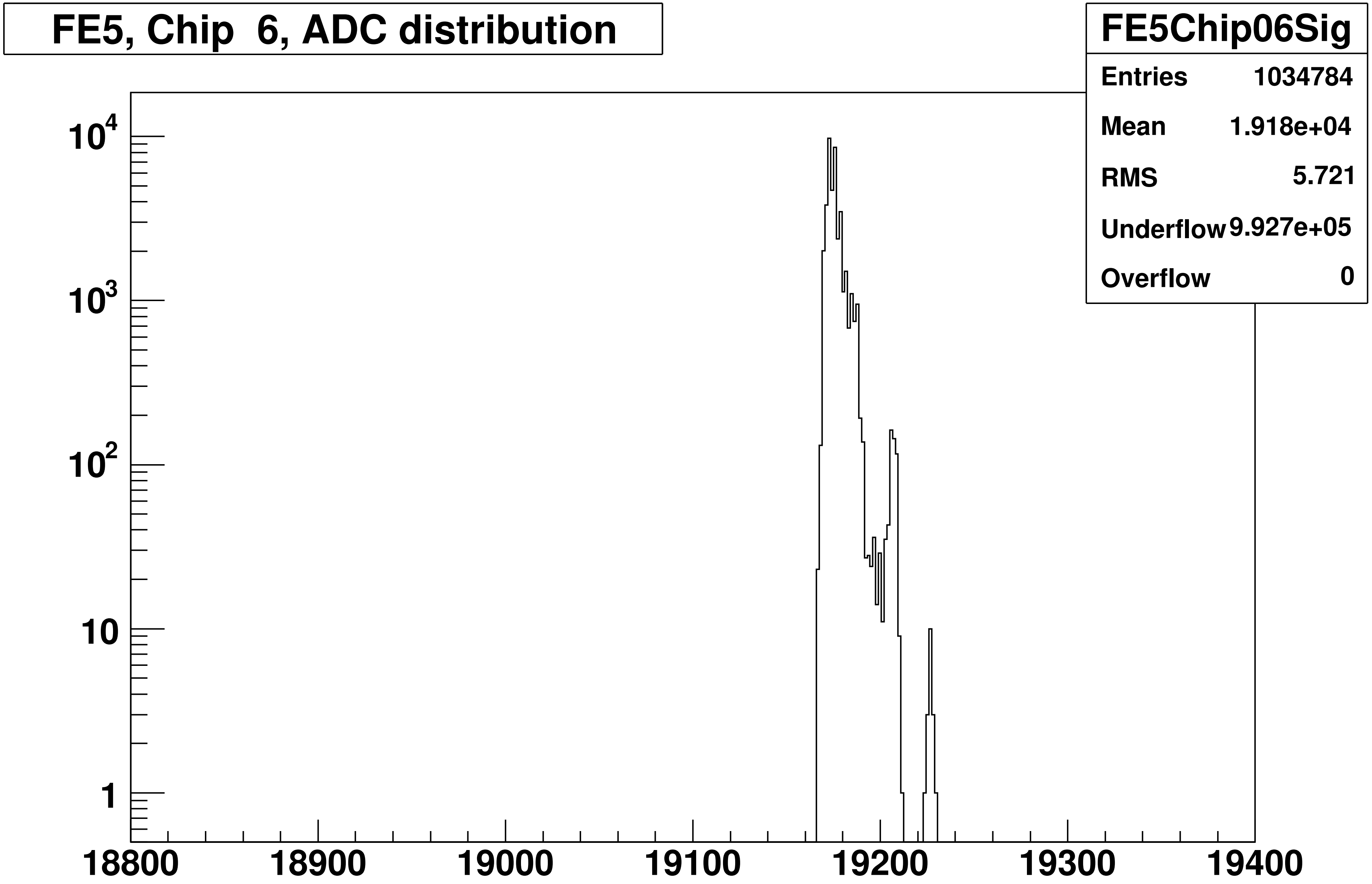
FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 1034784
Mean 1.918e+04
RMS 5.721
Underflow 9.927e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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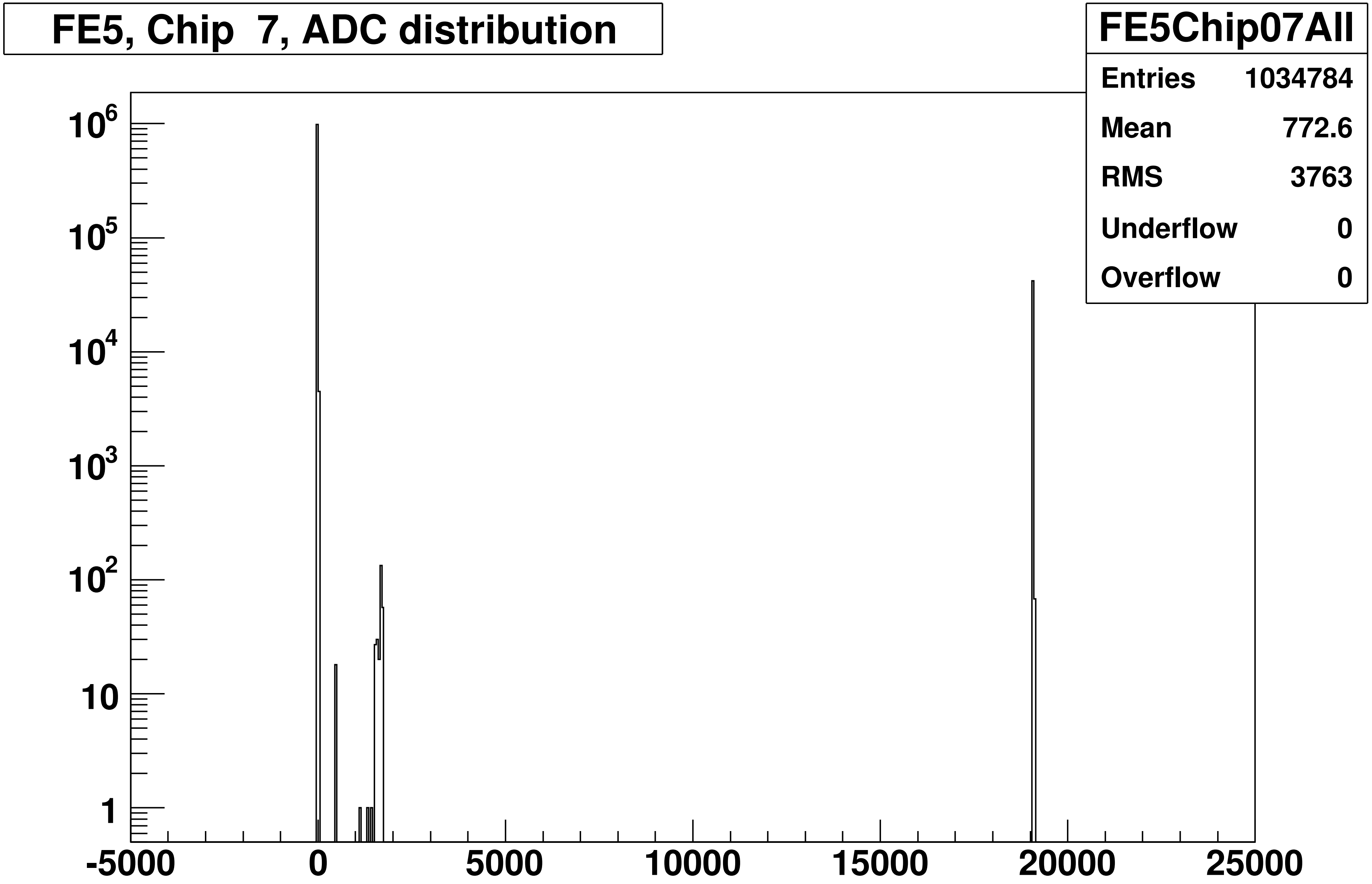
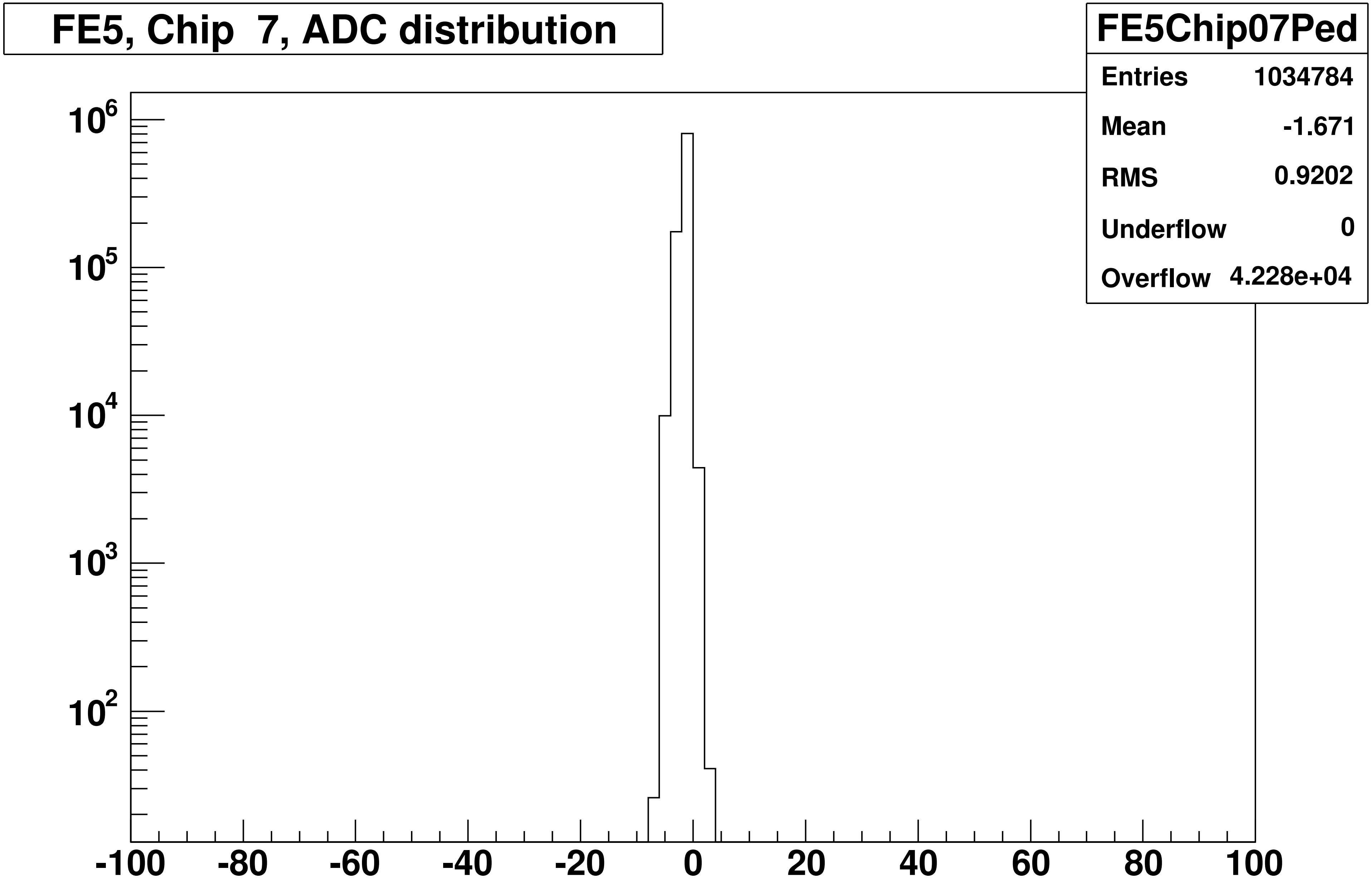
19200
19300
19400


FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 1034784
Mean 772.6
RMS 3763
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 1034784
Mean -1.671
RMS 0.9202
Underflow 0
Overflow 4.228e+04
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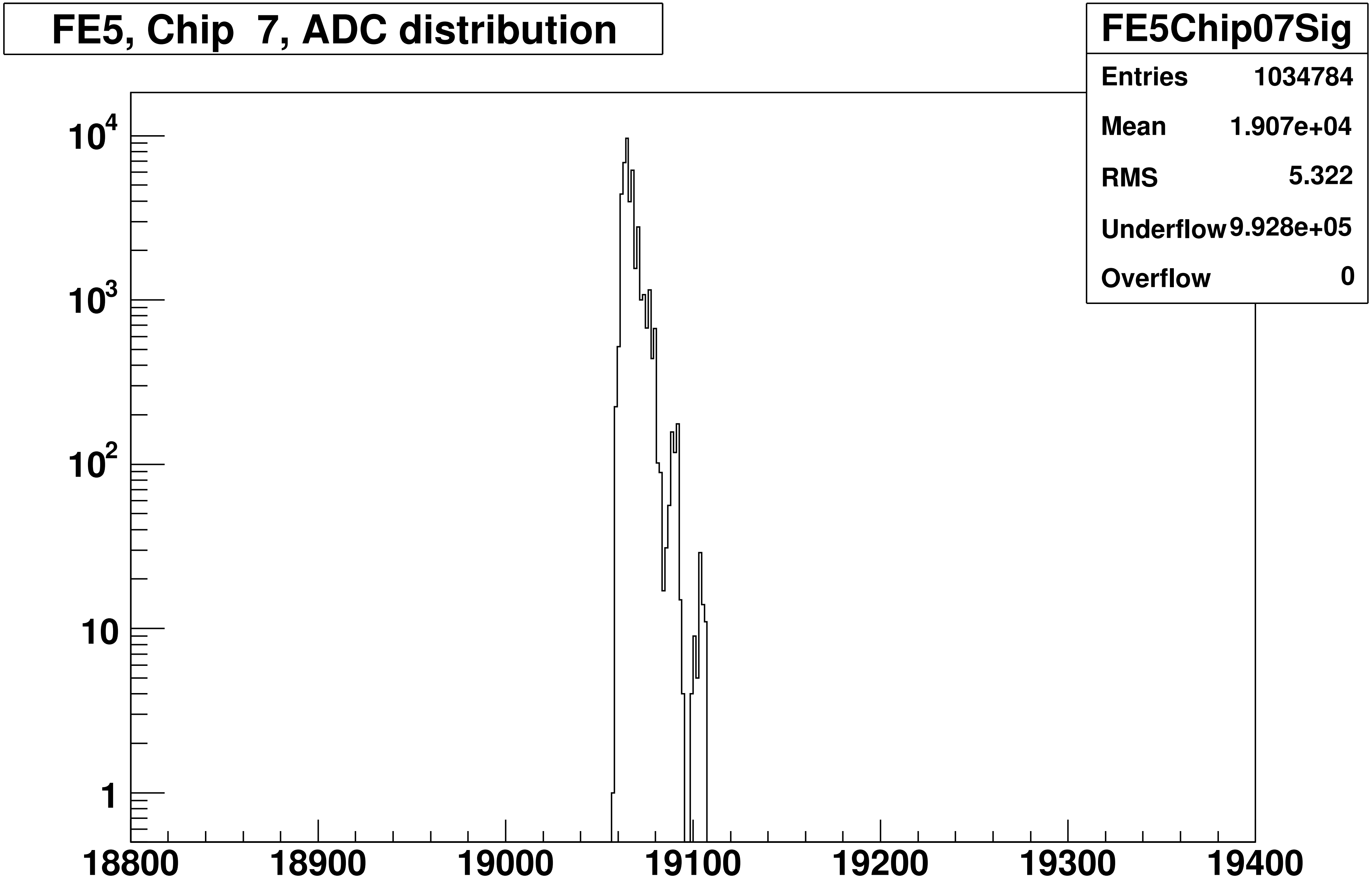
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 1034784
Mean 1.907e+04
RMS 5.322
Underflow 9.928e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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19200
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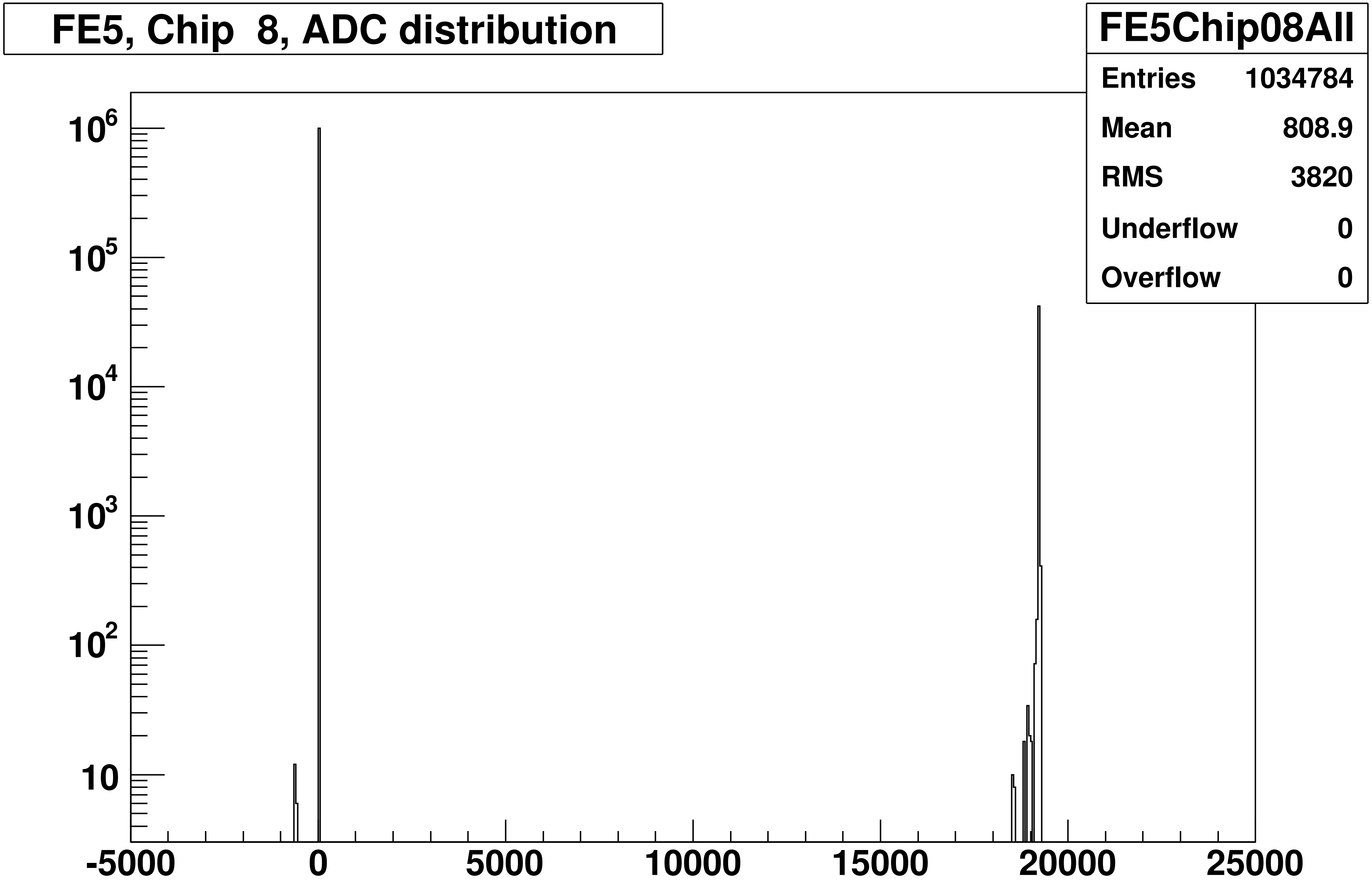

FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 1034784
Mean 808.9
RMS 3820
Underflow 0
Overflow 0
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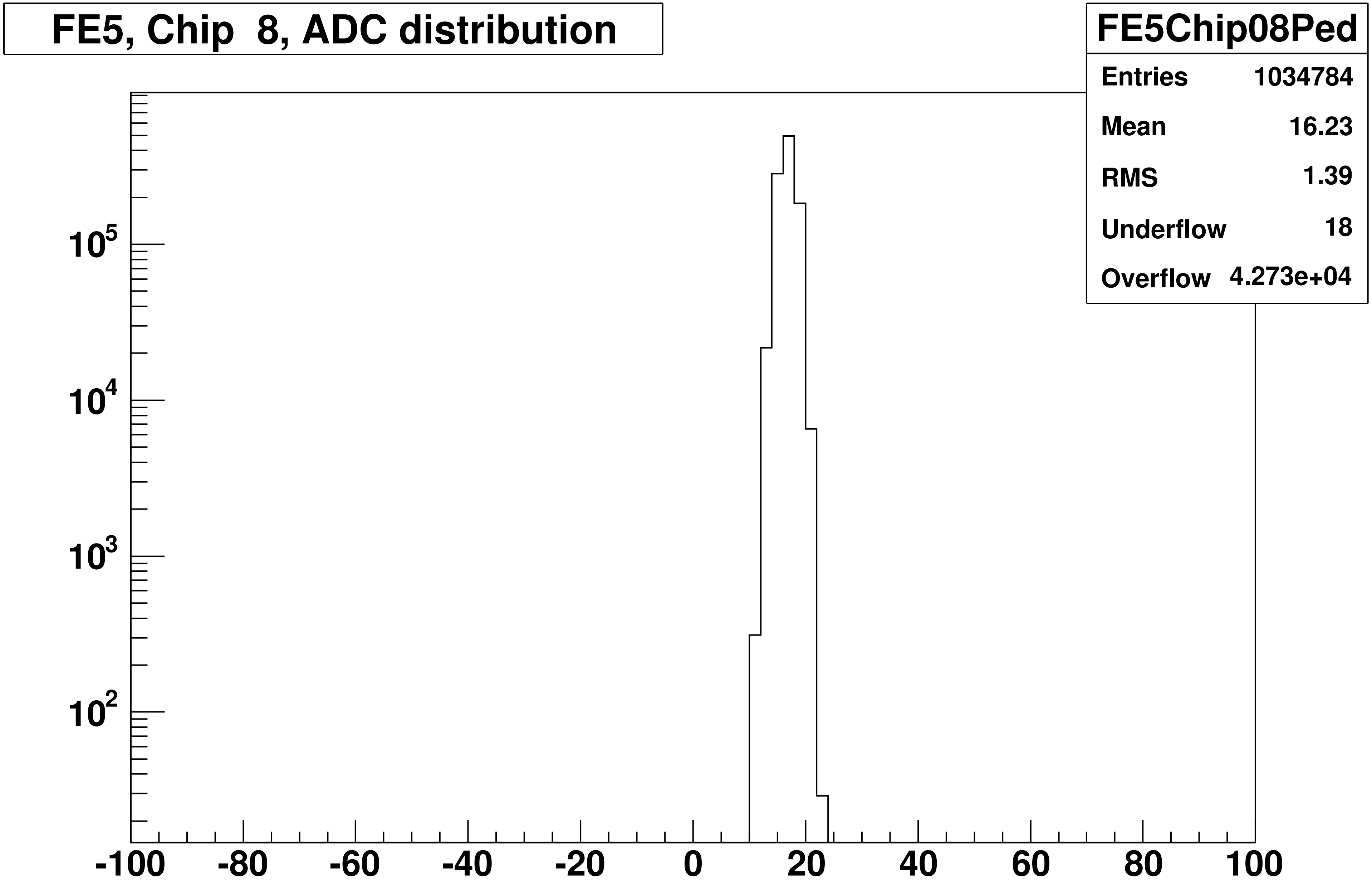
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 1034784
Mean 16.23
RMS 1.39
Underflow 18
Overflow 4.273e+04
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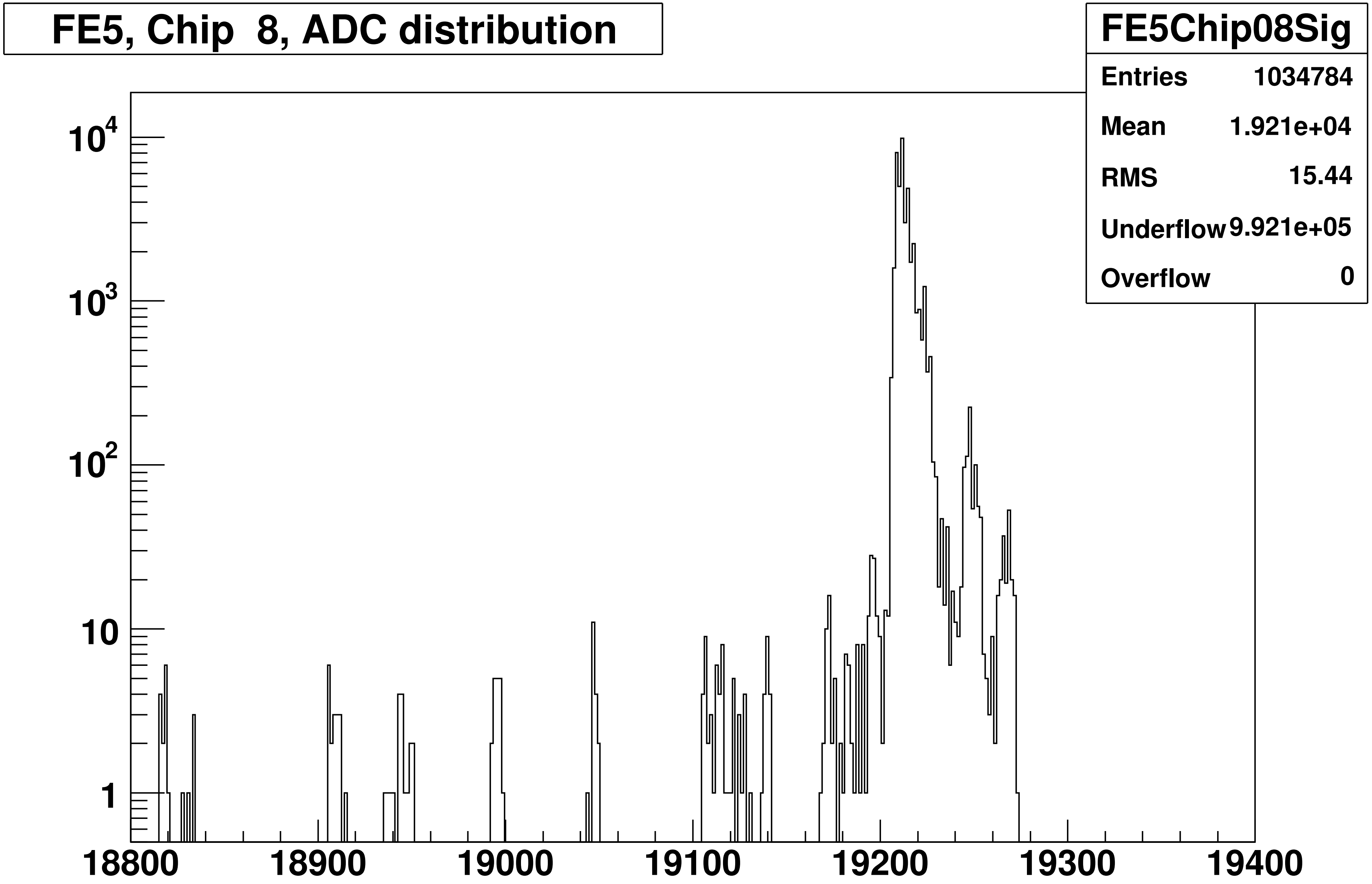
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 1034784
Mean 1.921e+04
RMS 15.44
Underflow 9.921e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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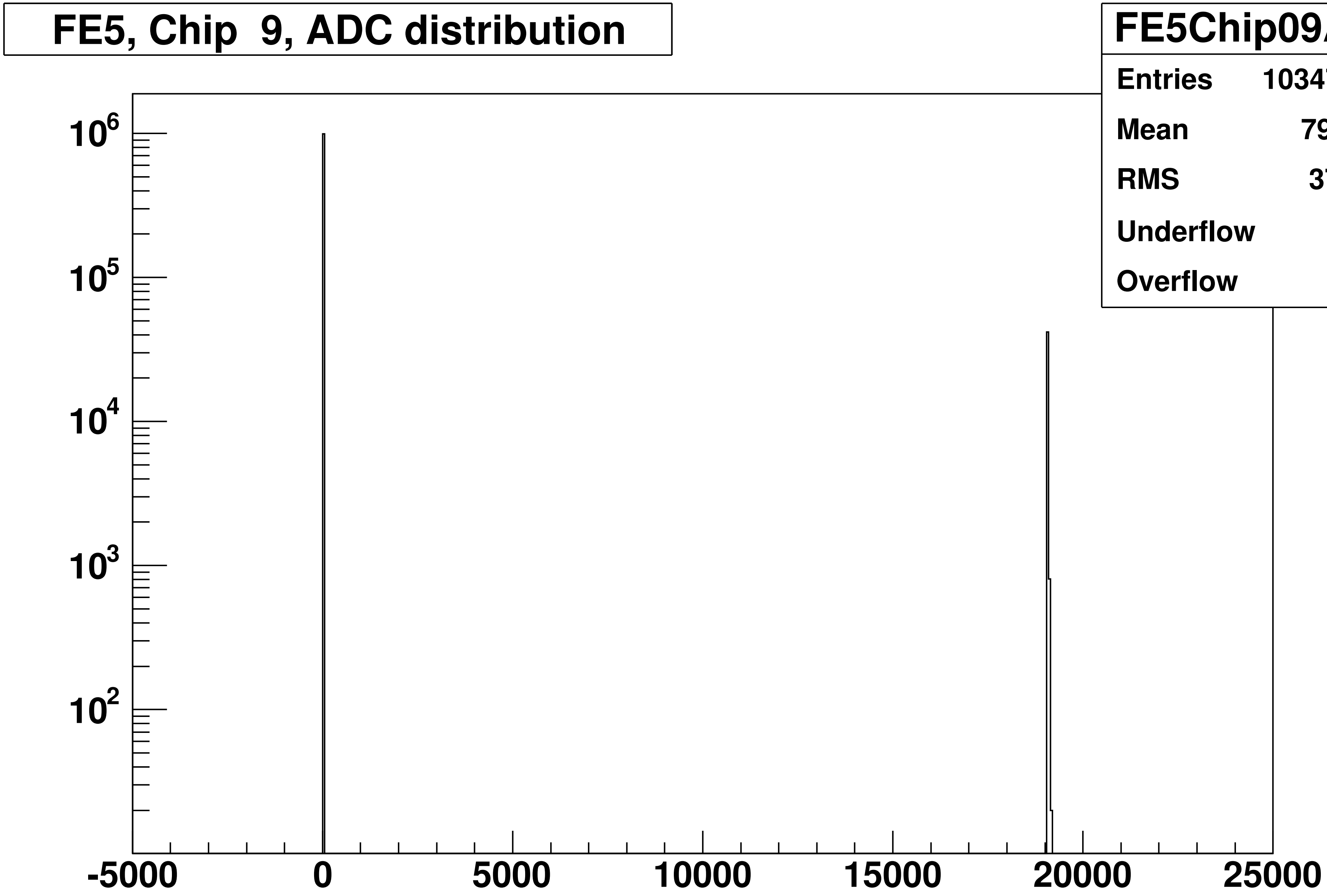
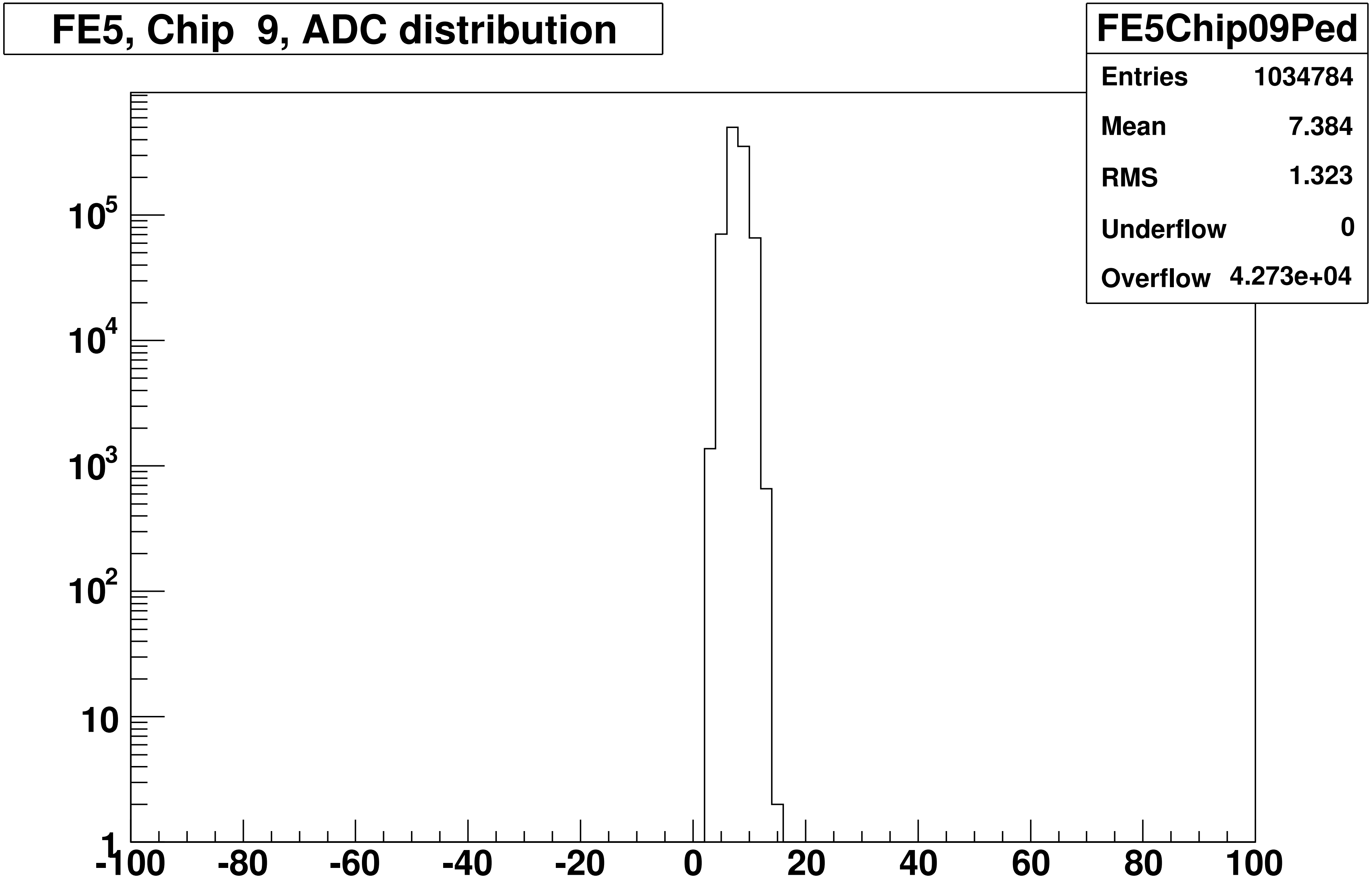
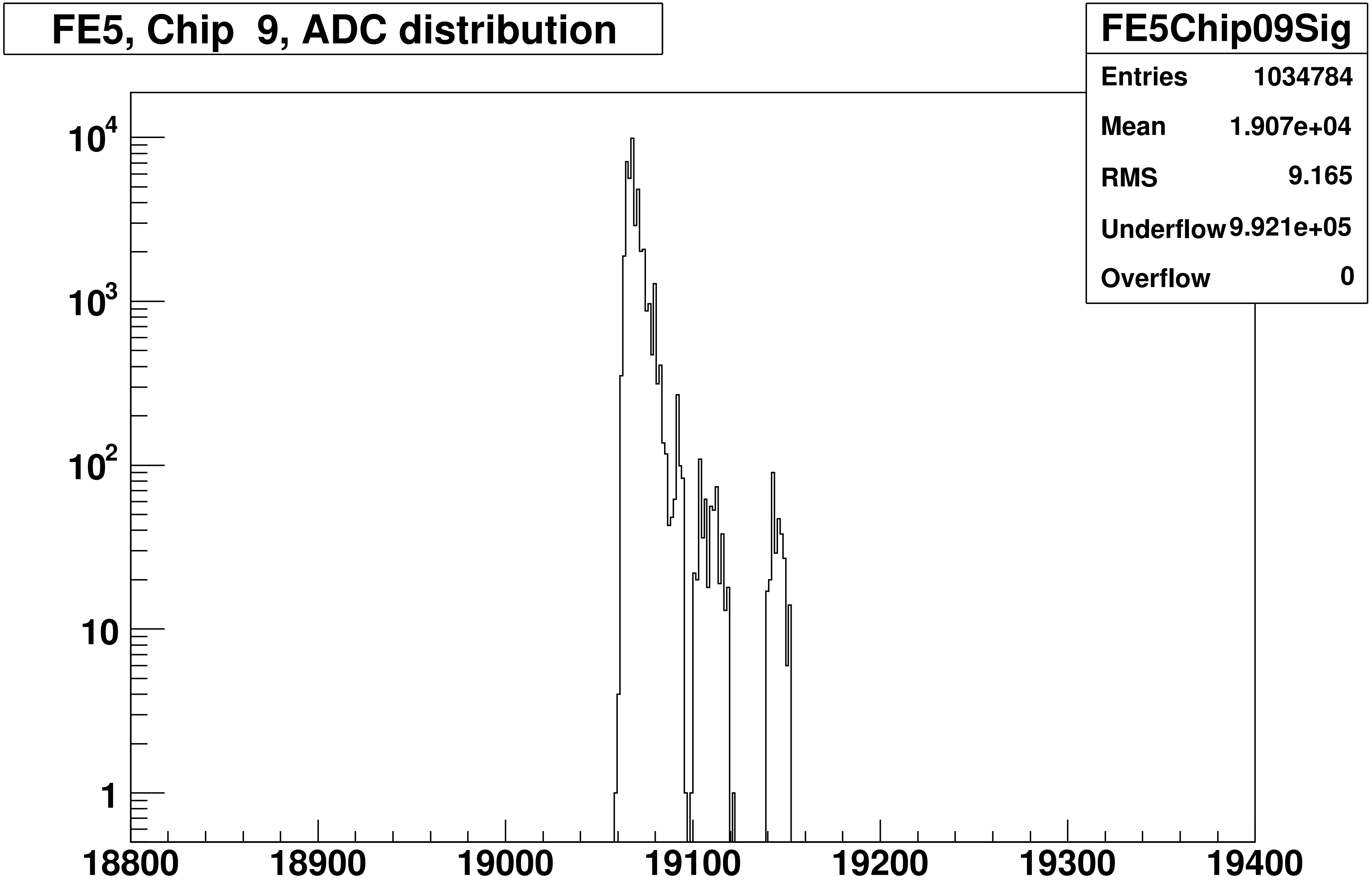


FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 1034784
Mean 794.6
RMS 3793
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 1034784
Mean 7.384
RMS 1.323
Underflow 0
Overflow 4.273e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 1034784
Mean 1.907e+04
RMS 9.165
Underflow 9.921e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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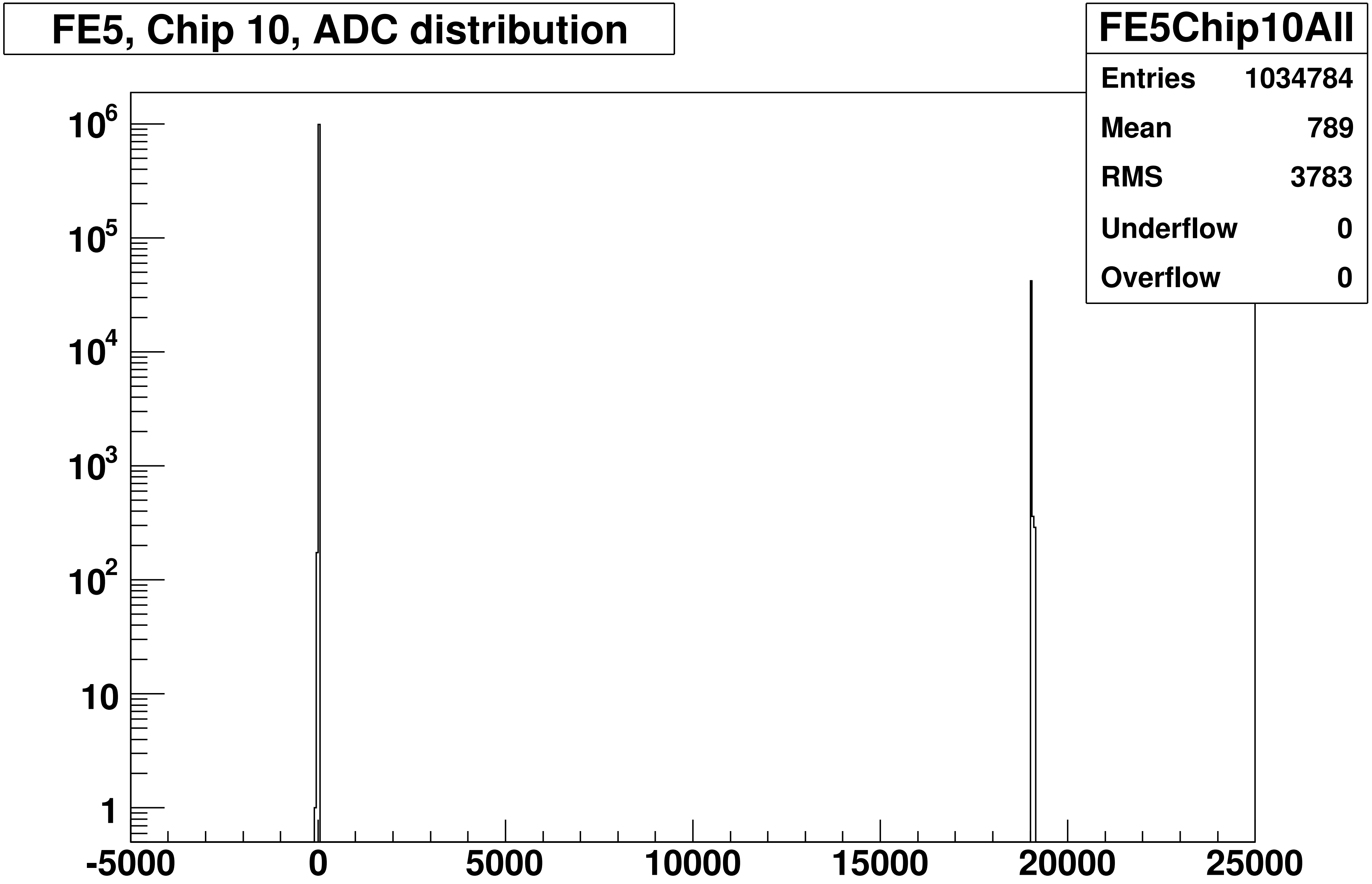
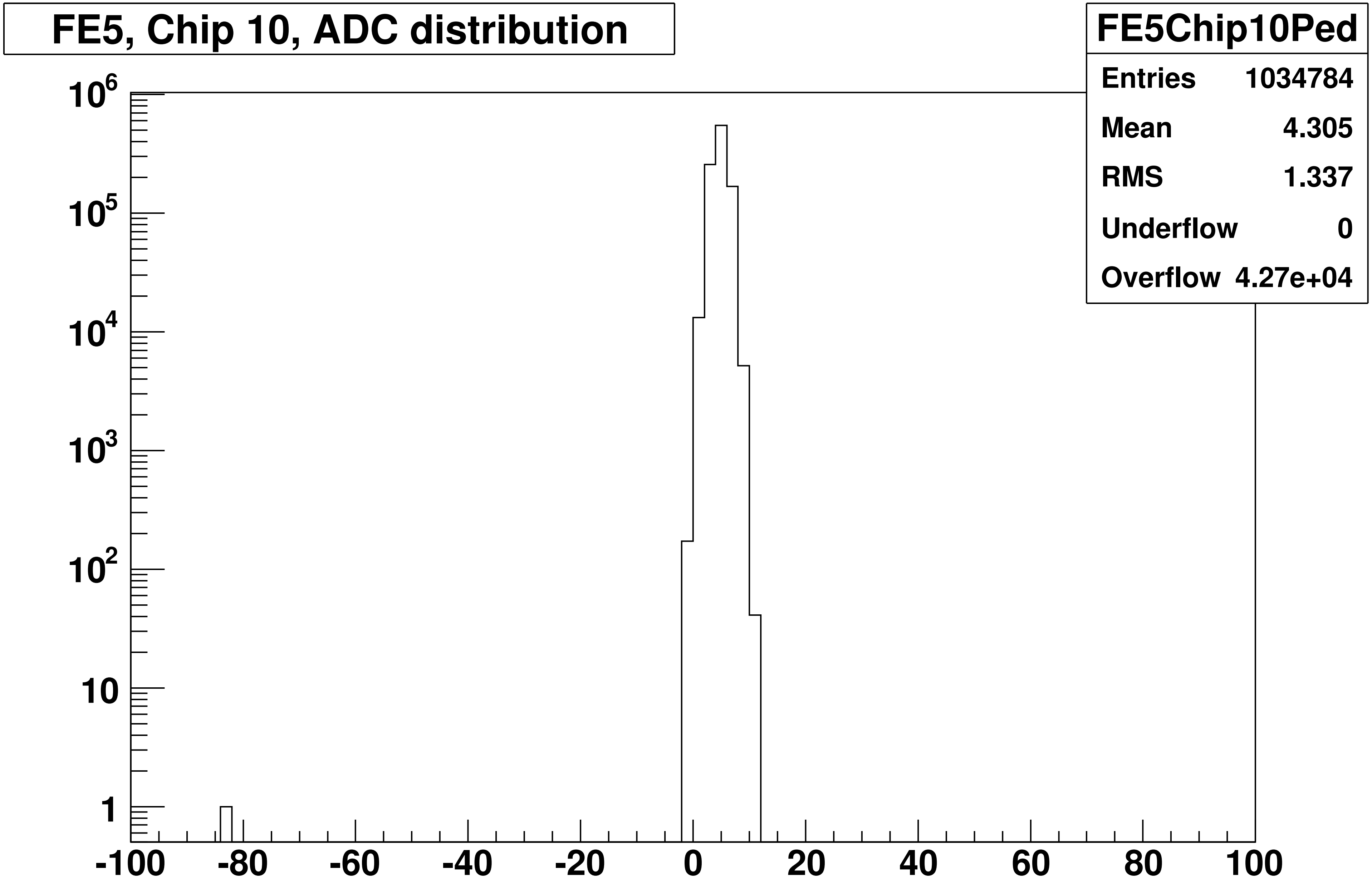
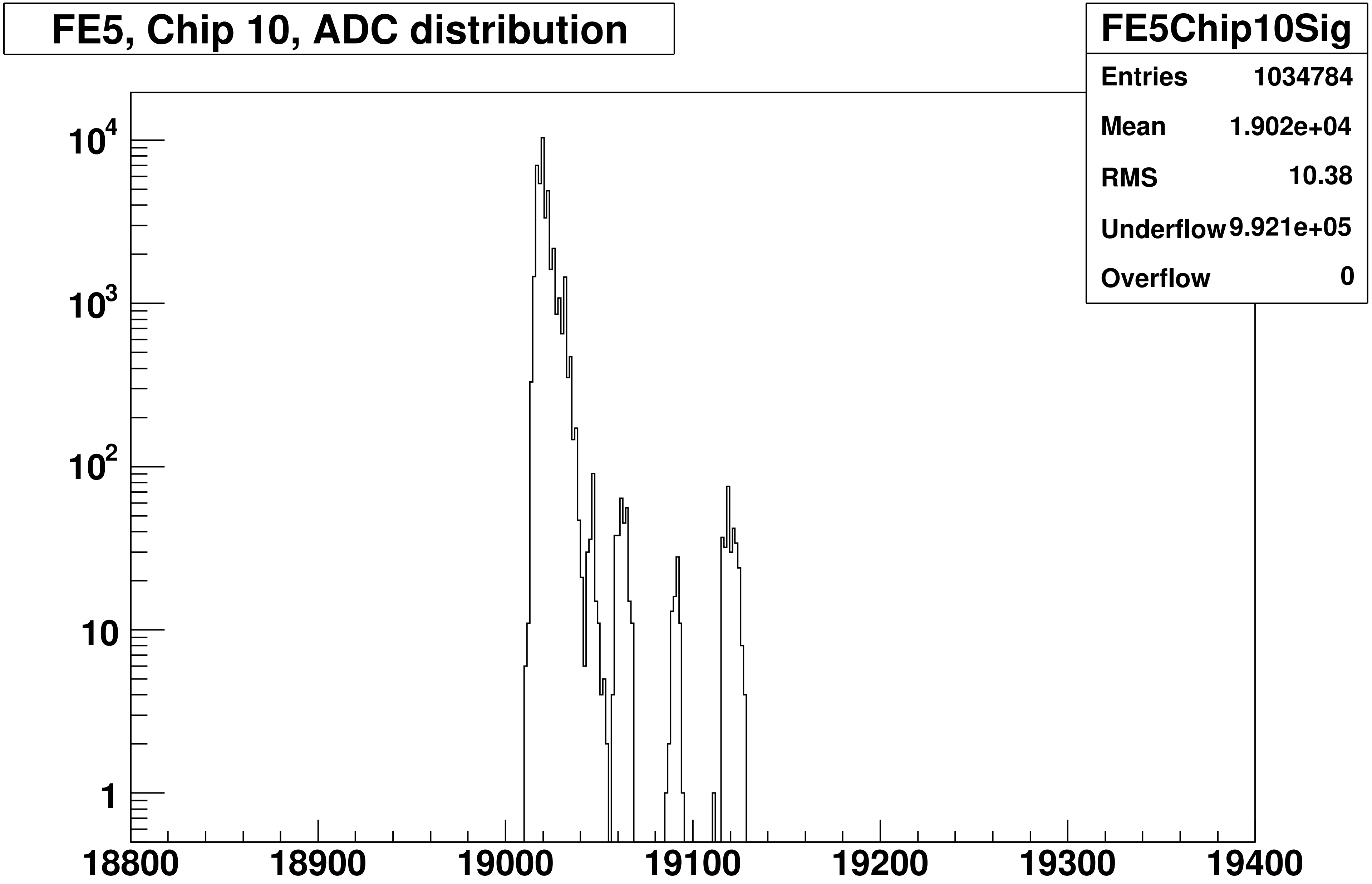
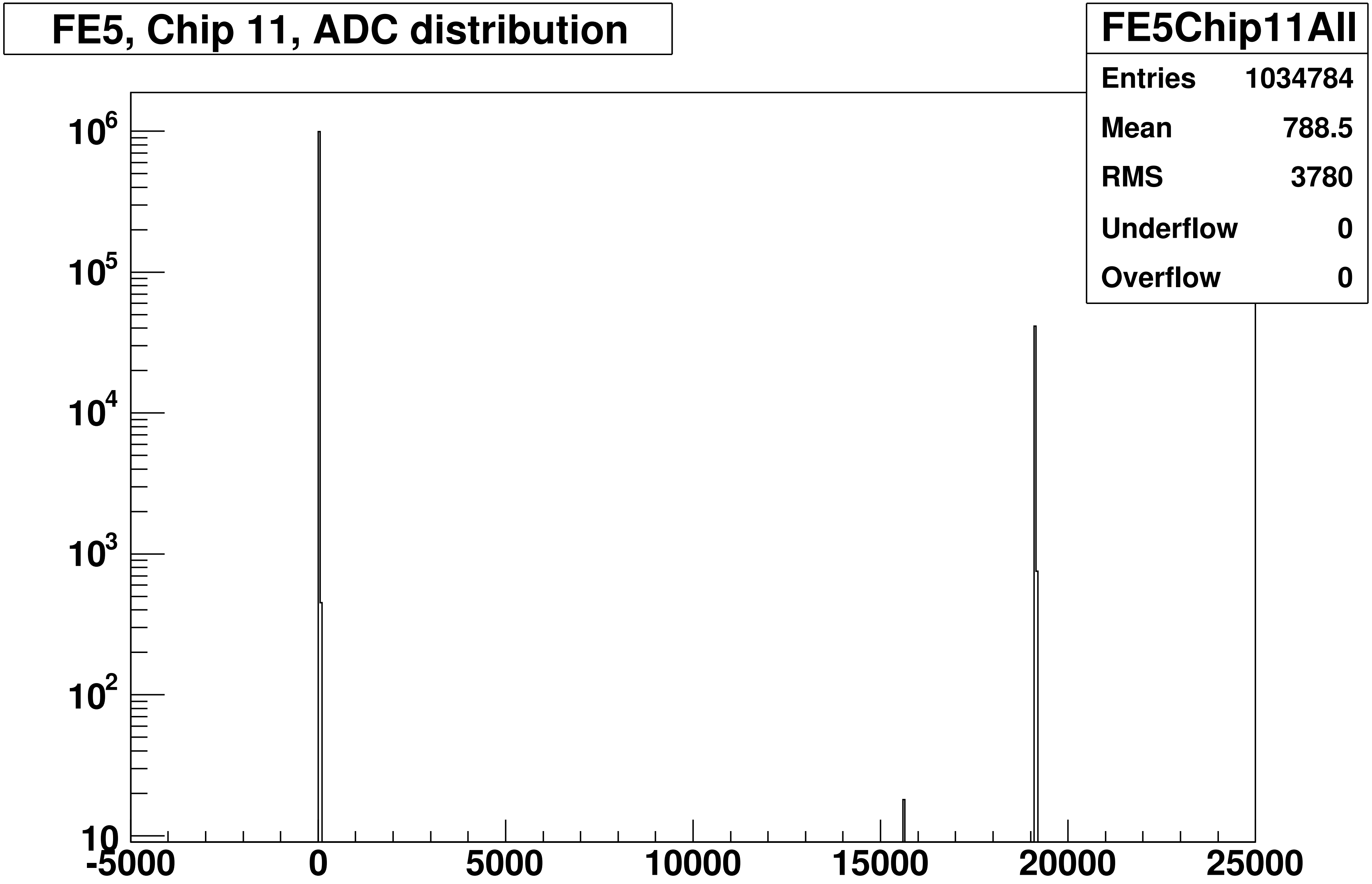
19100
19200
19300
19400


FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 1034784
Mean 789
RMS 3783
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 1034784
Mean 4.305
RMS 1.337
Underflow 0
Overflow 4.27e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 1034784
Mean 1.902e+04
RMS 10.38
Underflow 9.921e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 1034784
Mean 788.5
RMS 3780
Underflow 0
Overflow 0
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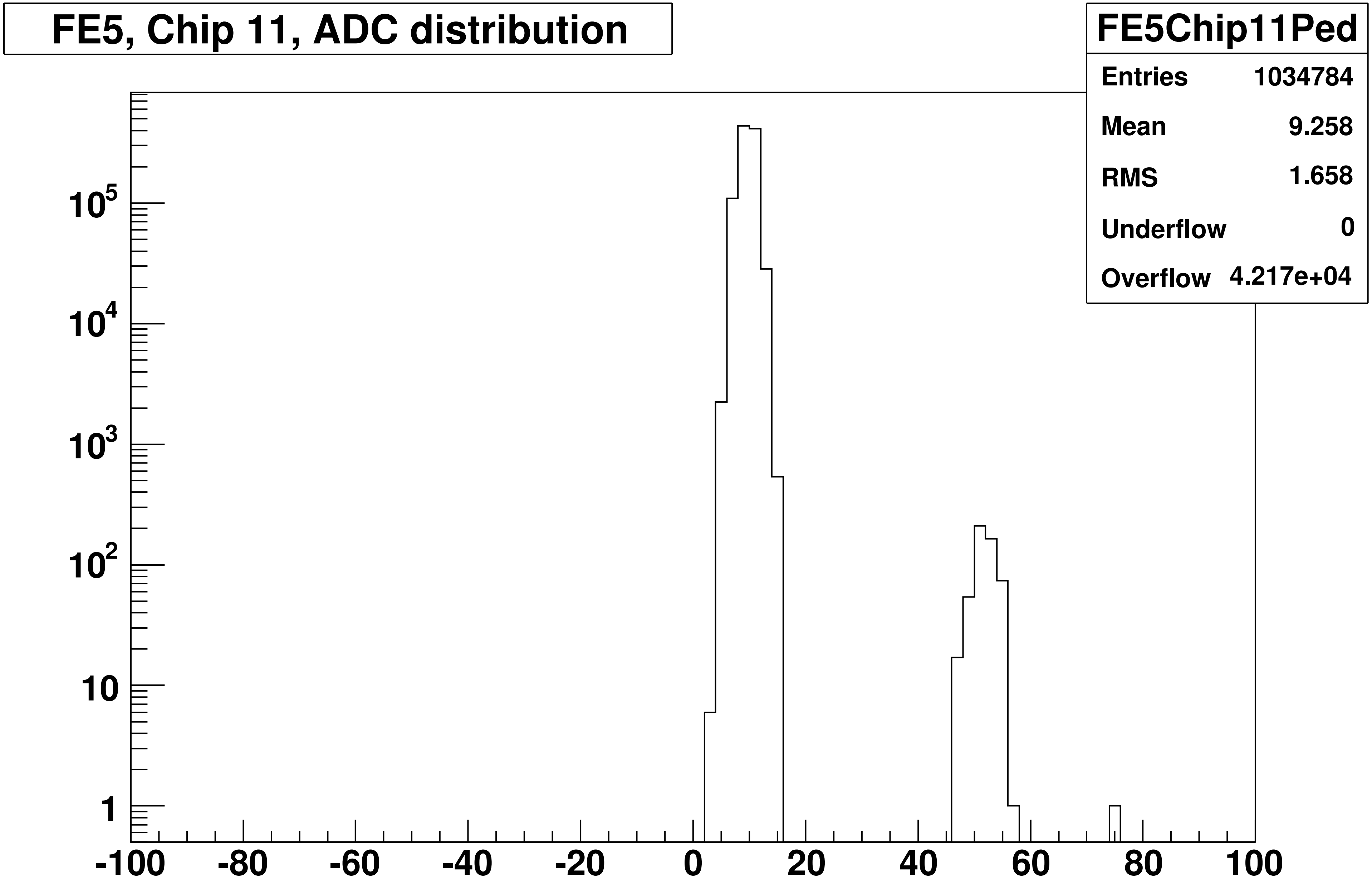
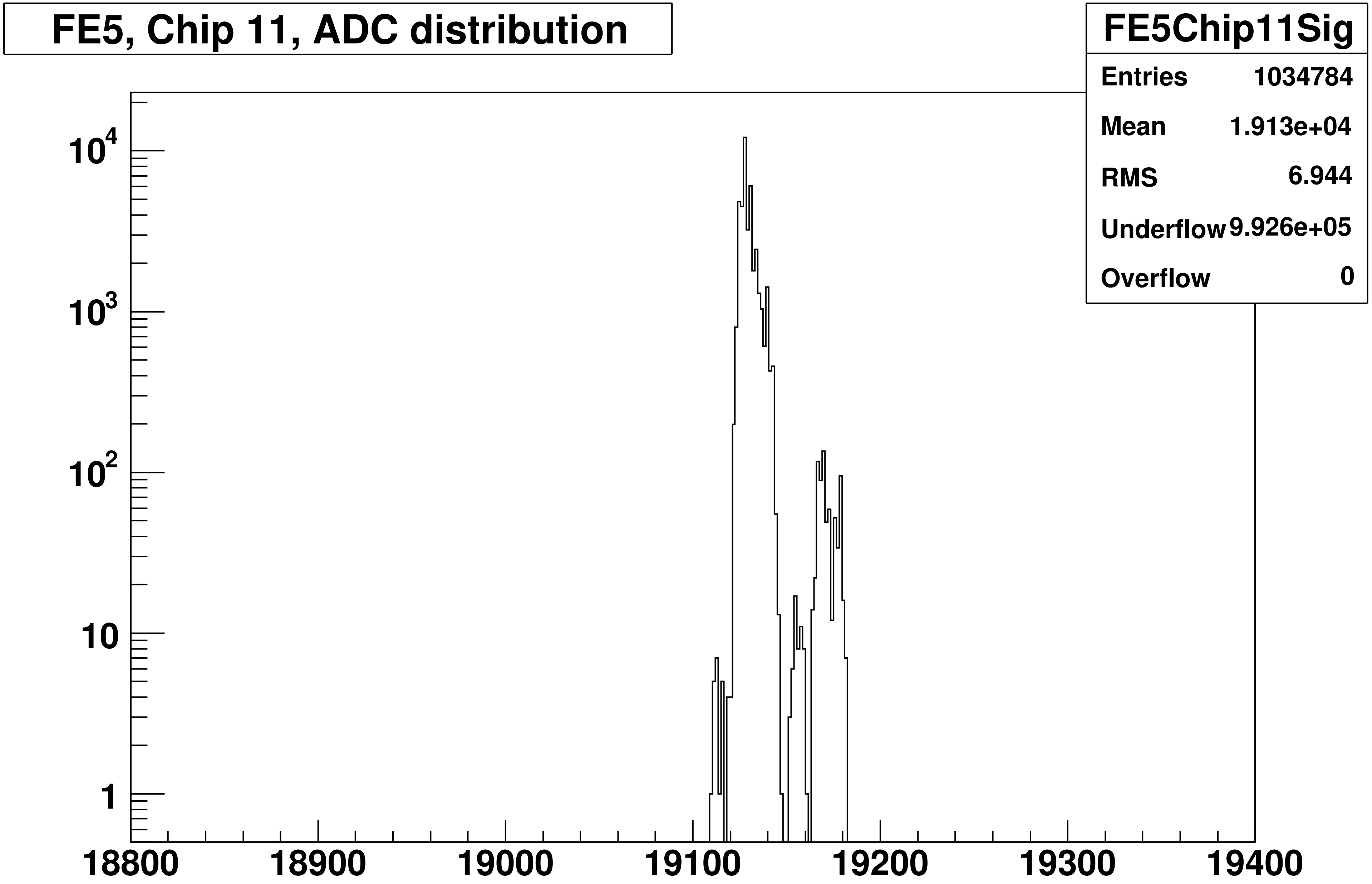
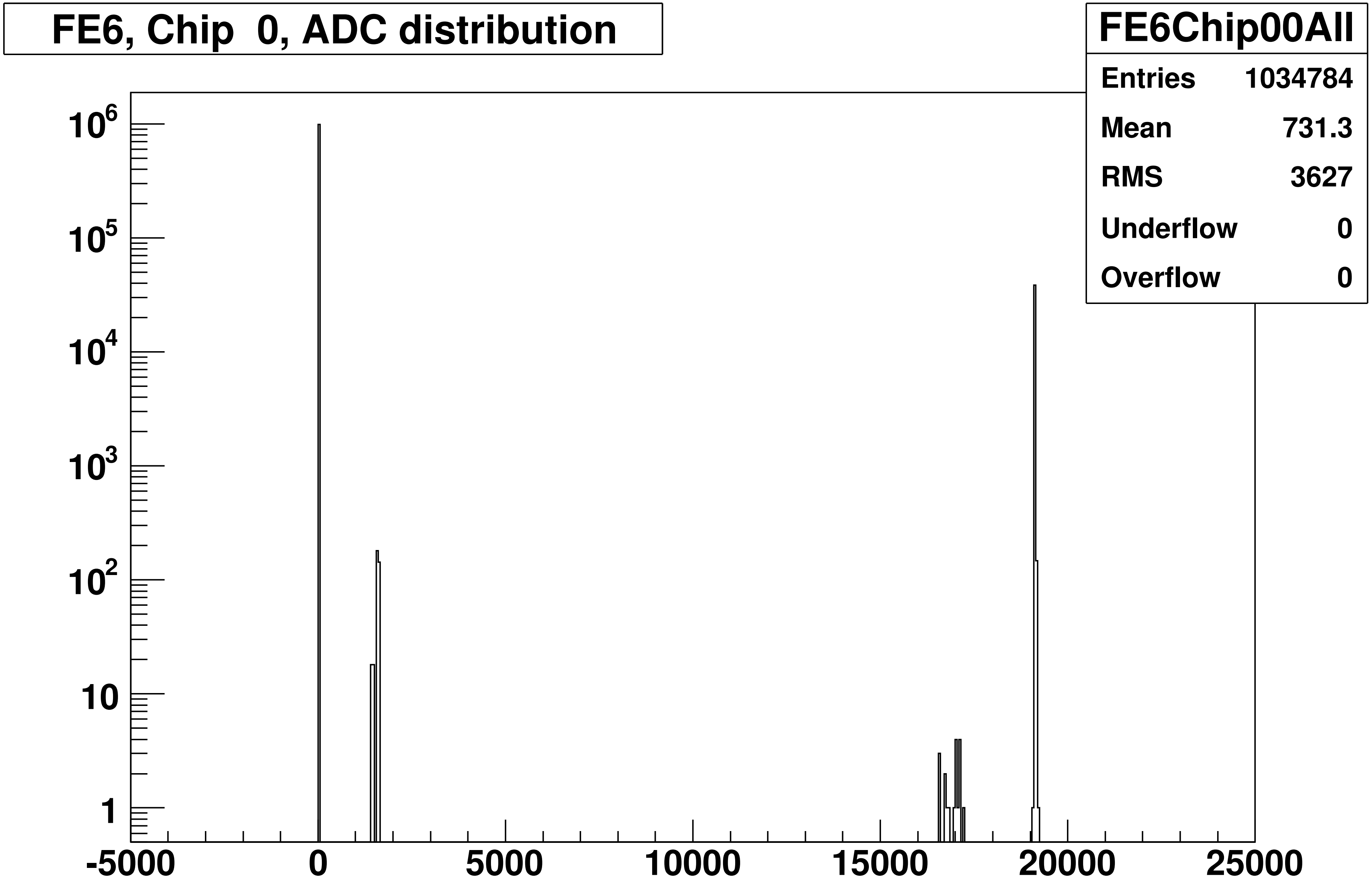
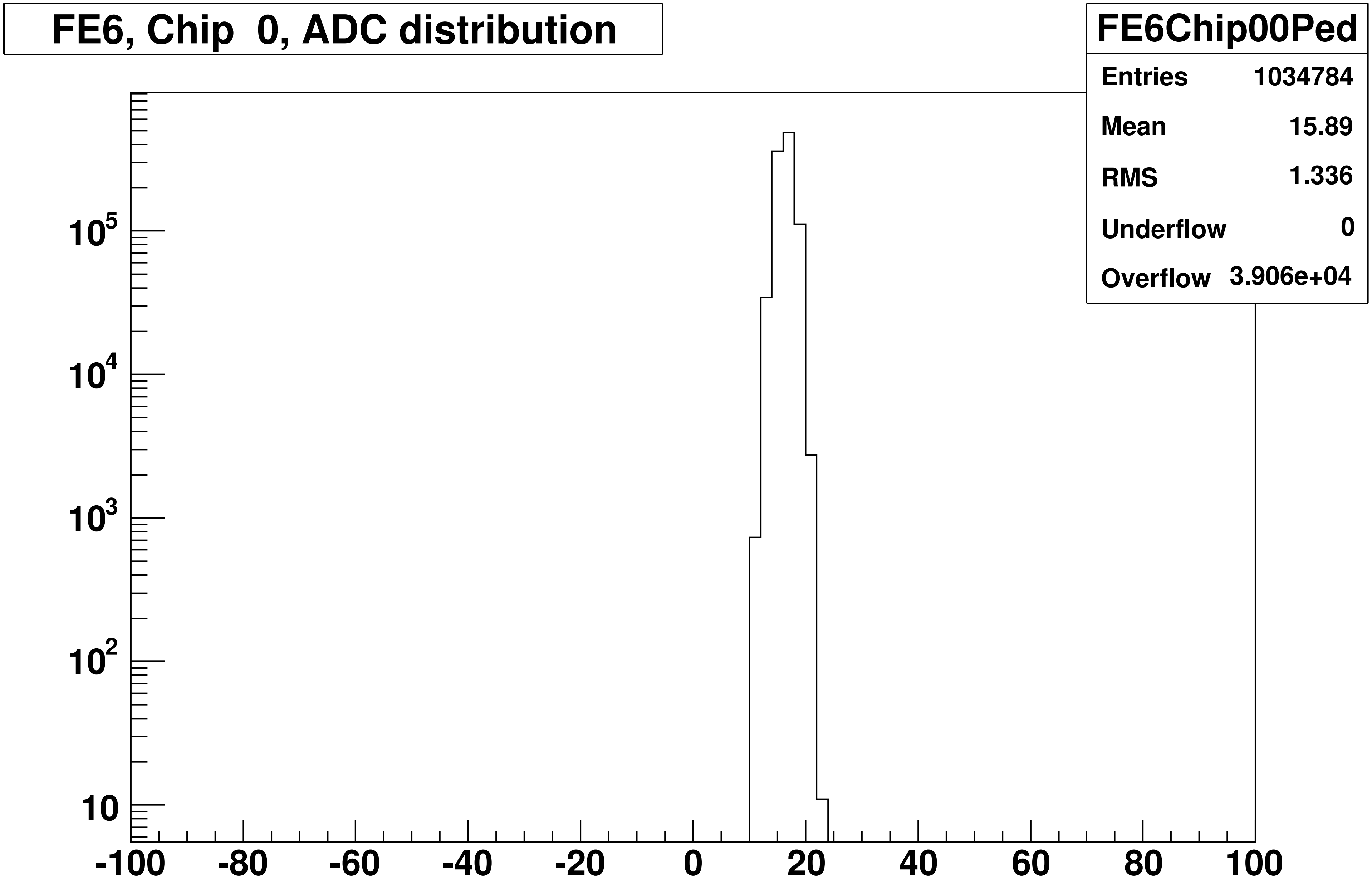
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



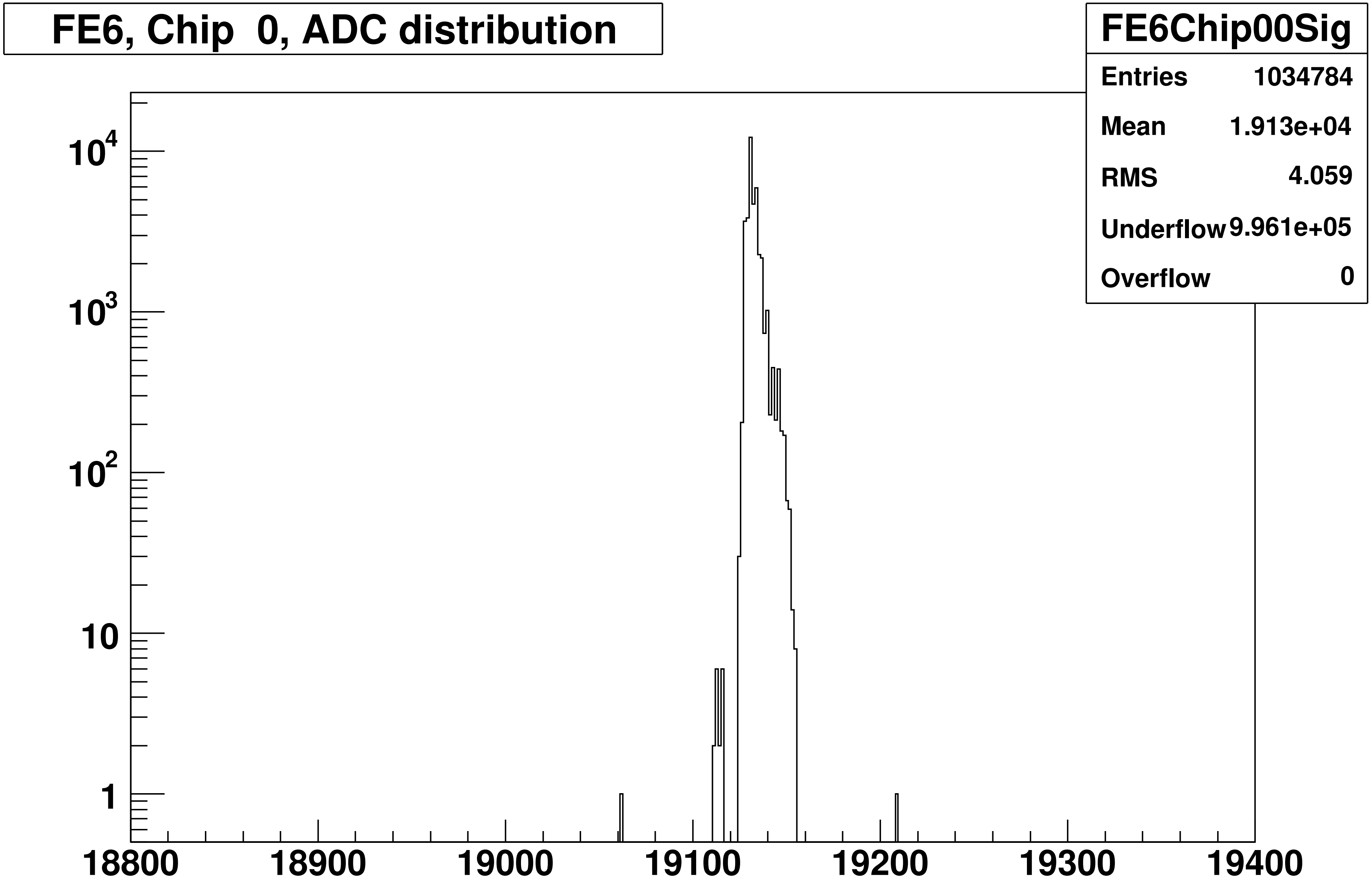
FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 1034784
Mean 9.258
RMS 1.658
Underflow 0
Overflow 4.217e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 1034784
Mean 1.913e+04
RMS 6.944
Underflow 9.926e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 1034784
Mean 731.3
RMS 3627
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 1034784
Mean 15.89
RMS 1.336
Underflow 0
Overflow 3.906e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 1034784
Mean 1.913e+04
RMS 4.059
Underflow 9.961e+05
Overflow 0
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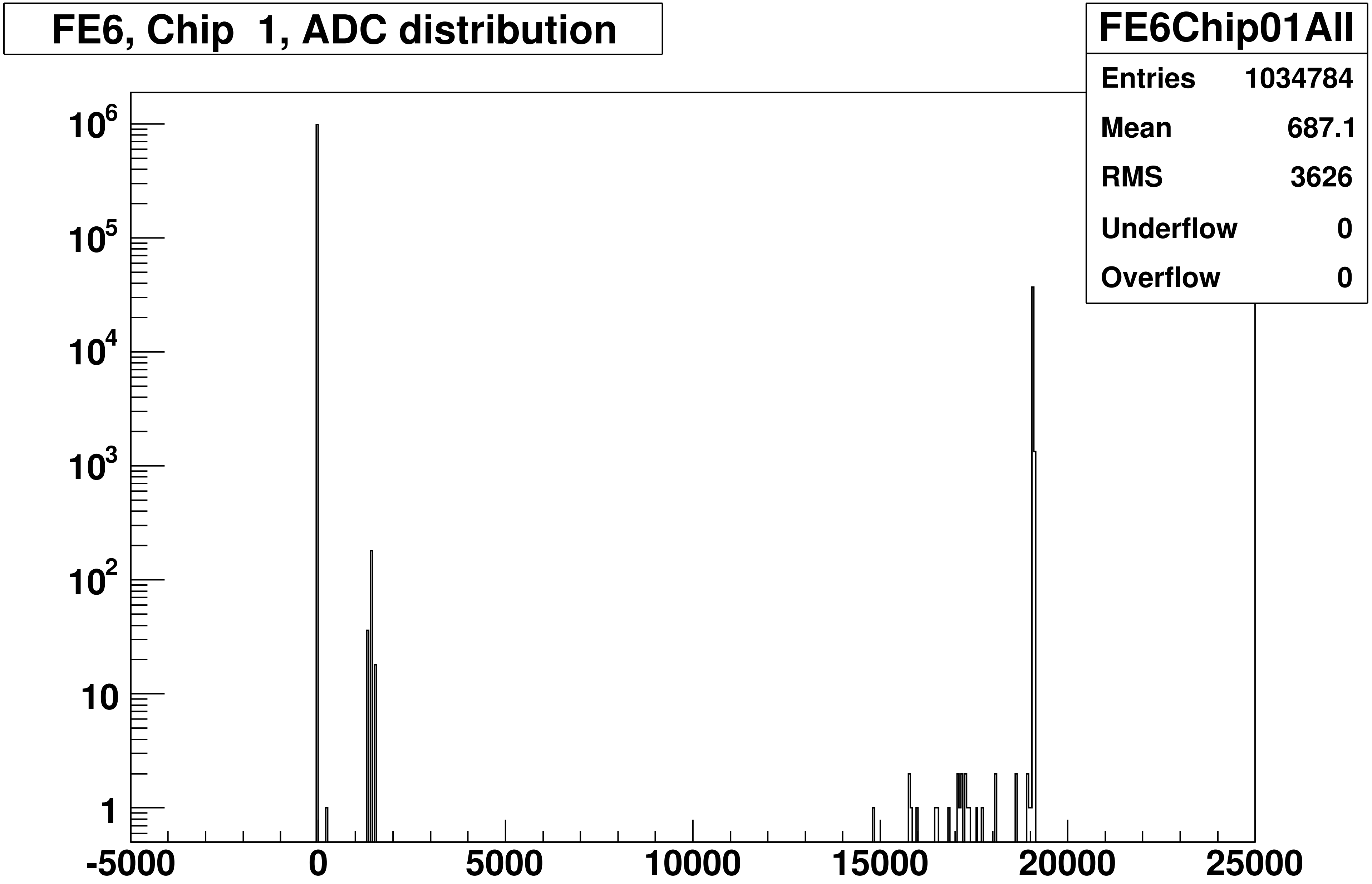
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 1034784
Mean 687.1
RMS 3626
Underflow 0
Overflow 0
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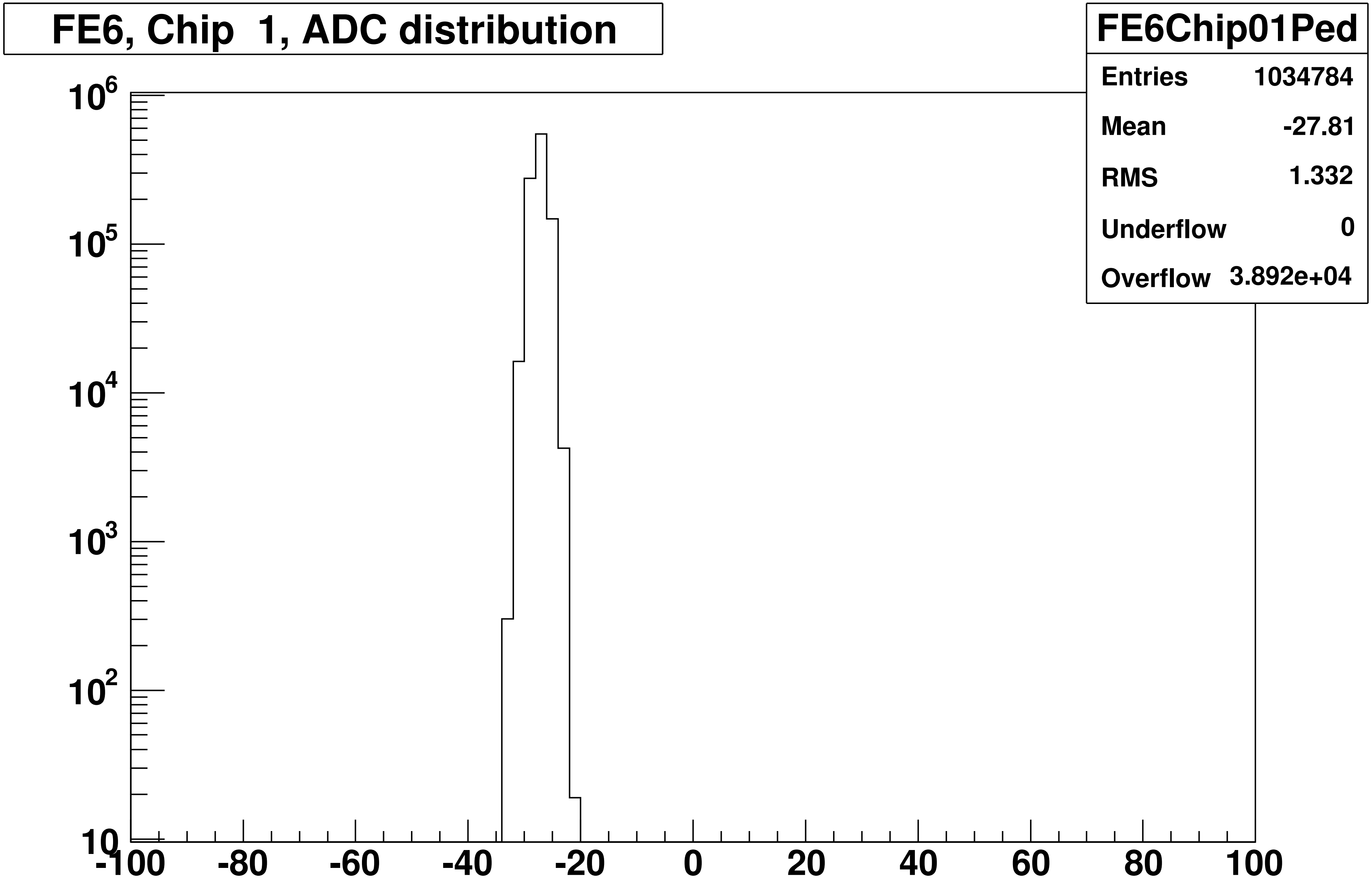
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 1034784
Mean -27.81
RMS 1.332
Underflow 0
Overflow 3.892e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
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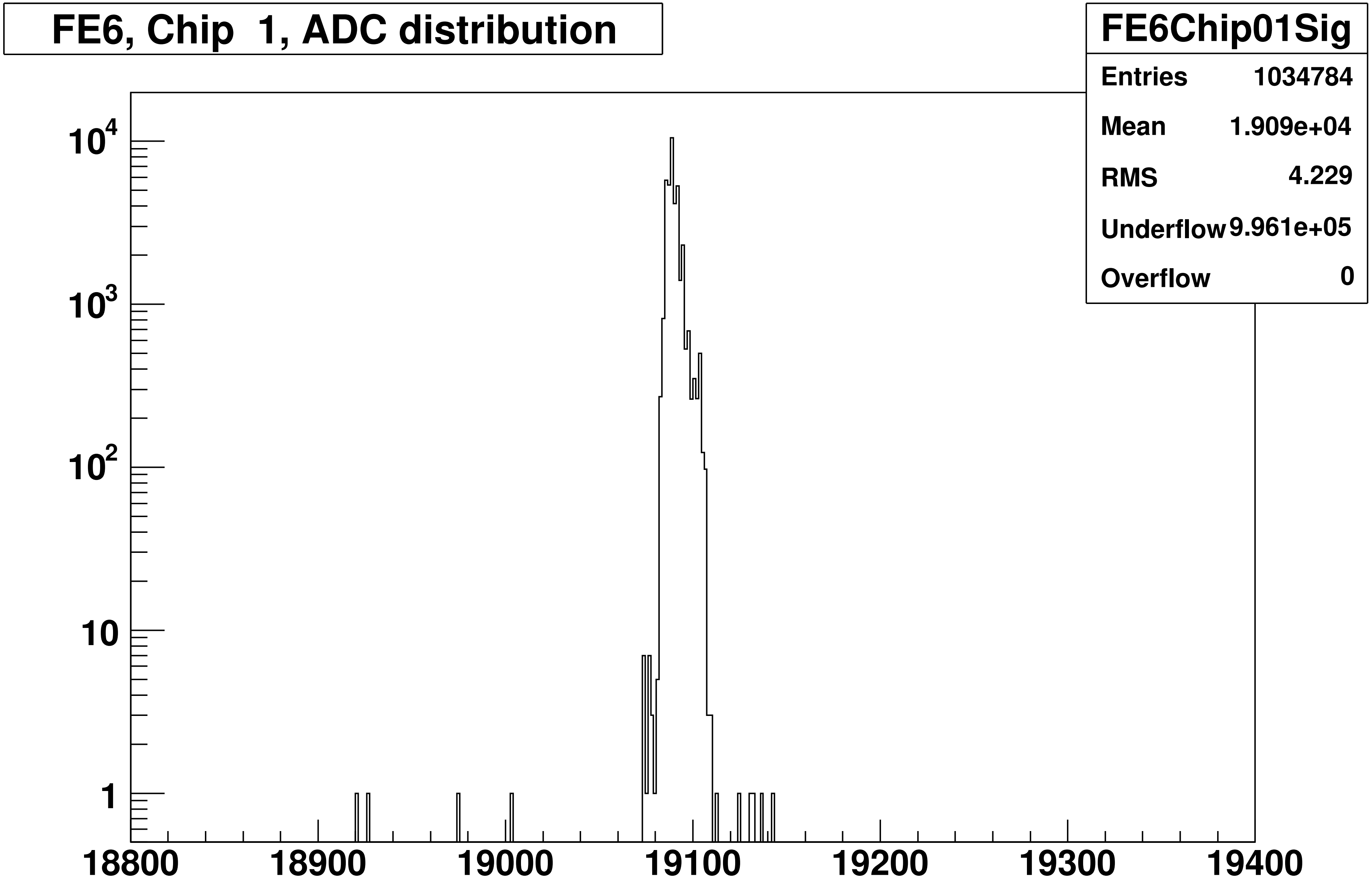
FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 1034784
Mean 1.909e+04
RMS 4.229
Underflow 9.961e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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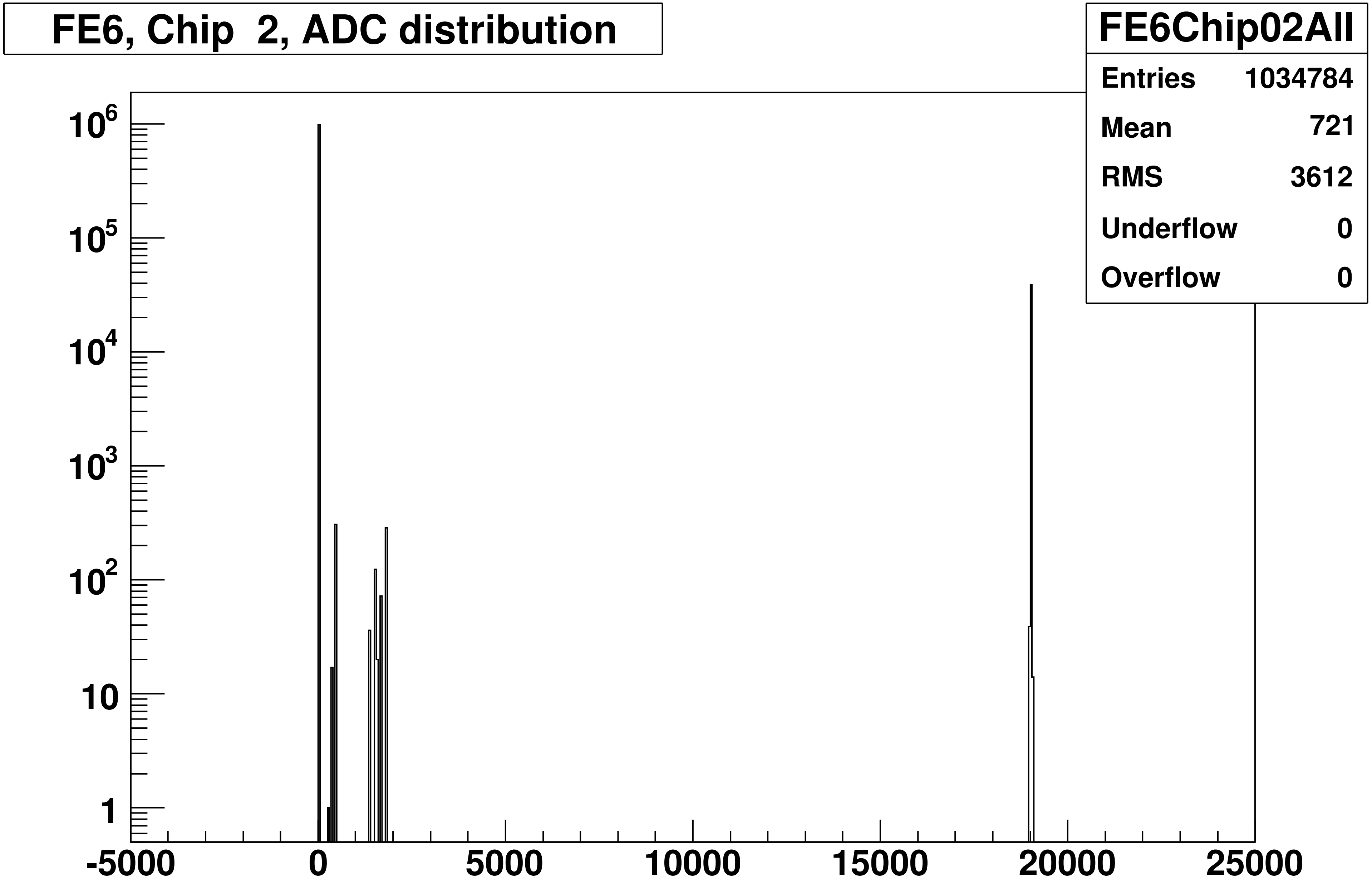
FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 1034784
Mean 721
RMS 3612
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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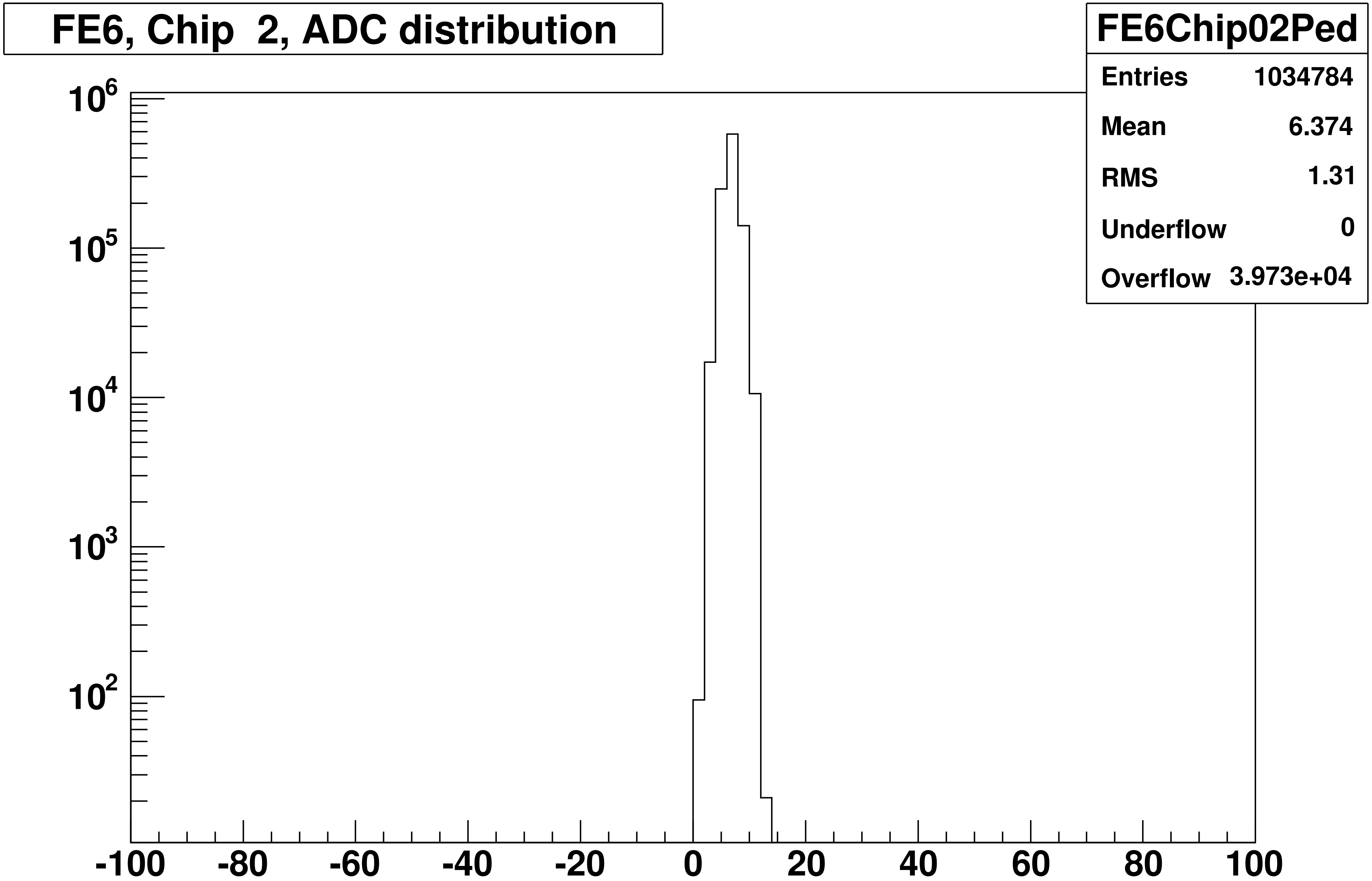
25000


FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 1034784
Mean 6.374
RMS 1.31
Underflow 0
Overflow 3.973e+04
10^6
10^5
10^4
10^3
10^2
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-80
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-20
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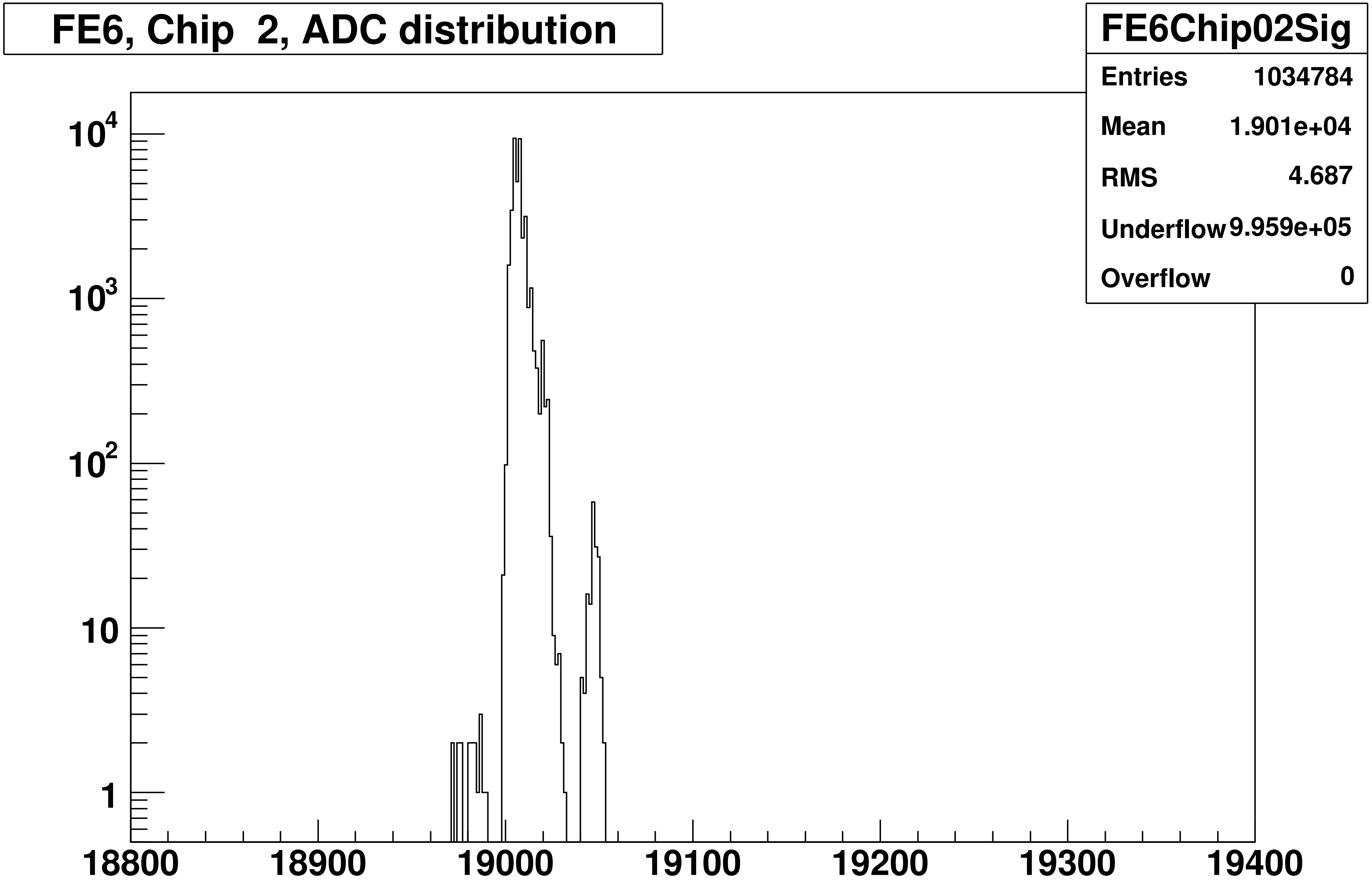
80
100



FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 1034784
Mean 1.901e+04
RMS 4.687
Underflow 9.959e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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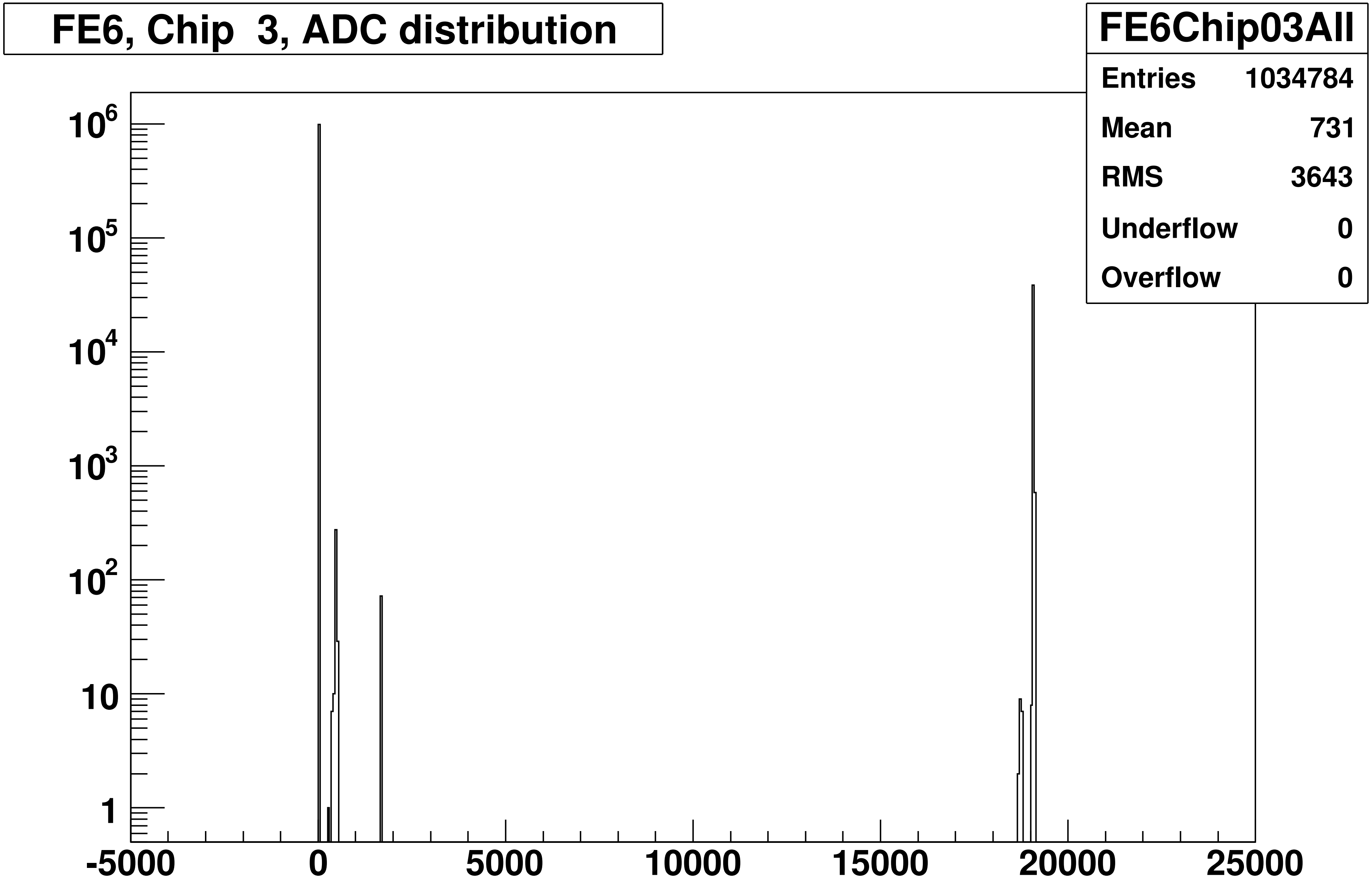
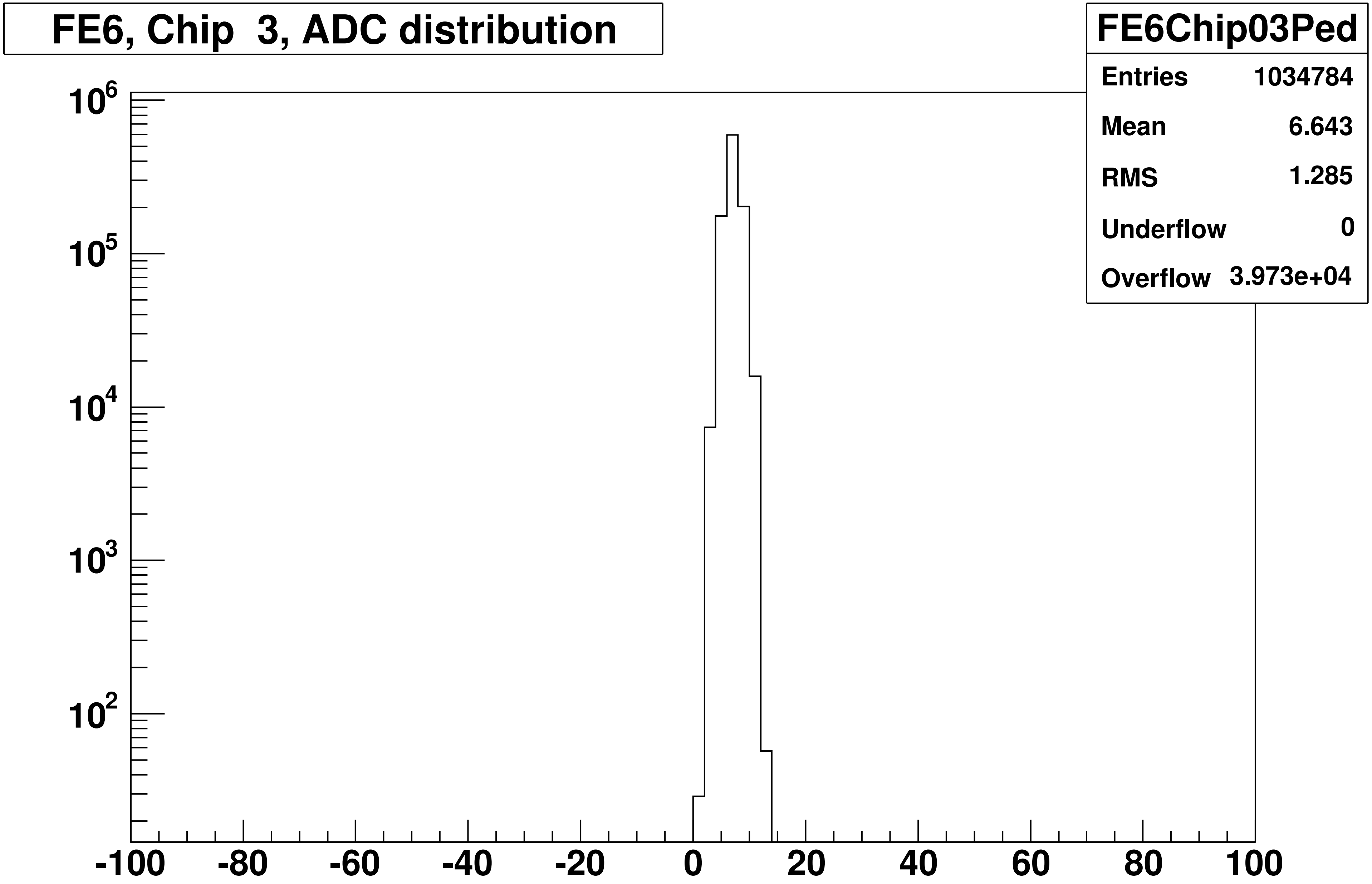


FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 1034784
Mean 731
RMS 3643
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 1034784
Mean 6.643
RMS 1.285
Underflow 0
Overflow 3.973e+04
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10^5
10^4
10^3
10^2
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-40
-20
0
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40
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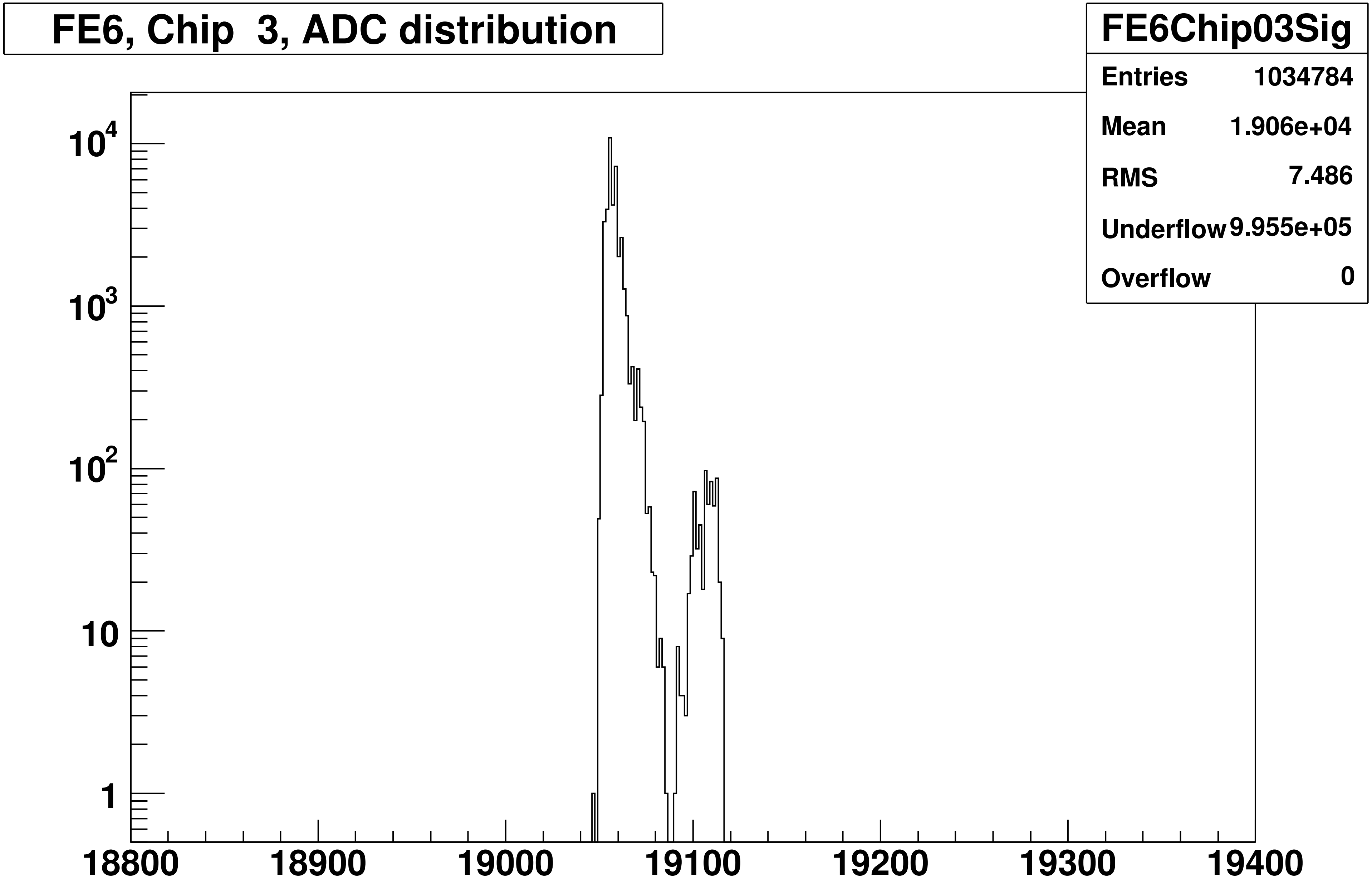
80
100


FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 1034784
Mean 1.906e+04
RMS 7.486
Underflow 9.955e+05
Overflow 0
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10^3
10^2
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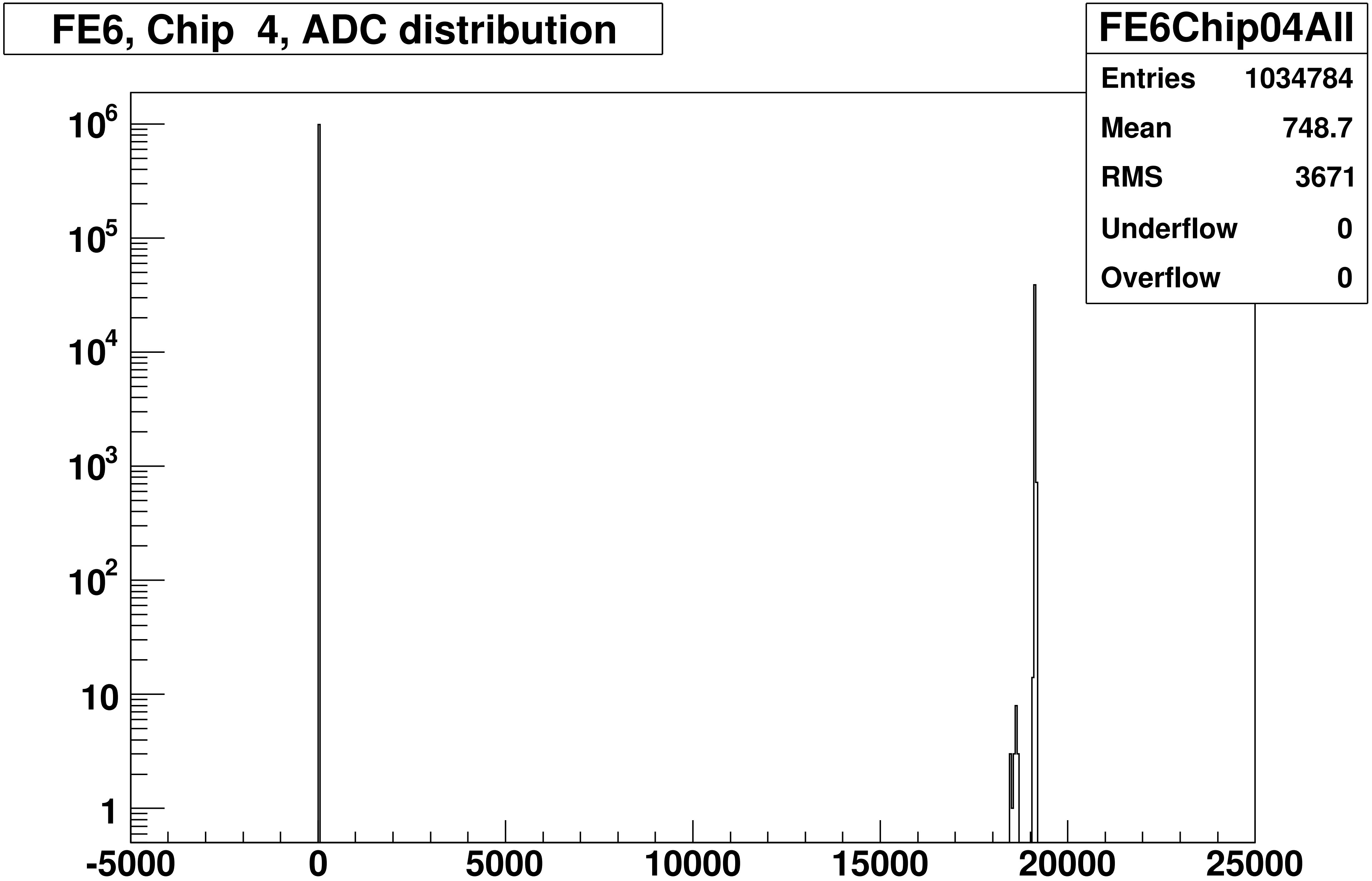

FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 1034784
Mean 748.7
RMS 3671
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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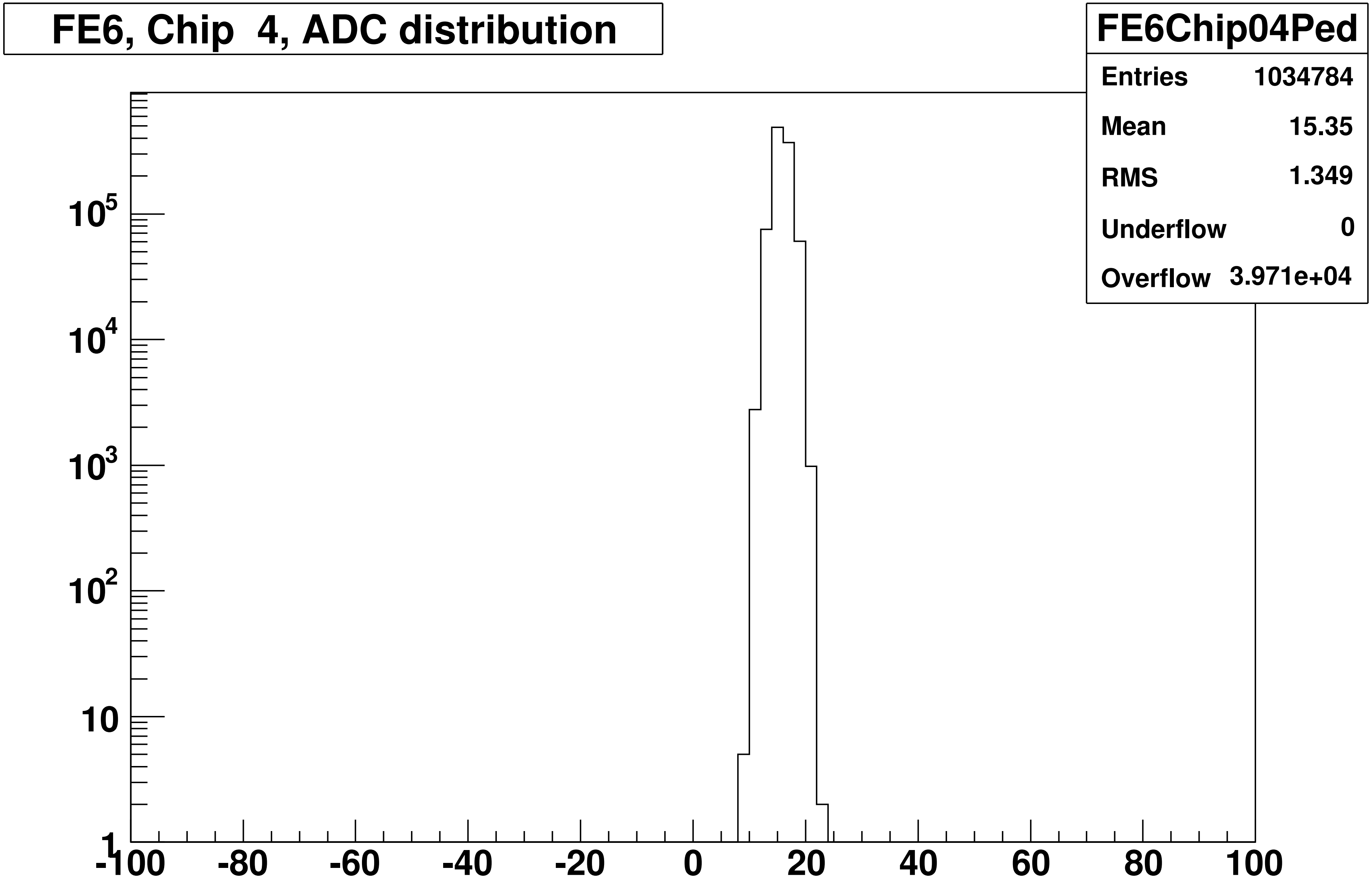
25000


FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 1034784
Mean 15.35
RMS 1.349
Underflow 0
Overflow 3.971e+04
10^5
10^4
10^3
10^2
10
1
-100
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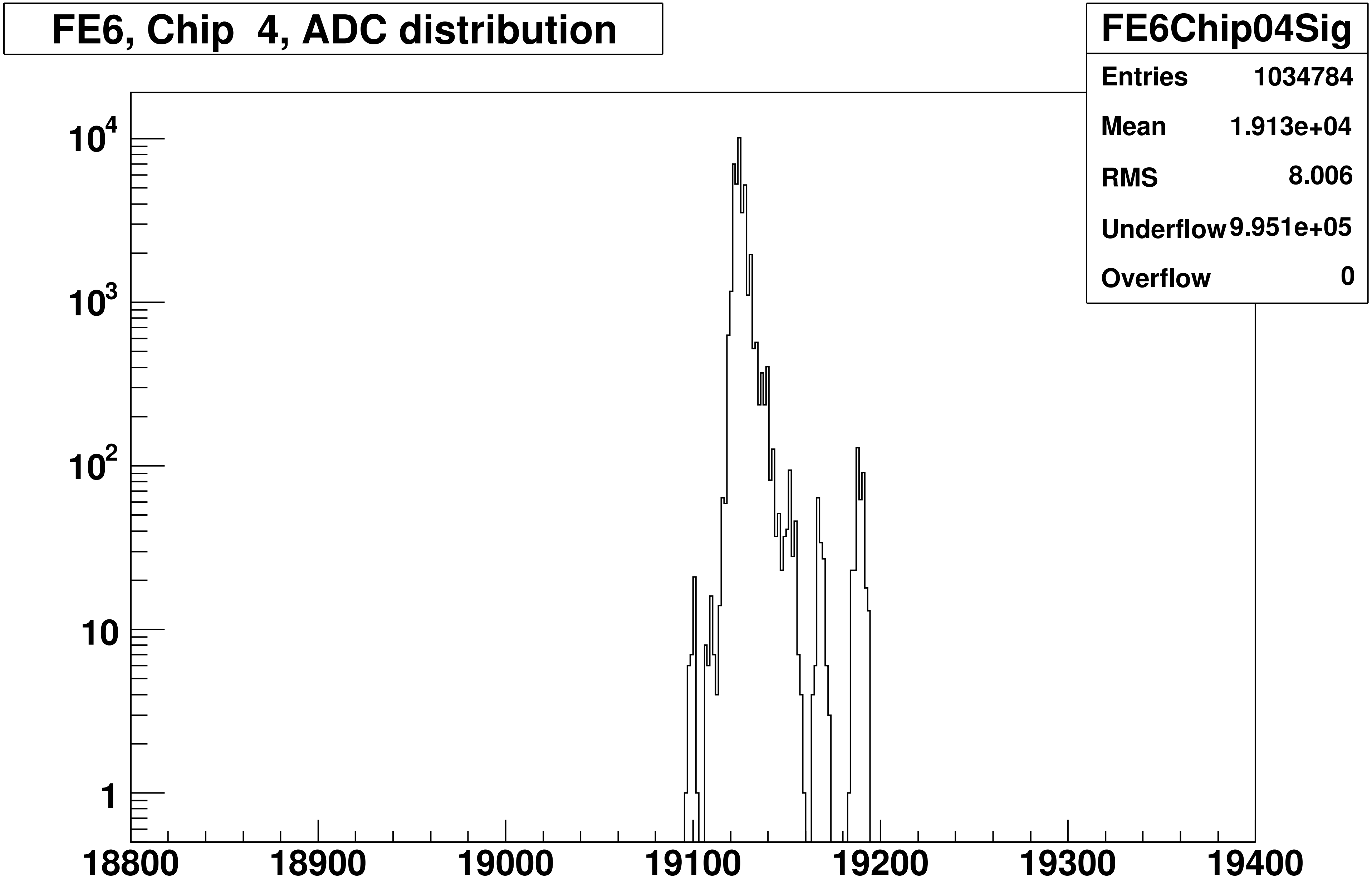
FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 1034784
Mean 1.913e+04
RMS 8.006
Underflow 9.951e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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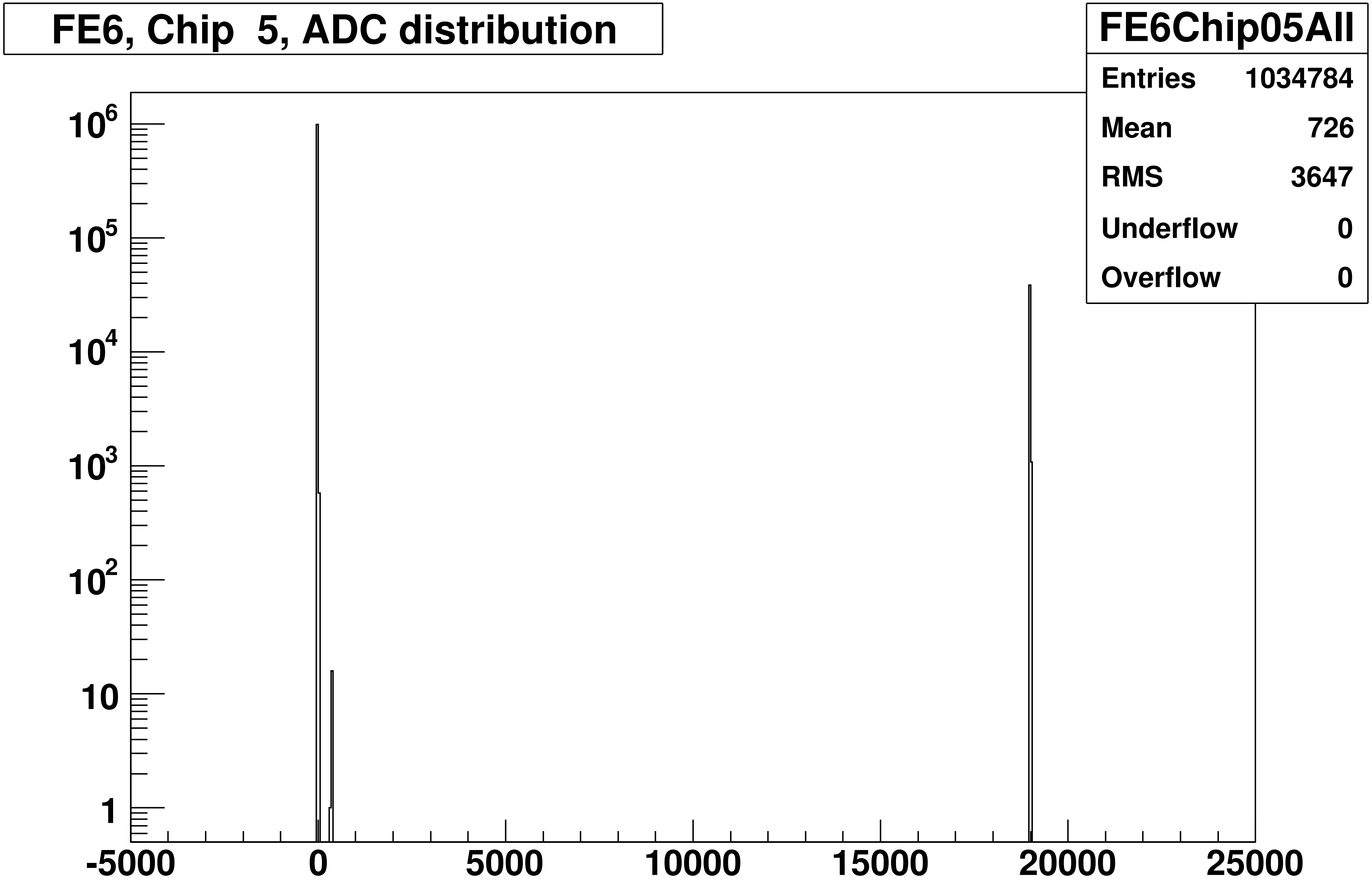
FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 1034784
Mean 726
RMS 3647
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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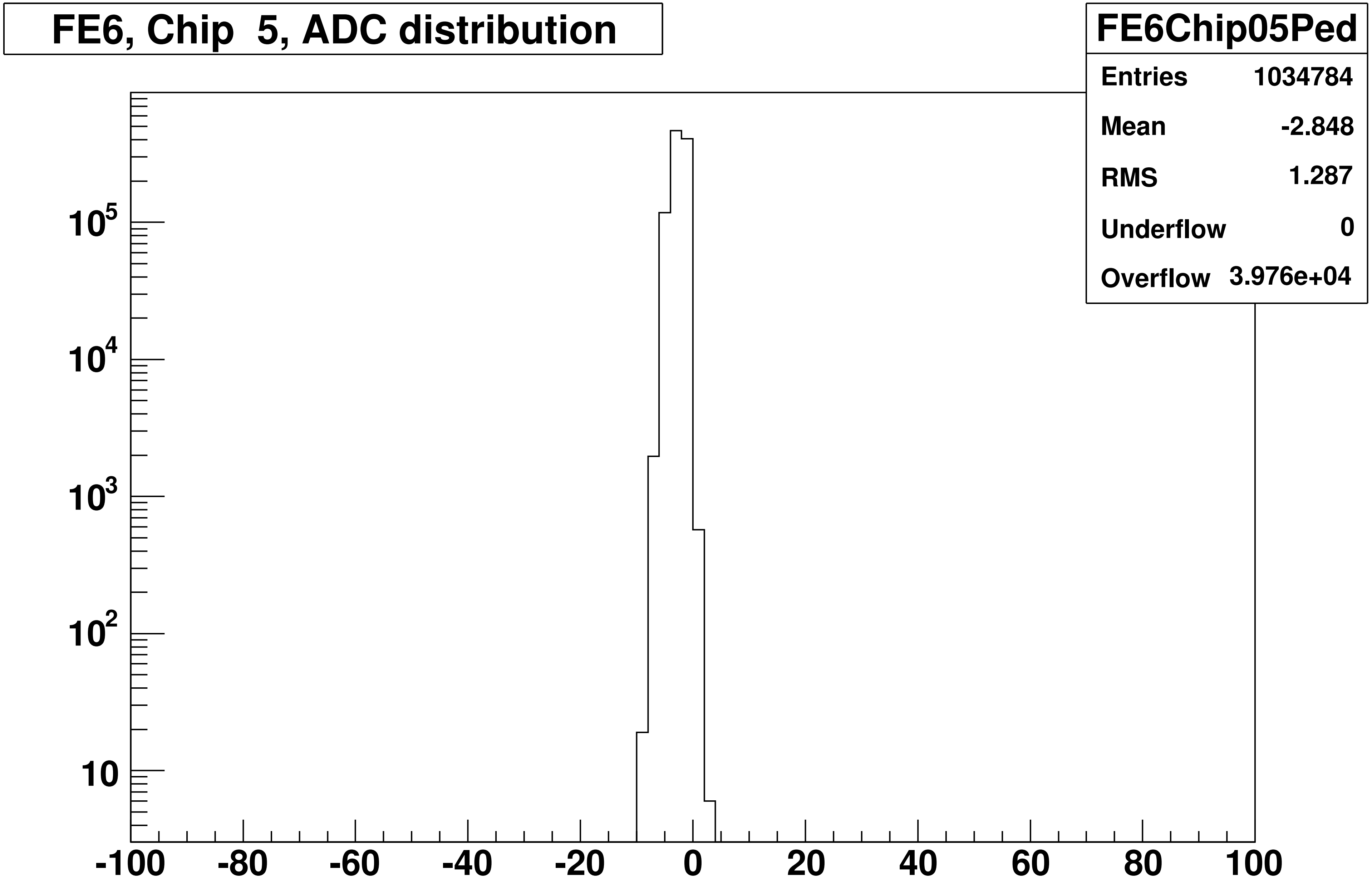
25000



FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 1034784
Mean -2.848
RMS 1.287
Underflow 0
Overflow 3.976e+04
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10^2
10^3
10^4
10^5
-100
-80
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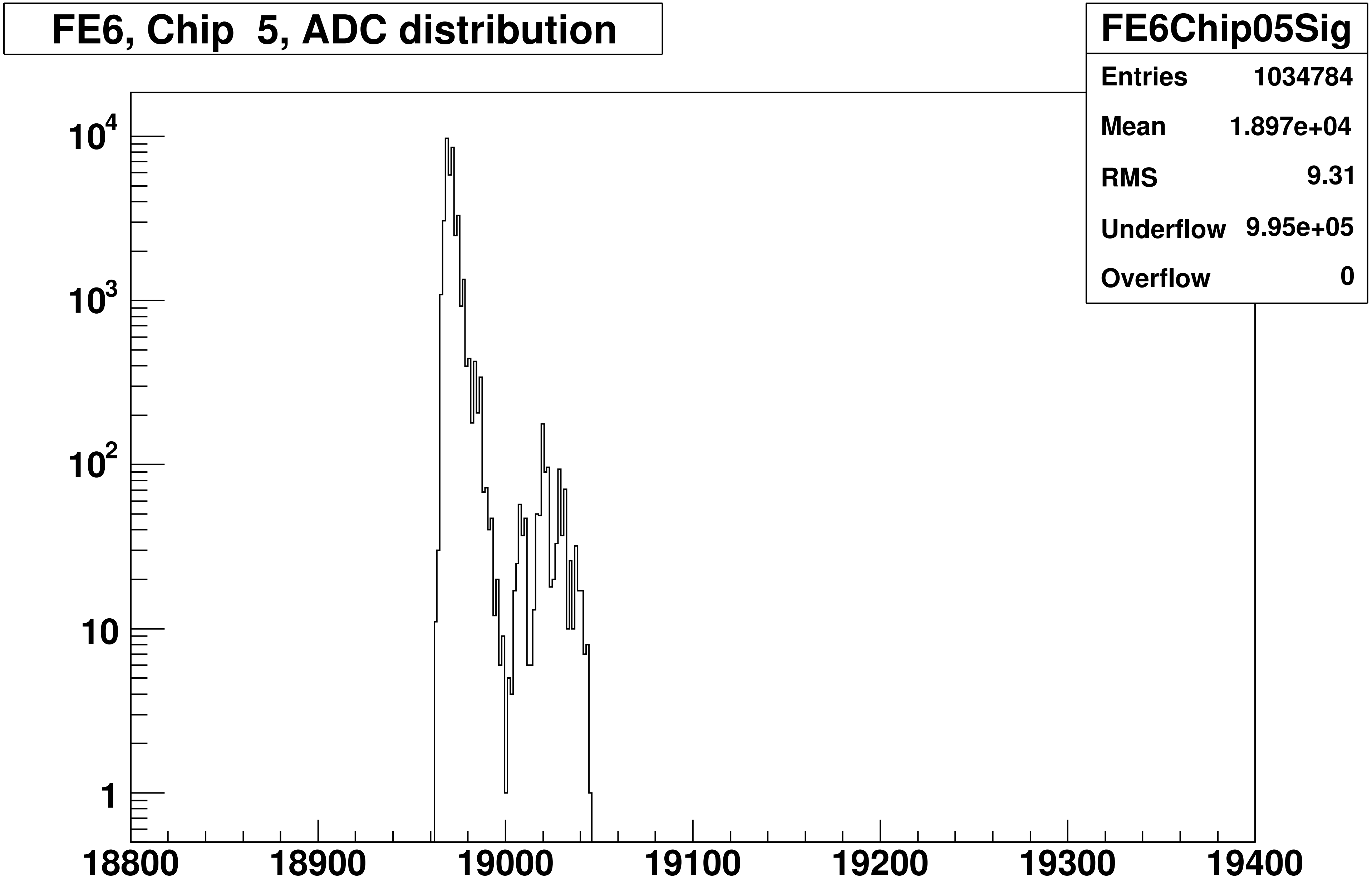
-40
-20
0
20
40
60
80
100



FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 1034784
Mean 1.897e+04
RMS 9.31
Underflow 9.95e+05
Overflow 0
10^4
10^3
10^2
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1
18800
18900
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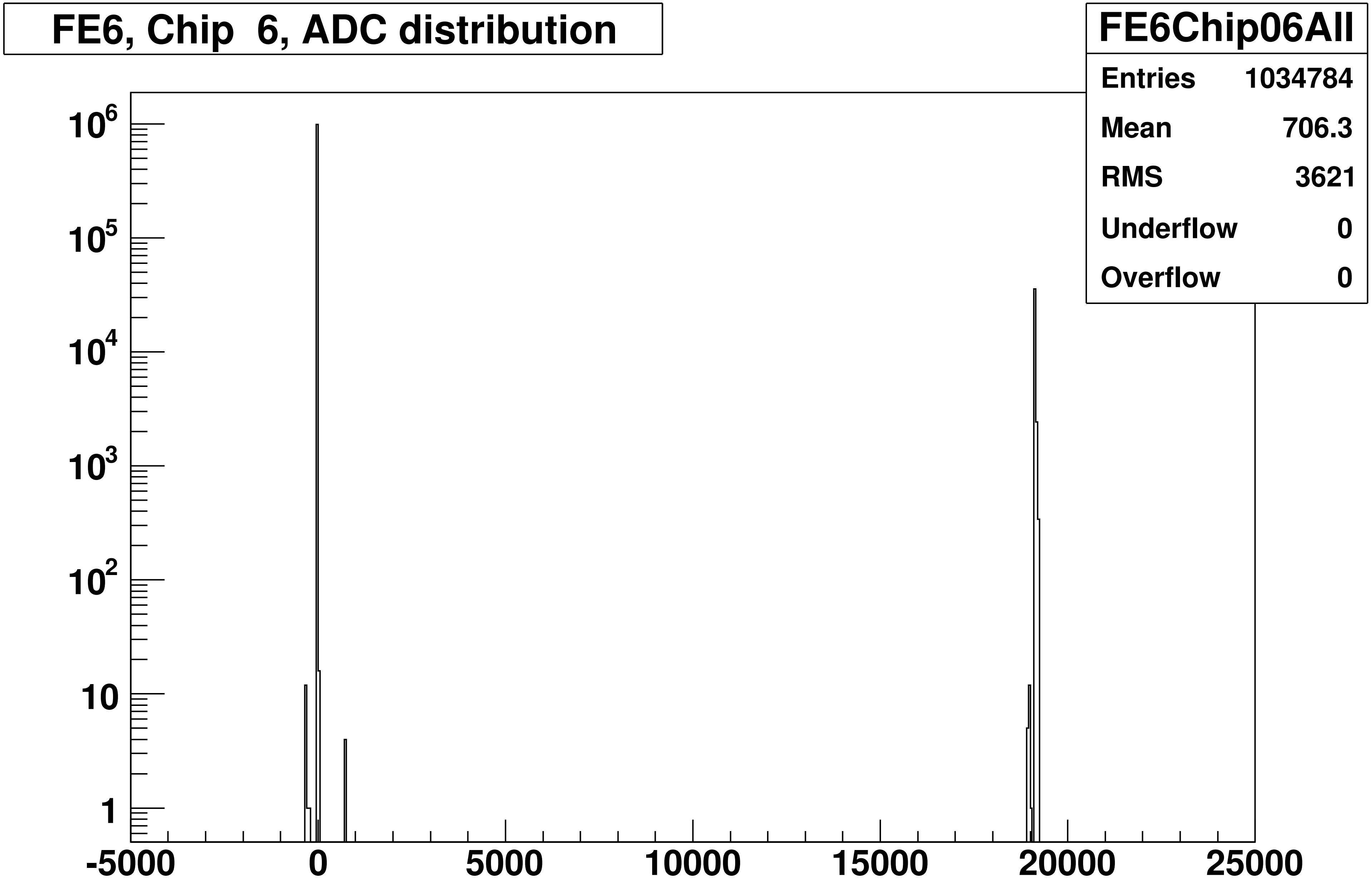
19400


FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 1034784
Mean 706.3
RMS 3621
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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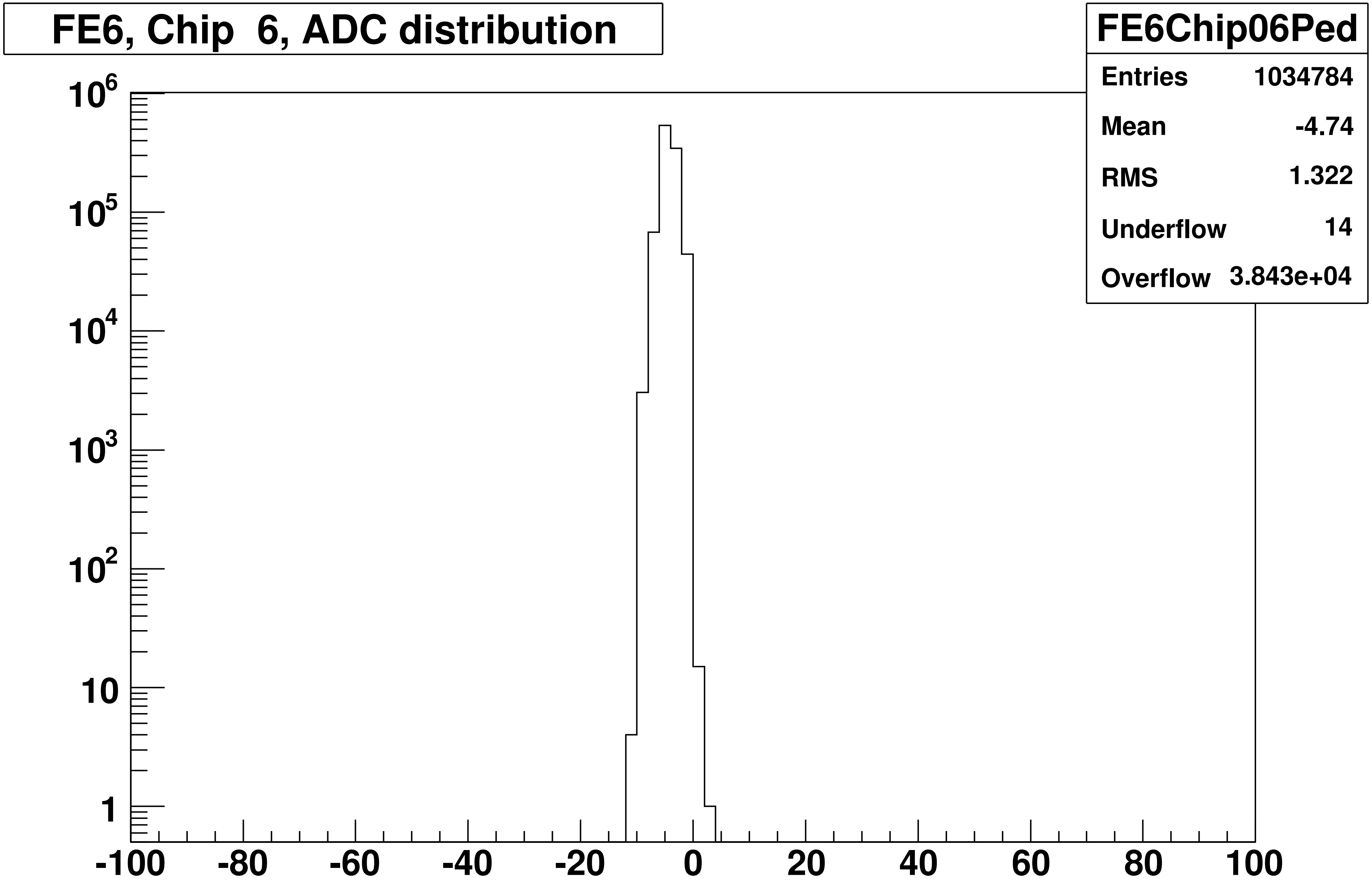
FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 1034784
Mean -4.74
RMS 1.322
Underflow 14
Overflow 3.843e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
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-60
-40
-20
0
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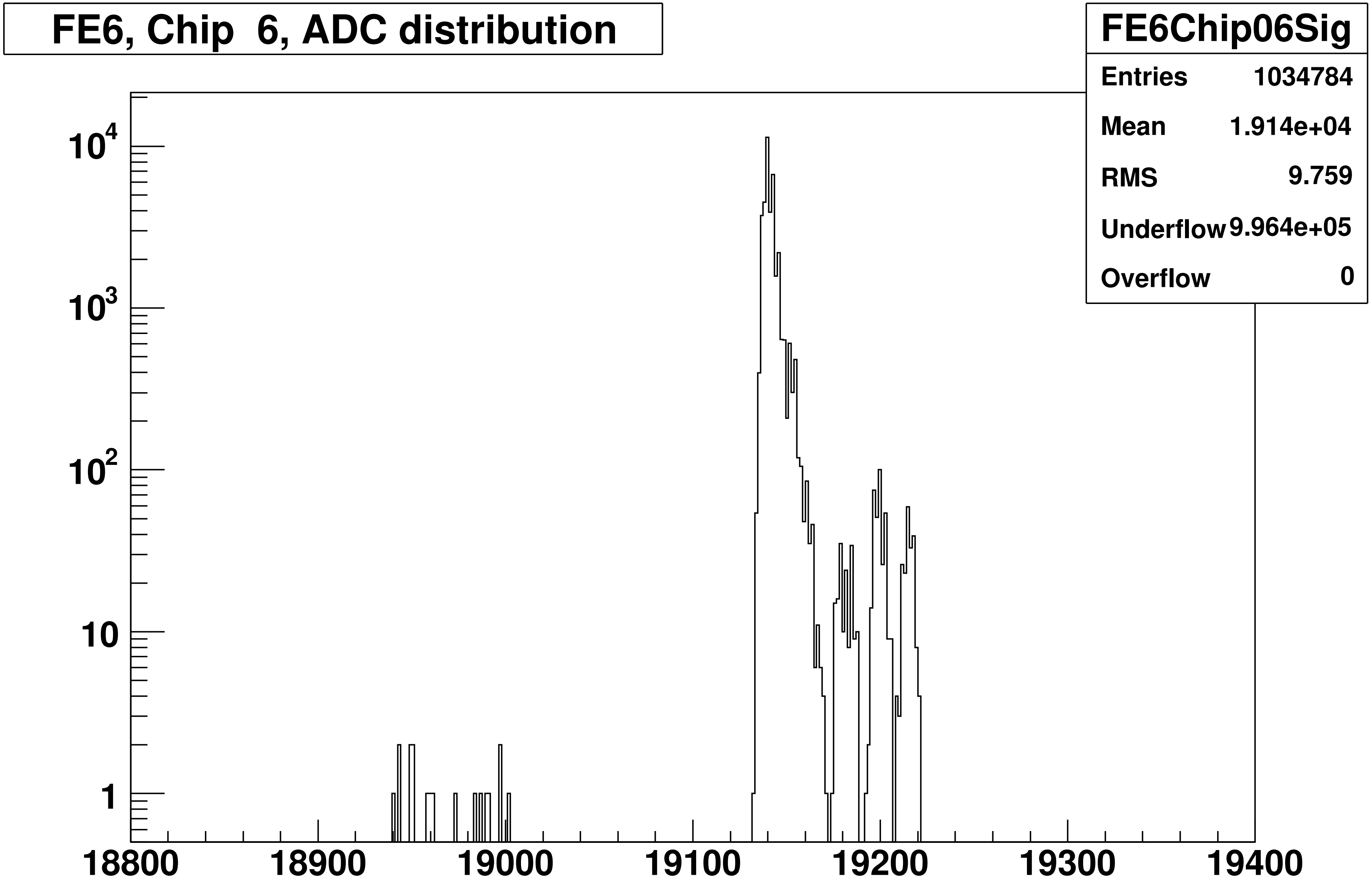
60
80
100


FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 1034784
Mean 1.914e+04
RMS 9.759
Underflow 9.964e+05
Overflow 0
10^4
10^3
10^2
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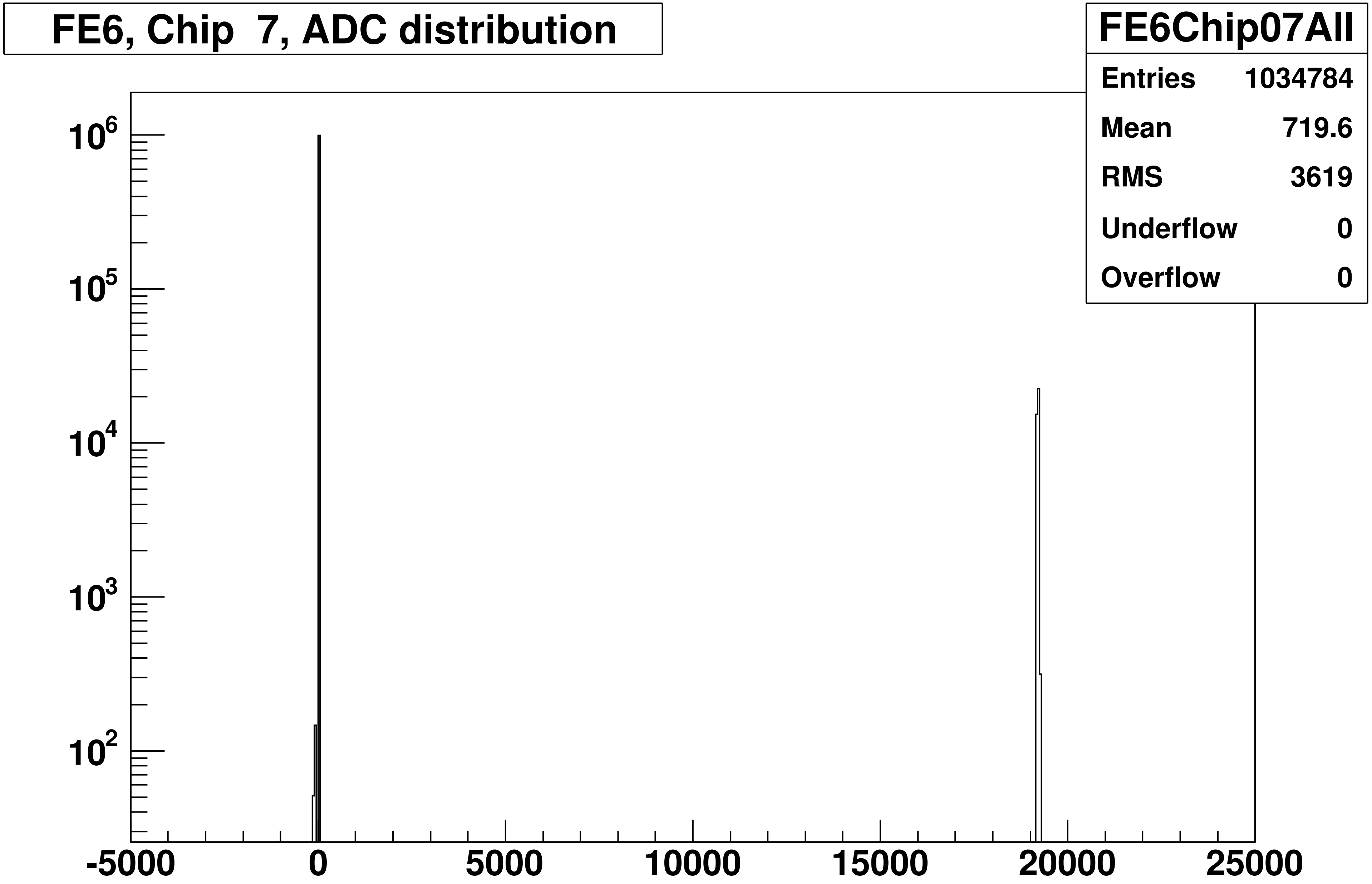
FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 1034784
Mean 719.6
RMS 3619
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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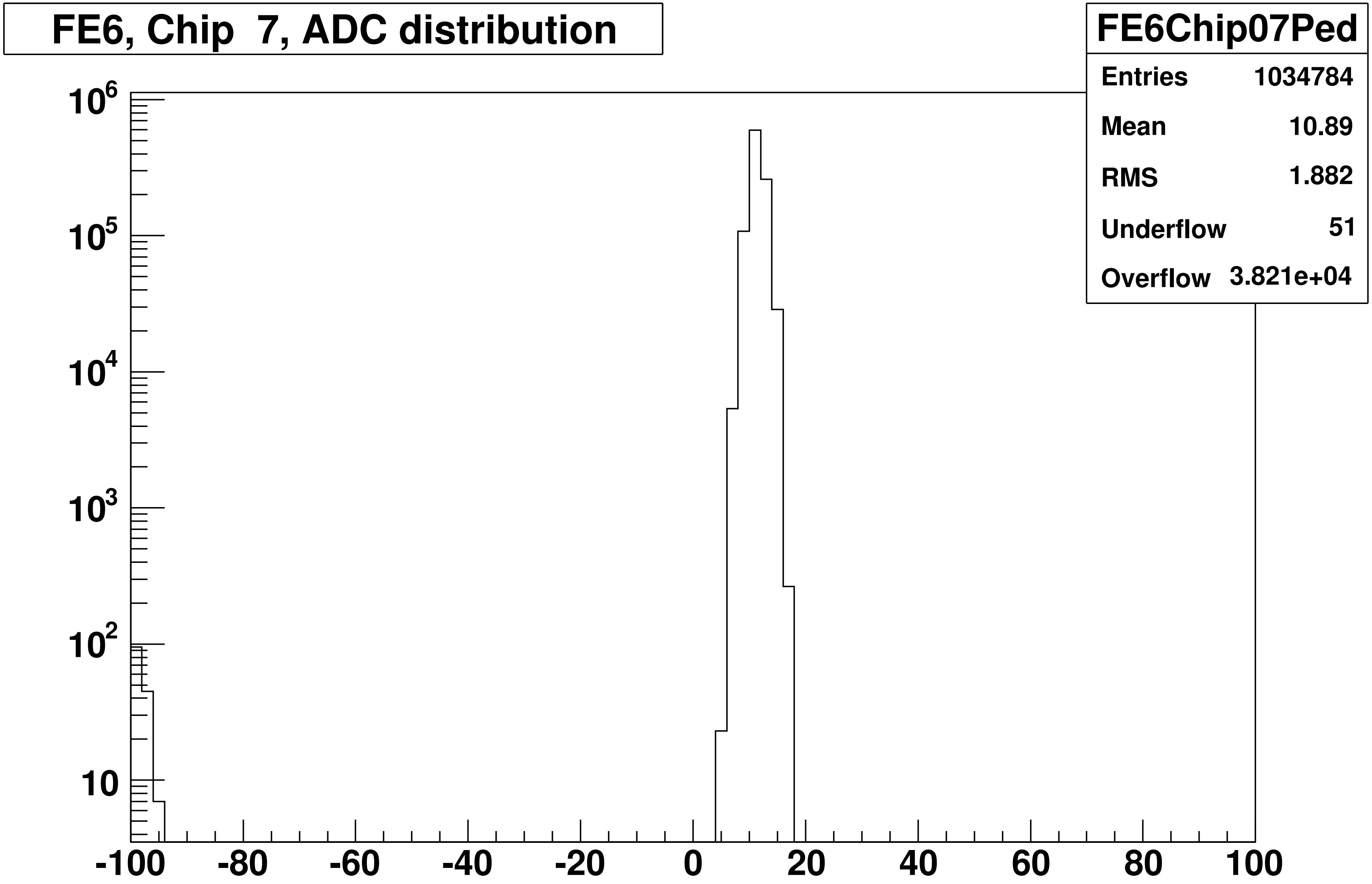
FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 1034784
Mean 10.89
RMS 1.882
Underflow 51
Overflow 3.821e+04
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10^5
10^4
10^3
10^2
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-20
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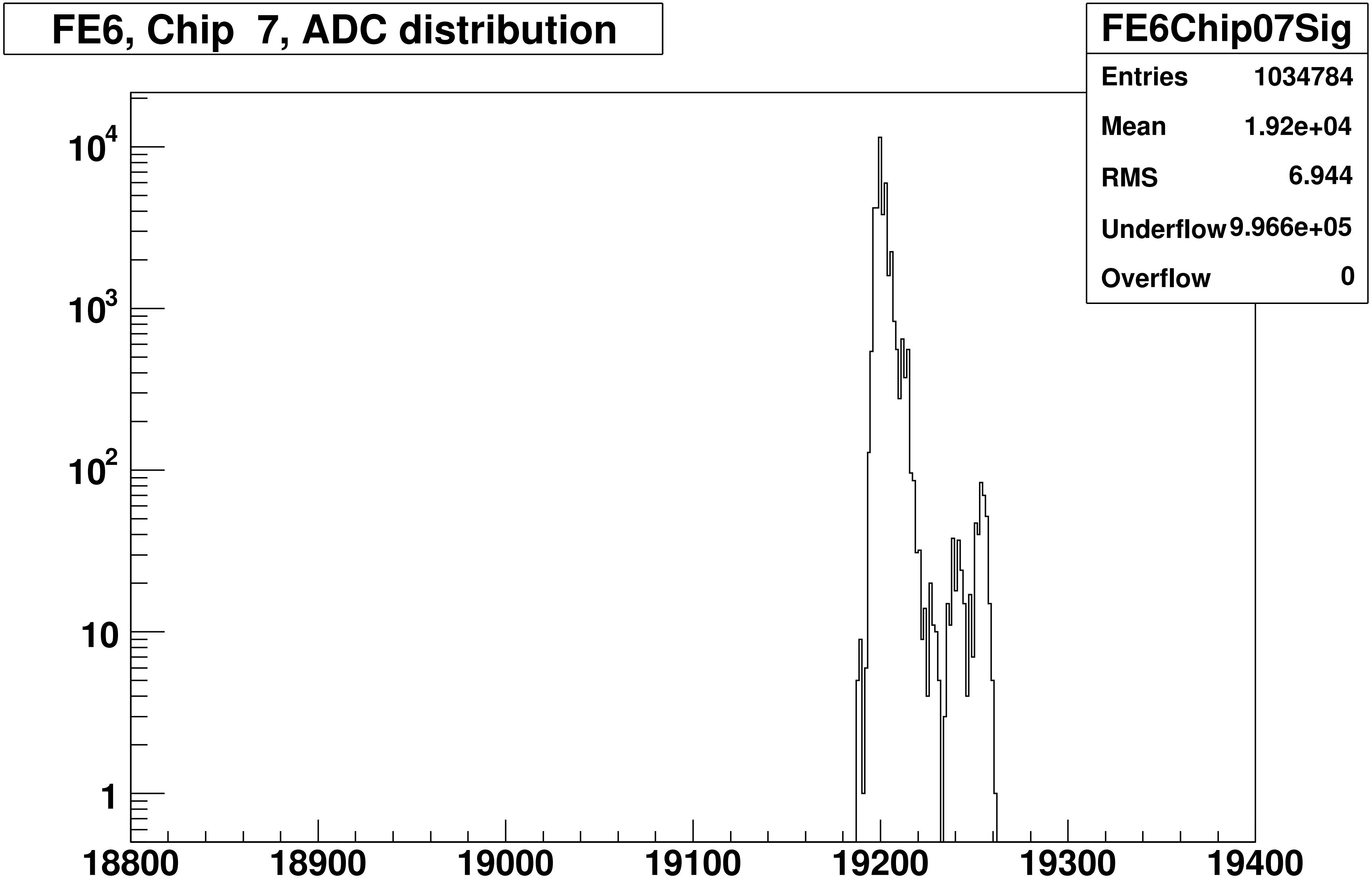

FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 1034784
Mean 1.92e+04
RMS 6.944
Underflow 9.966e+05
Overflow 0
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10^3
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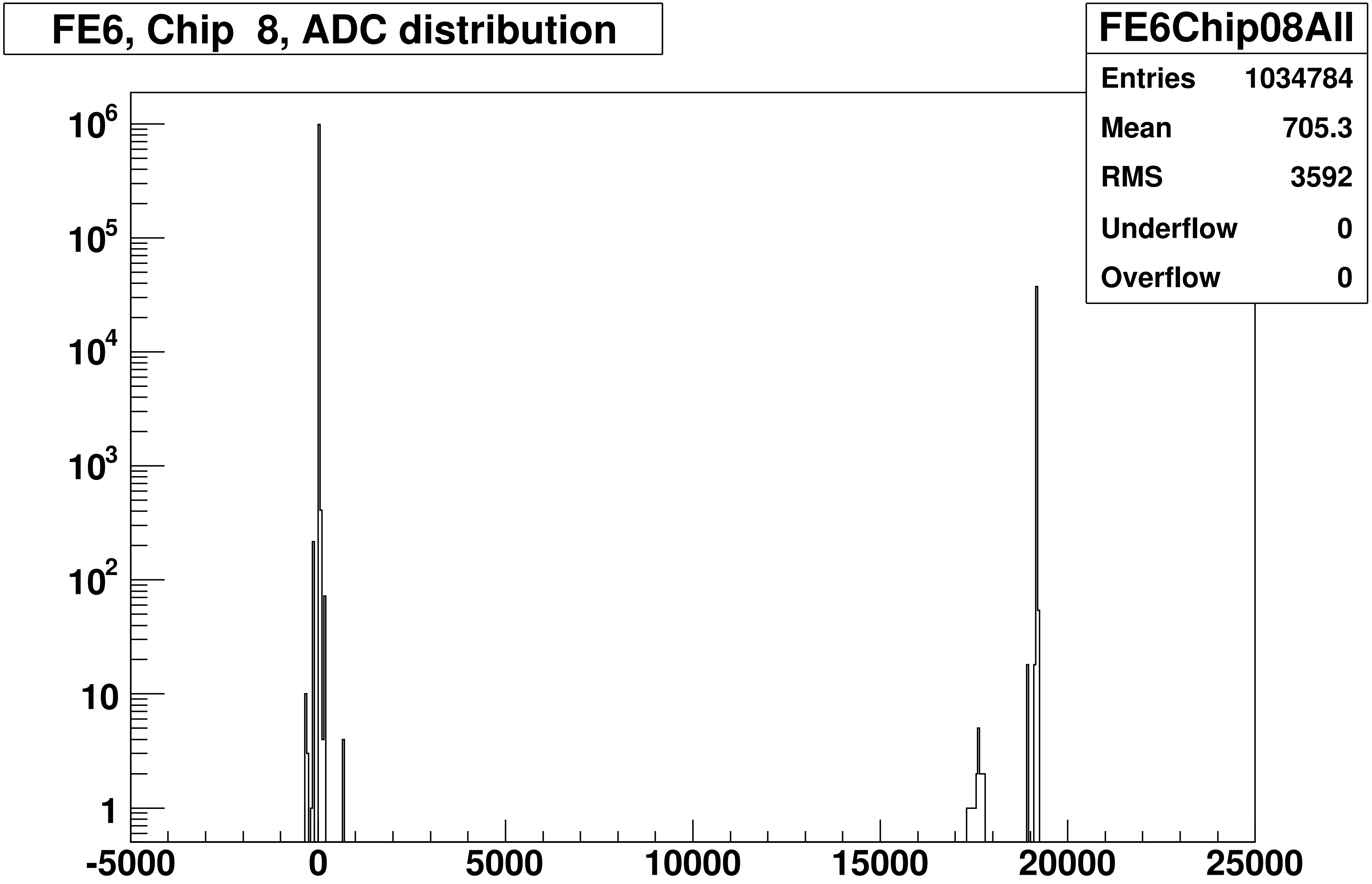
FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 1034784
Mean 705.3
RMS 3592
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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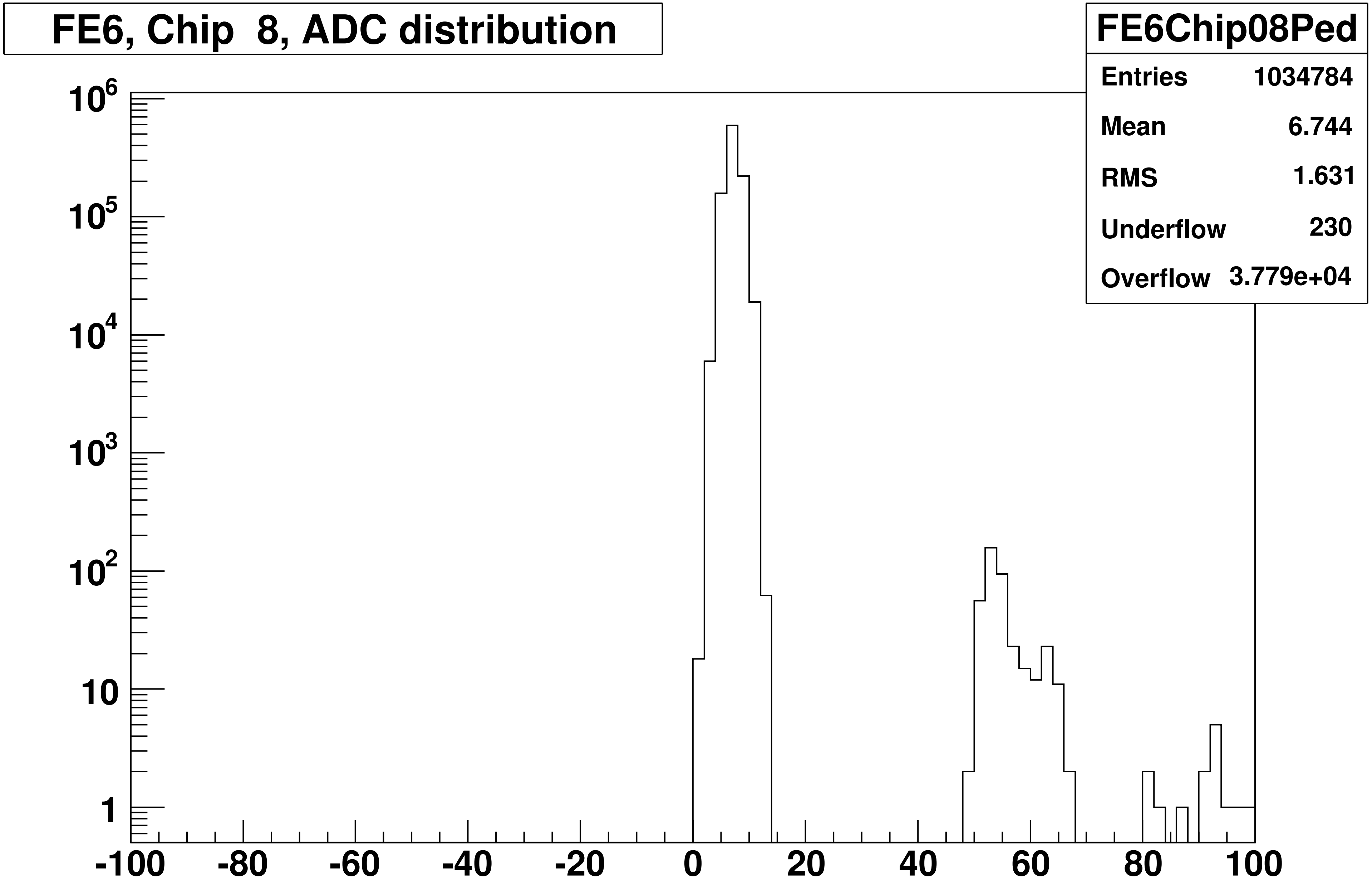

FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 1034784
Mean 6.744
RMS 1.631
Underflow 230
Overflow 3.779e+04
10^6
10^5
10^4
10^3
10^2
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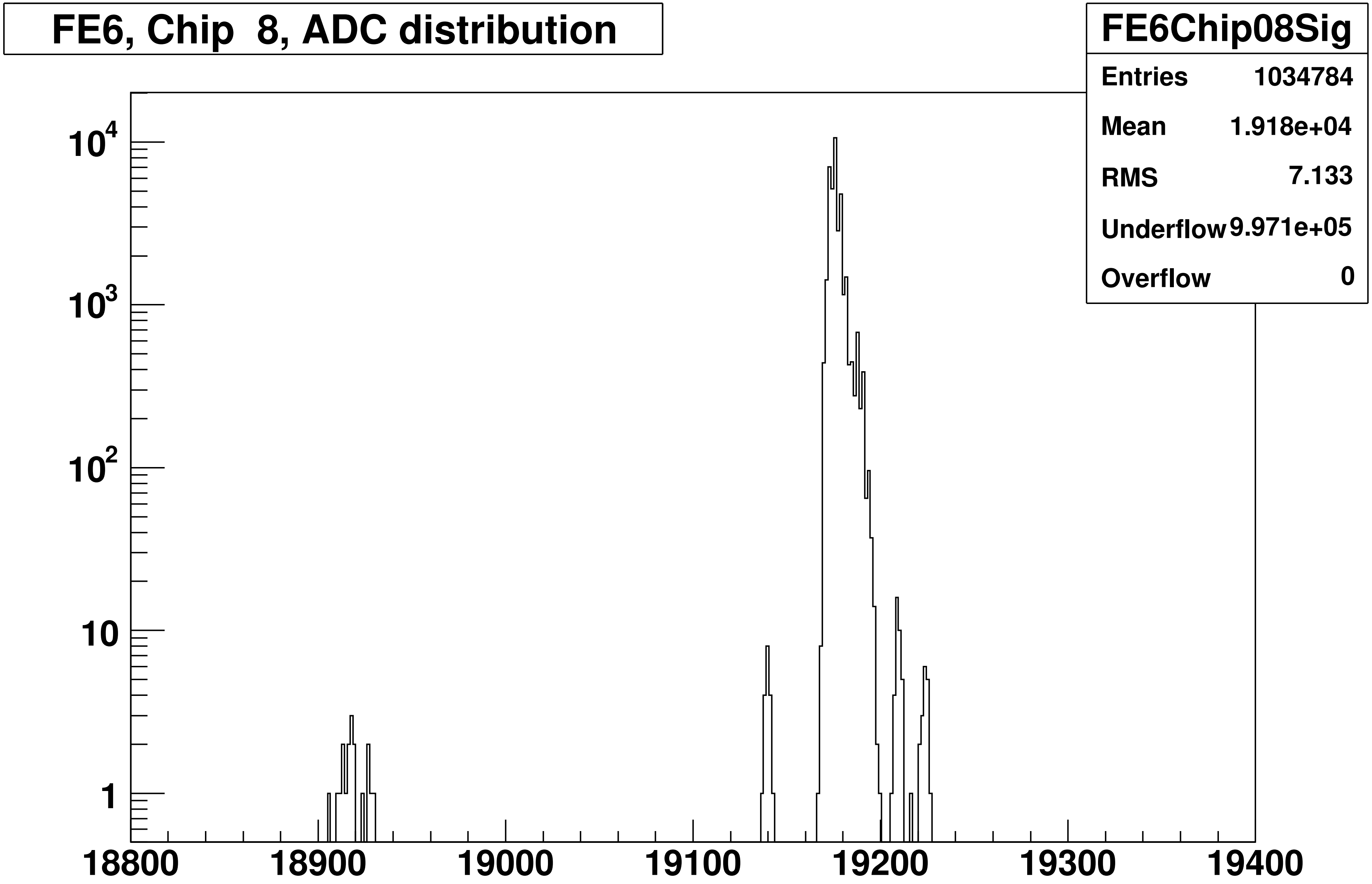
FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 1034784
Mean 1.918e+04
RMS 7.133
Underflow 9.971e+05
Overflow 0
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10^3
10^2
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18900
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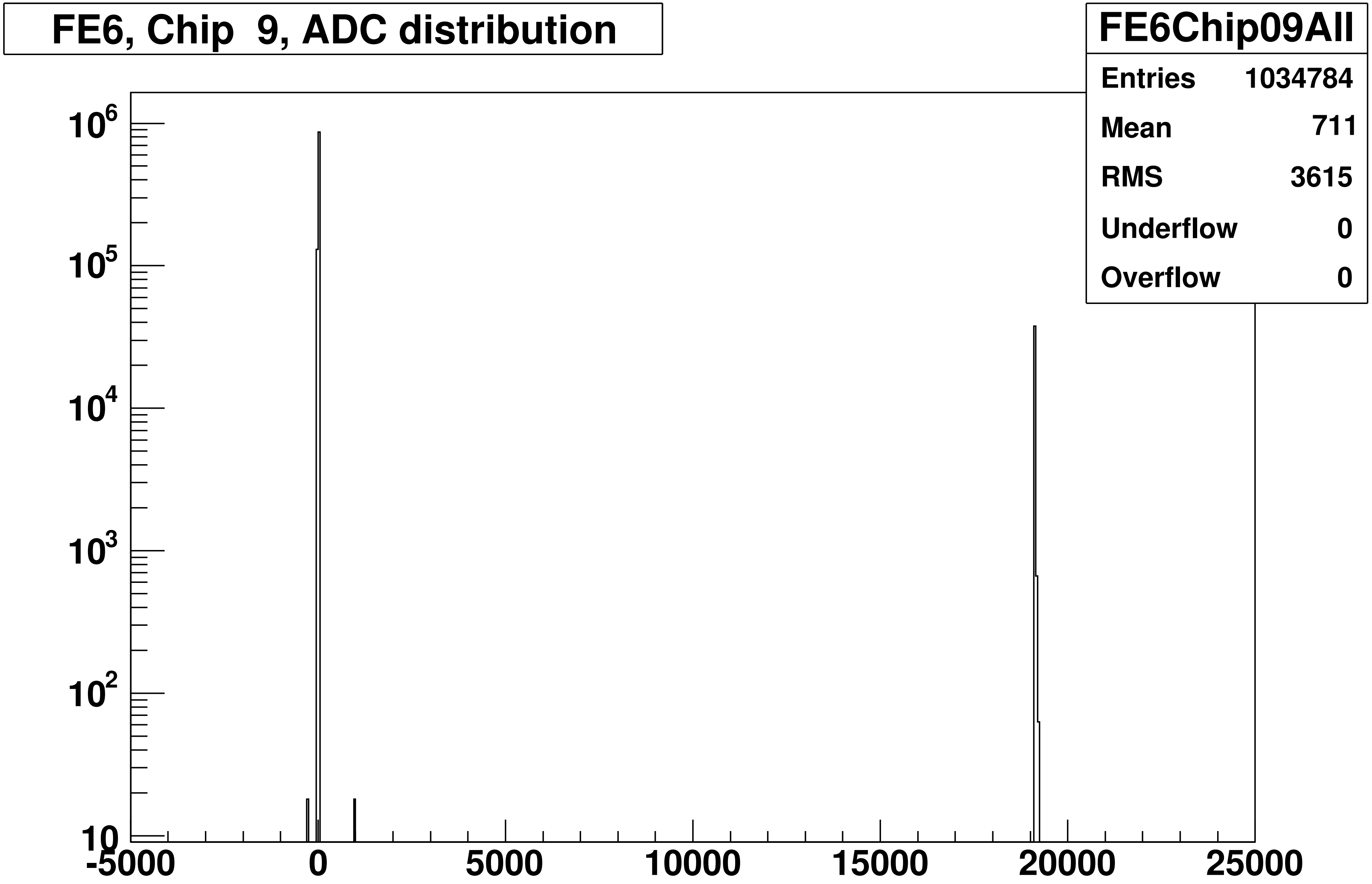

FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 1034784
Mean 711
RMS 3615
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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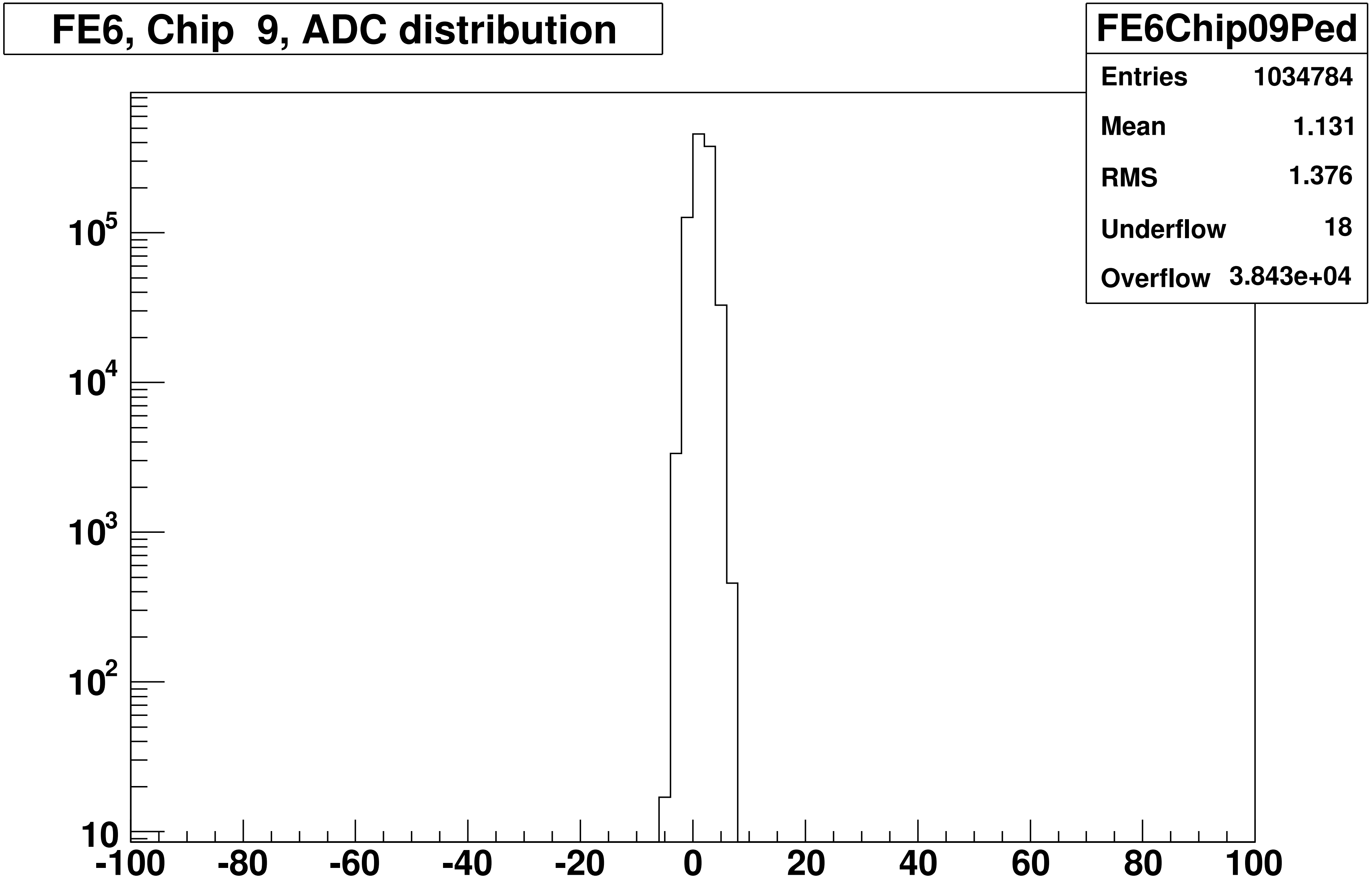
FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 1034784
Mean 1.131
RMS 1.376
Underflow 18
Overflow 3.843e+04
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10^2
10^3
10^4
10^5
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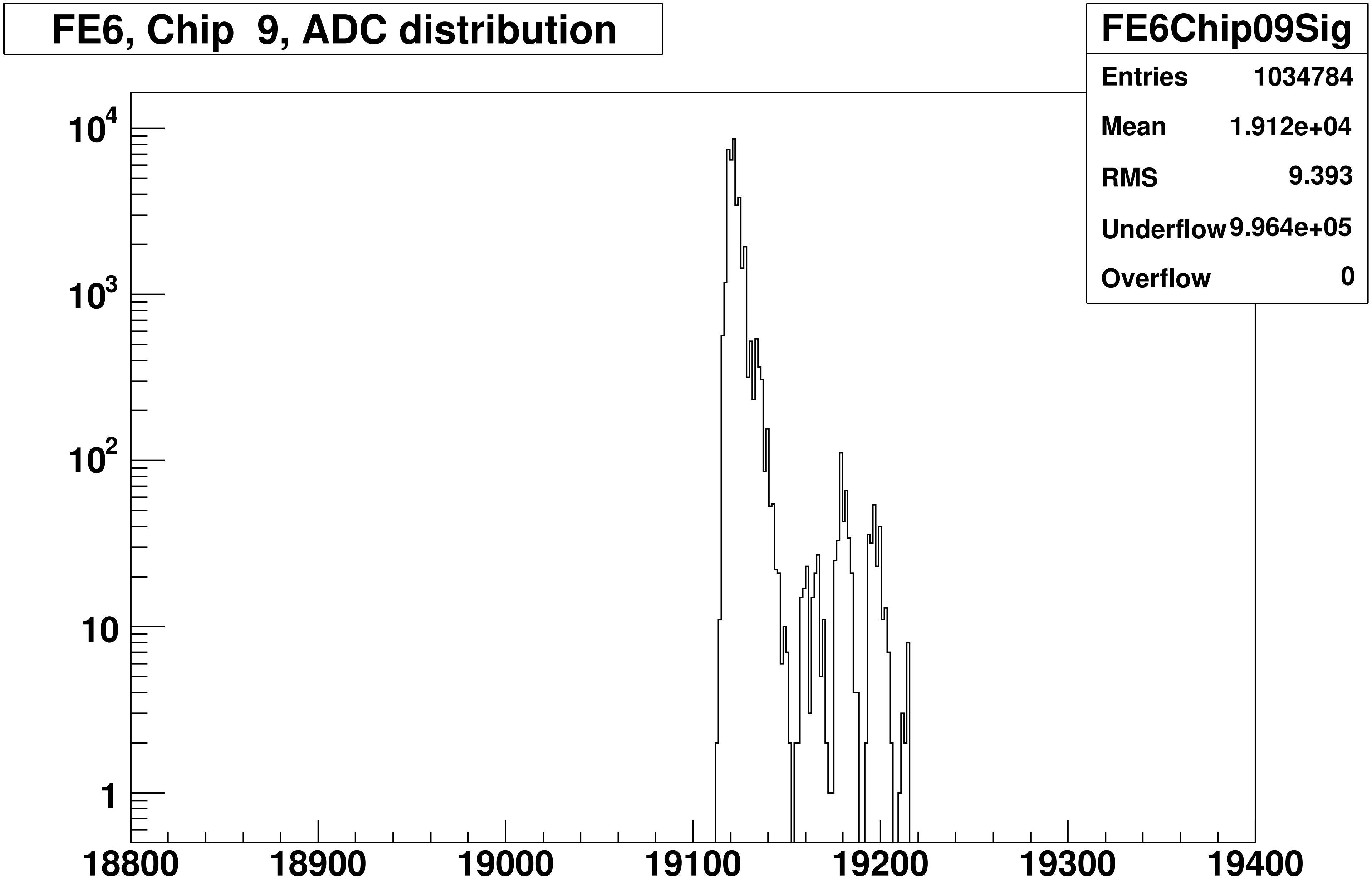
FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 1034784
Mean 1.912e+04
RMS 9.393
Underflow 9.964e+05
Overflow 0
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10^3
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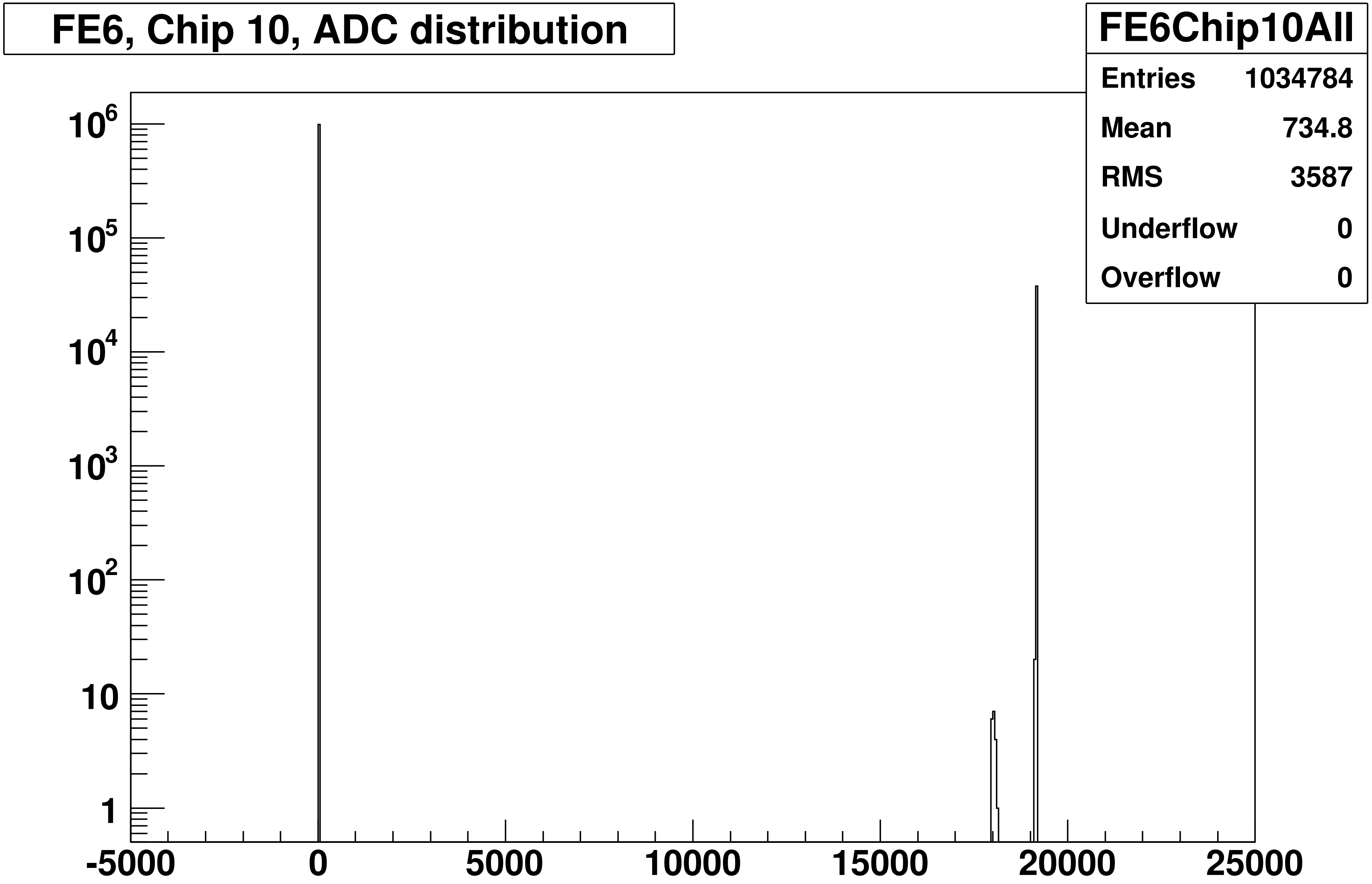
FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 1034784
Mean 734.8
RMS 3587
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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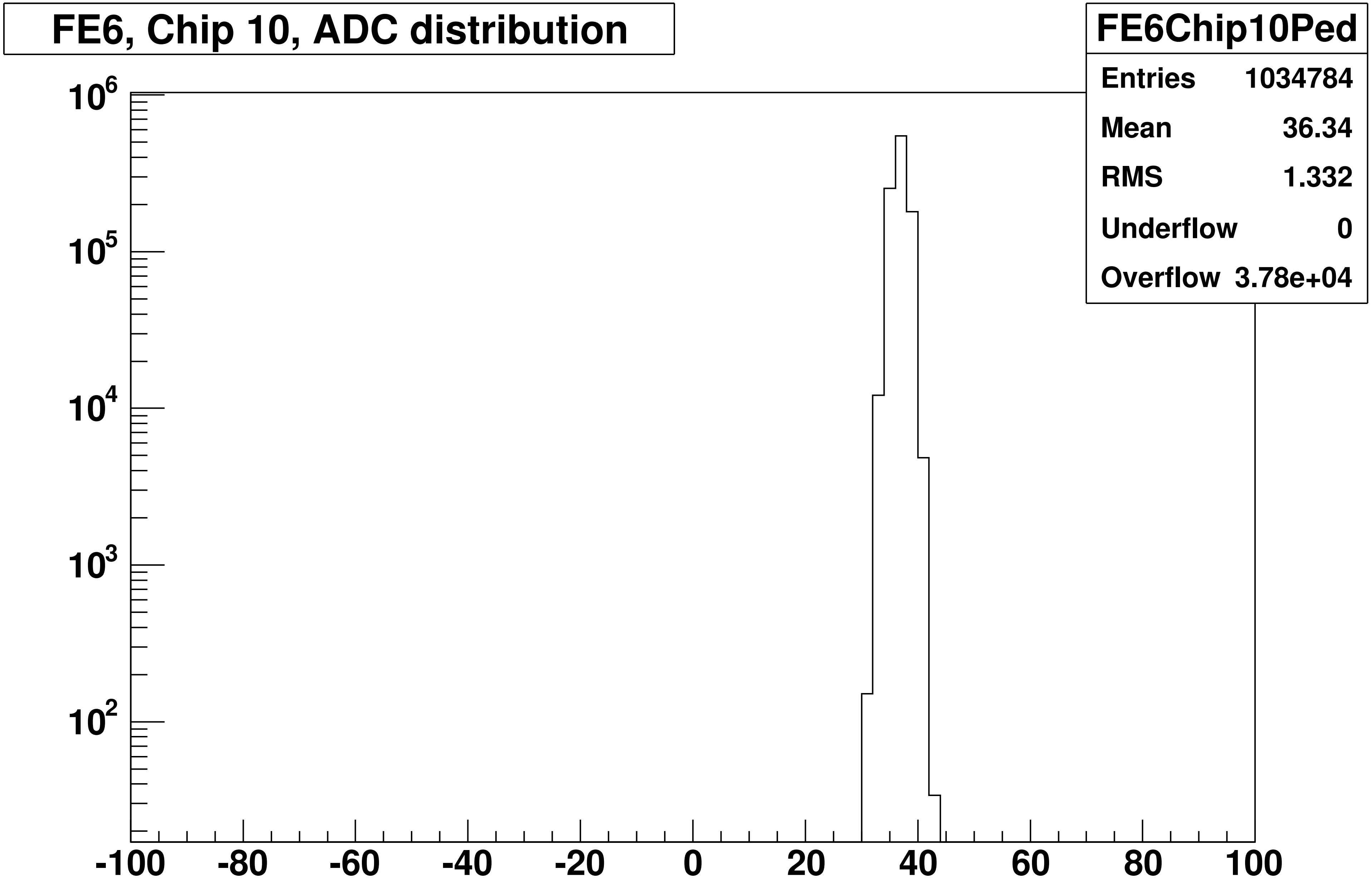
20000
25000



FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 1034784
Mean 36.34
RMS 1.332
Underflow 0
Overflow 3.78e+04
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10^3
10^2
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-40
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80
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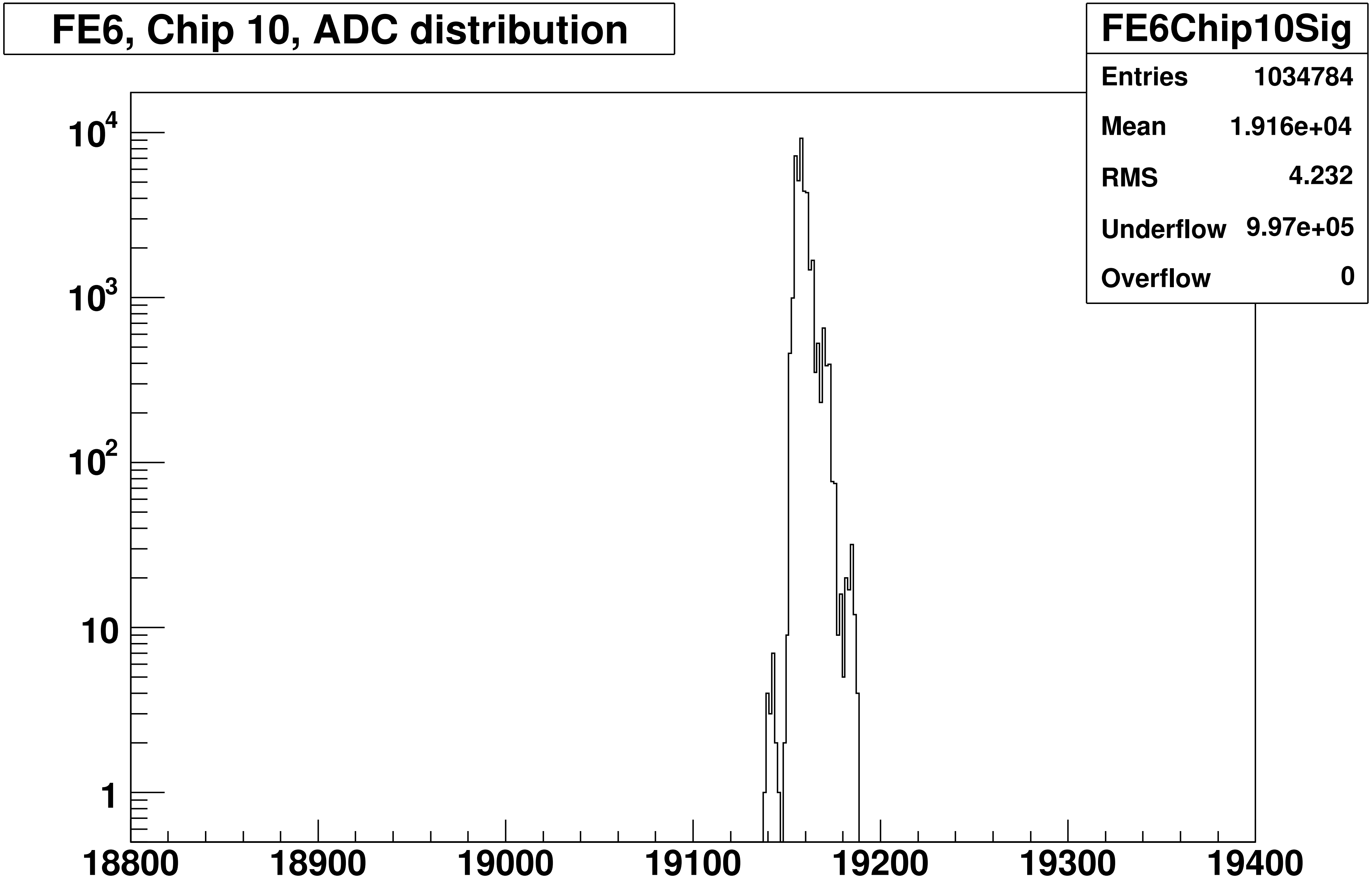
FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 1034784
Mean 1.916e+04
RMS 4.232
Underflow 9.97e+05
Overflow 0
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10^3
10^2
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18900
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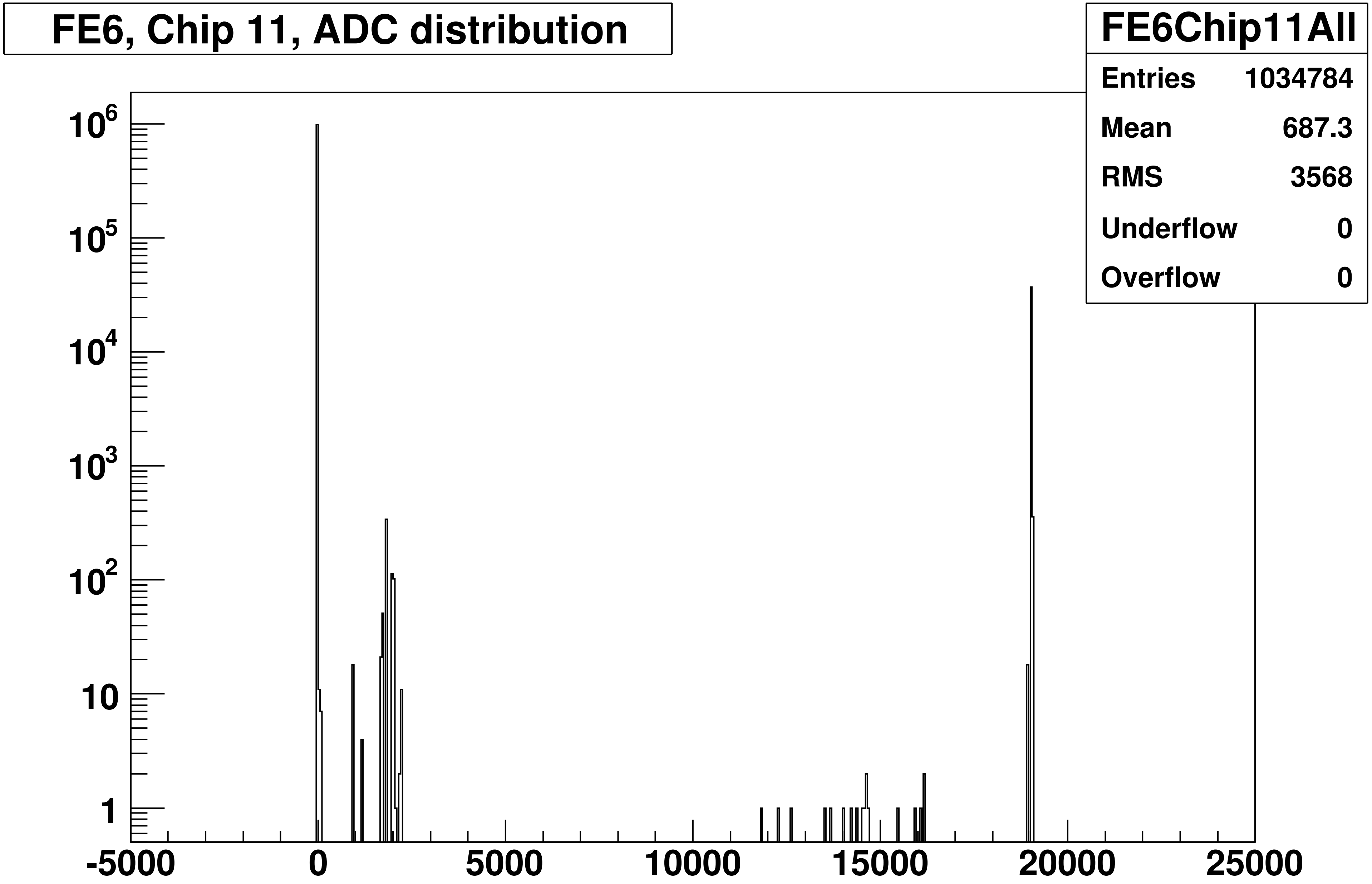

FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 1034784
Mean 687.3
RMS 3568
Underflow 0
Overflow 0
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10^4
10^3
10^2
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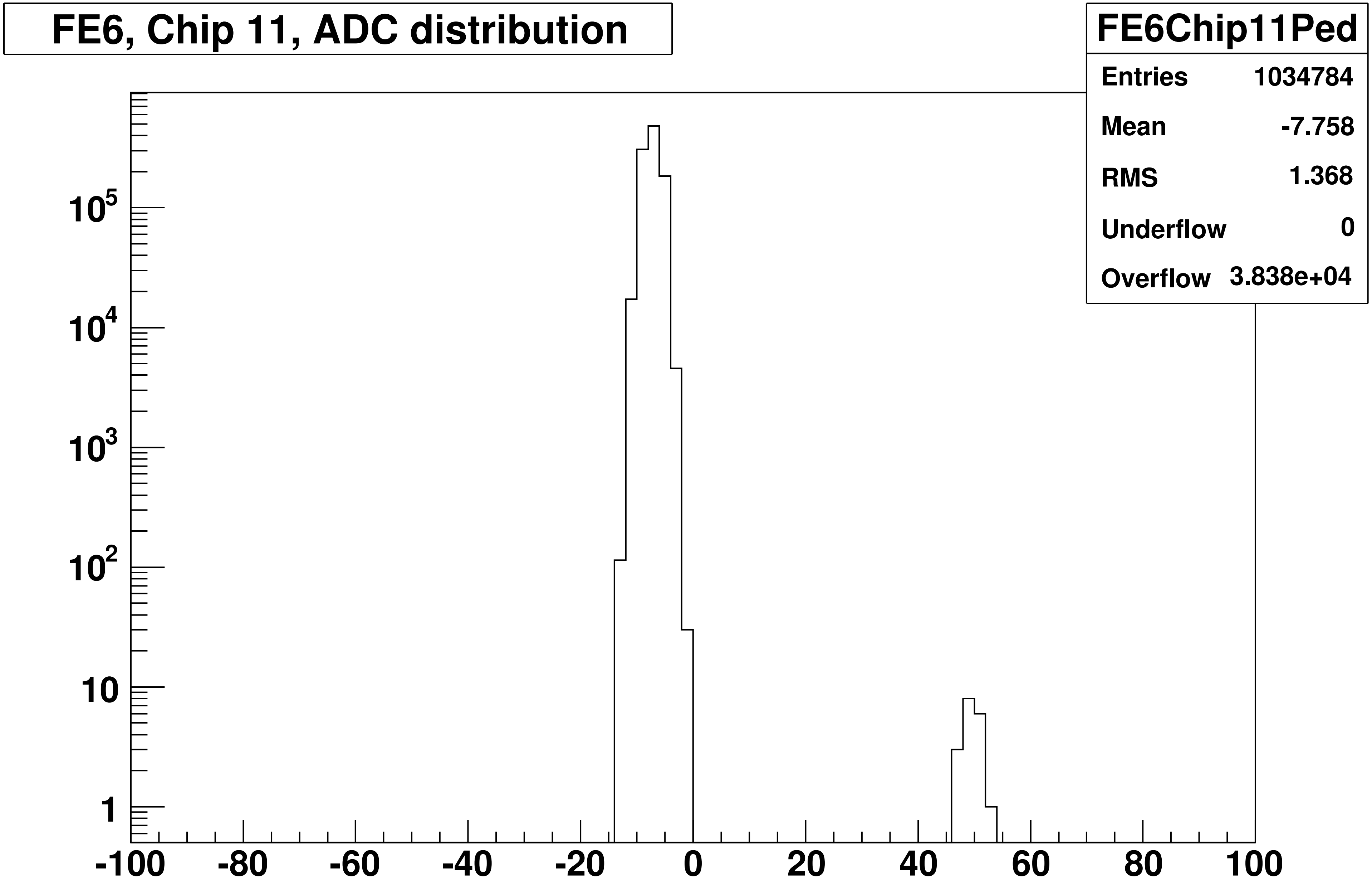
15000
20000
25000



FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 1034784
Mean -7.758
RMS 1.368
Underflow 0
Overflow 3.838e+04
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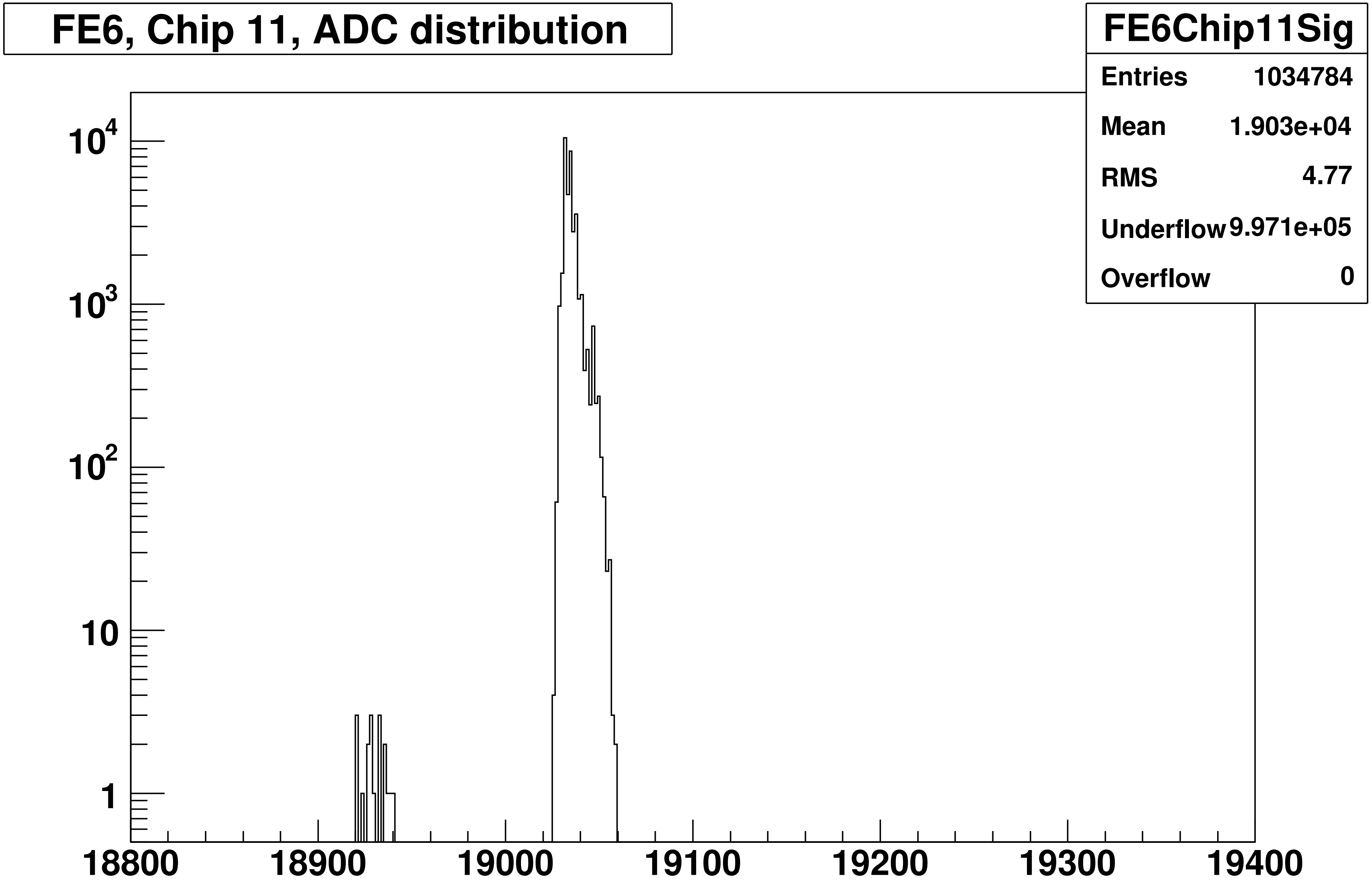
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 1034784
Mean 1.903e+04
RMS 4.77
Underflow 9.971e+05
Overflow 0
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10^3
10^2
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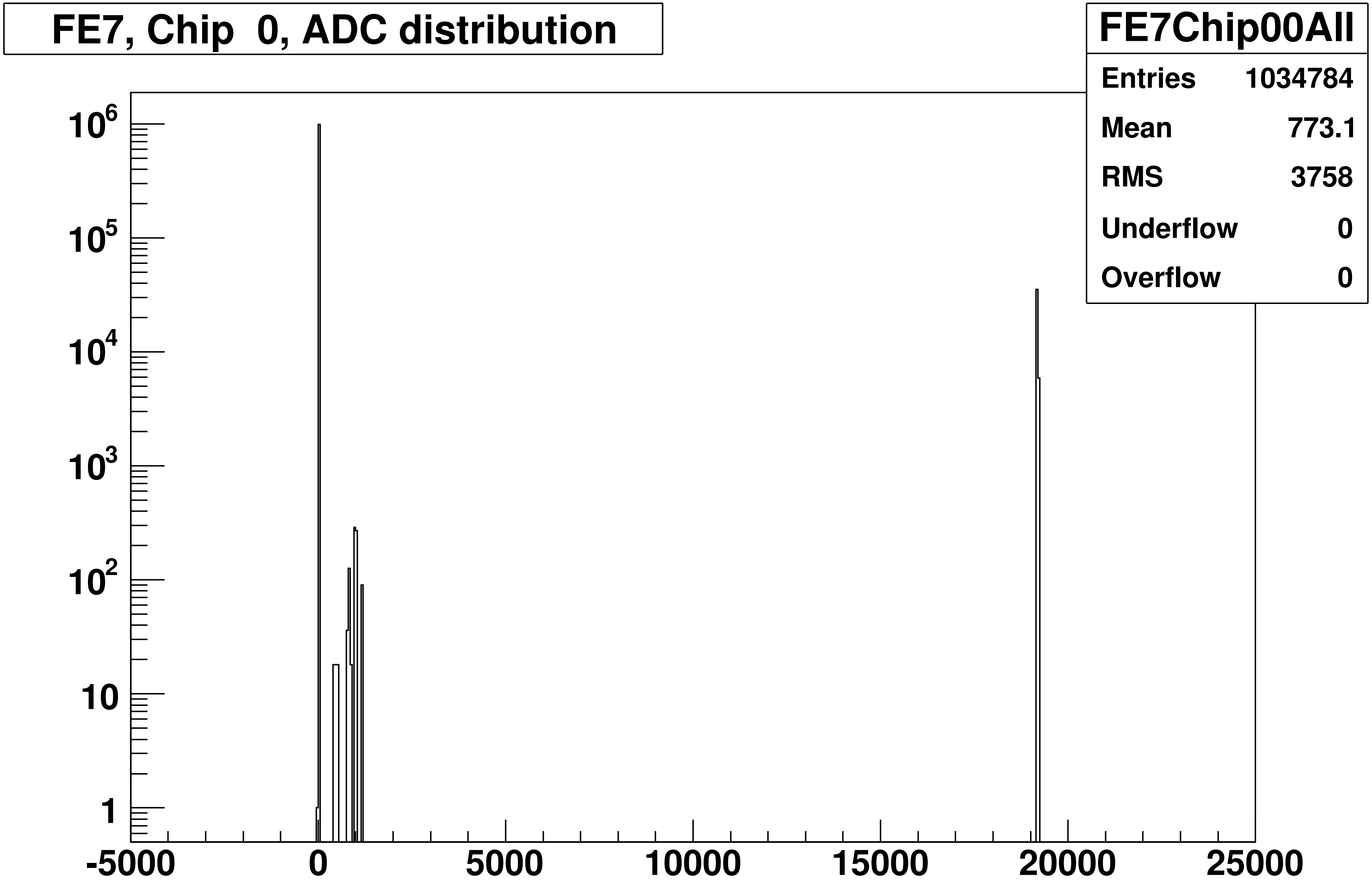

FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 1034784
Mean 773.1
RMS 3758
Underflow 0
Overflow 0
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10^4
10^3
10^2
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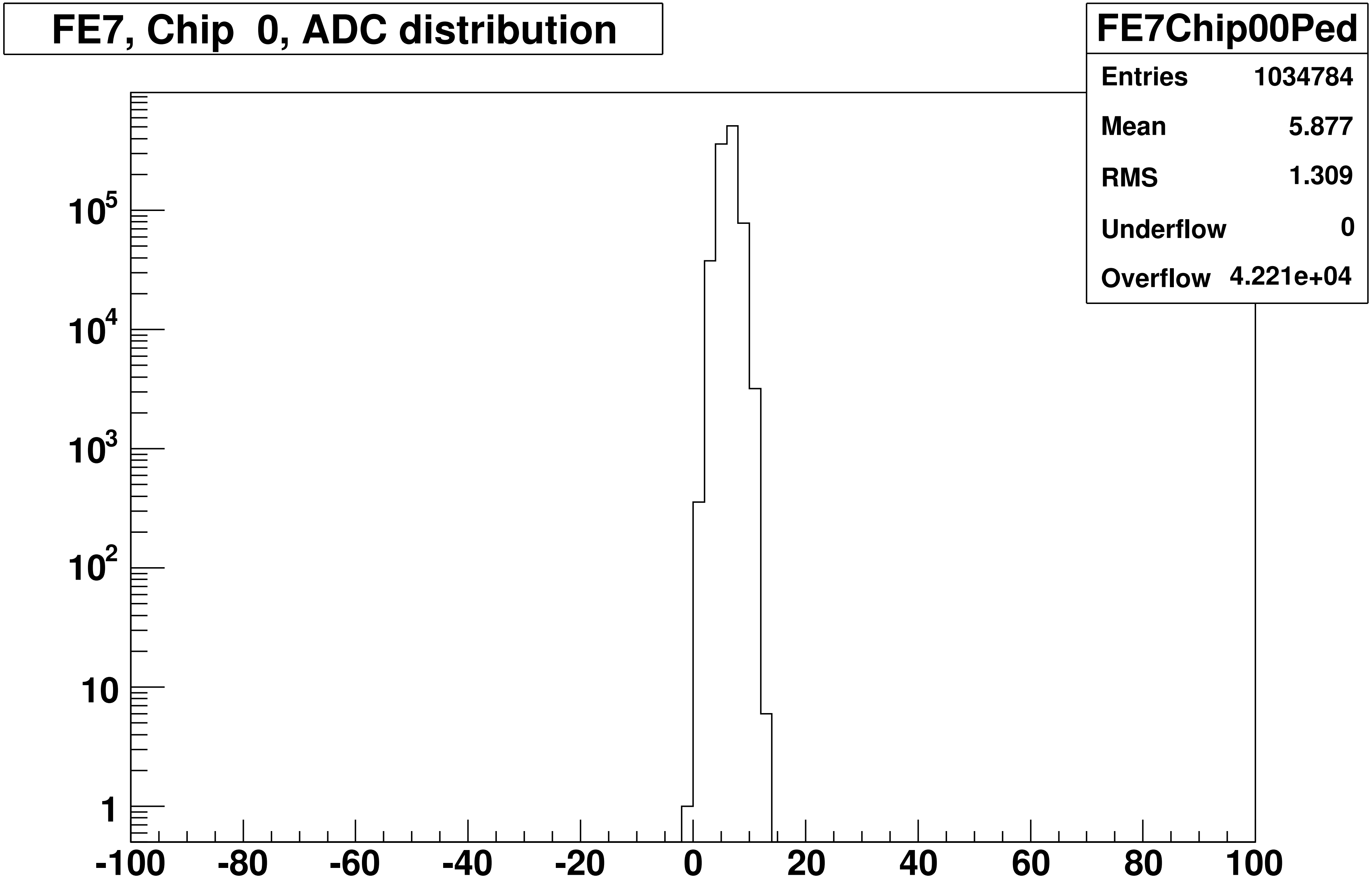
25000


FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 1034784
Mean 5.877
RMS 1.309
Underflow 0
Overflow 4.221e+04
10^5
10^4
10^3
10^2
10
1
-100
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-40
-20
0
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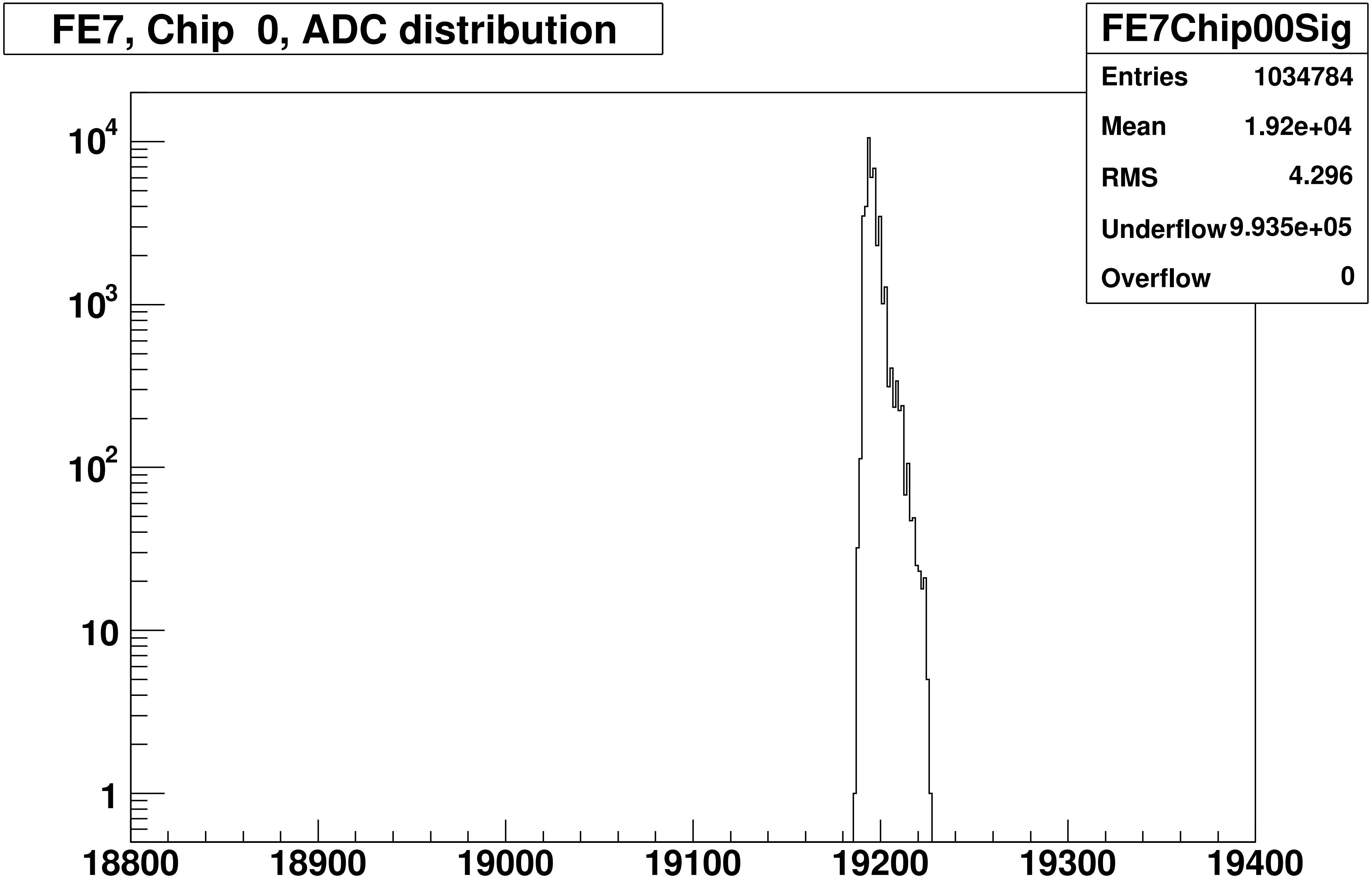
40
60
80
100


FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 1034784
Mean 1.92e+04
RMS 4.296
Underflow 9.935e+05
Overflow 0
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10^3
10^2
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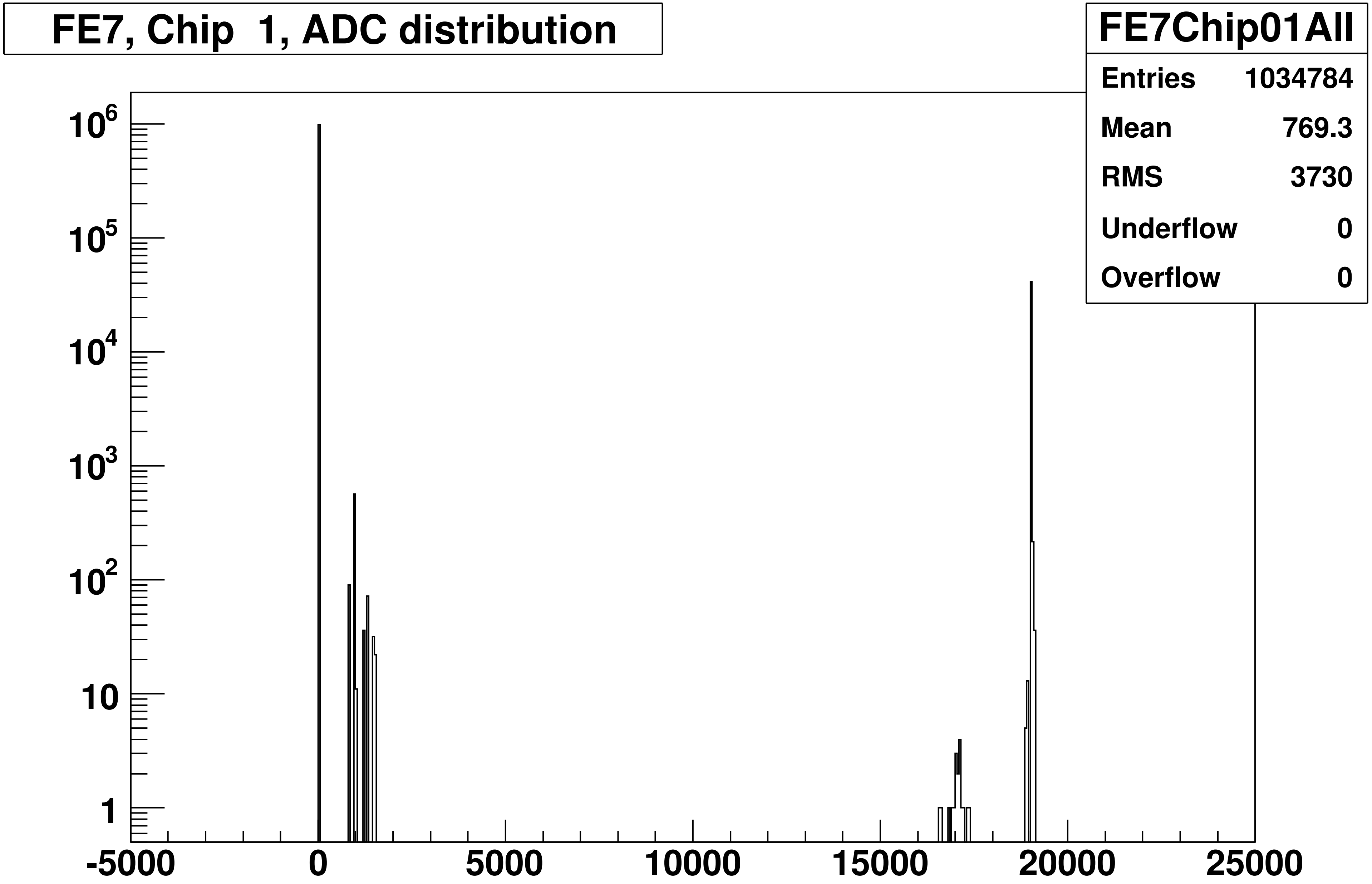

FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 1034784
Mean 769.3
RMS 3730
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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0
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10000
15000
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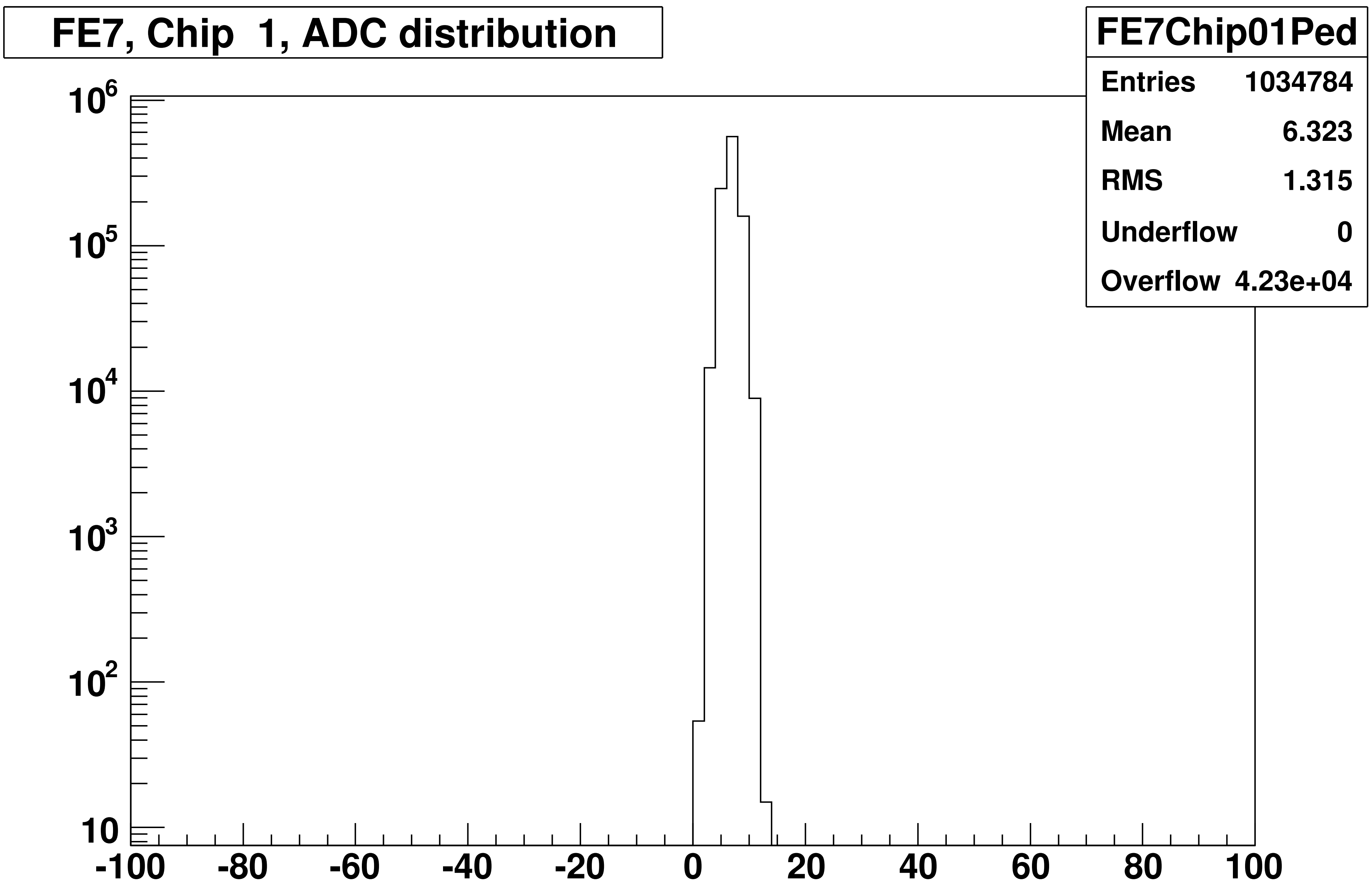
FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 1034784
Mean 6.323
RMS 1.315
Underflow 0
Overflow 4.23e+04
10^6
10^5
10^4
10^3
10^2
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-40
-20
0
20
40
60
80
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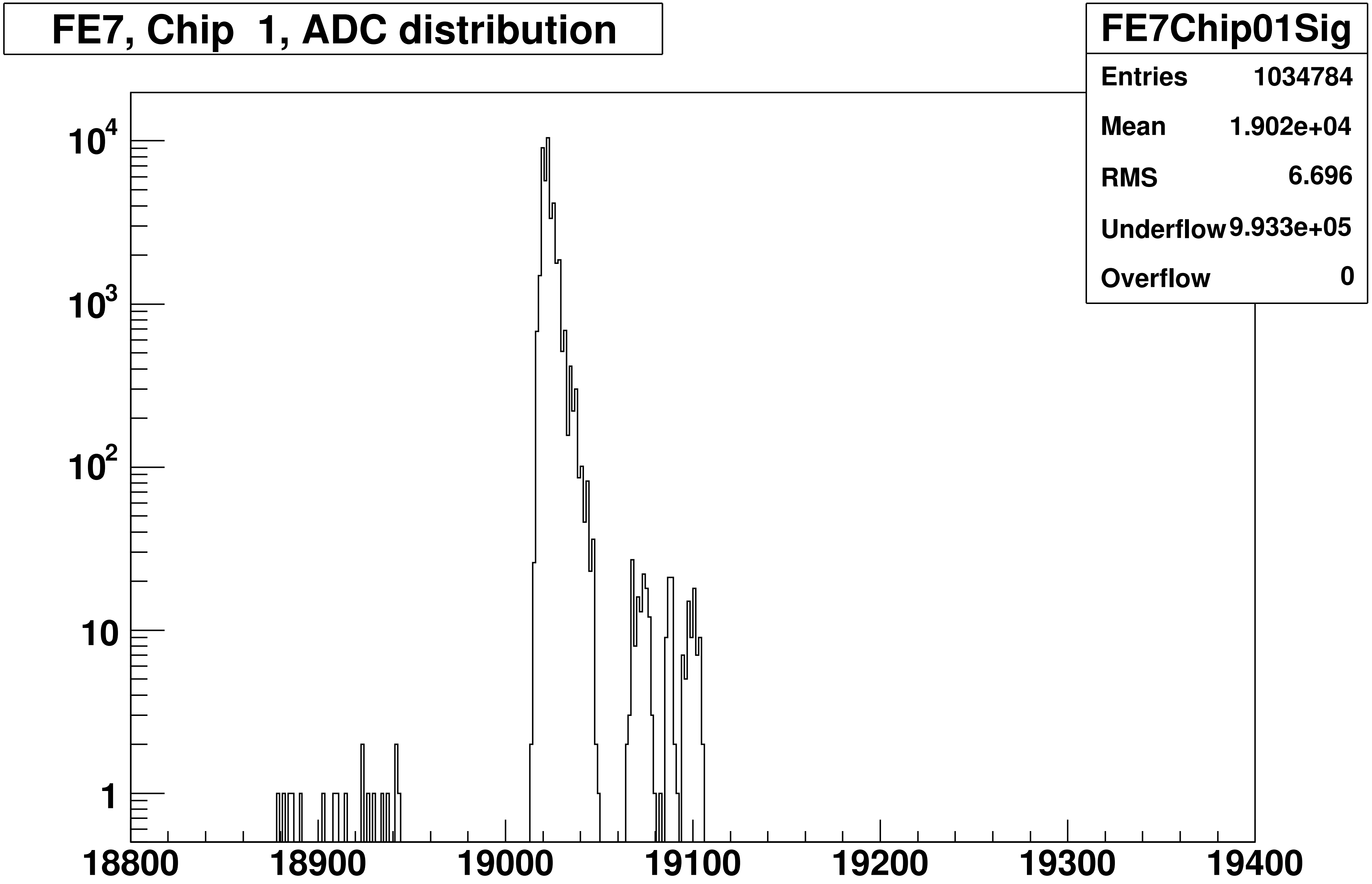
FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 1034784
Mean 1.902e+04
RMS 6.696
Underflow 9.933e+05
Overflow 0
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10^3
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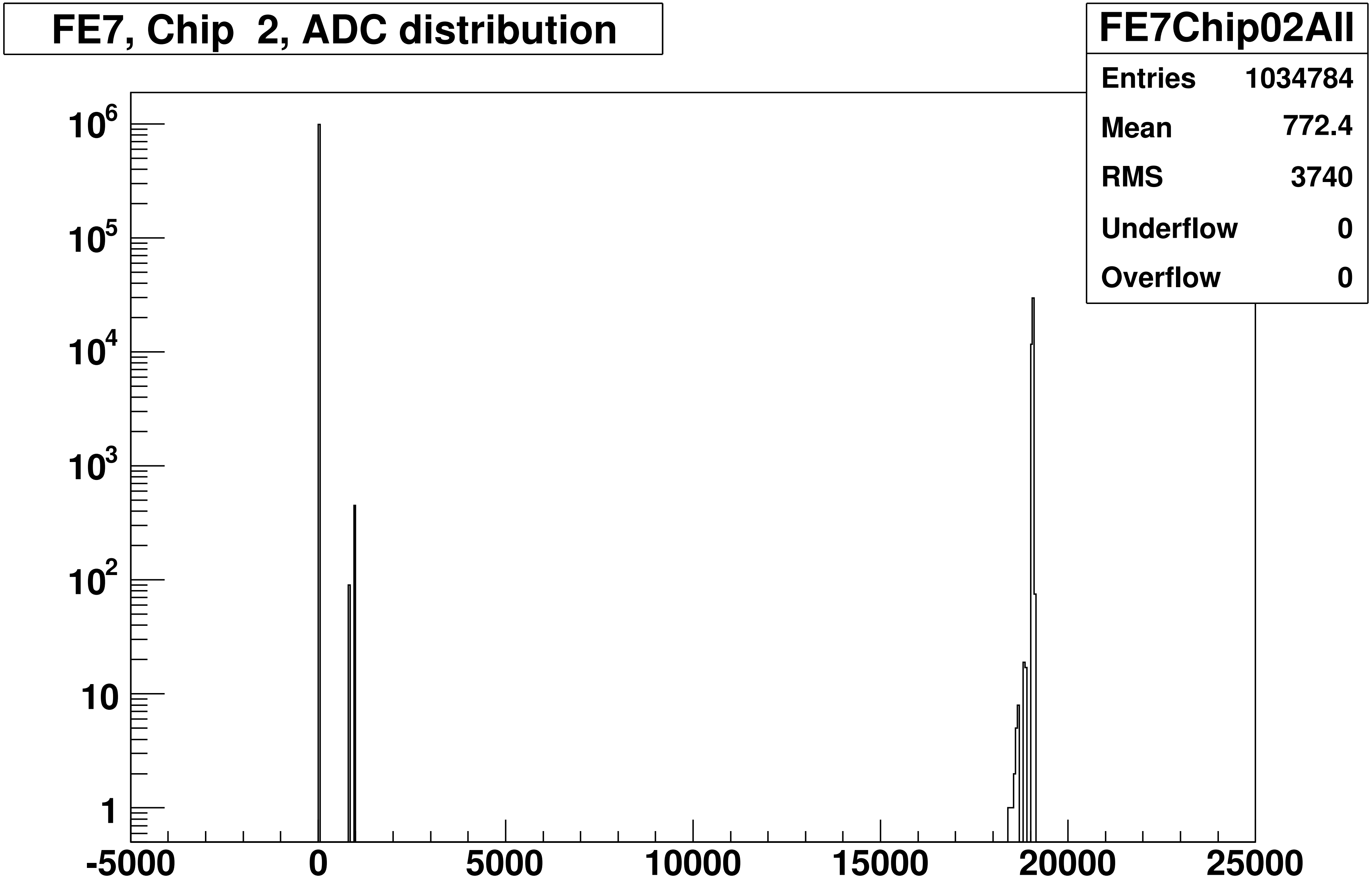
FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 1034784
Mean 772.4
RMS 3740
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
15000
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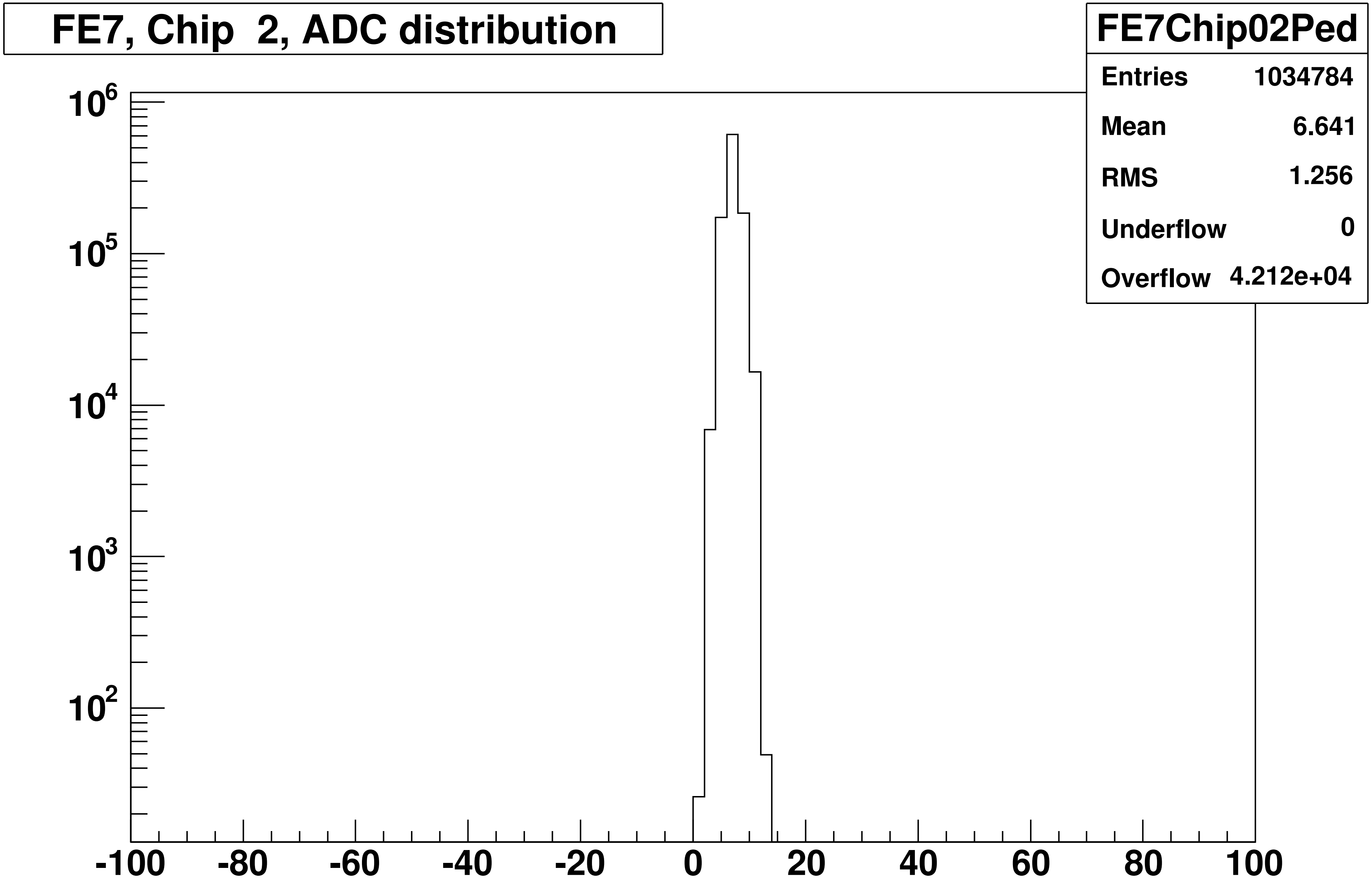

FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 1034784
Mean 6.641
RMS 1.256
Underflow 0
Overflow 4.212e+04
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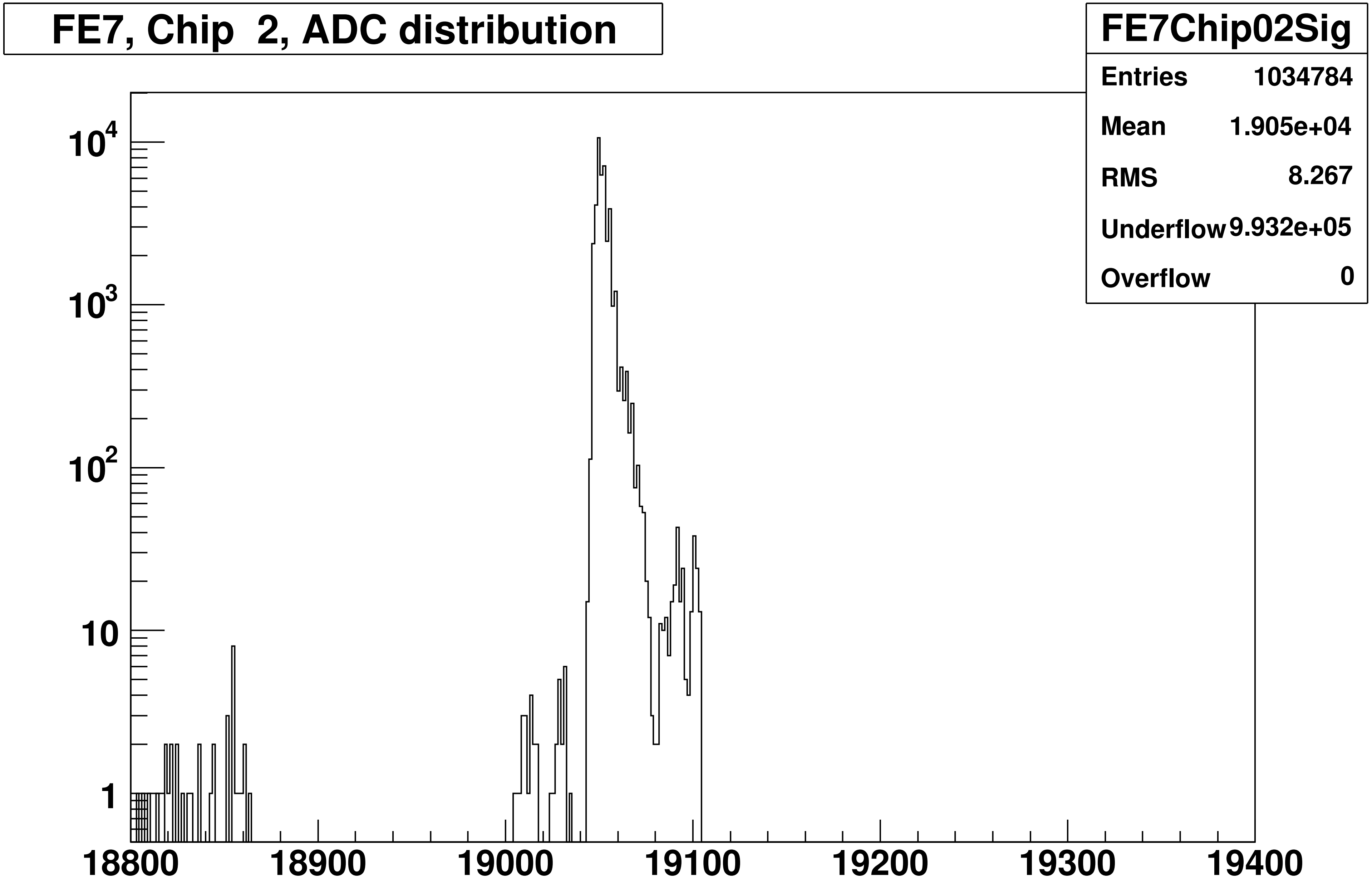
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 1034784
Mean 1.905e+04
RMS 8.267
Underflow 9.932e+05
Overflow 0
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10^3
10^2
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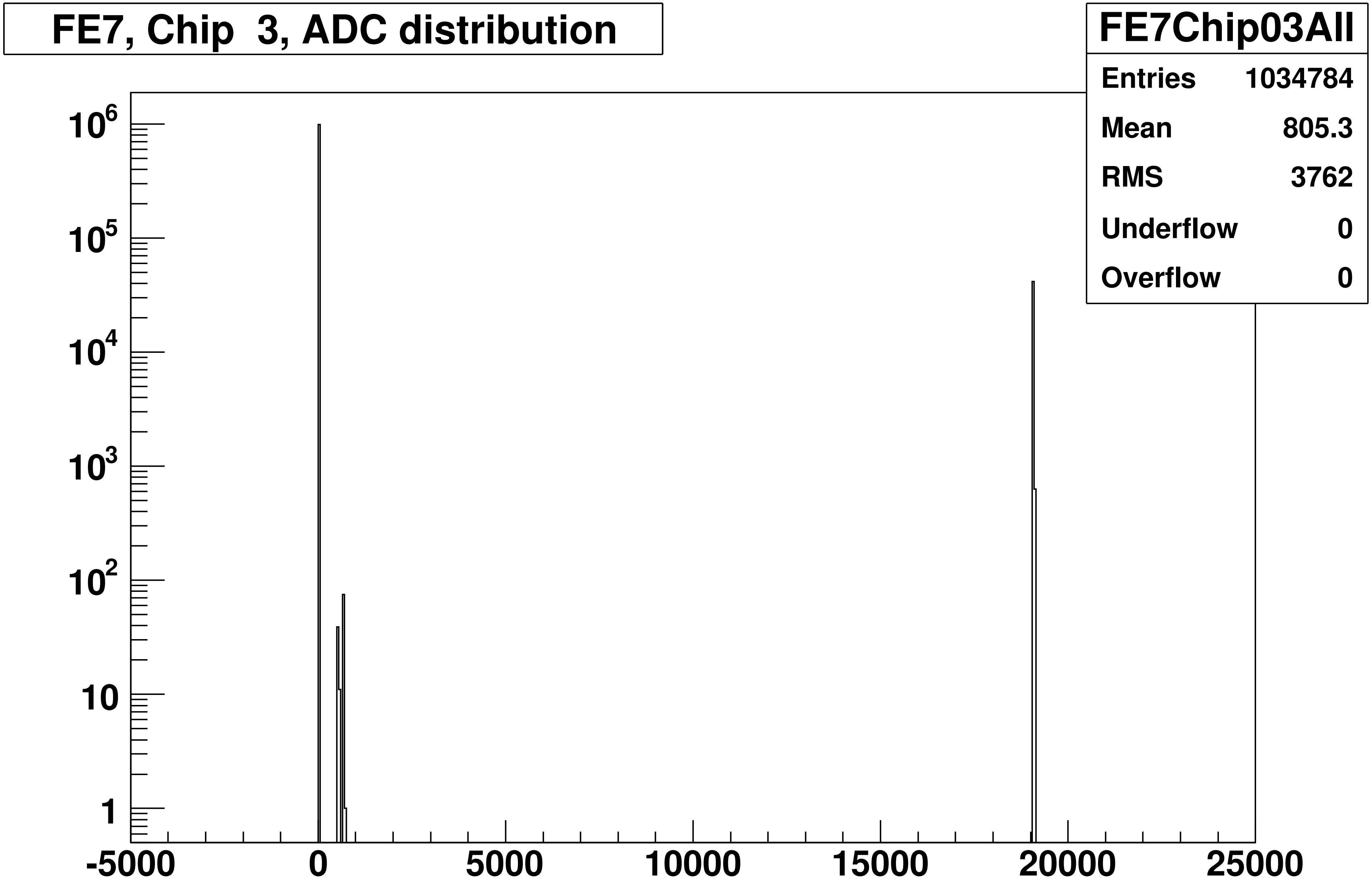
FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 1034784
Mean 805.3
RMS 3762
Underflow 0
Overflow 0
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10^4
10^3
10^2
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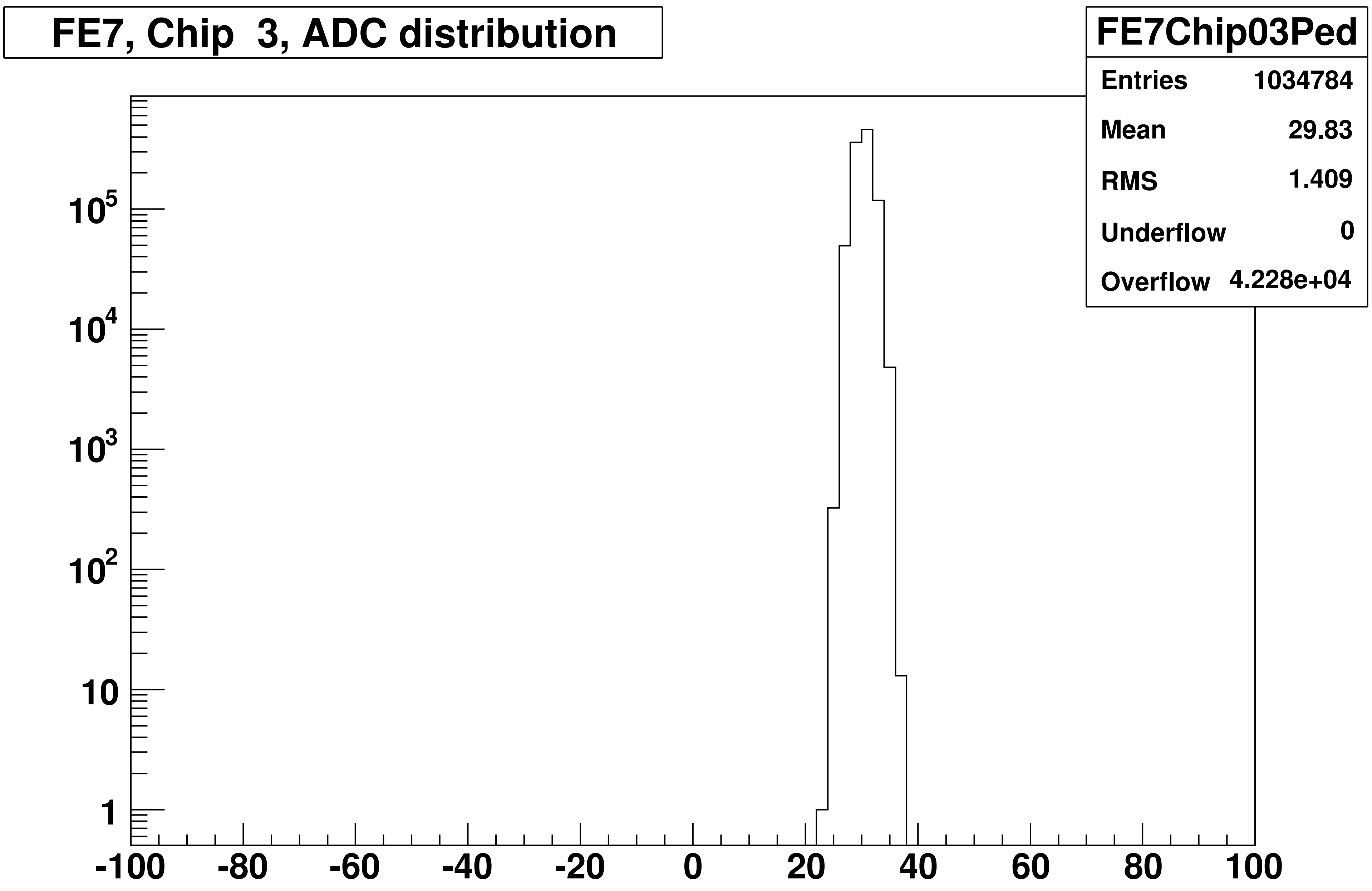

FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 1034784
Mean 29.83
RMS 1.409
Underflow 0
Overflow 4.228e+04
10^5
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10^3
10^2
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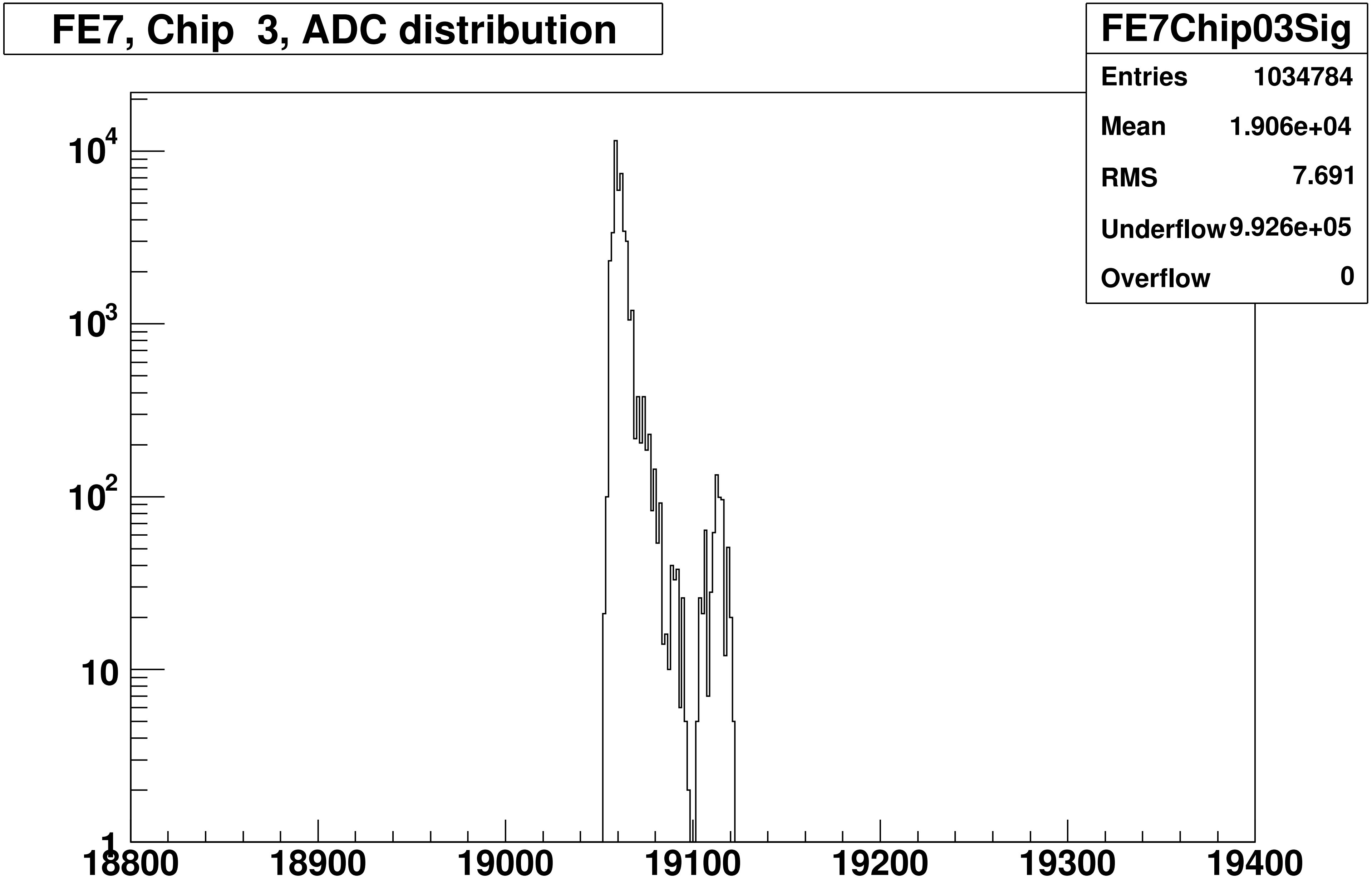
FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 1034784
Mean 1.906e+04
RMS 7.691
Underflow 9.926e+05
Overflow 0
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10^3
10^2
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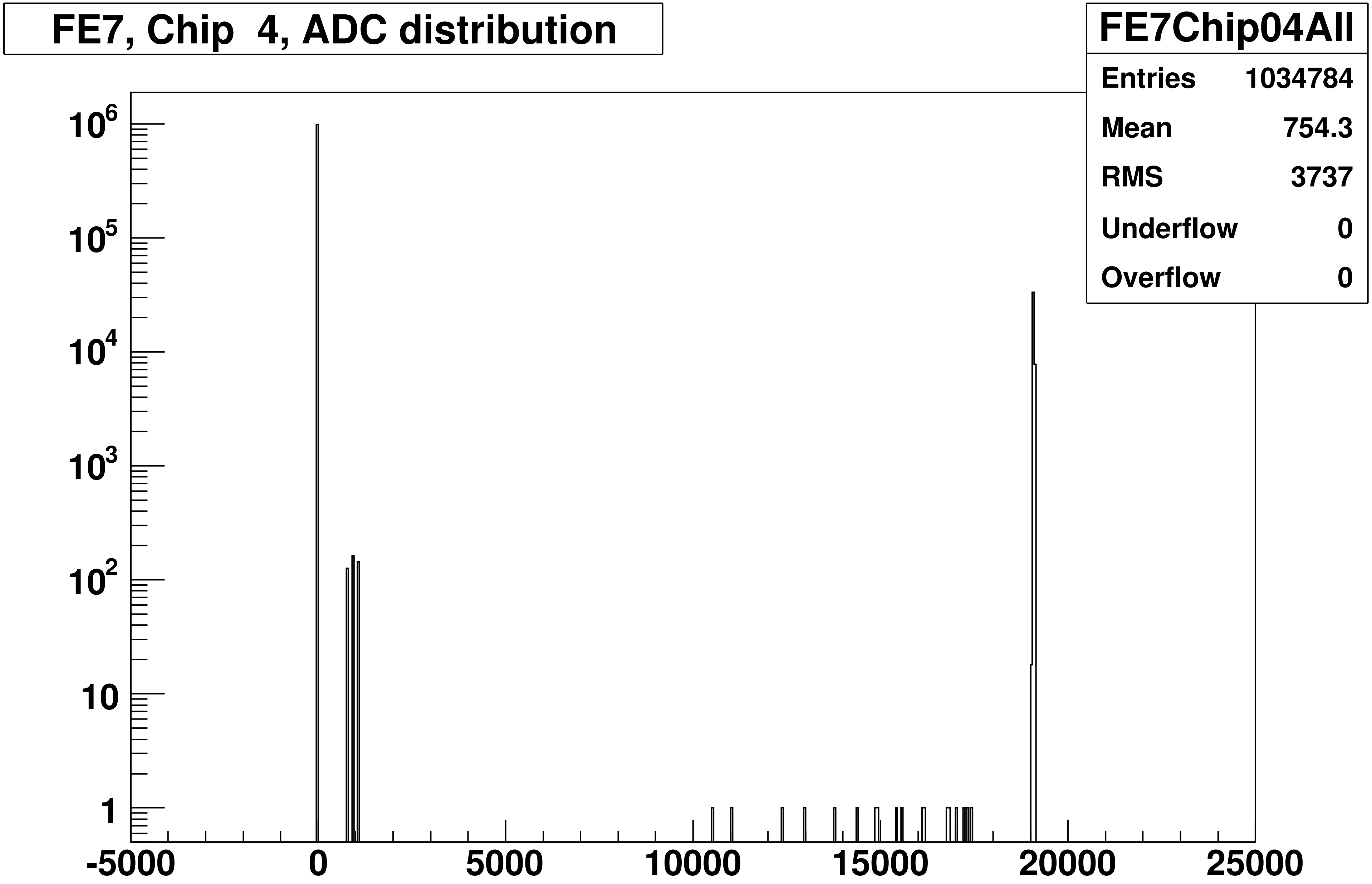

FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 1034784
Mean 754.3
RMS 3737
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
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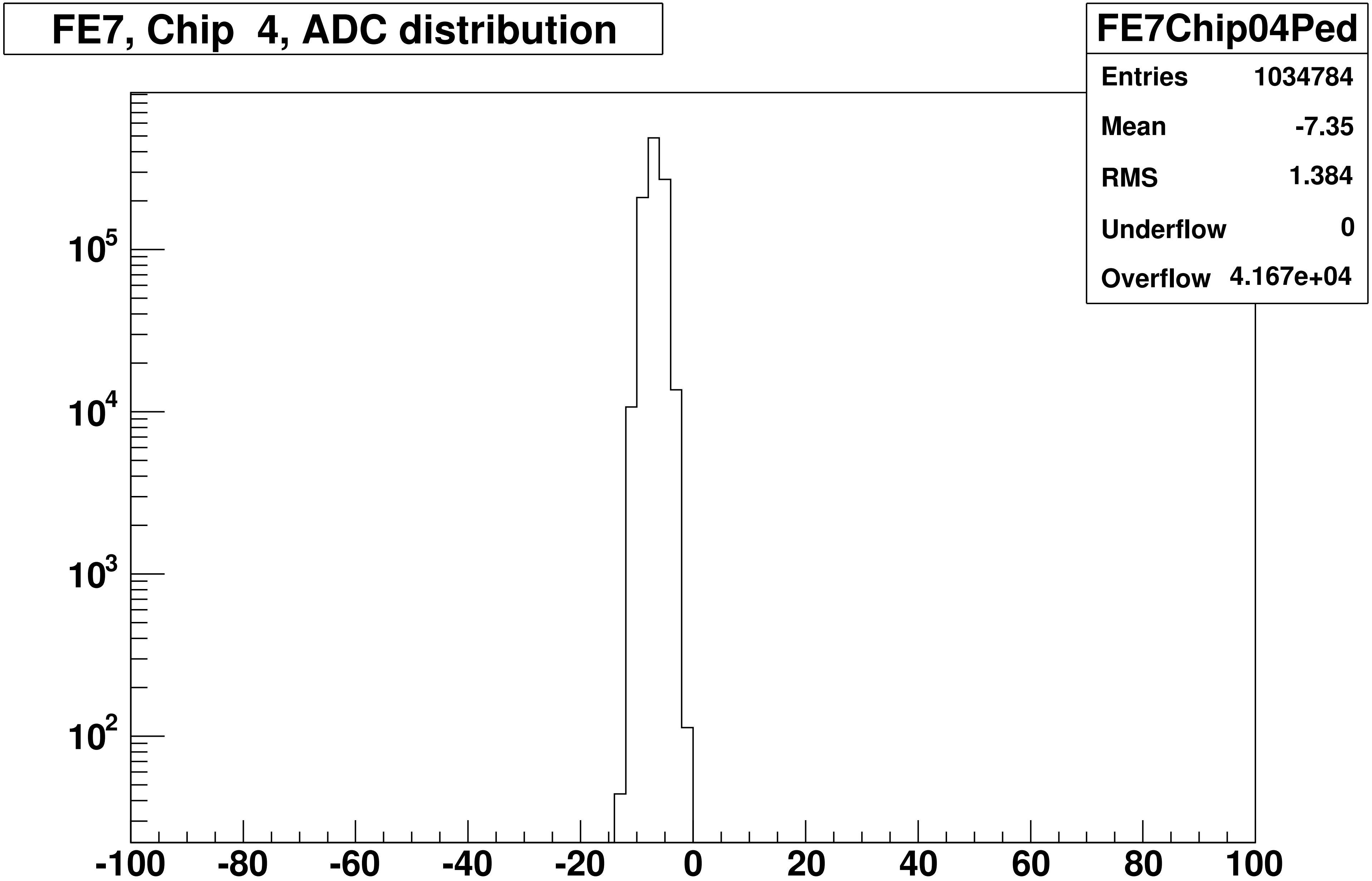
FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 1034784
Mean -7.35
RMS 1.384
Underflow 0
Overflow 4.167e+04
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10^3
10^2
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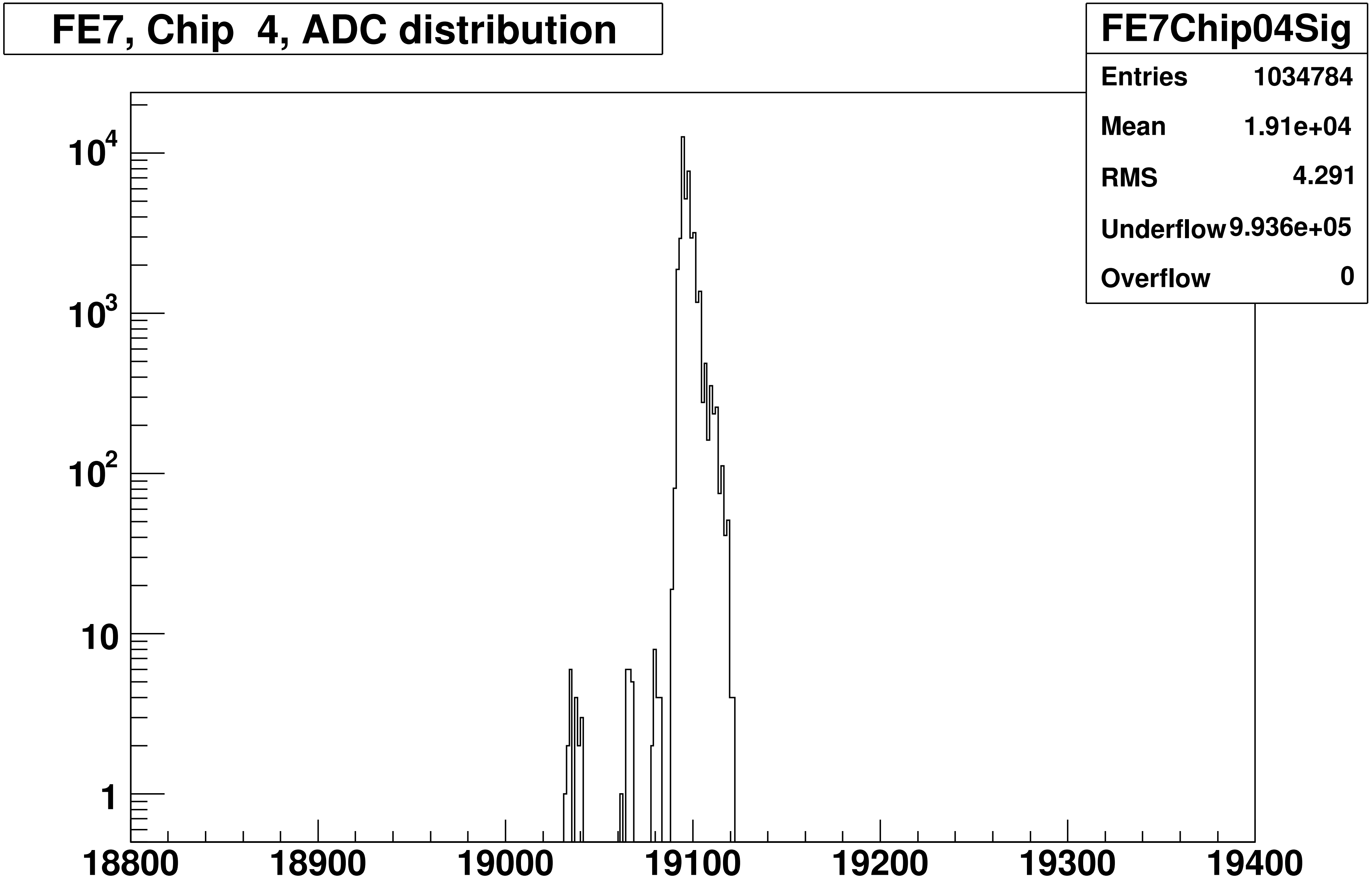
FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 1034784
Mean 1.91e+04
RMS 4.291
Underflow 9.936e+05
Overflow 0
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10^3
10^2
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1
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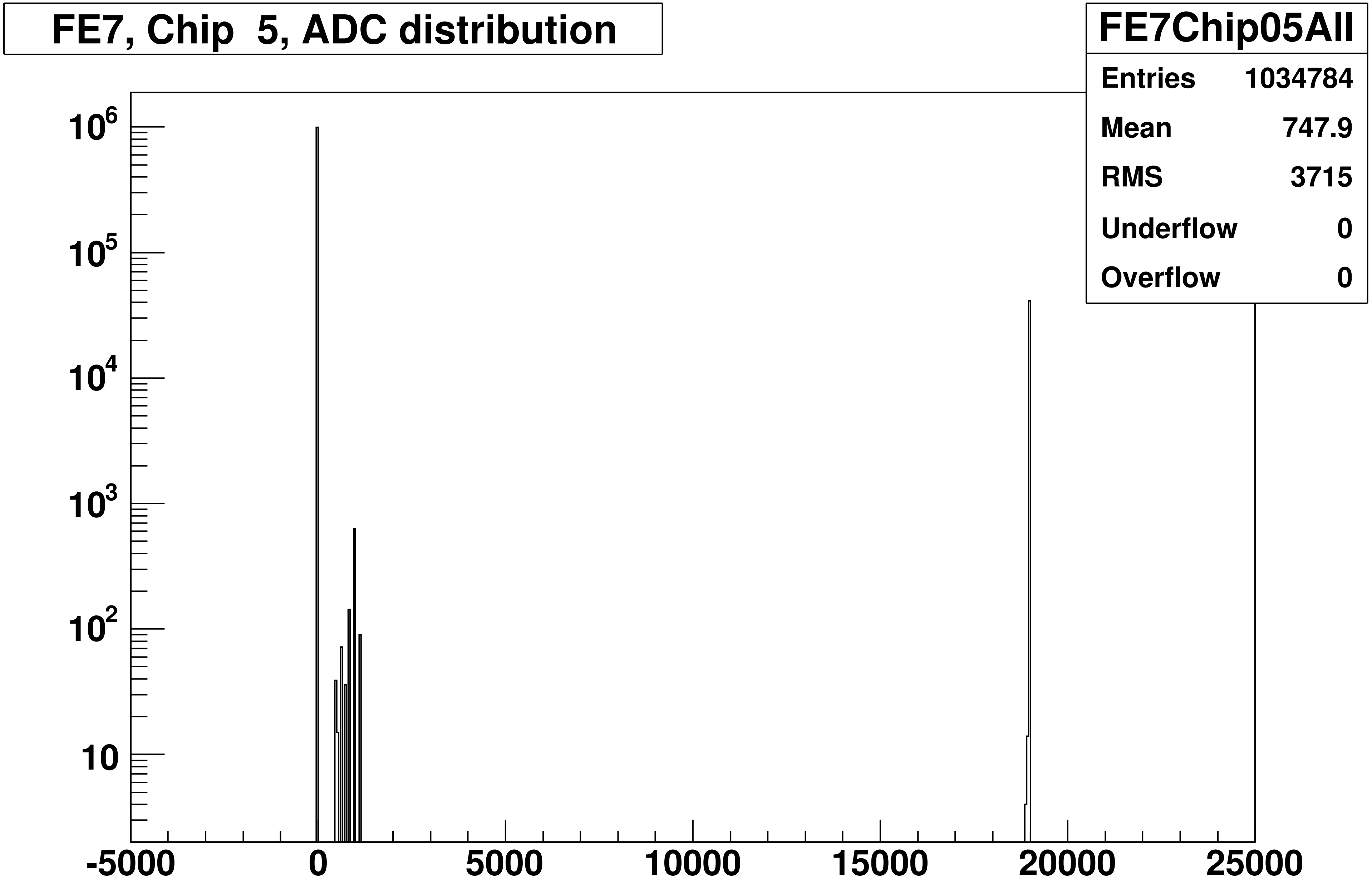

FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 1034784
Mean 747.9
RMS 3715
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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20000
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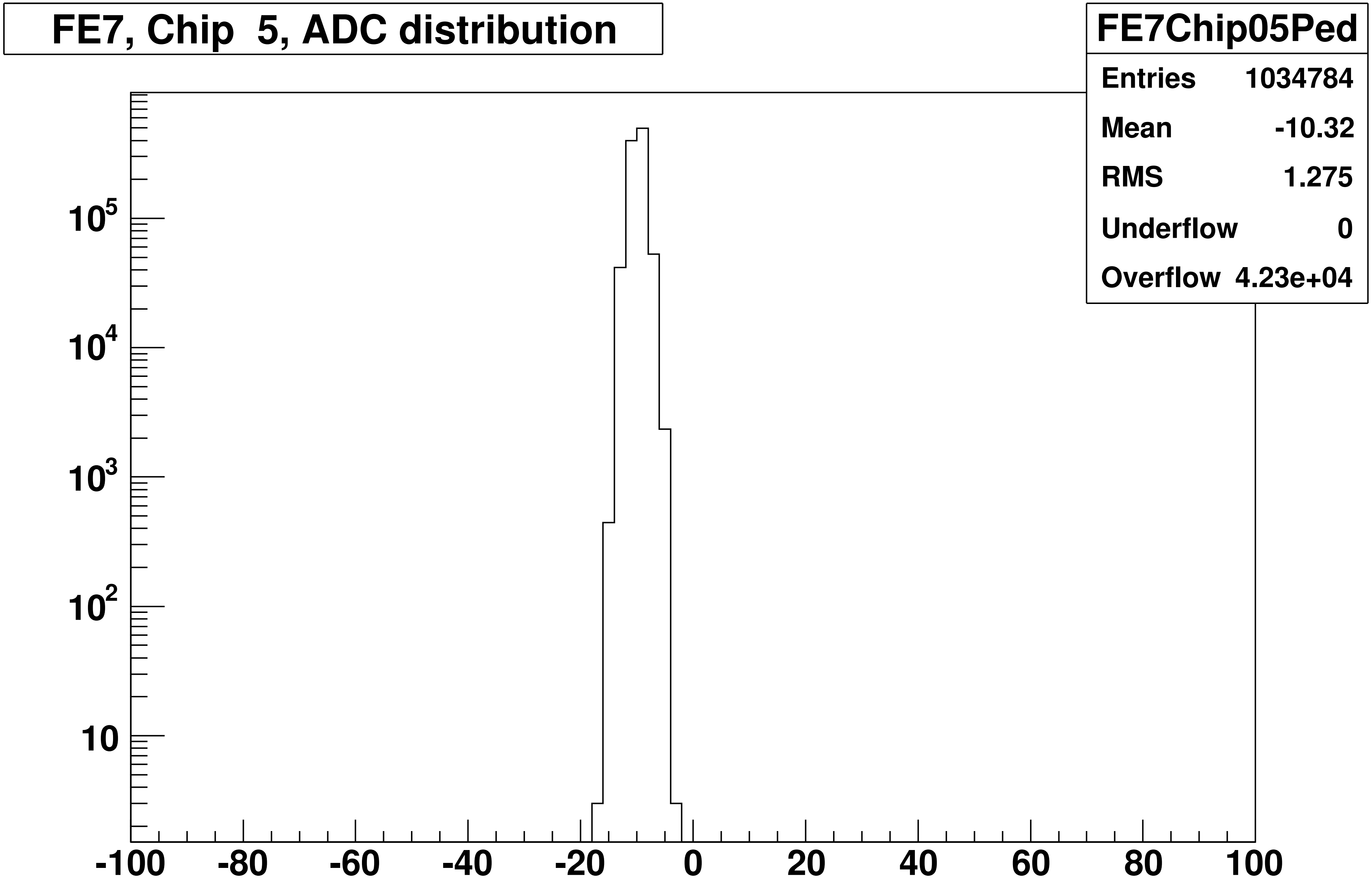
FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 1034784
Mean -10.32
RMS 1.275
Underflow 0
Overflow 4.23e+04
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10^2
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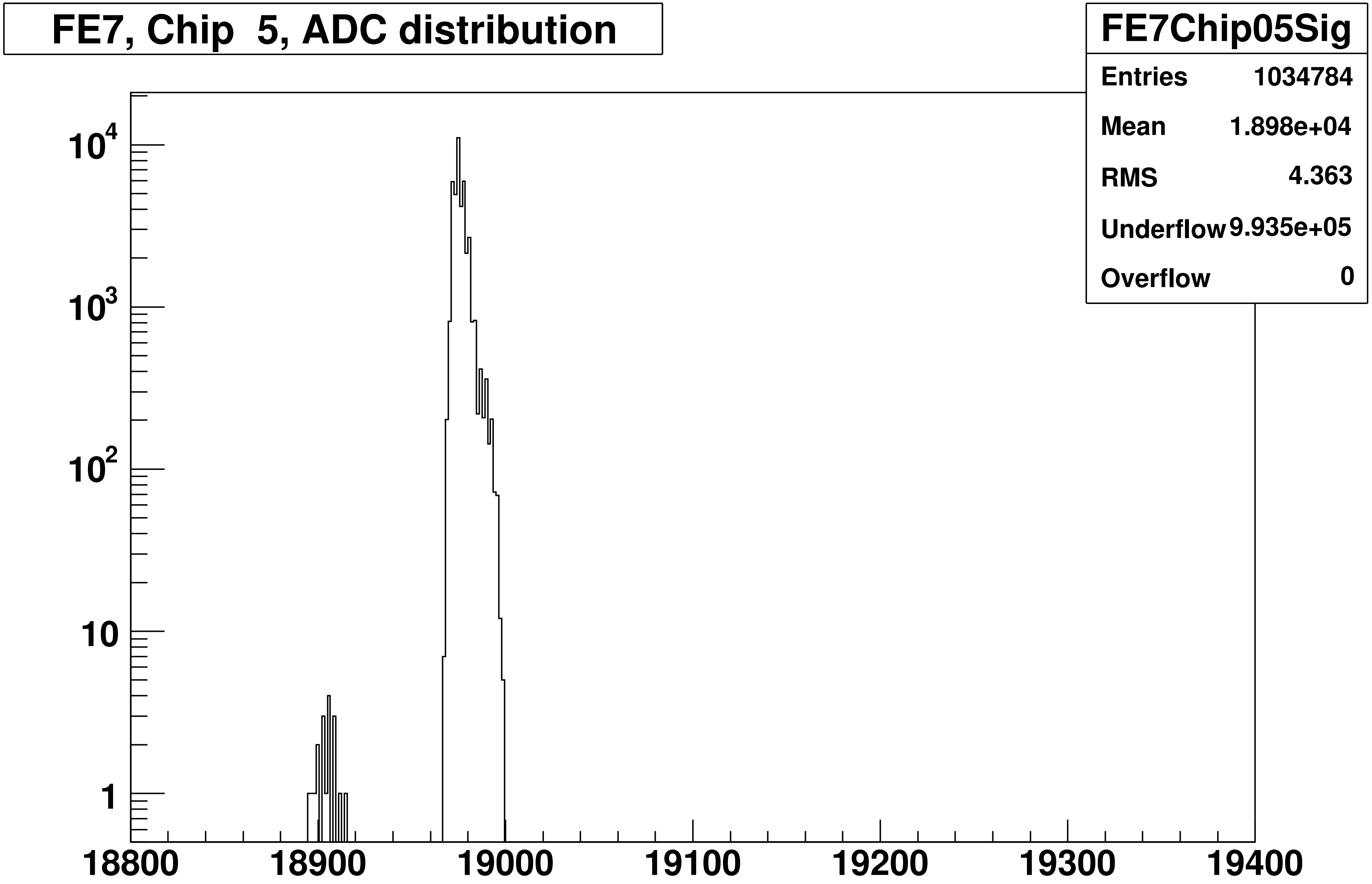

FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 1034784
Mean 1.898e+04
RMS 4.363
Underflow 9.935e+05
Overflow 0
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10^3
10^2
10
1
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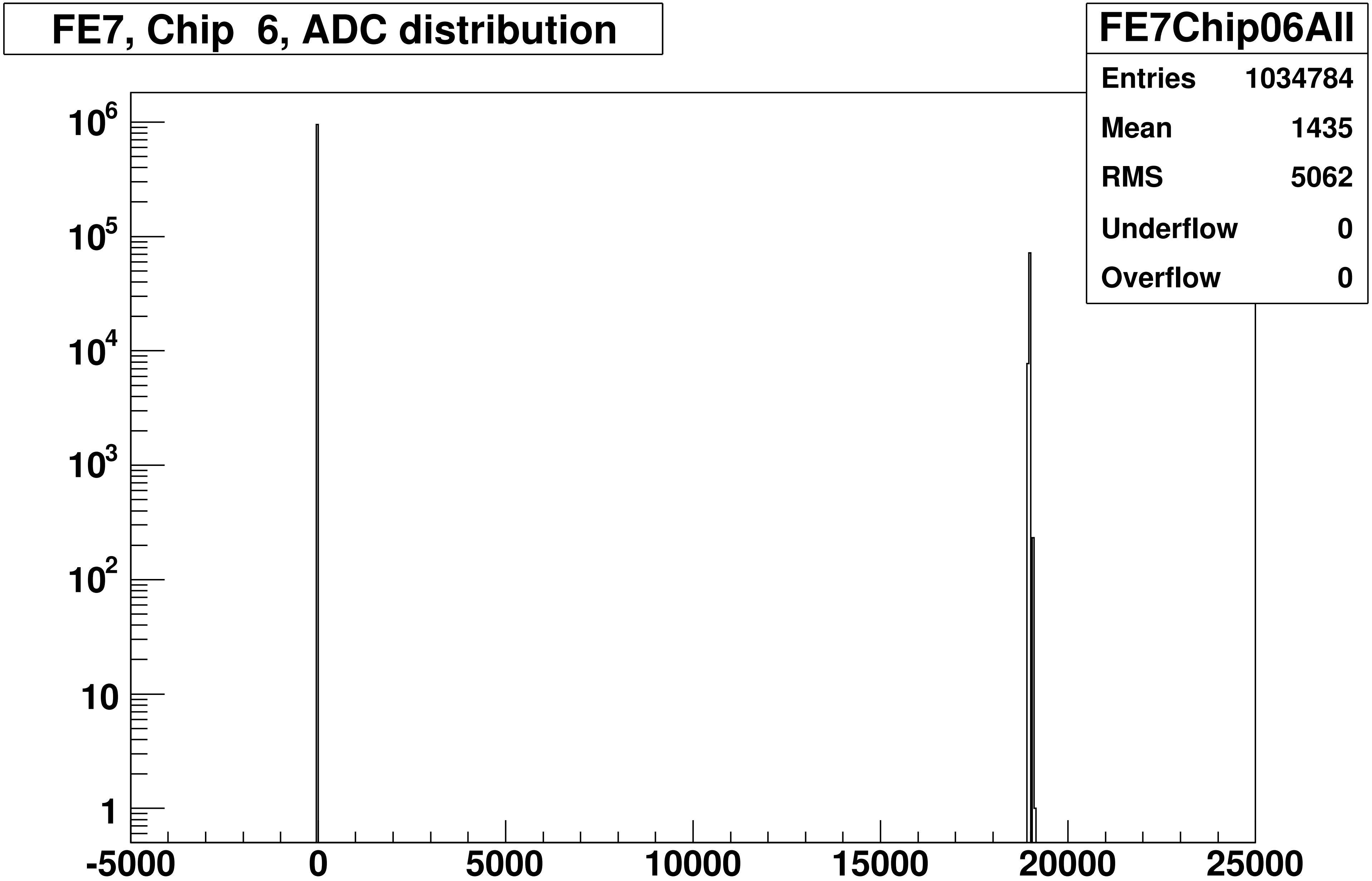

FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 1034784
Mean 1435
RMS 5062
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
5000
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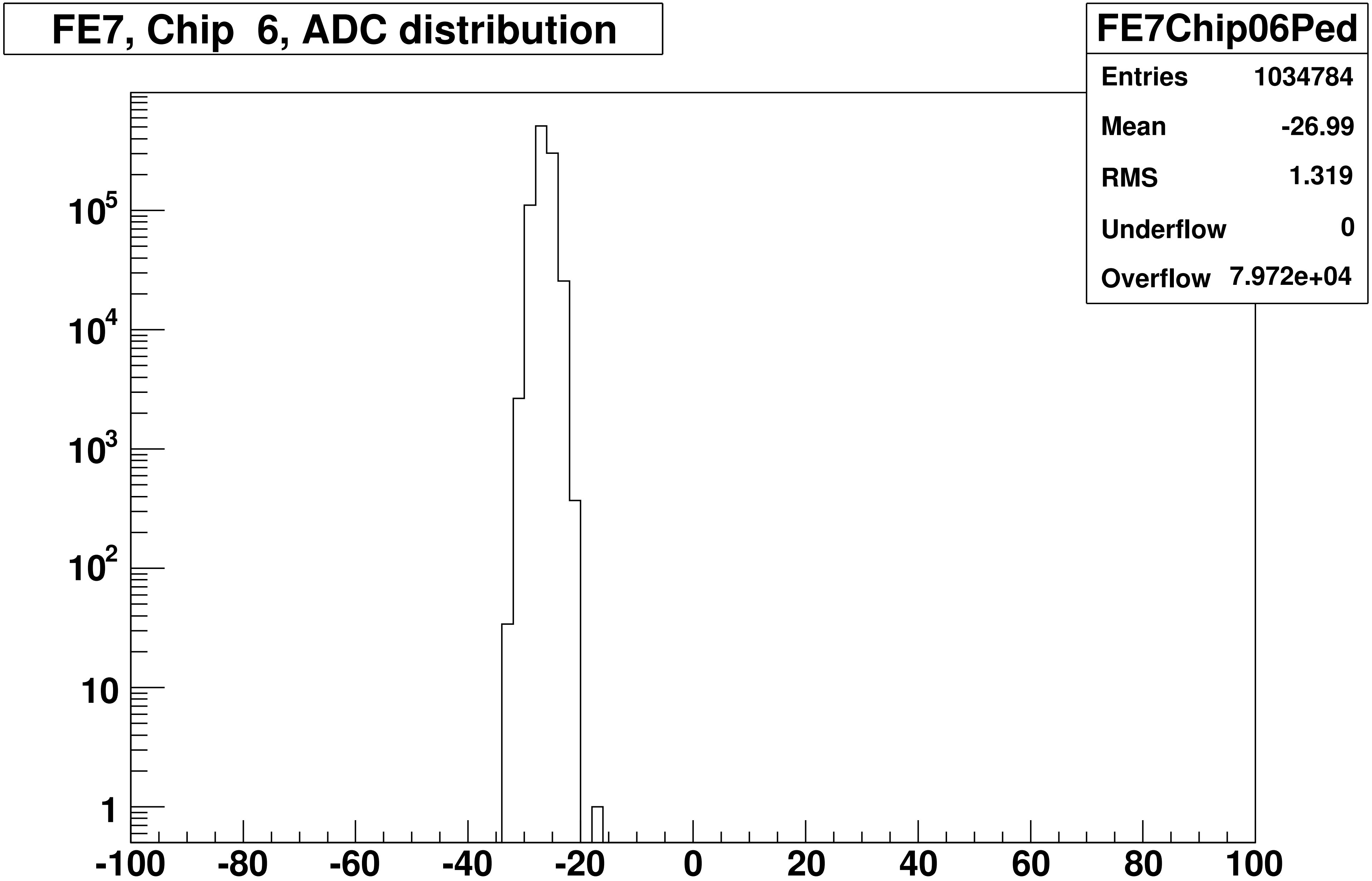
25000


FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 1034784
Mean -26.99
RMS 1.319
Underflow 0
Overflow 7.972e+04
10^5
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80
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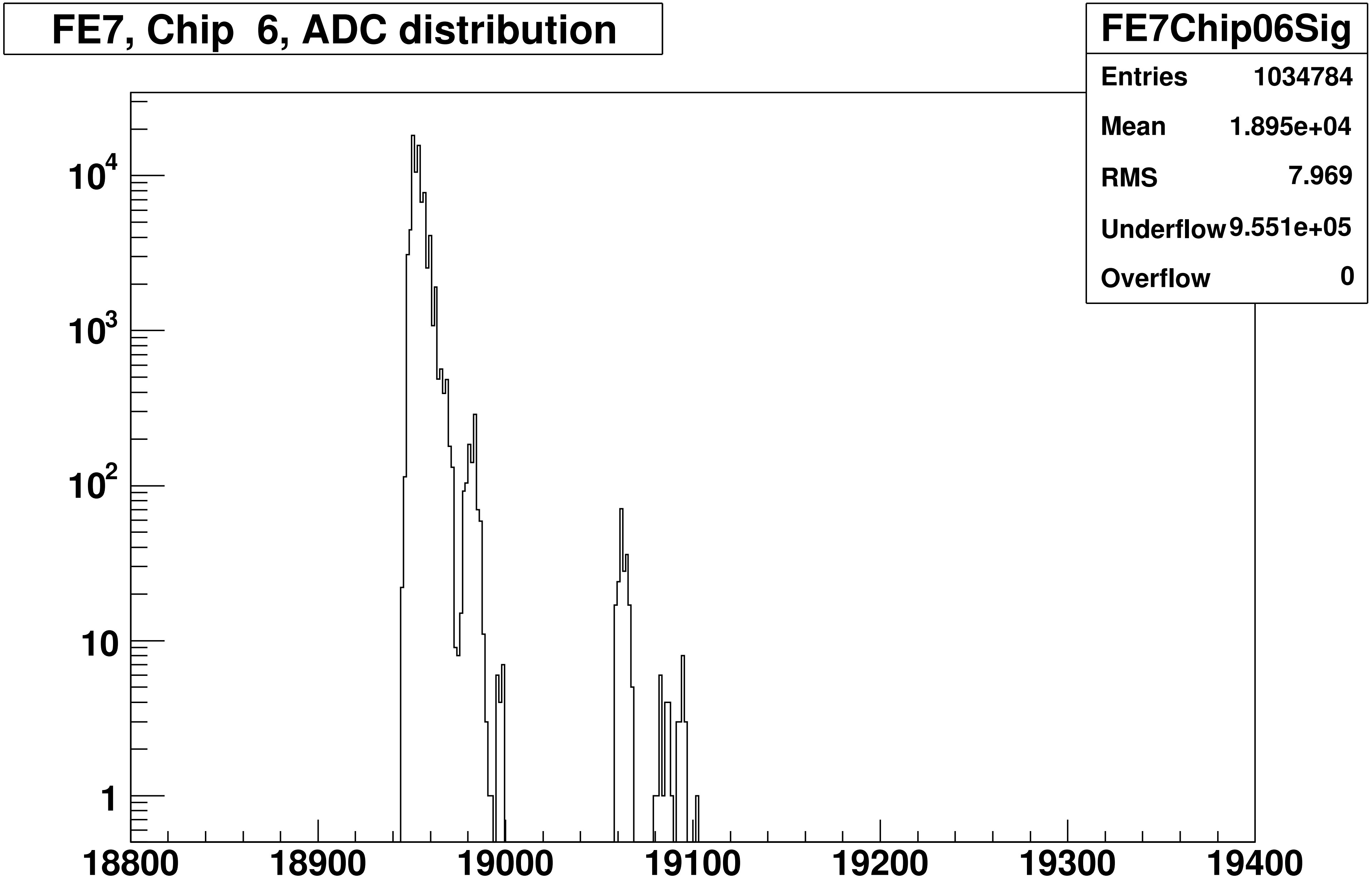

FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 1034784
Mean 1.895e+04
RMS 7.969
Underflow 9.551e+05
Overflow 0
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10^3
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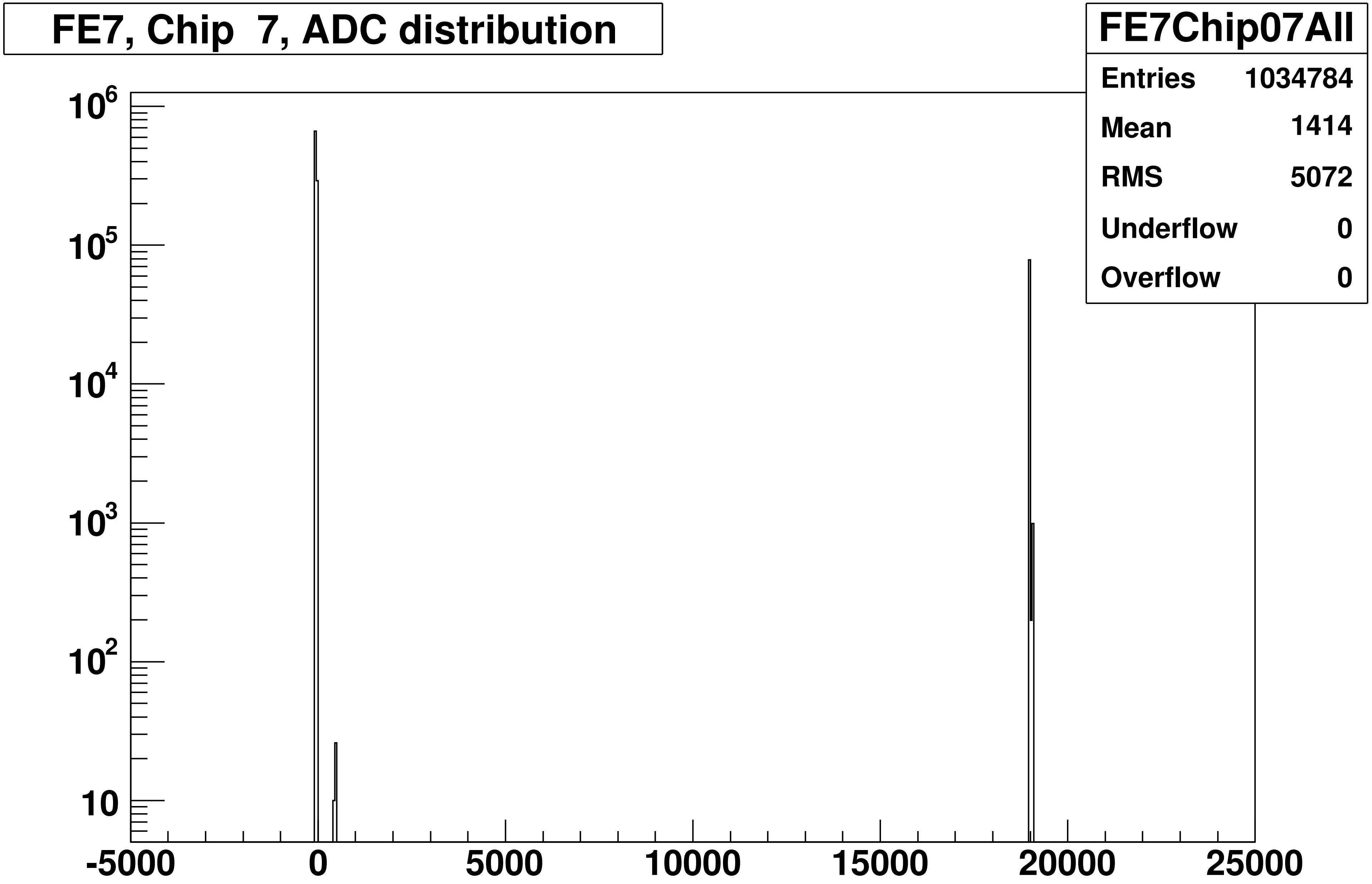

FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 1034784
Mean 1414
RMS 5072
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
5000
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20000
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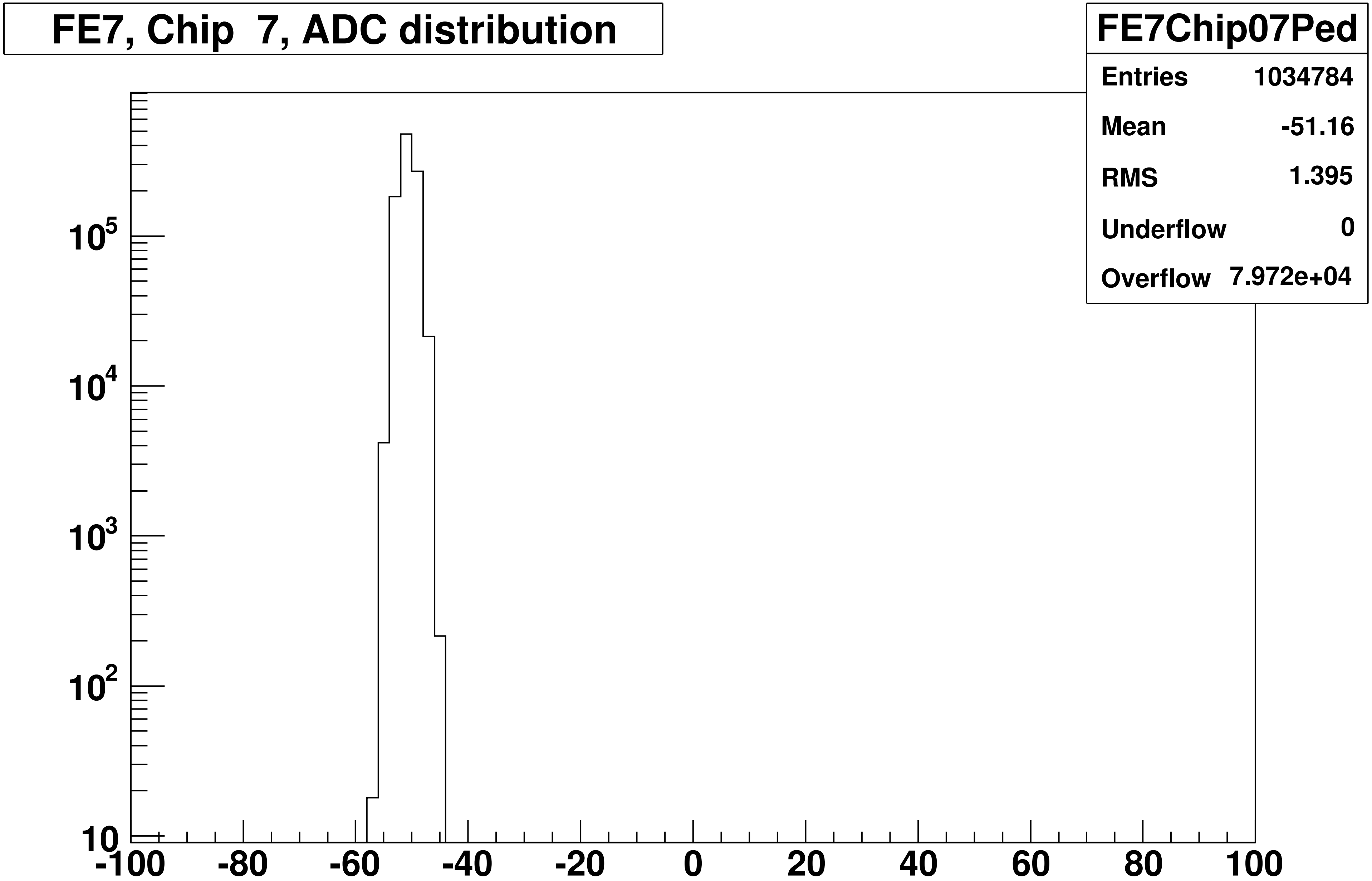
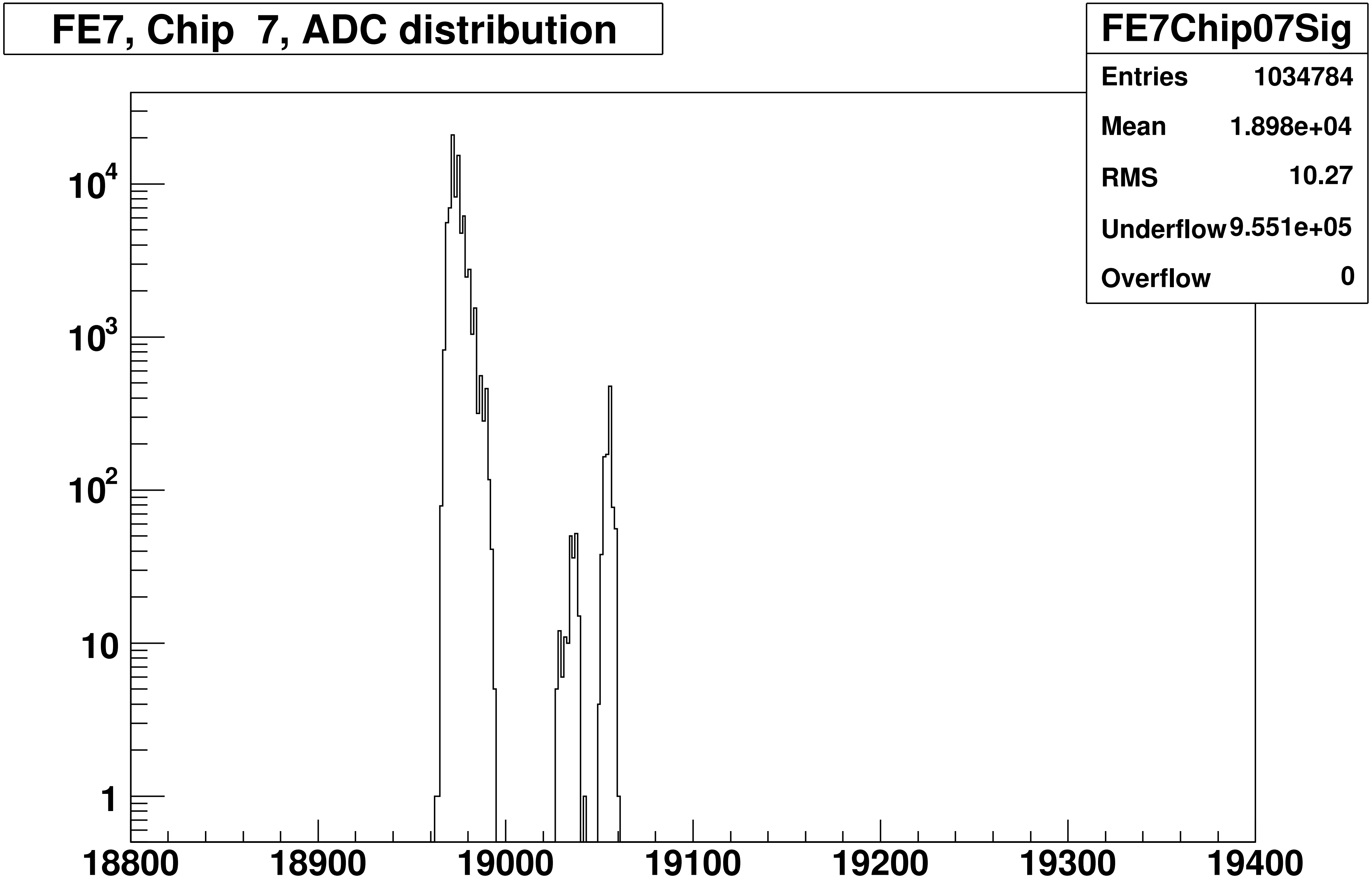
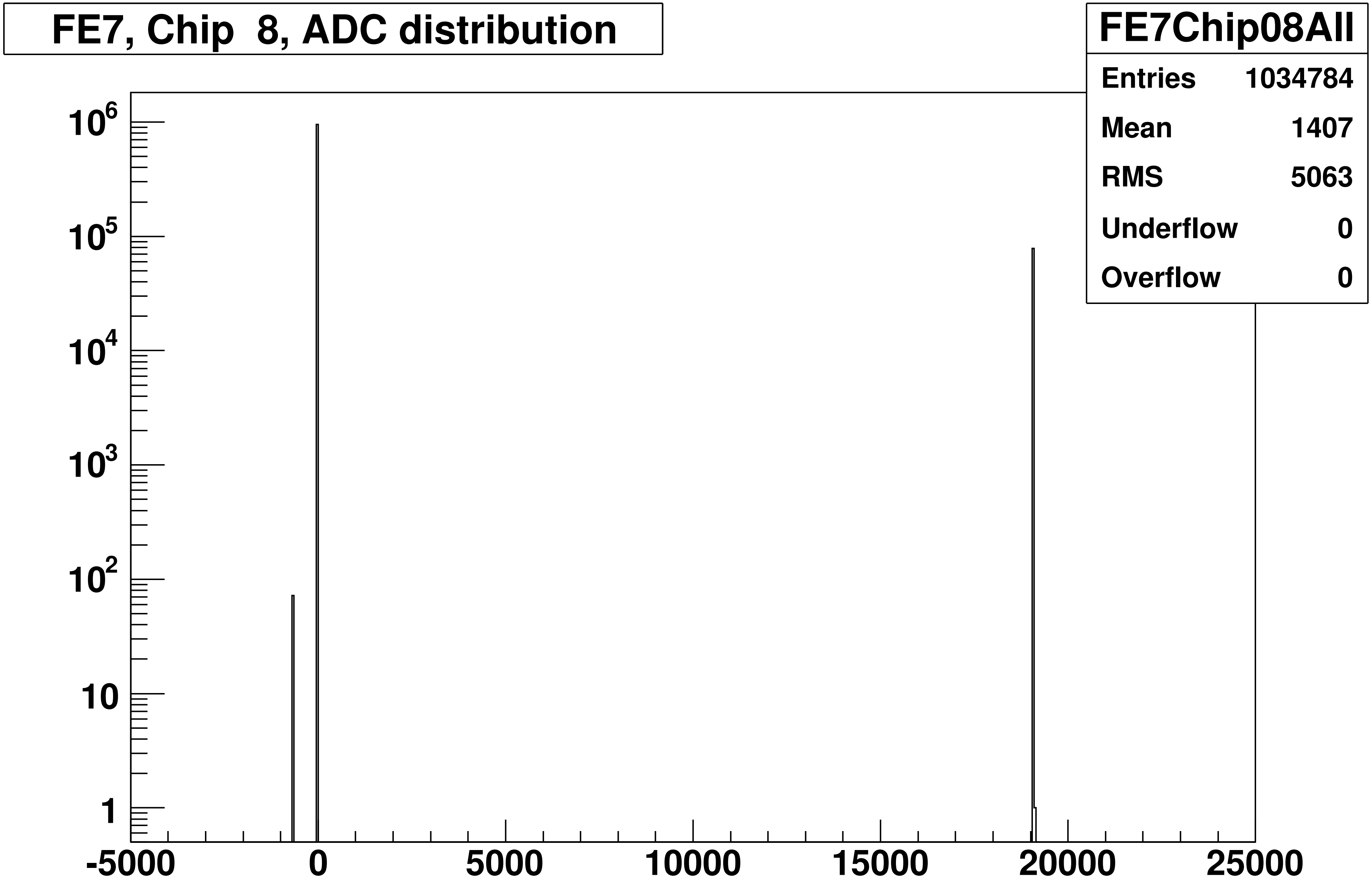
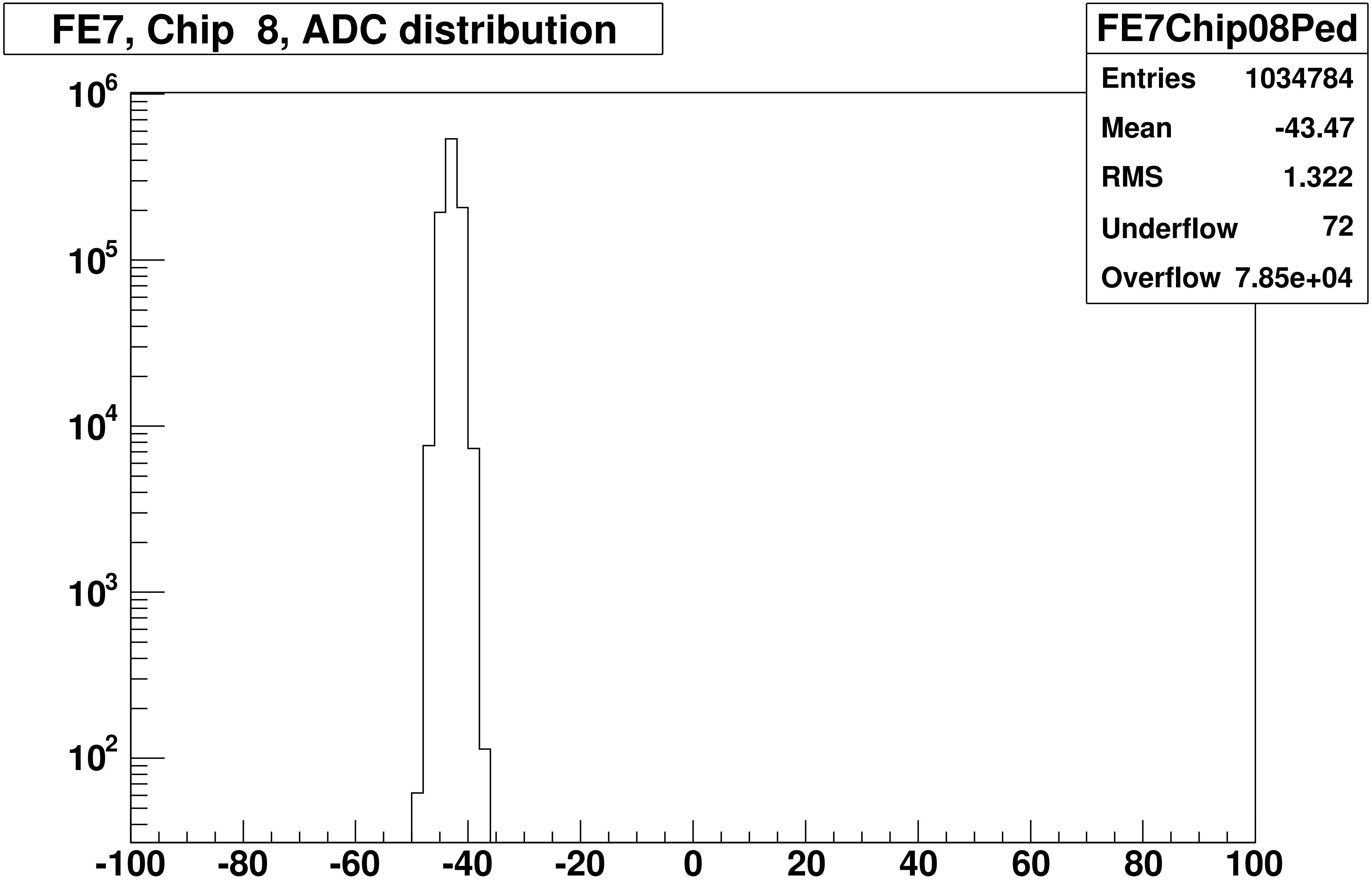


FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 1034784
Mean -51.16
RMS 1.395
Underflow 0
Overflow 7.972e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

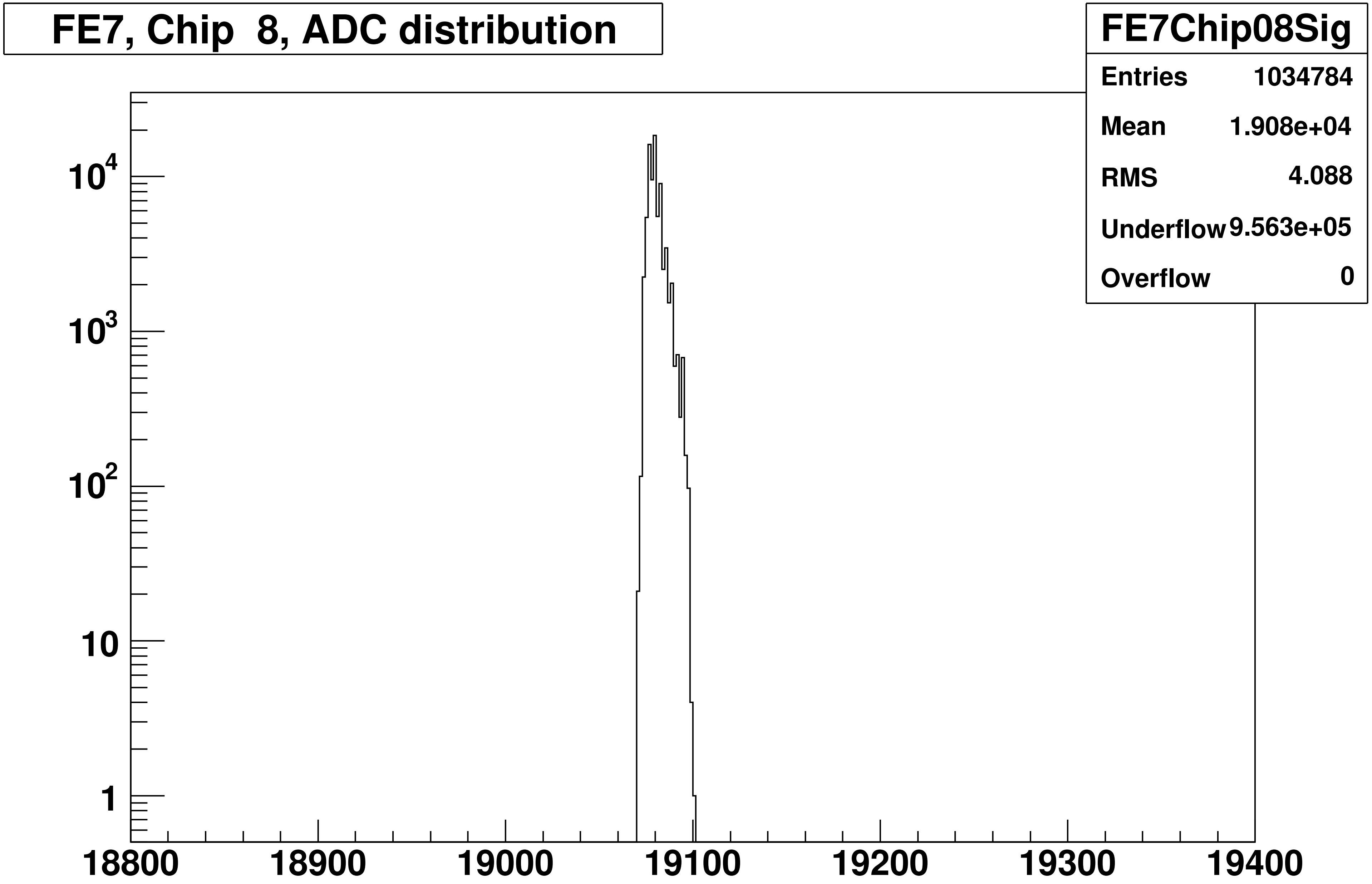

FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 1034784
Mean 1.898e+04
RMS 10.27
Underflow 9.551e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

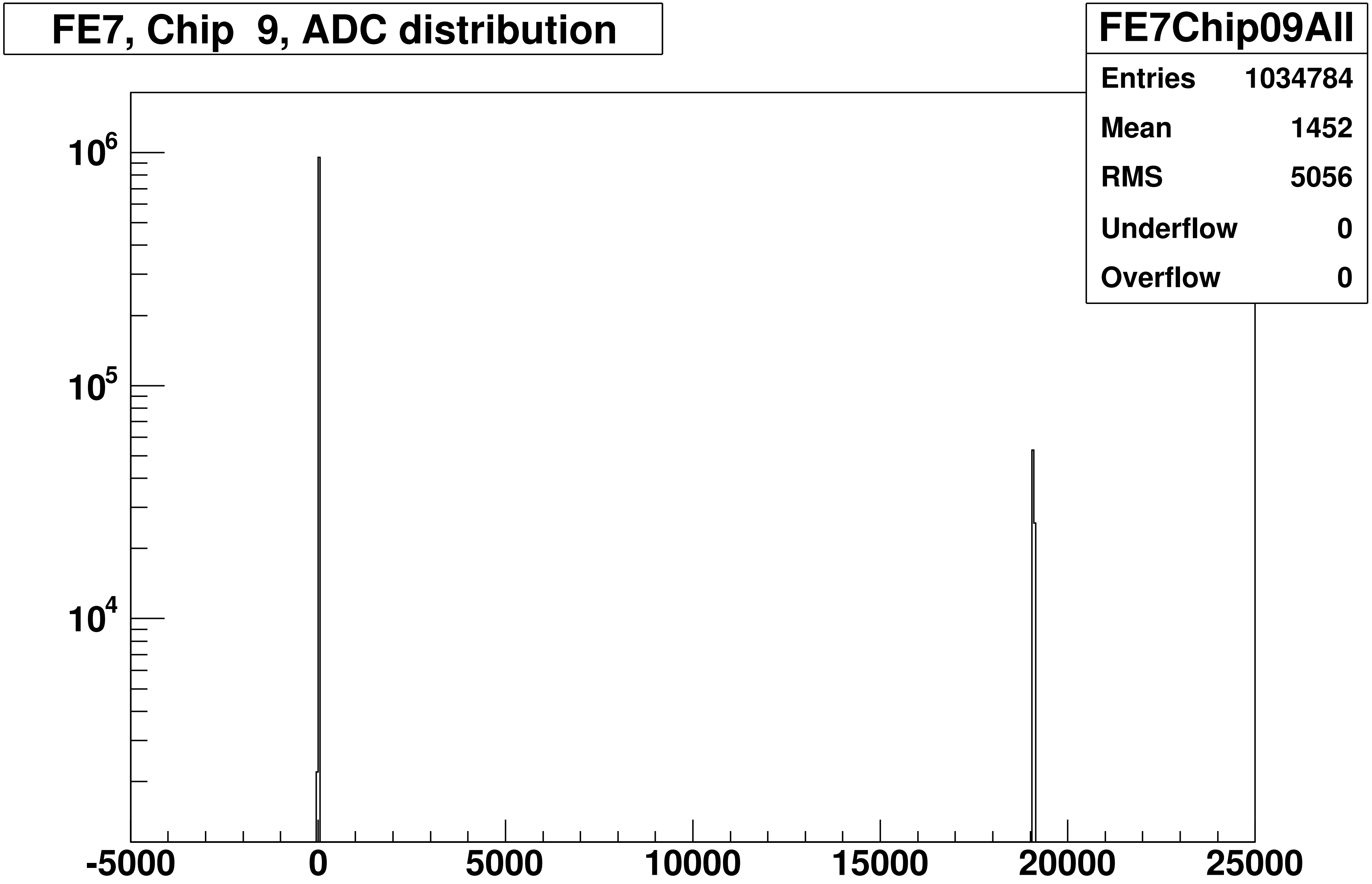


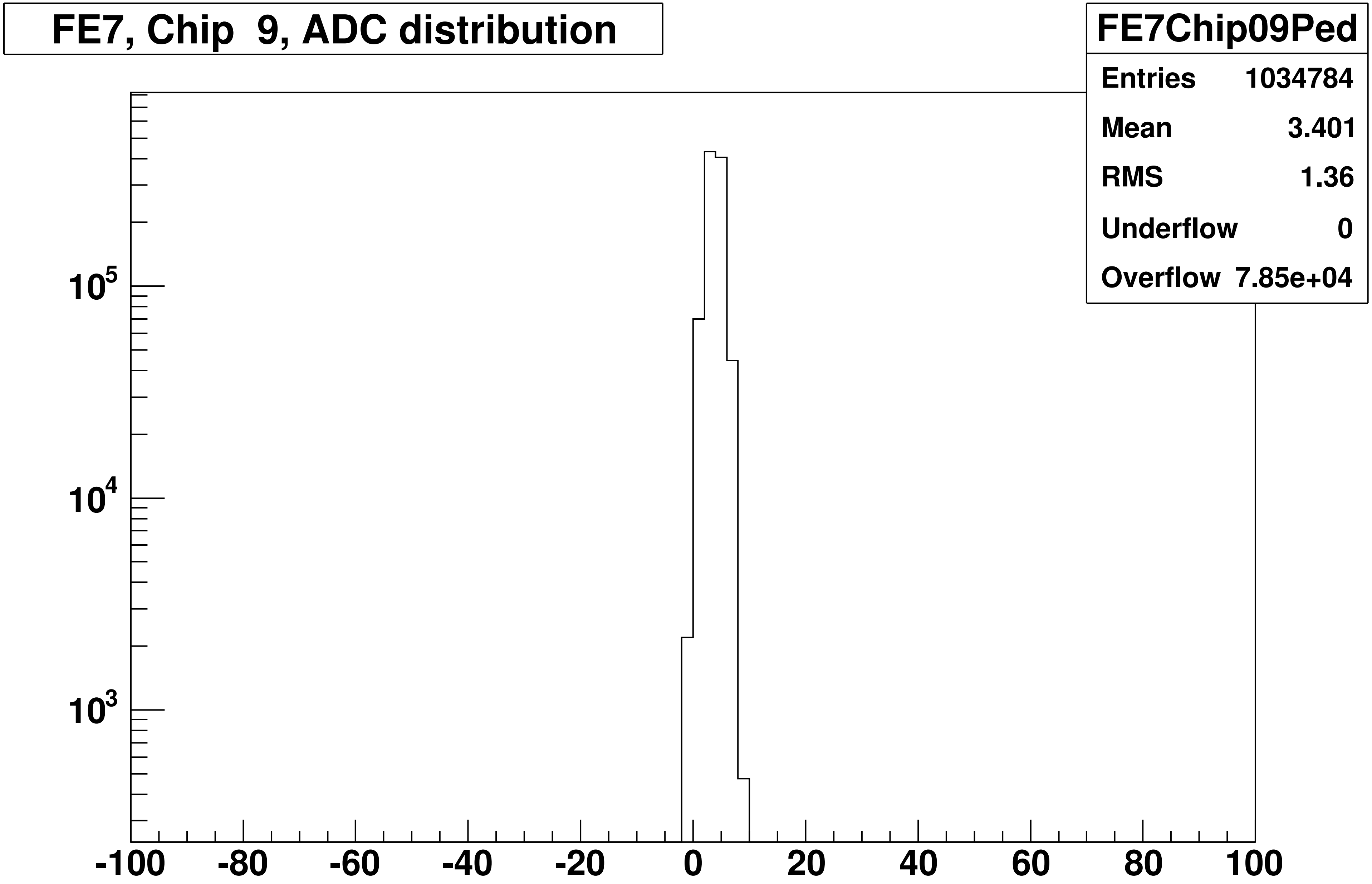
FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 1034784
Mean 1407
RMS 5063
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

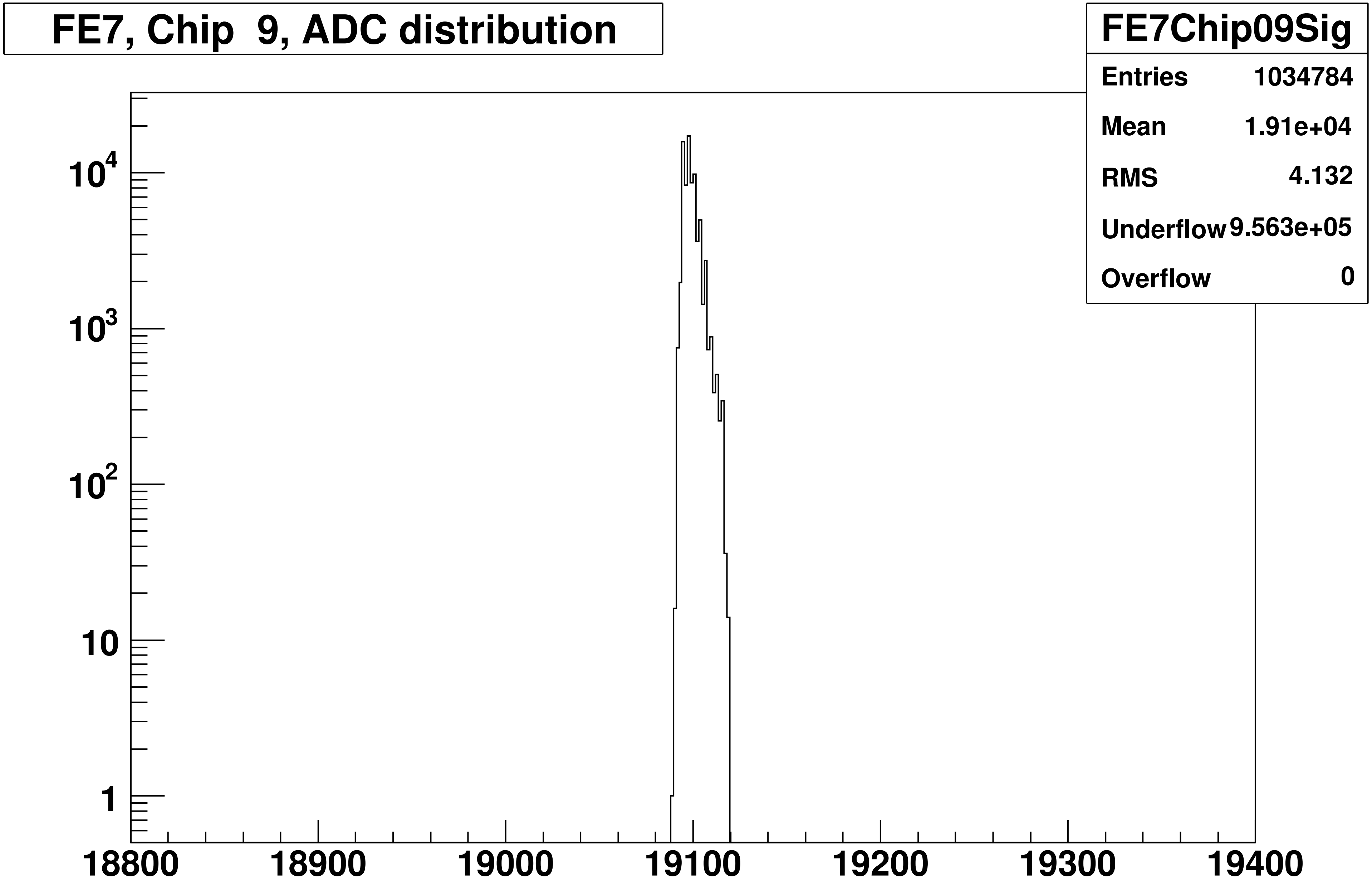


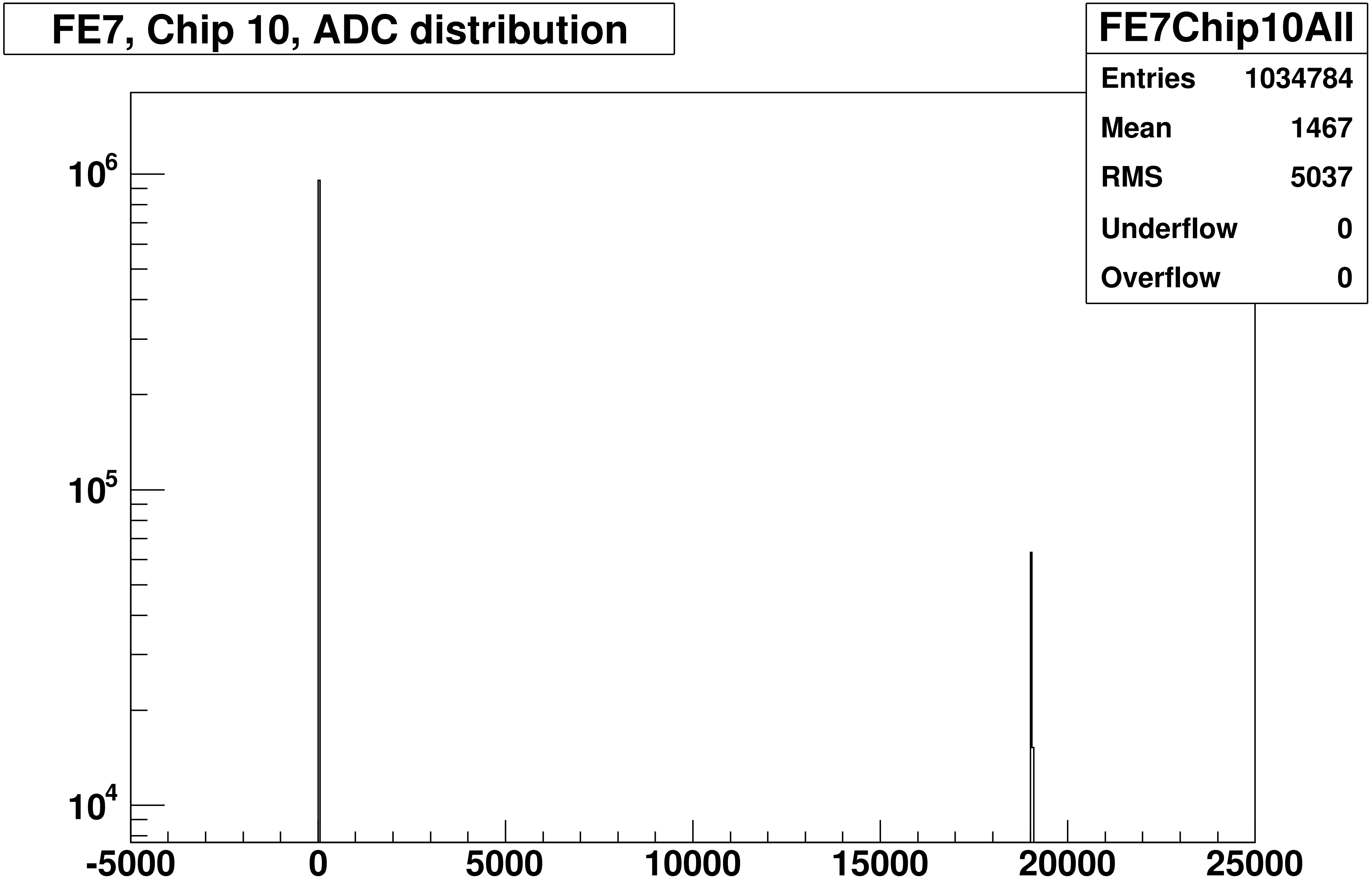
FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 1034784
Mean -43.47
RMS 1.322
Underflow 72
Overflow 7.85e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 1034784
Mean 1.908e+04
RMS 4.088
Underflow 9.563e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

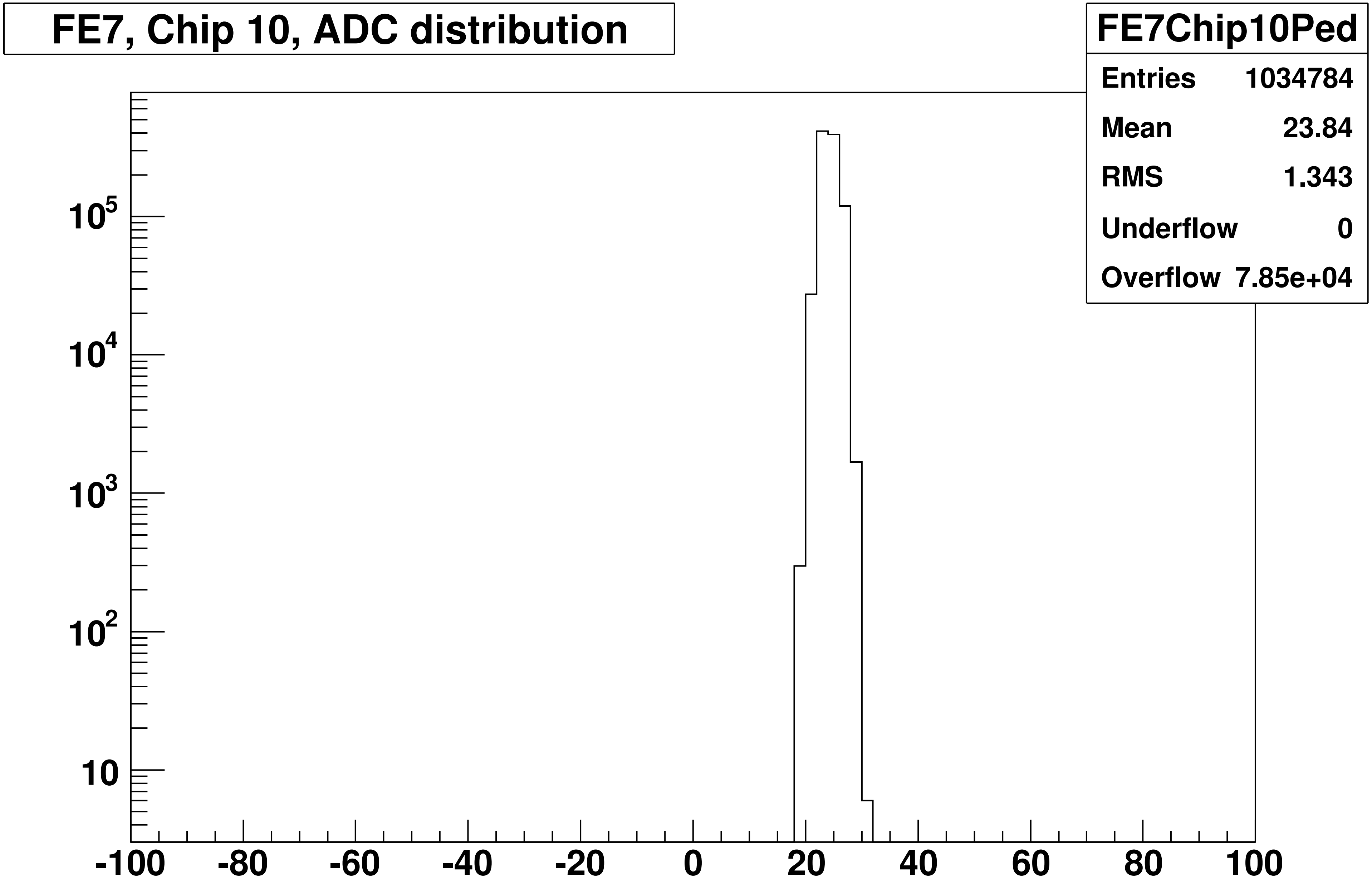

FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 1034784
Mean 1452
RMS 5056
Underflow 0
Overflow 0
10^6
10^5
10^4
-5000
0
5000
10000
15000
20000
25000

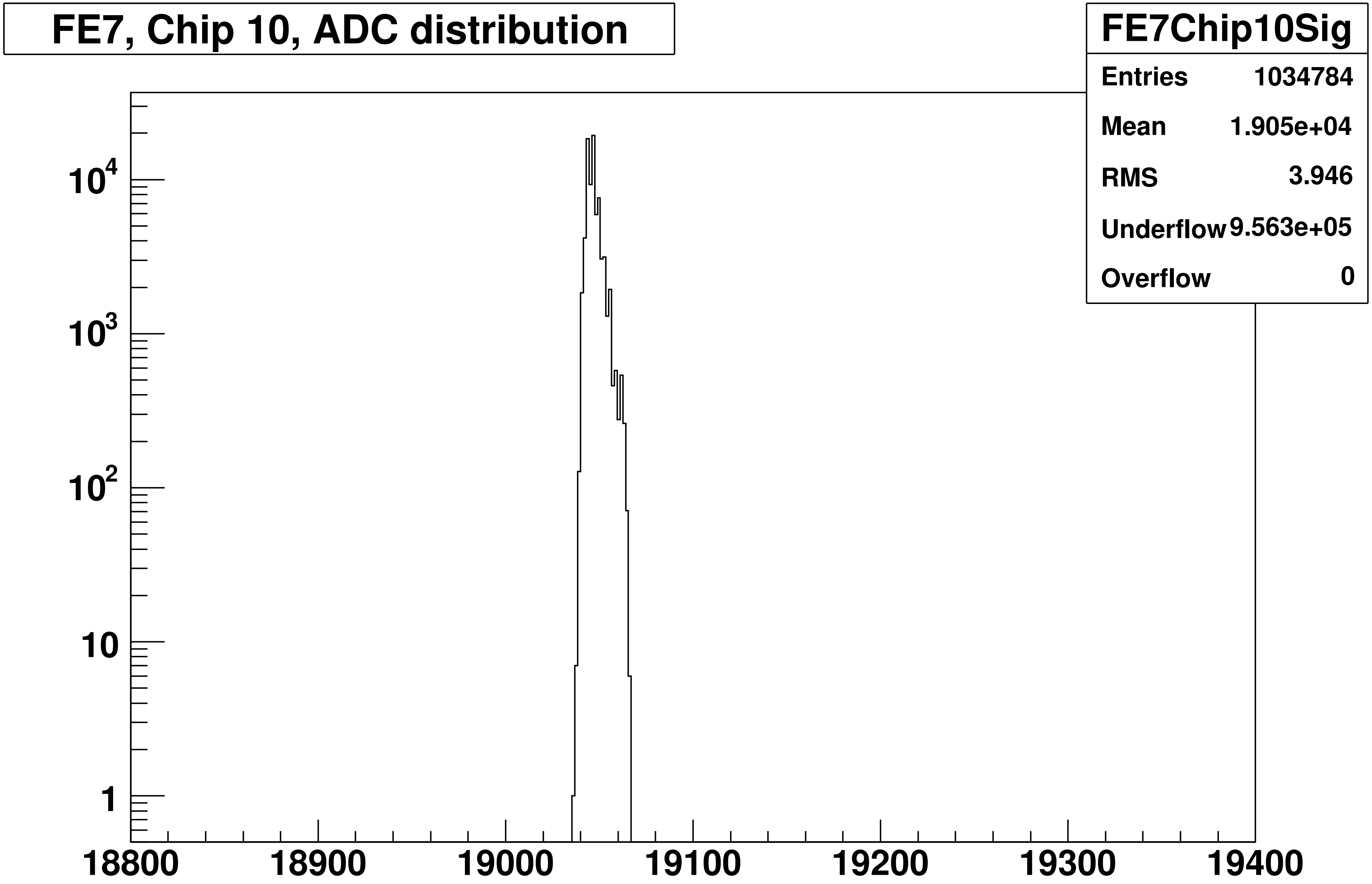

FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 1034784
Mean 3.401
RMS 1.36
Underflow 0
Overflow 7.85e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 1034784
Mean 1.91e+04
RMS 4.132
Underflow 9.563e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

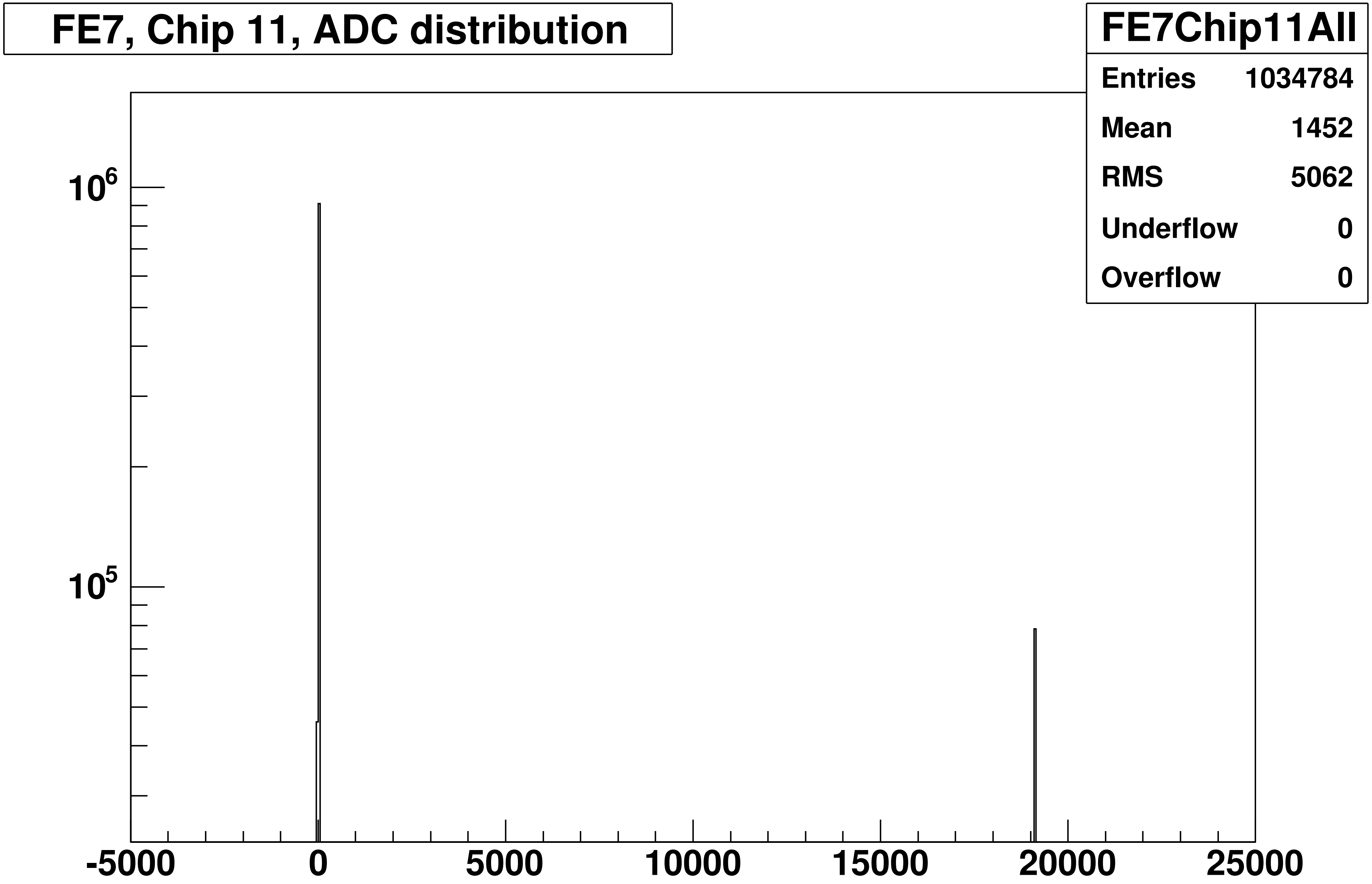


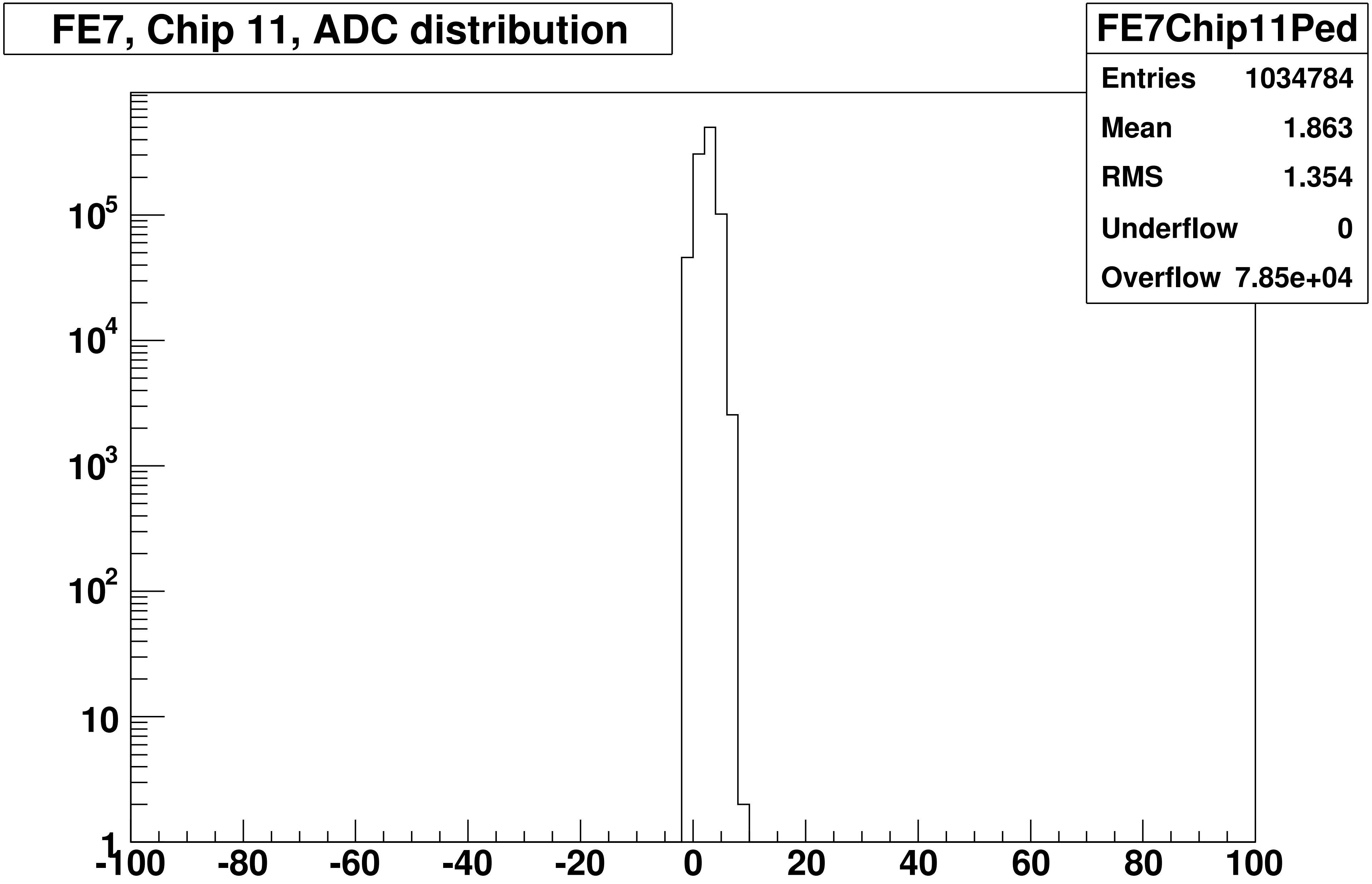
FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 1034784
Mean 1467
RMS 5037
Underflow 0
Overflow 0
10^6
10^5
10^4
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 1034784
Mean 23.84
RMS 1.343
Underflow 0
Overflow 7.85e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

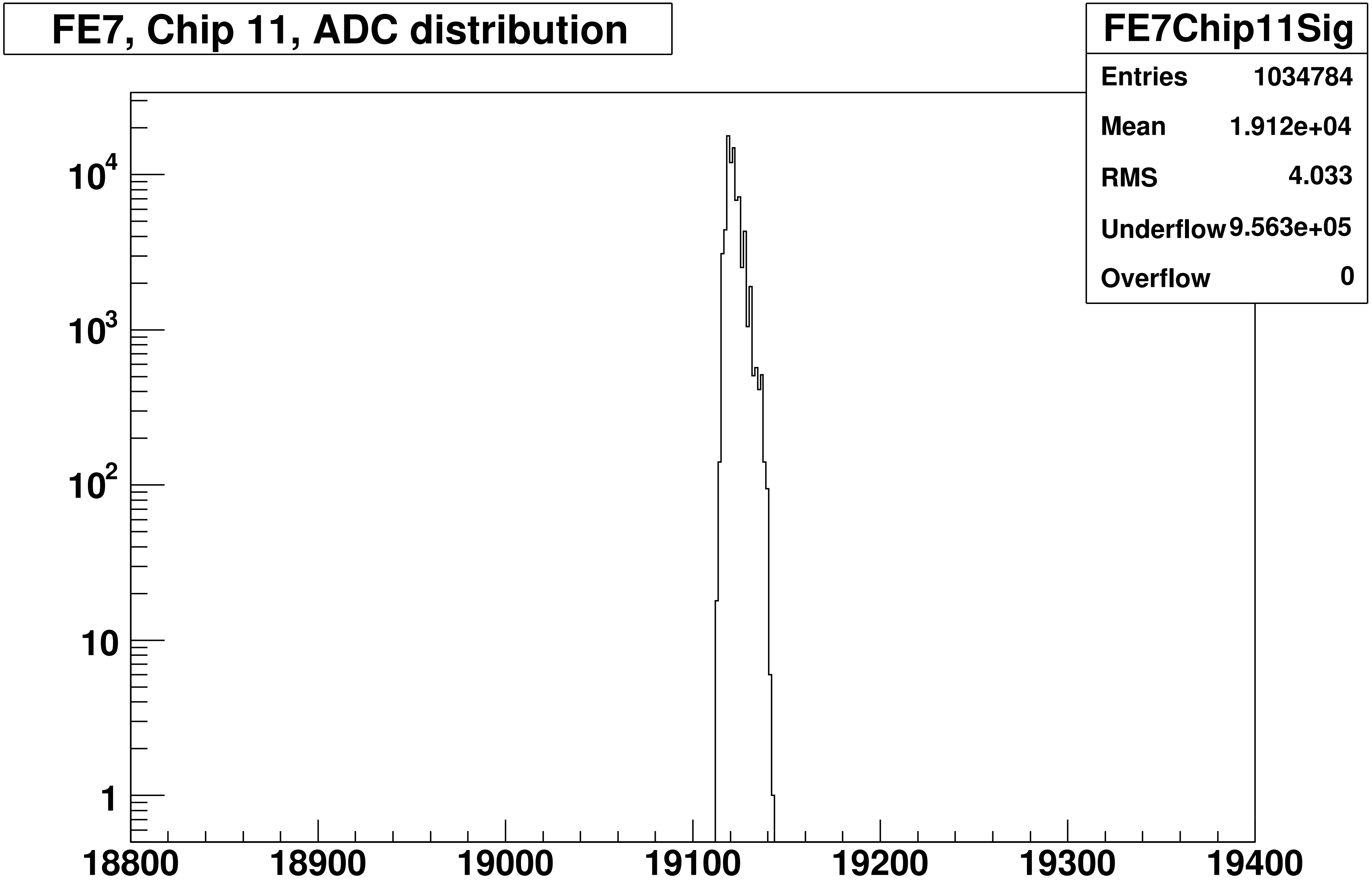


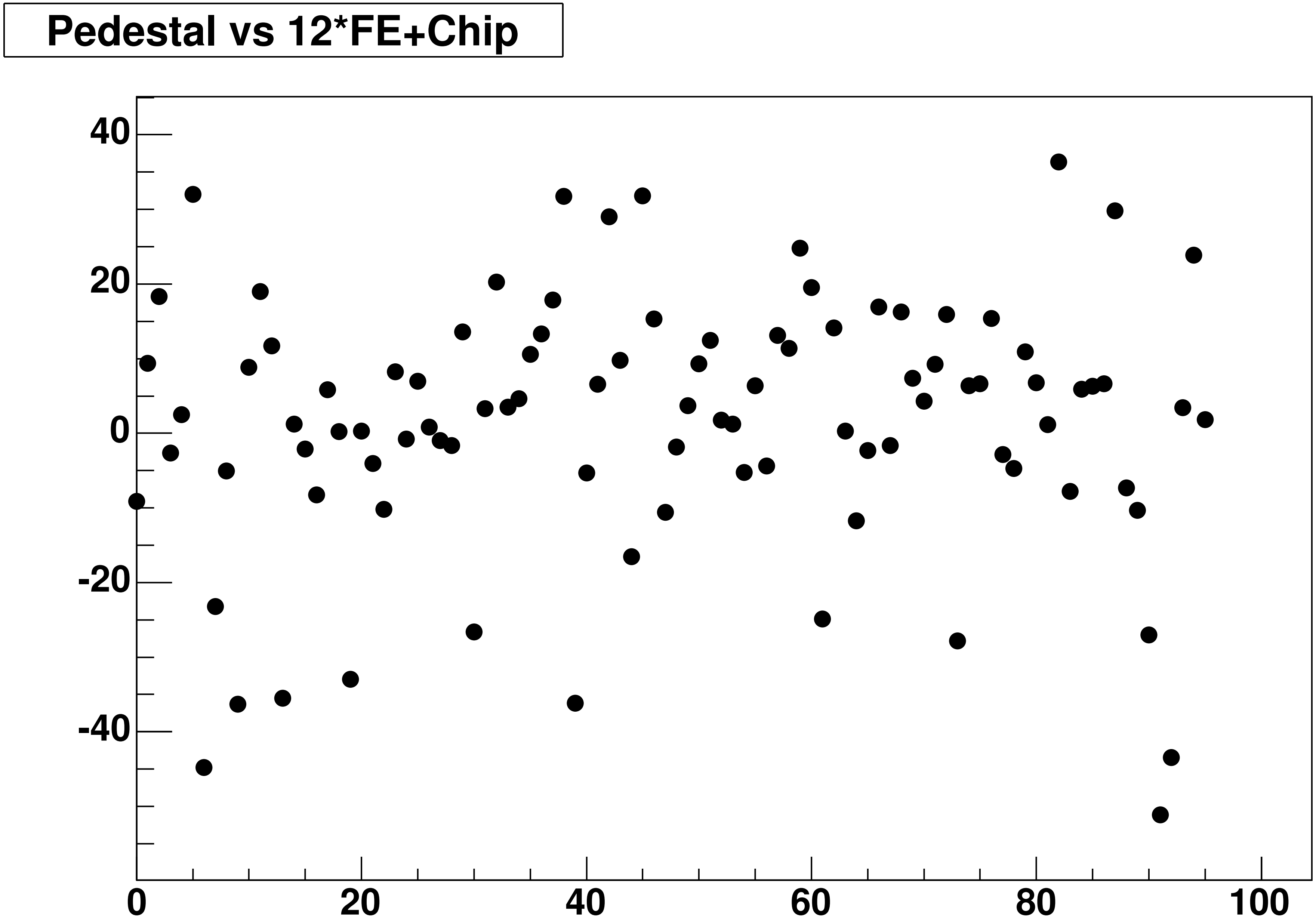
FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 1034784
Mean 1.905e+04
RMS 3.946
Underflow 9.563e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

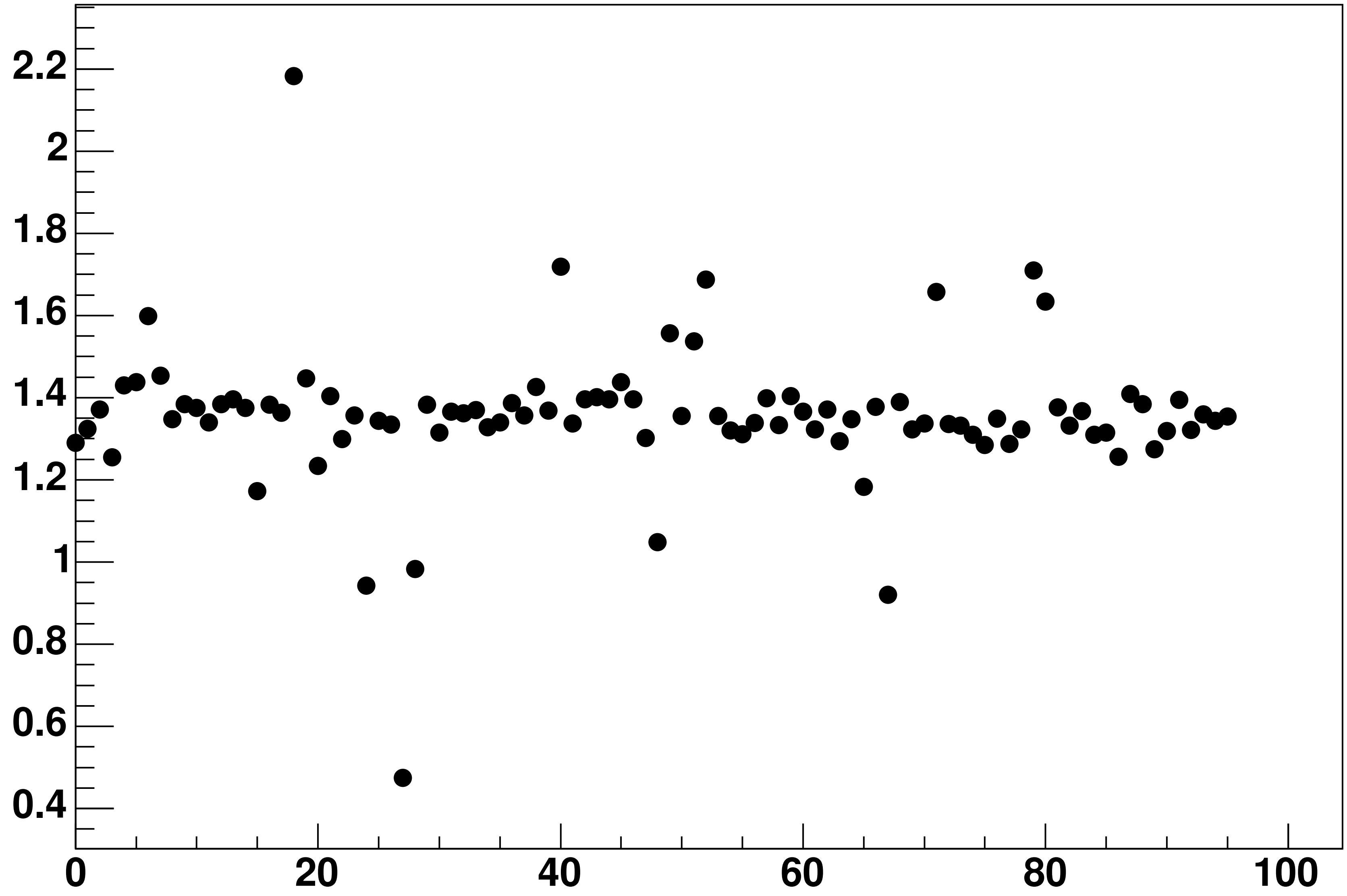

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 1034784
Mean 1452
RMS 5062
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000



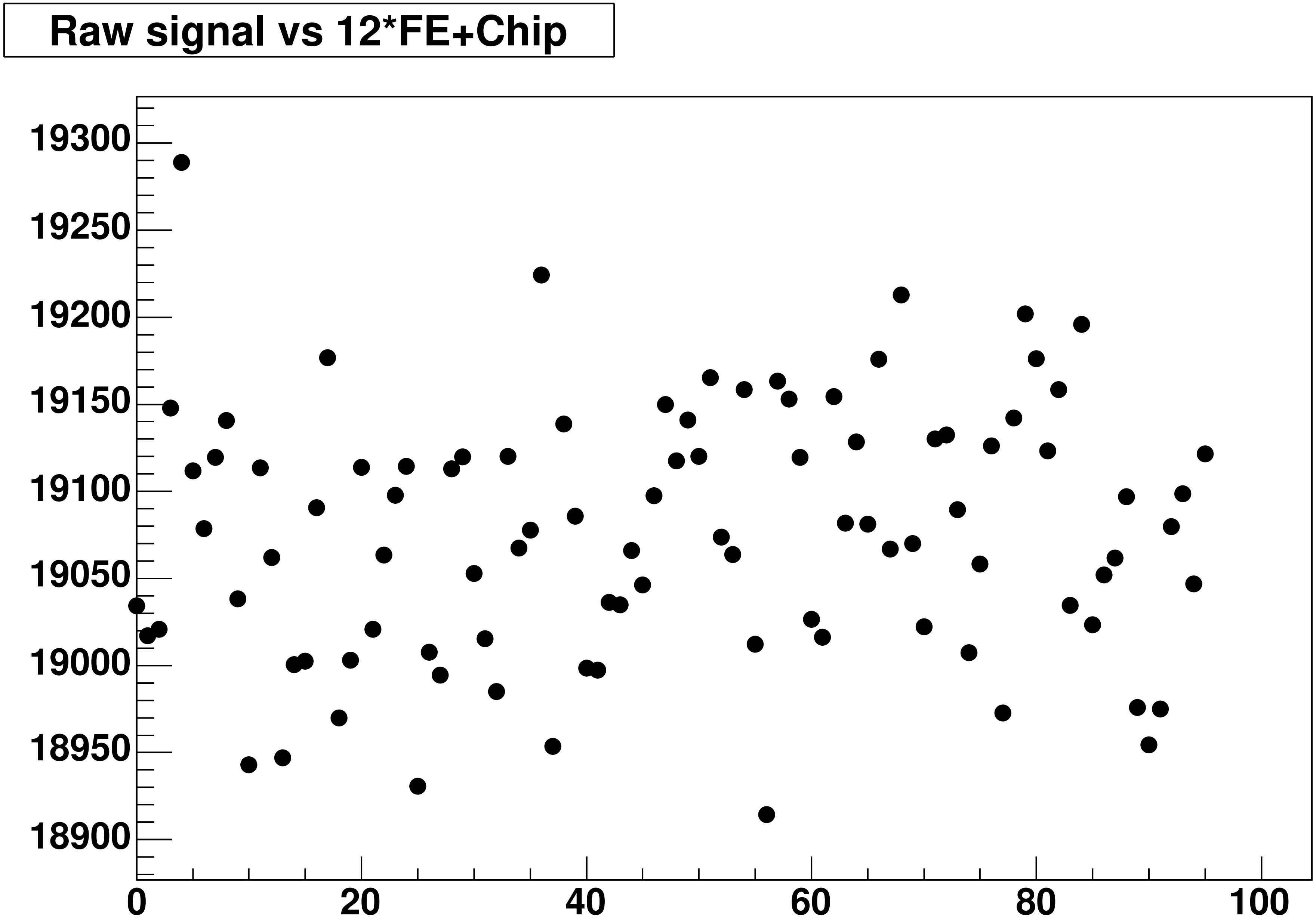
FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 1034784
Mean 1.863
RMS 1.354
Underflow 0
Overflow 7.85e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

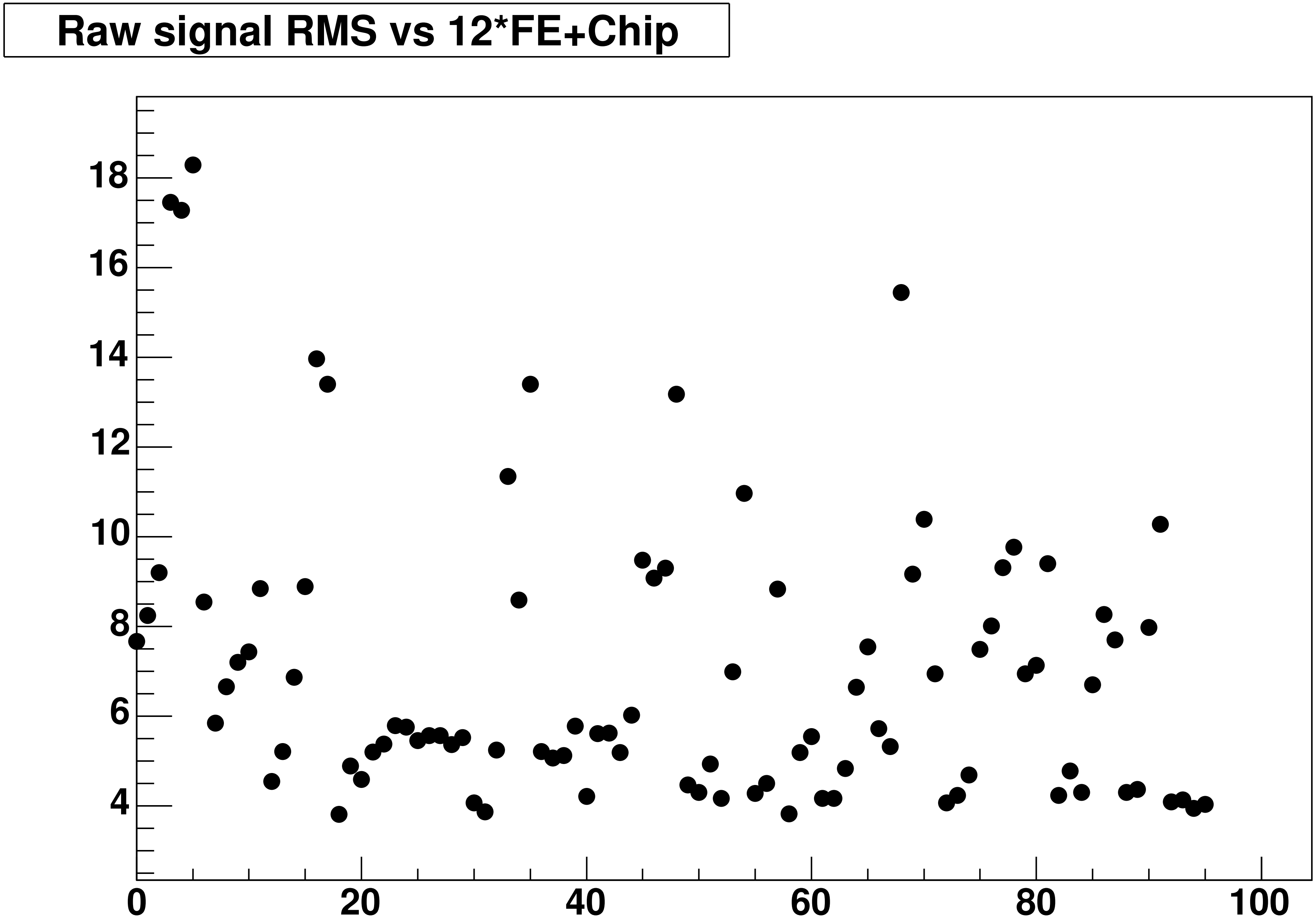

FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 1034784
Mean 1.912e+04
RMS 4.033
Underflow 9.563e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

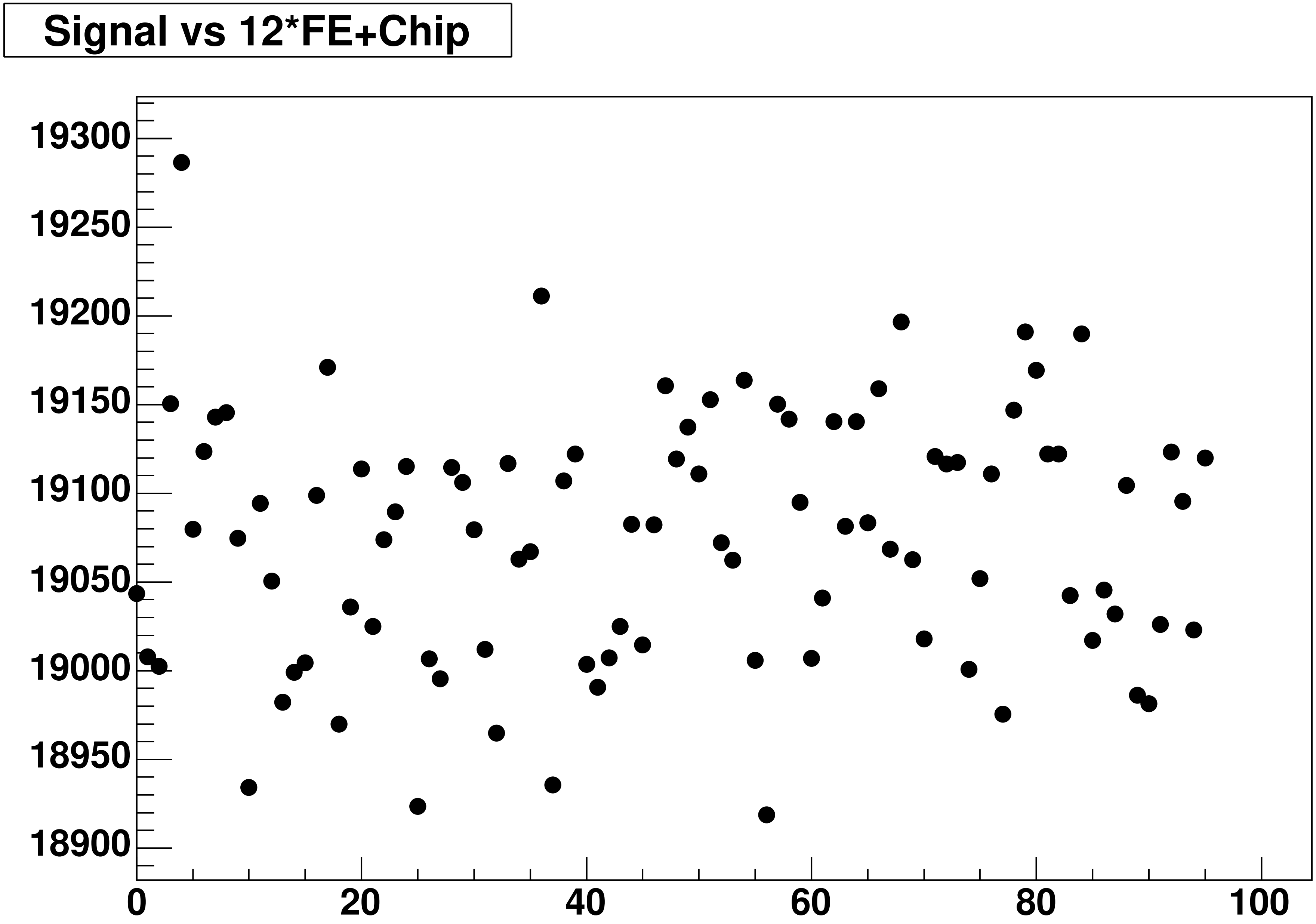

Pedestal vs 12*FE+Chip
40
20
0
-20
-40
0
20
40
60
80
100

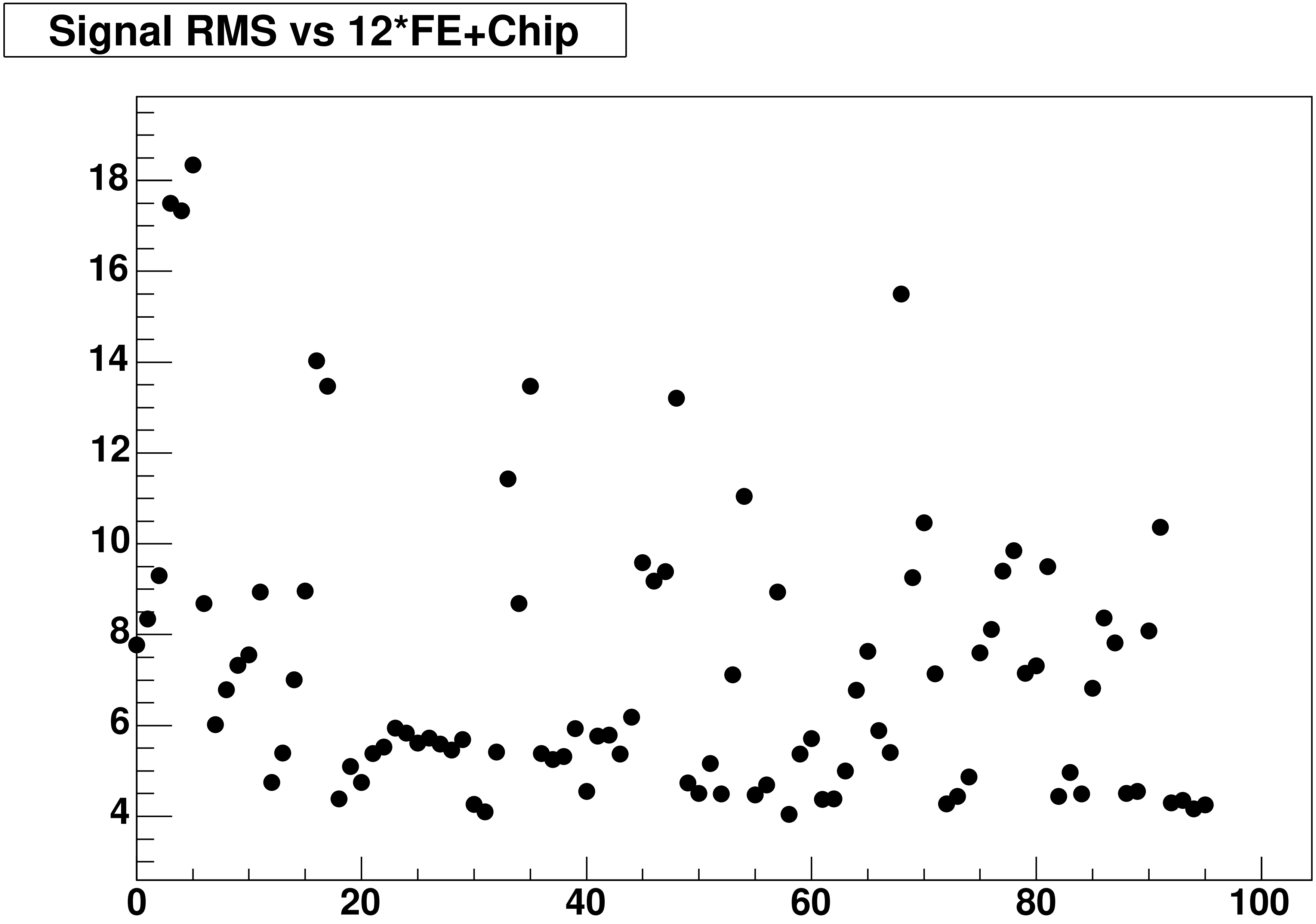


Pedestal RMS vs 12*FE+Chip


Raw signal vs 12*FE+Chip
19300
19250
19200
19150
19100
19050
19000
18950
18900
0
20
40
60
80
100


Raw signal RMS vs 12*FE+Chip
18
16
14
12
10
8
6
4
0
20
40
60
80
100


Signal vs 12*FE+Chip
19300
19250
19200
19150
19100
19050
19000
18950
18900
0
20
40
60
80
100



Signal RMS vs 12*FE+Chip
4
6
8
10
12
14
16
18
0
20
40
60
80
100