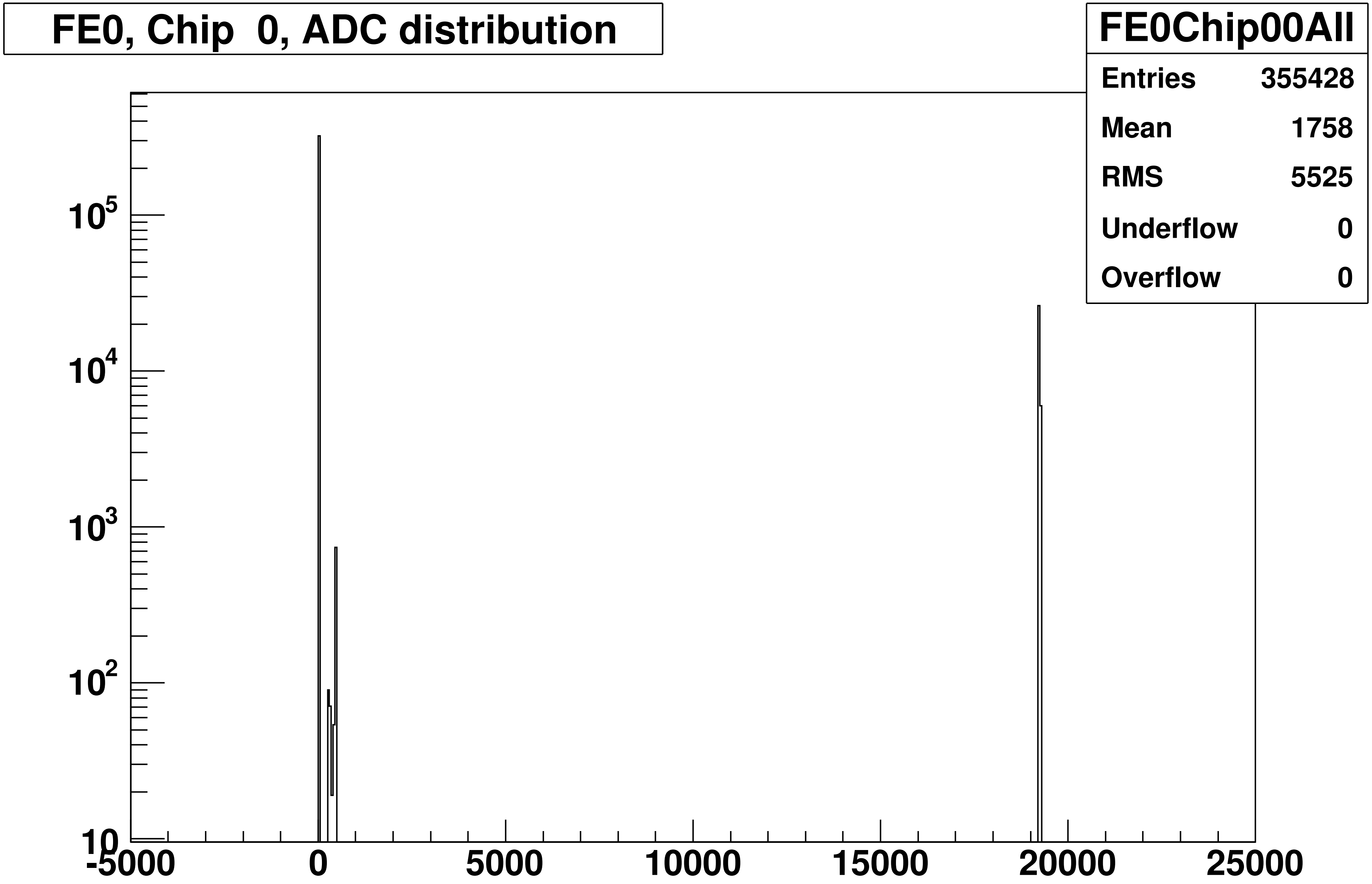
FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 355428
Mean 1758
RMS 5525
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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-5000
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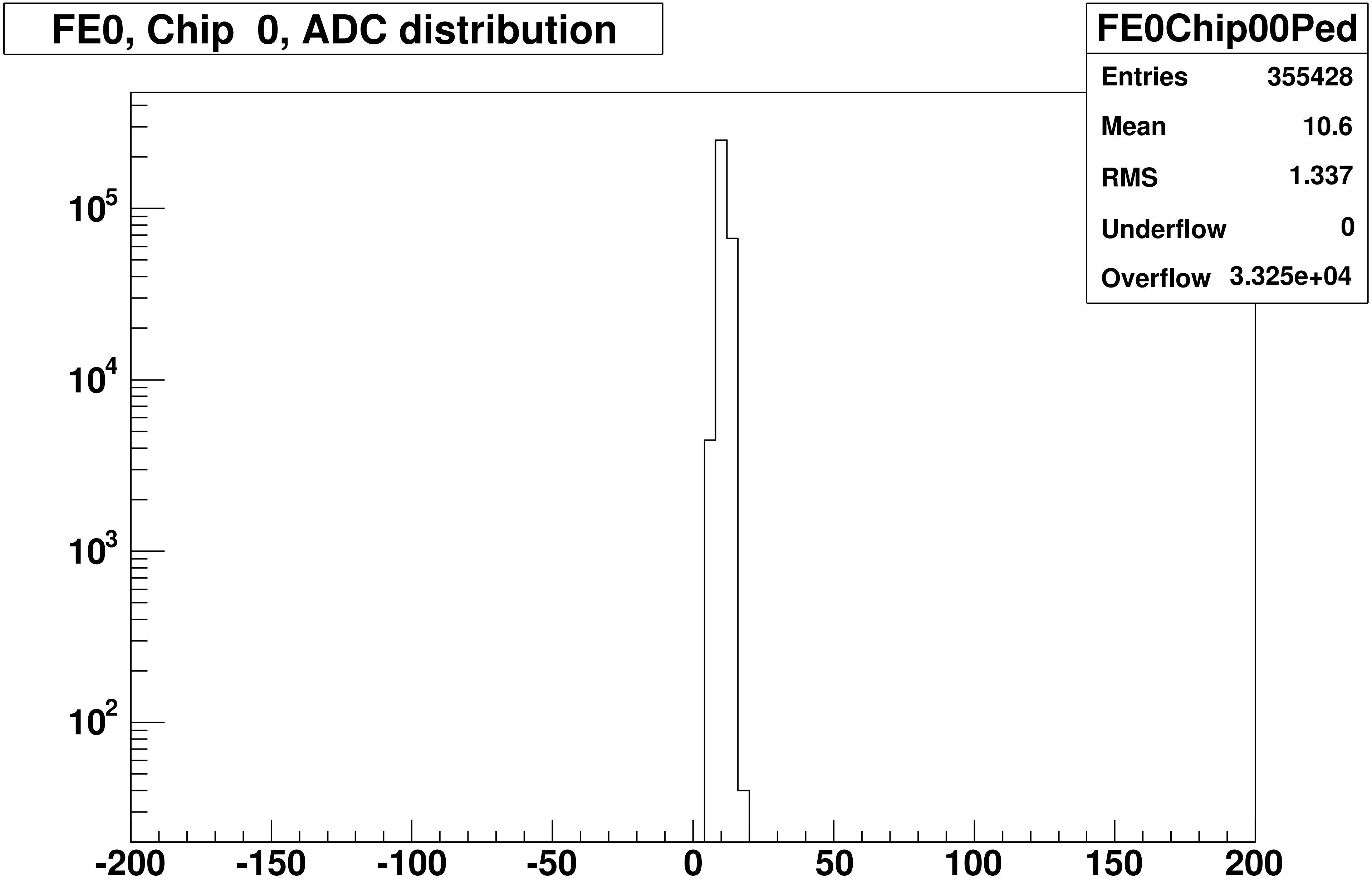
20000
25000


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 355428
Mean 10.6
RMS 1.337
Underflow 0
Overflow 3.325e+04
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10^4
10^3
10^2
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-150
-100
-50
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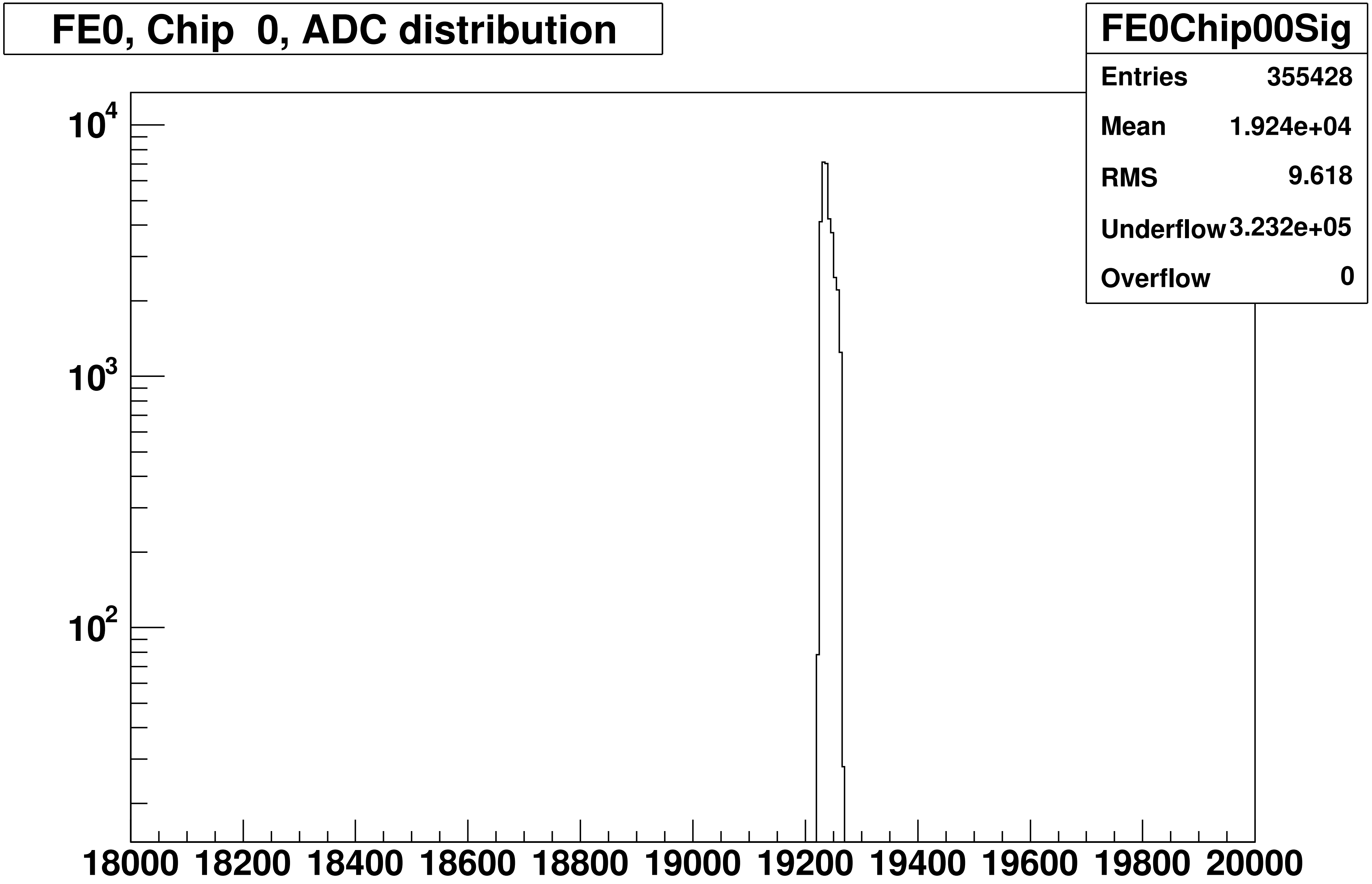
50
100
150
200



FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 355428
Mean 1.924e+04
RMS 9.618
Underflow 3.232e+05
Overflow 0
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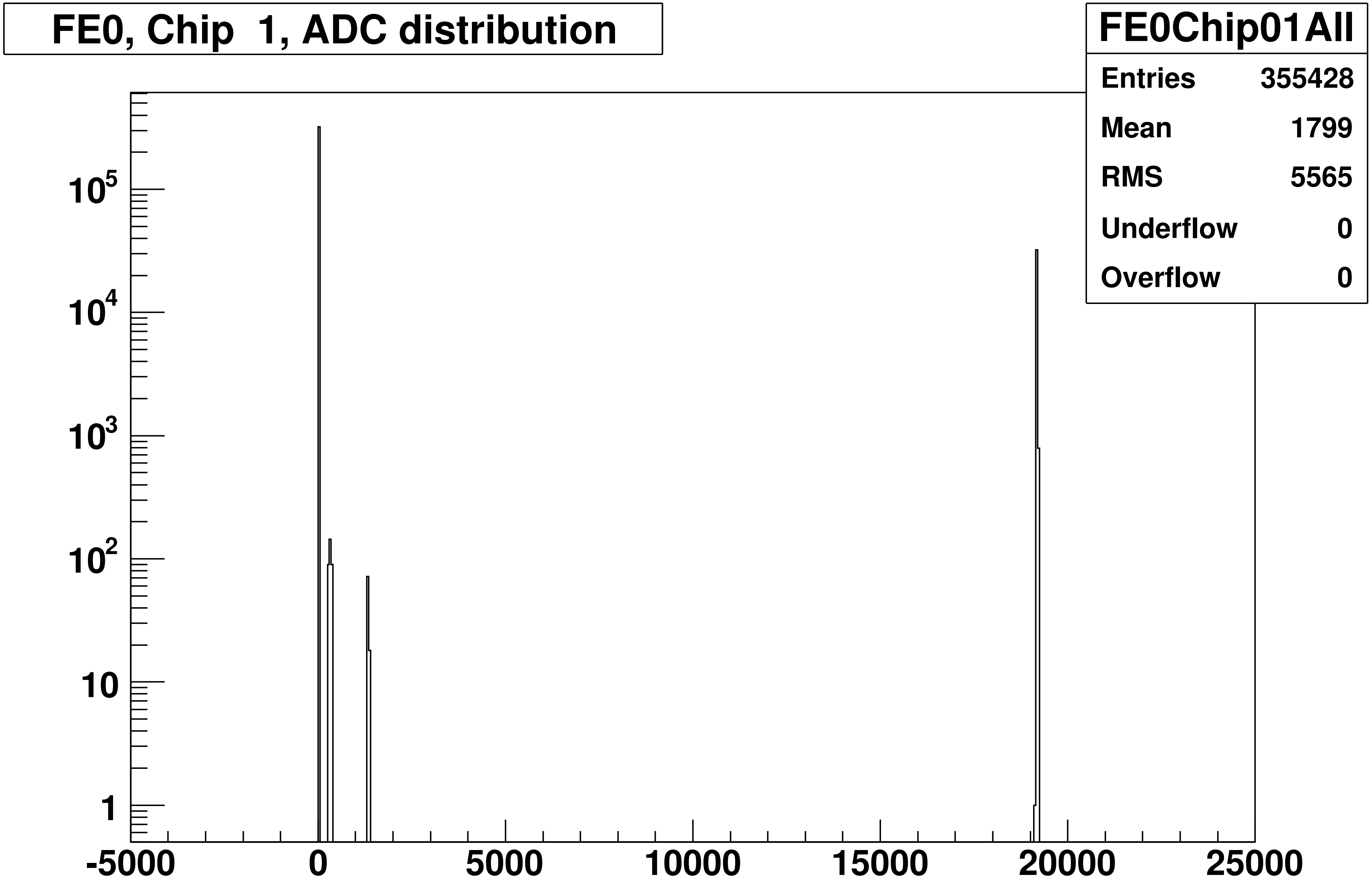
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 355428
Mean 1799
RMS 5565
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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15000
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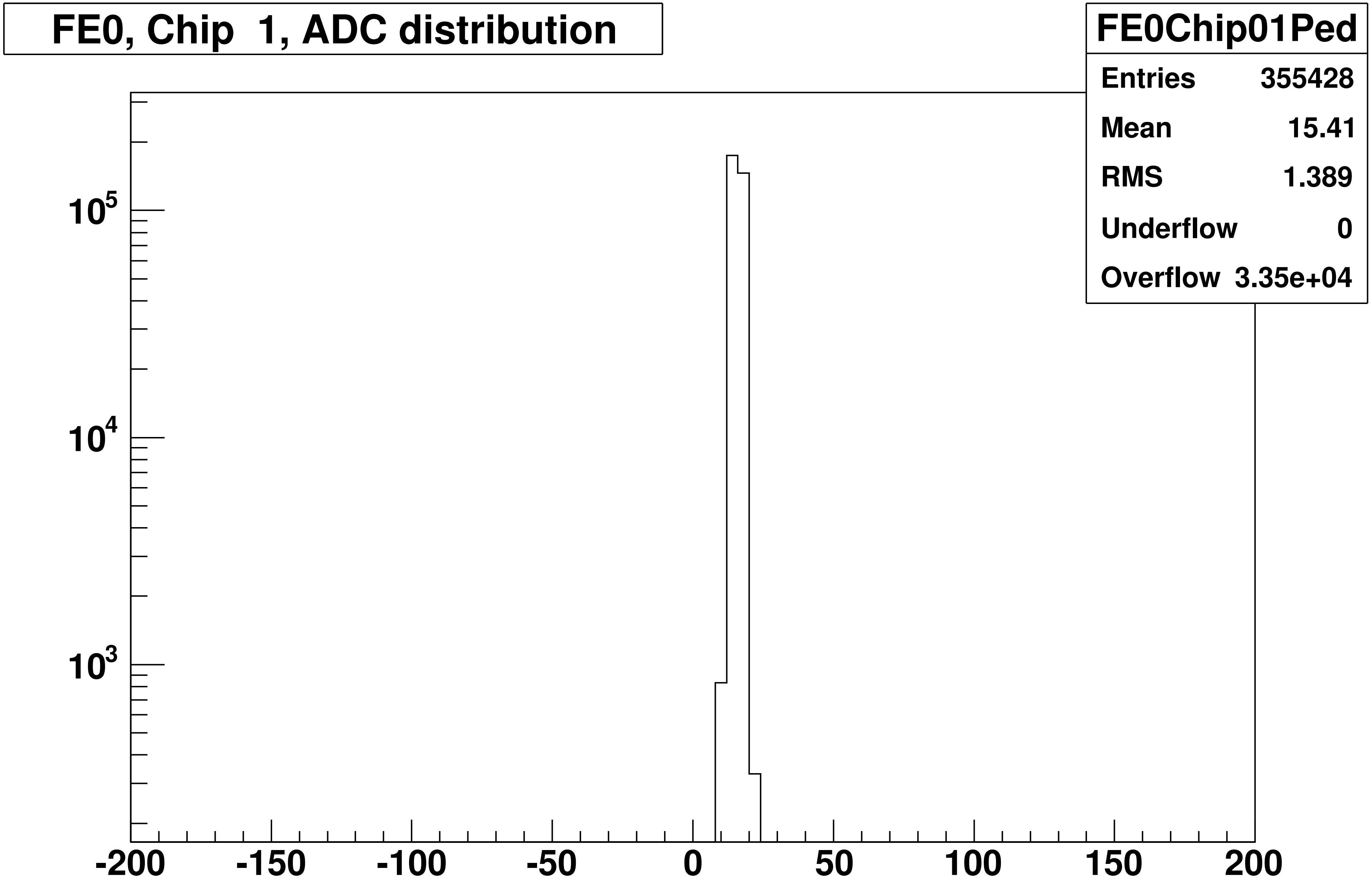
FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 355428
Mean 15.41
RMS 1.389
Underflow 0
Overflow 3.35e+04
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10^4
10^3
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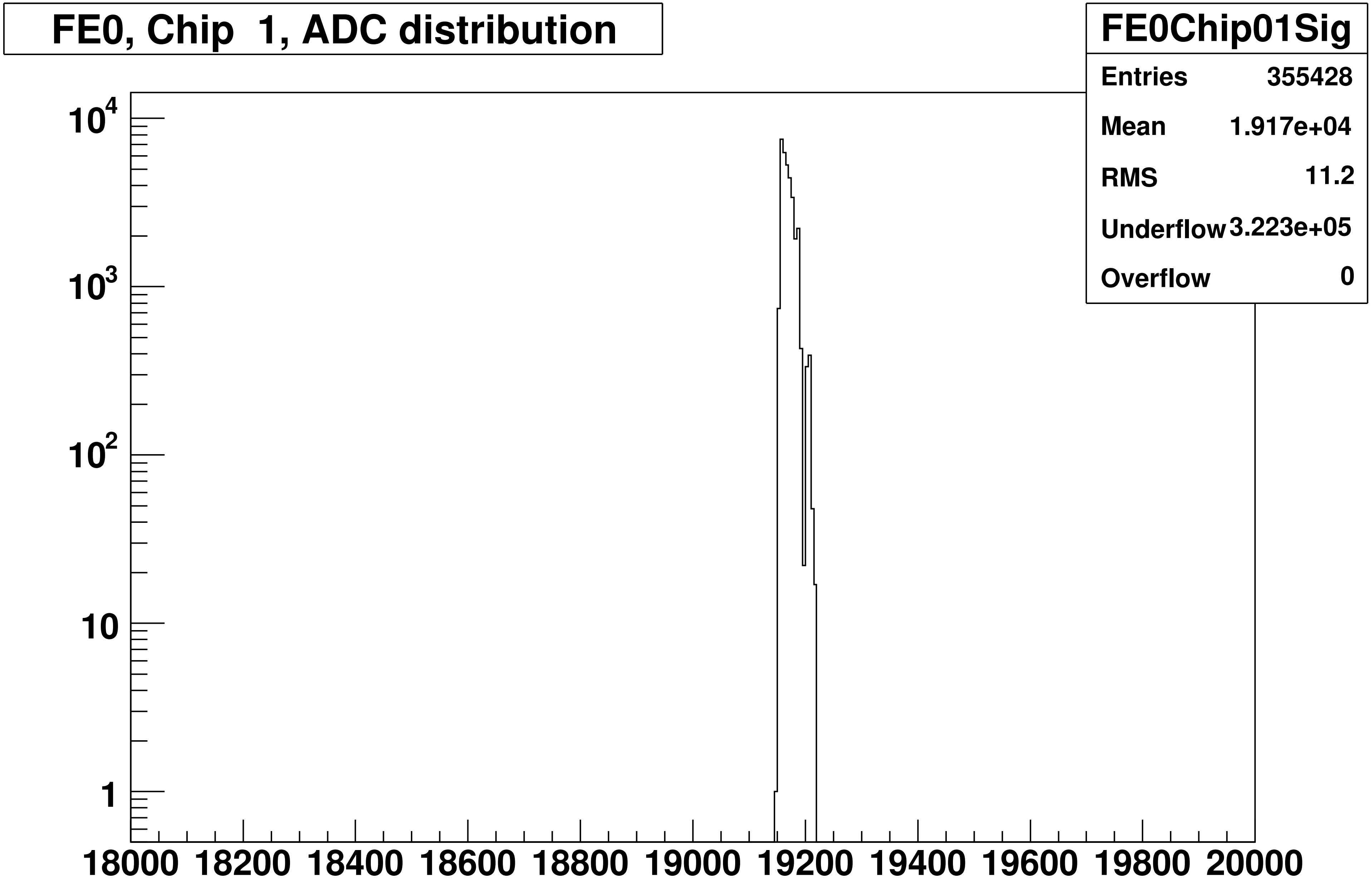
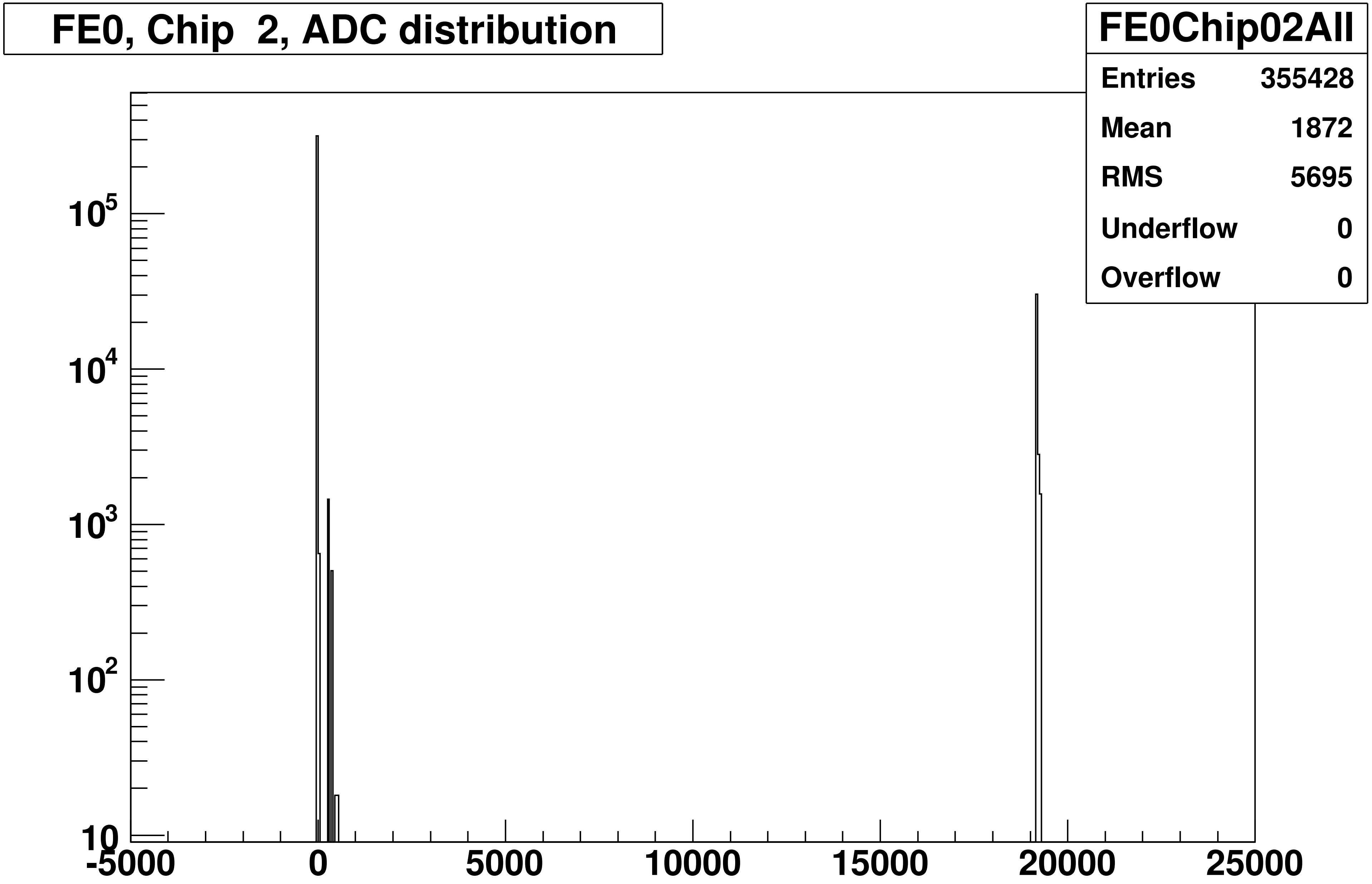
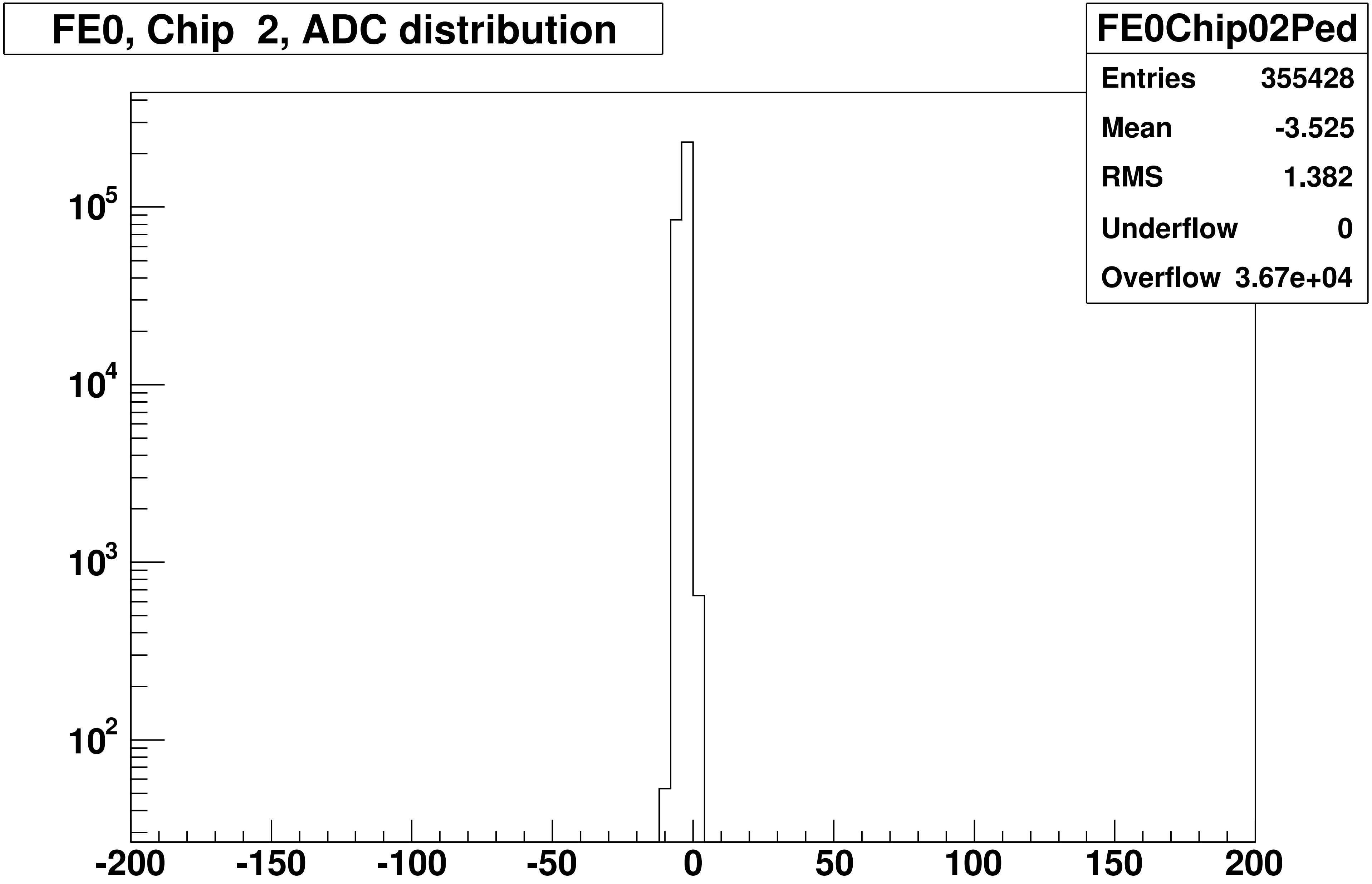
100
150
200


FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 355428
Mean 1.917e+04
RMS 11.2
Underflow 3.223e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 355428
Mean 1872
RMS 5695
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 355428
Mean -3.525
RMS 1.382
Underflow 0
Overflow 3.67e+04
10^5
10^4
10^3
10^2
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-150
-100
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0
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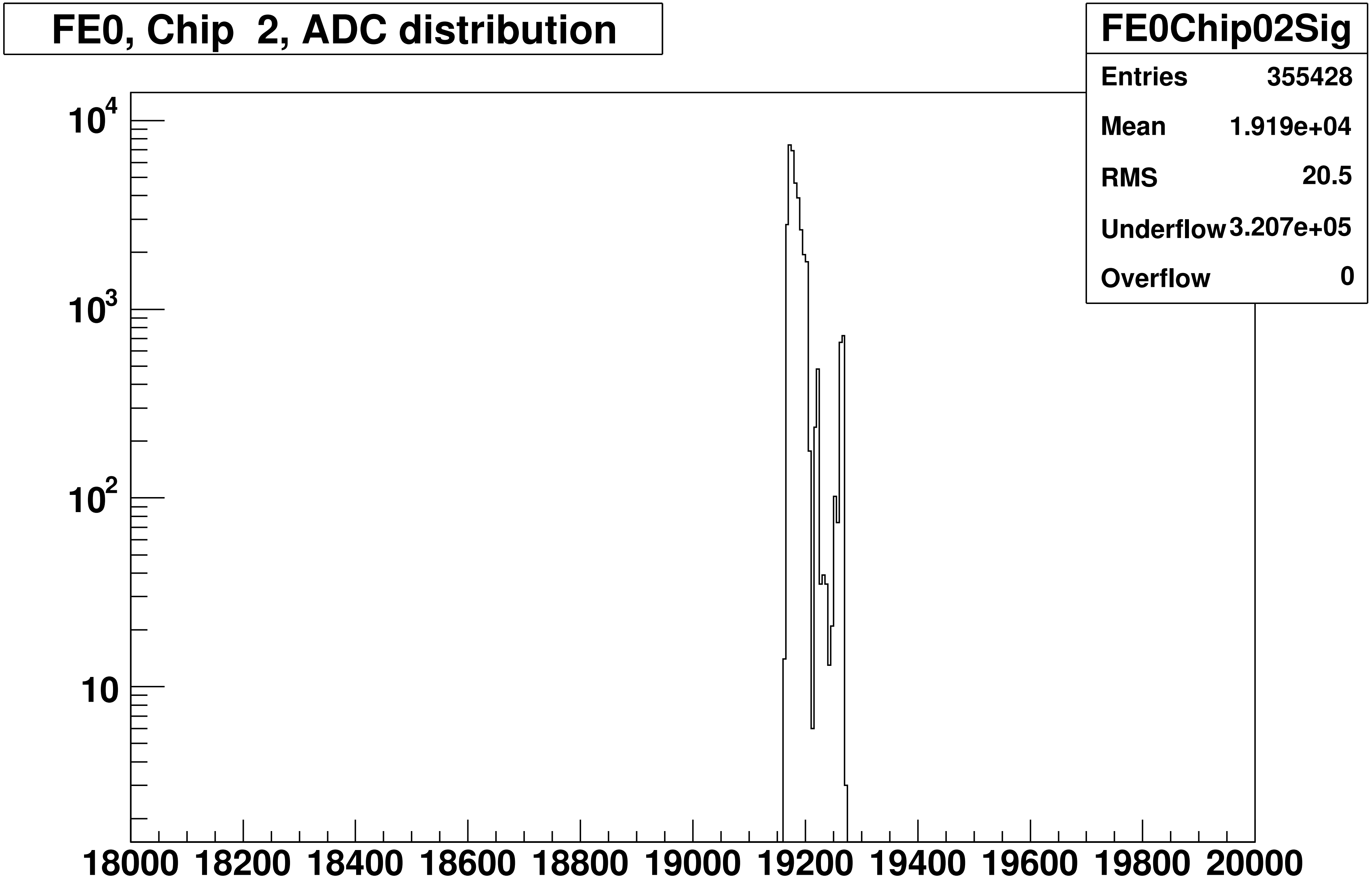
100
150
200


FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 355428
Mean 1.919e+04
RMS 20.5
Underflow 3.207e+05
Overflow 0
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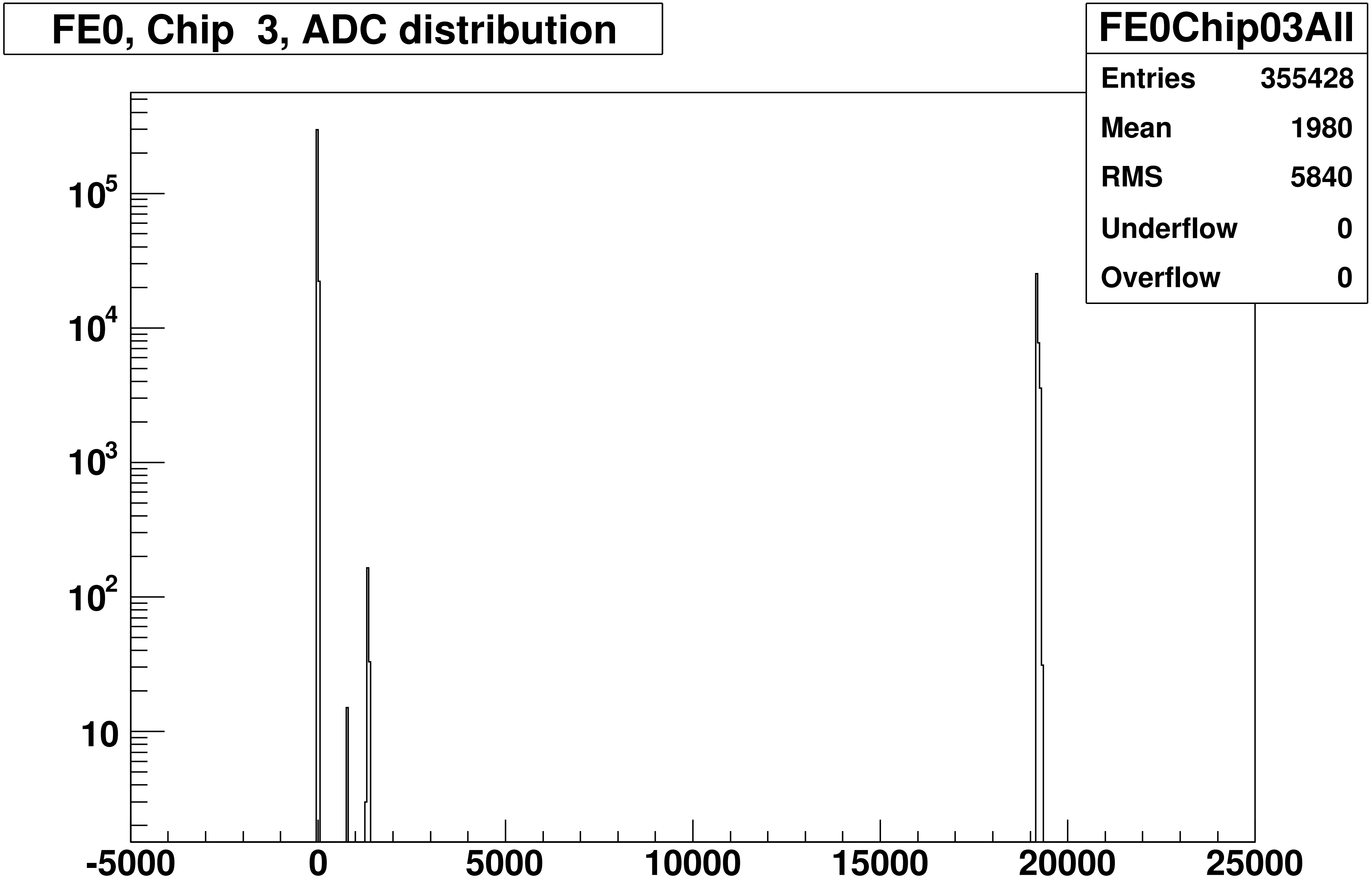
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 355428
Mean 1980
RMS 5840
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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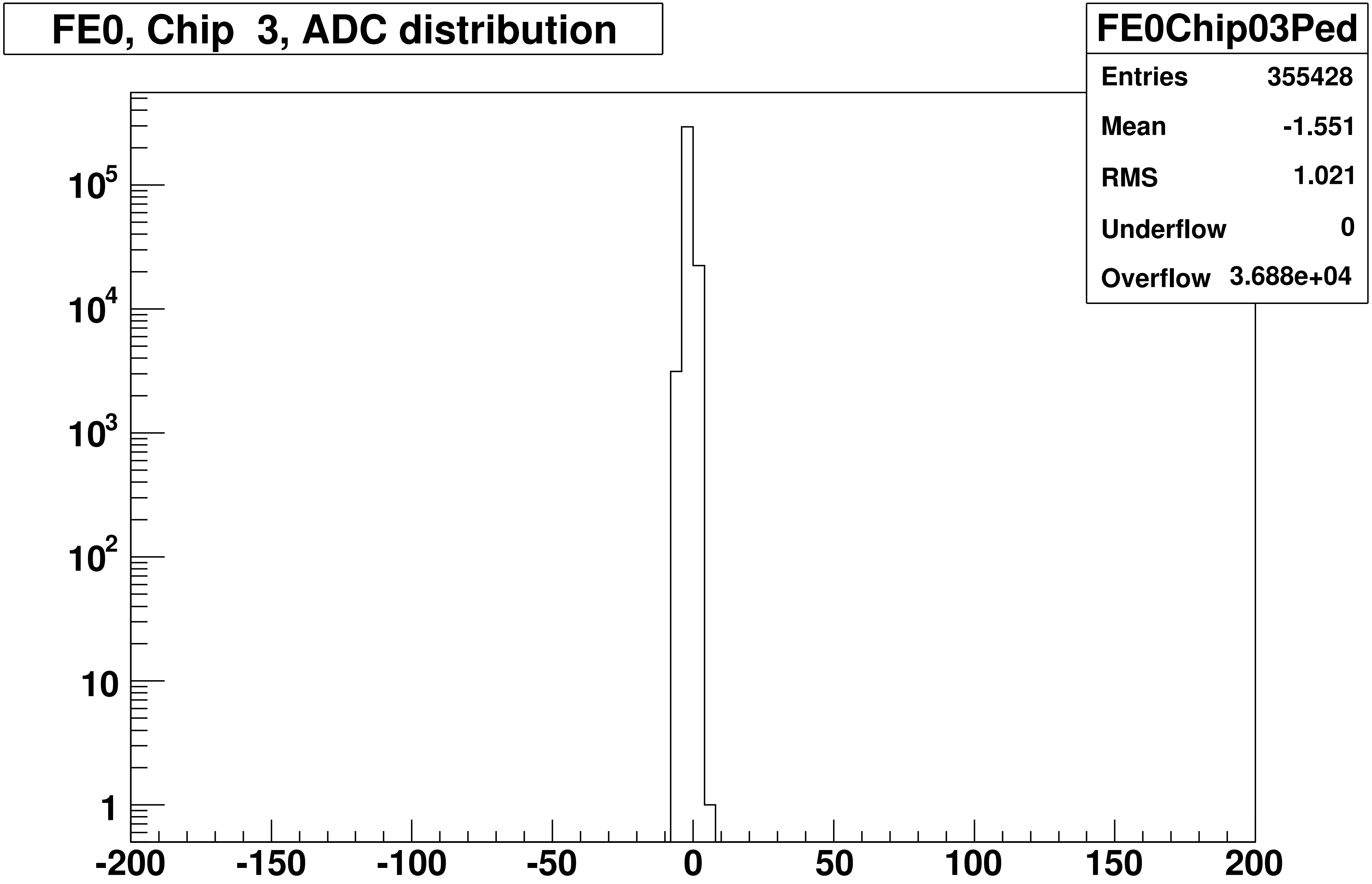
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 355428
Mean -1.551
RMS 1.021
Underflow 0
Overflow 3.688e+04
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10^4
10^3
10^2
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-100
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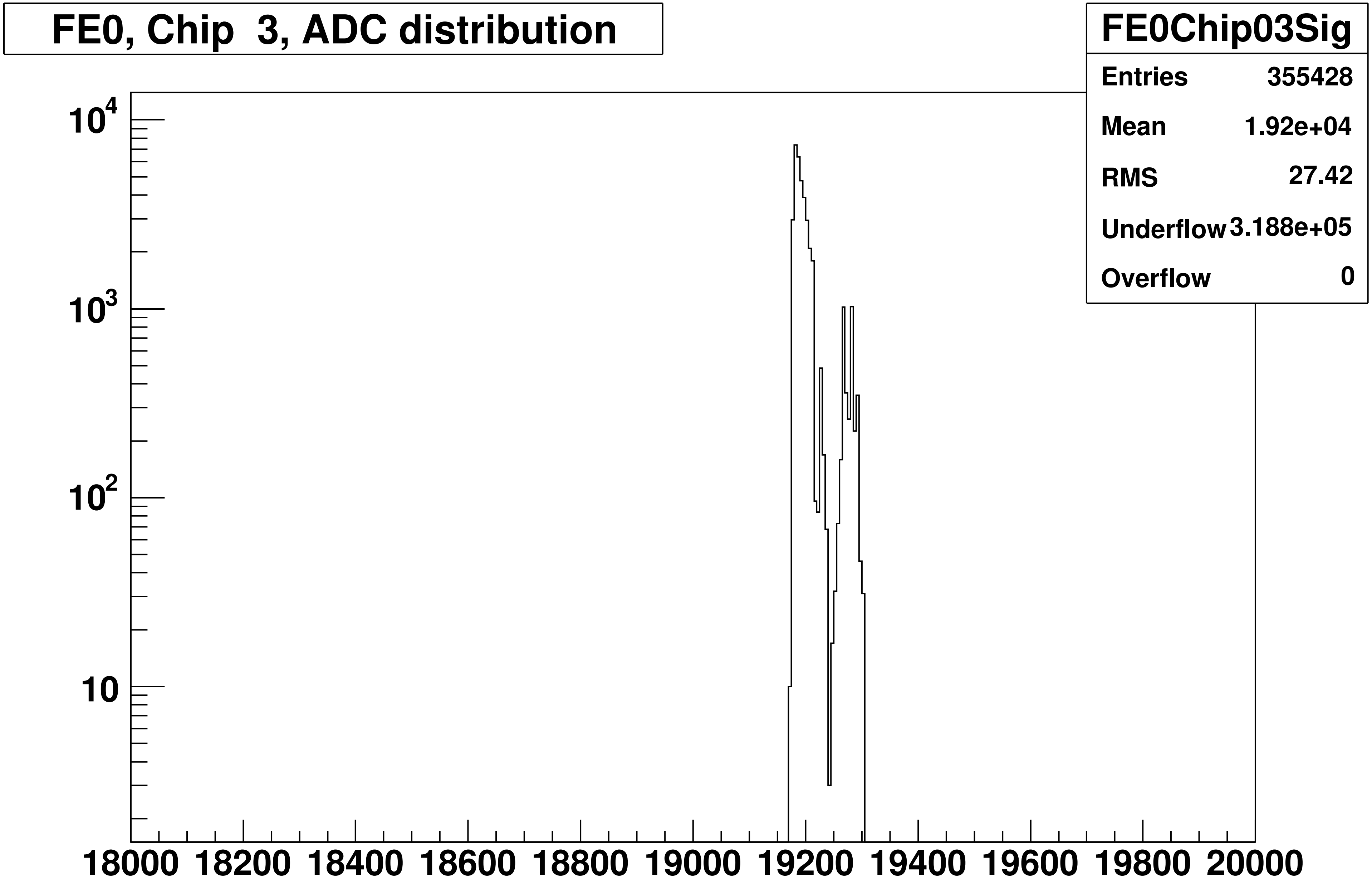
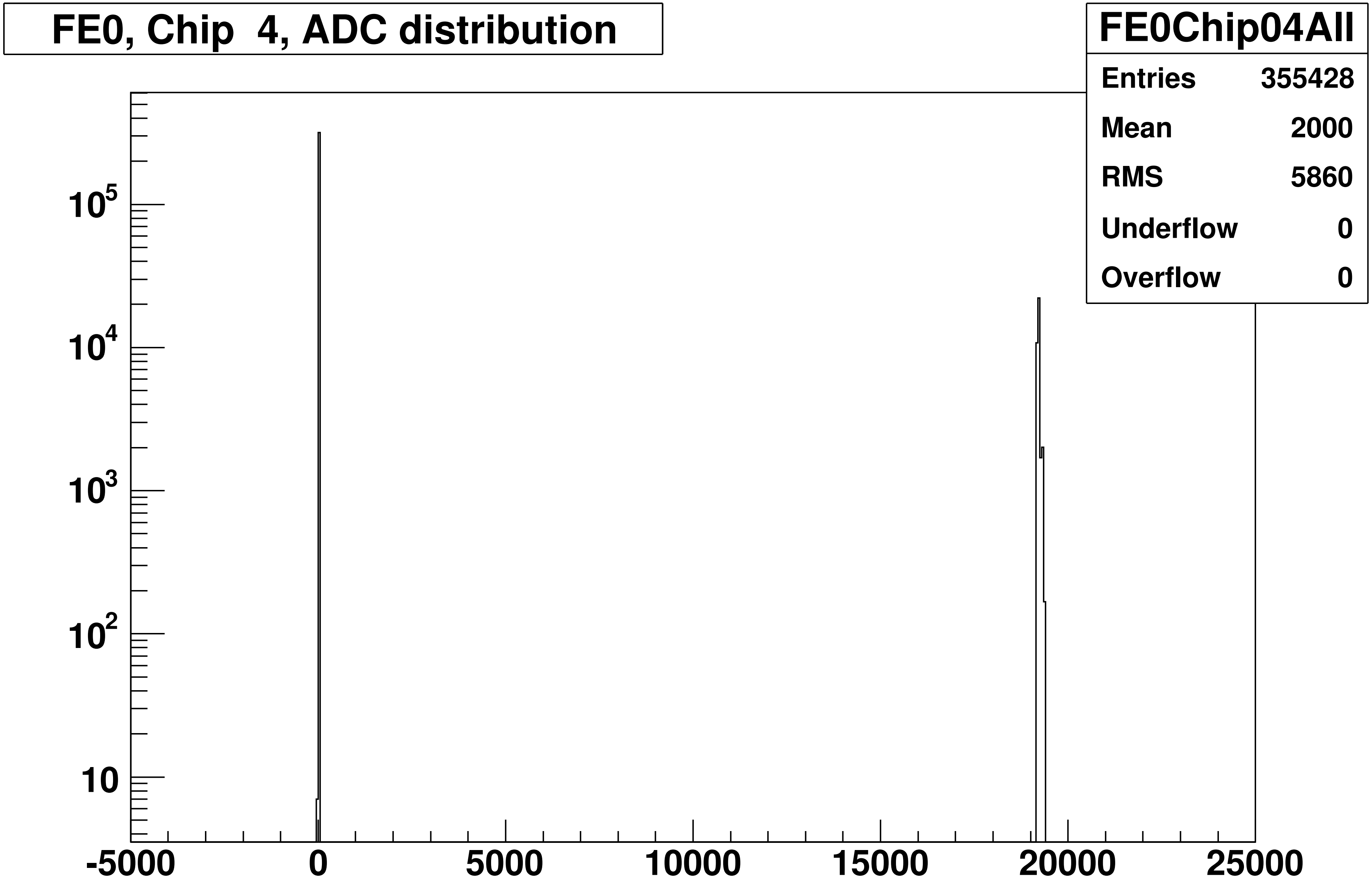
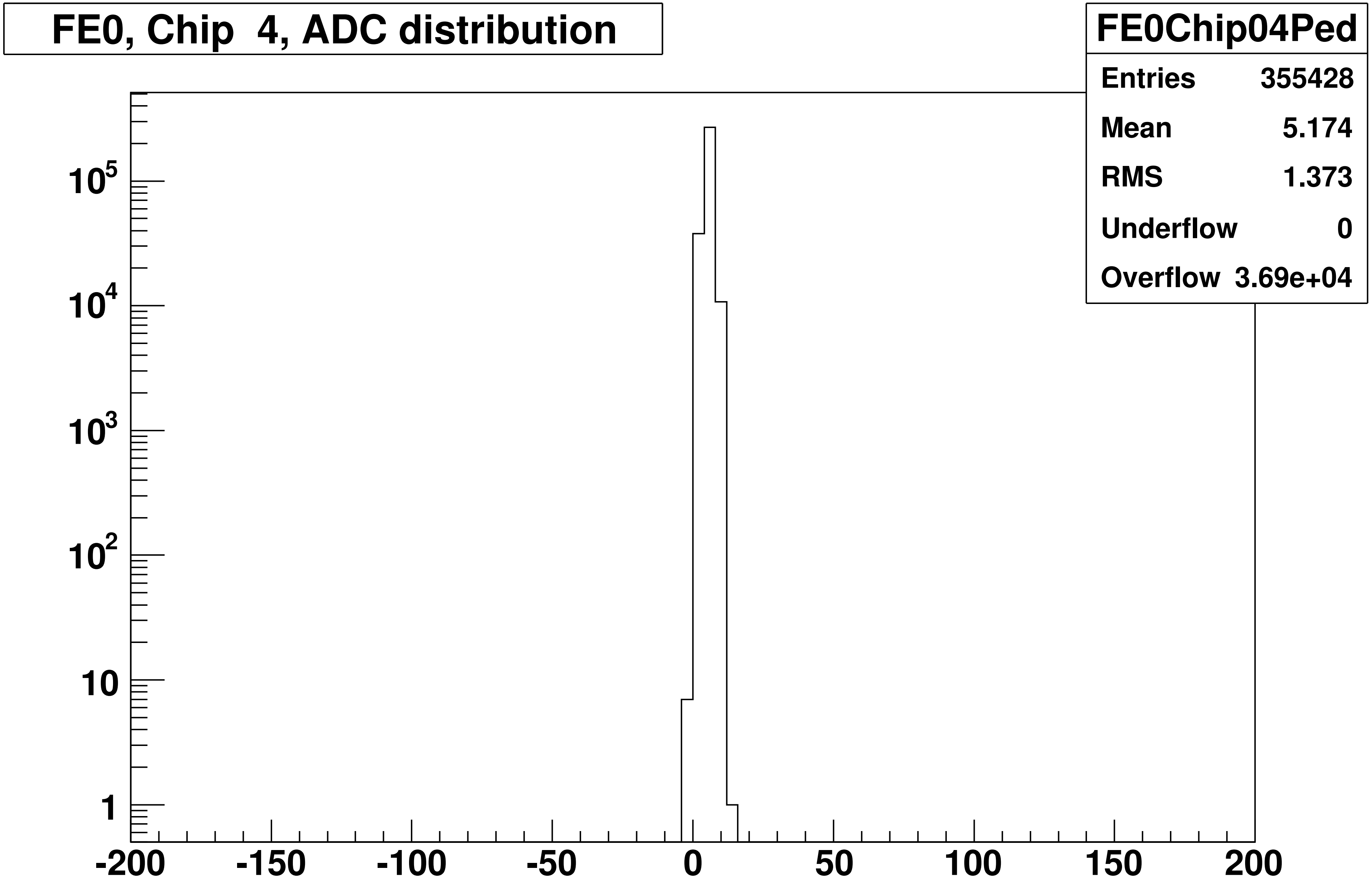
50
100
150
200


FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 355428
Mean 1.92e+04
RMS 27.42
Underflow 3.188e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 355428
Mean 2000
RMS 5860
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000



FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 355428
Mean 5.174
RMS 1.373
Underflow 0
Overflow 3.69e+04
10^5
10^4
10^3
10^2
10
1
-200
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-100
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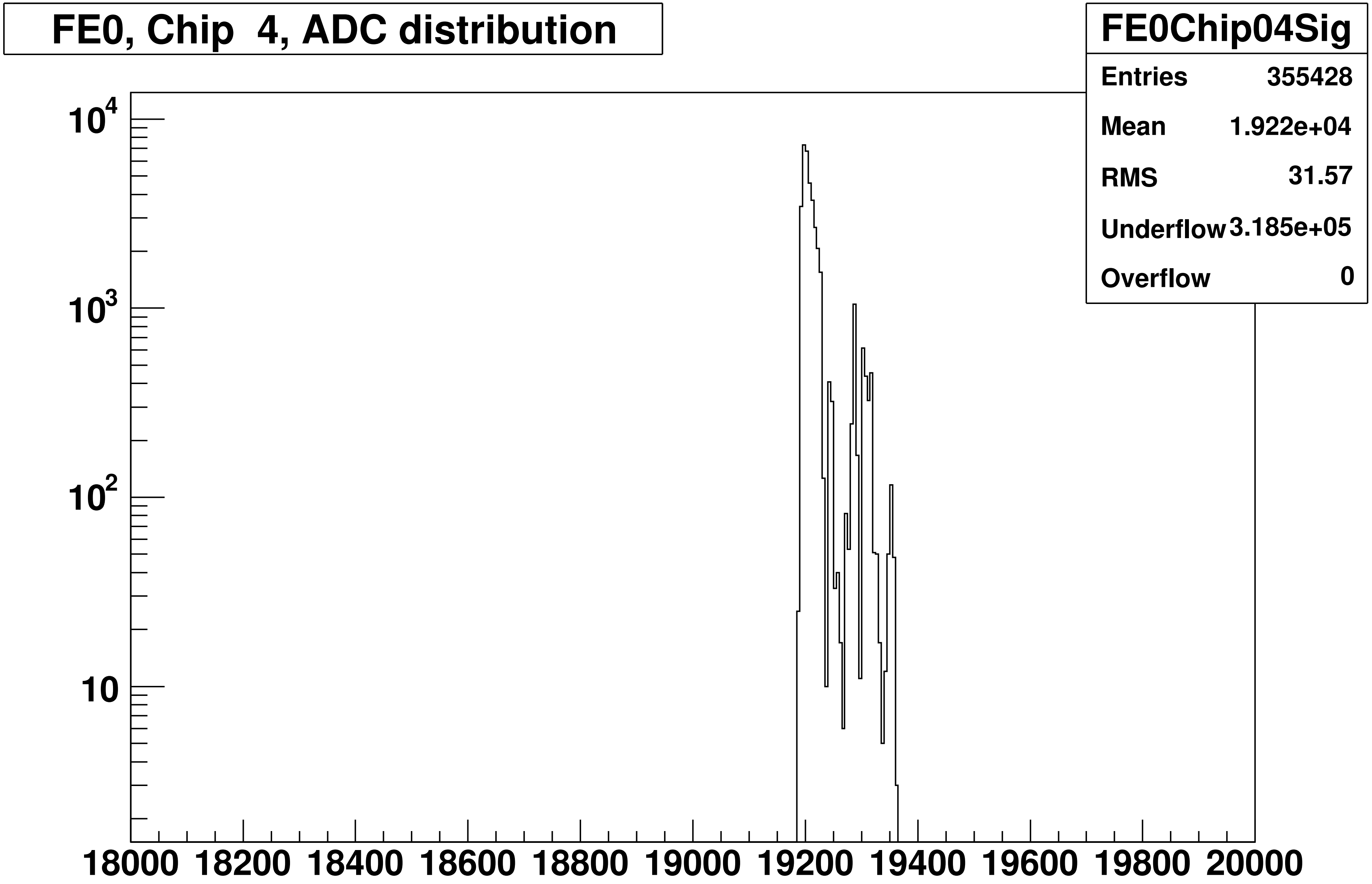
0
50
100
150
200



FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 355428
Mean 1.922e+04
RMS 31.57
Underflow 3.185e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
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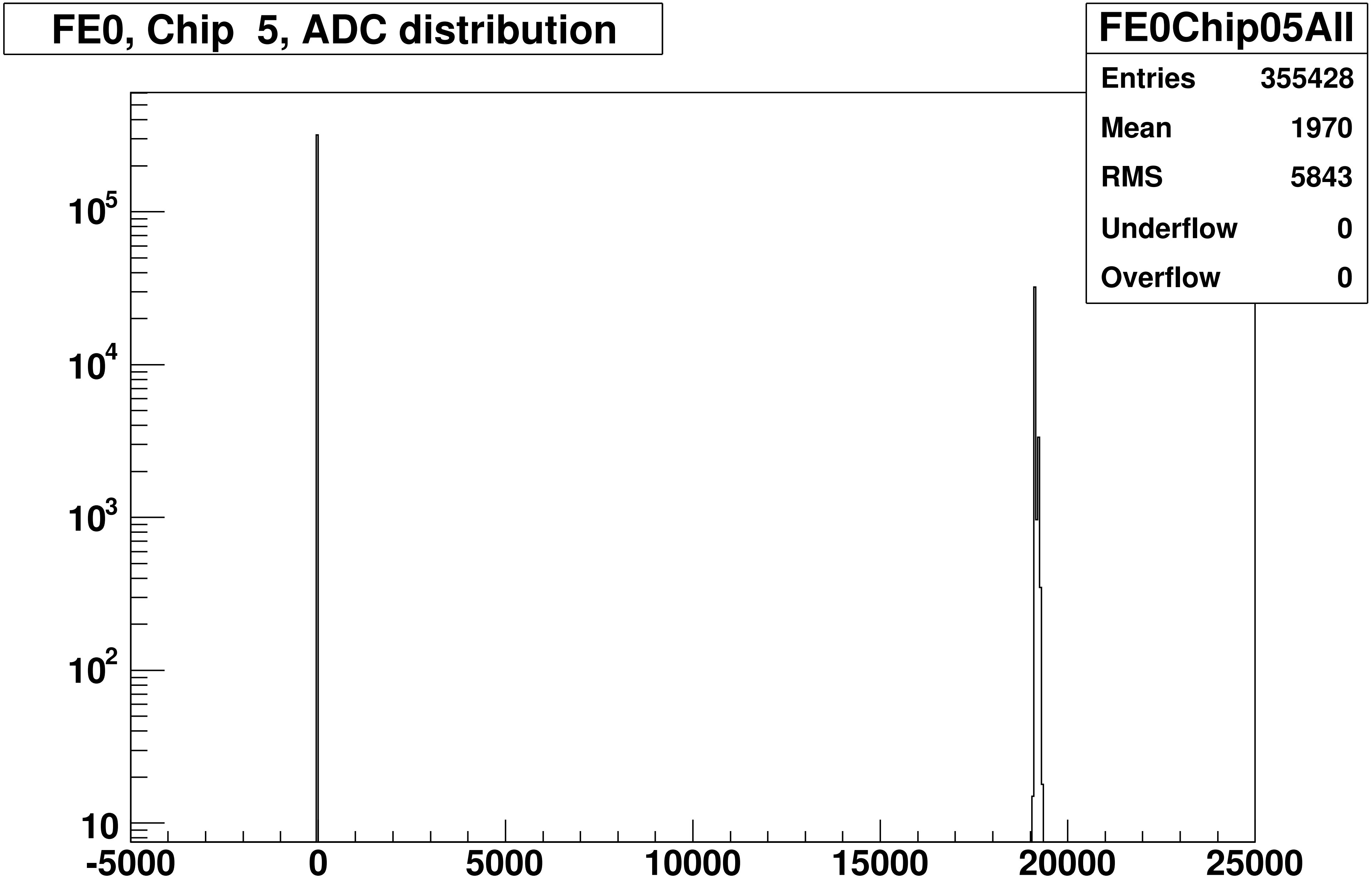
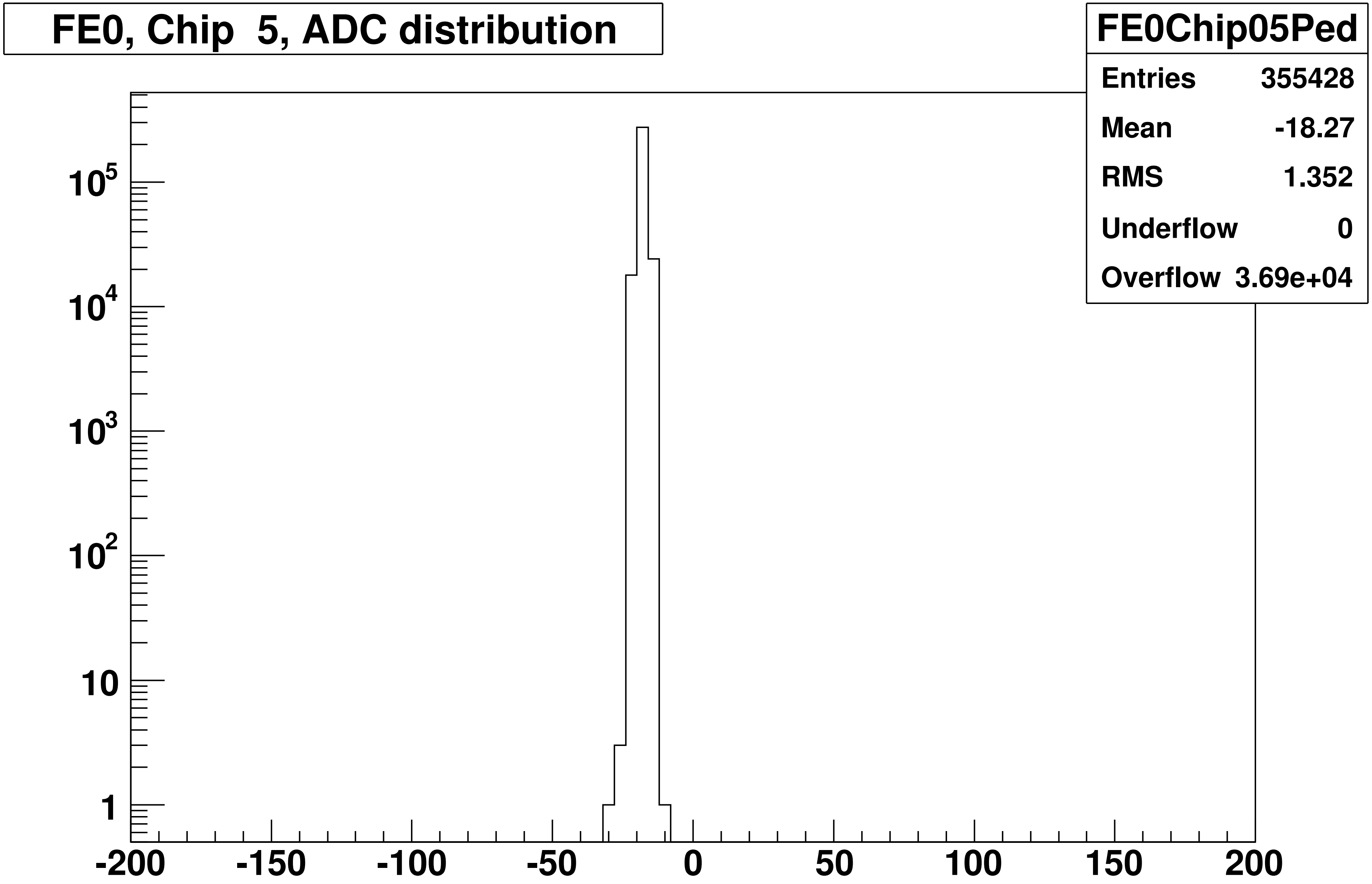
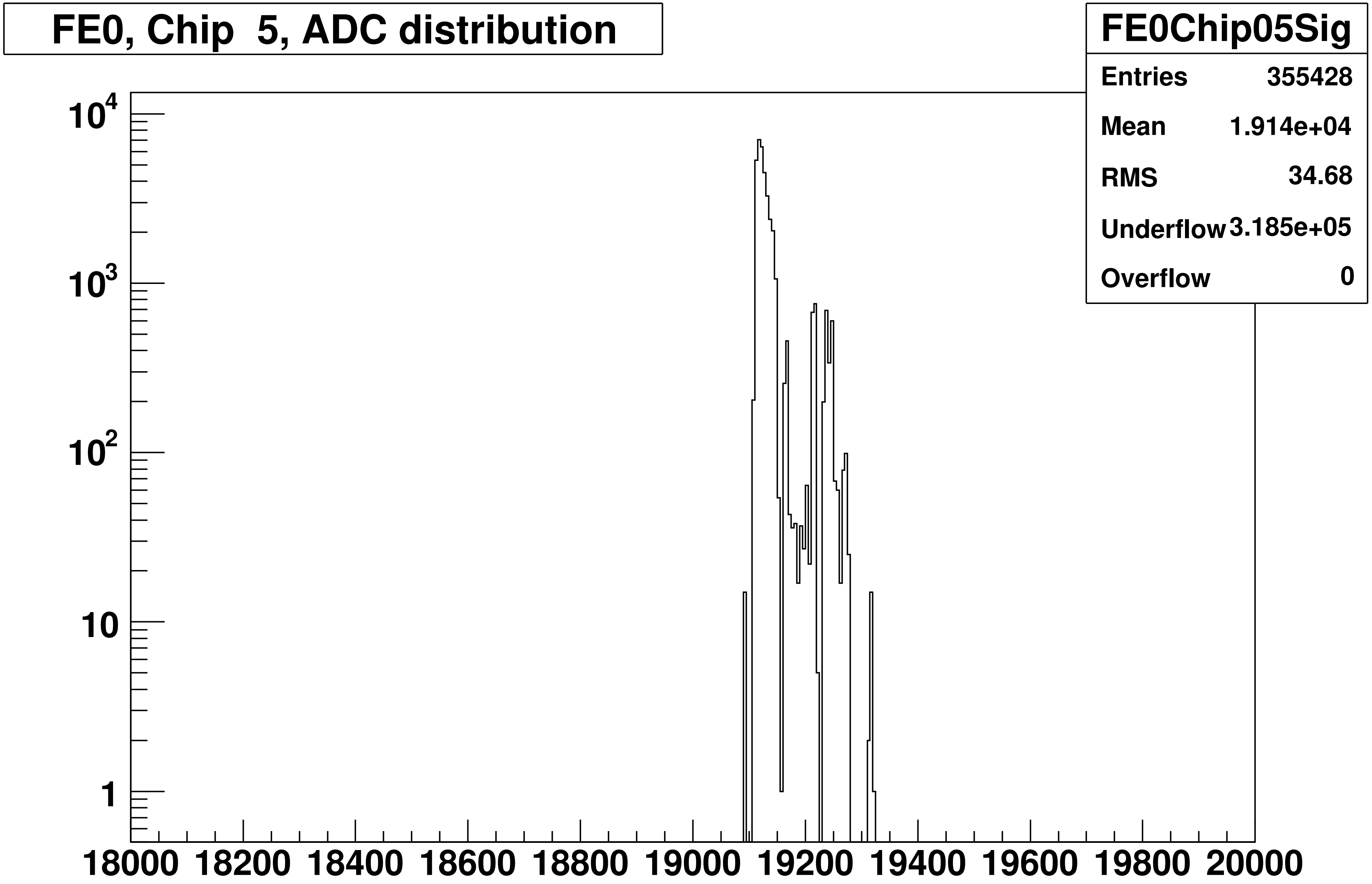
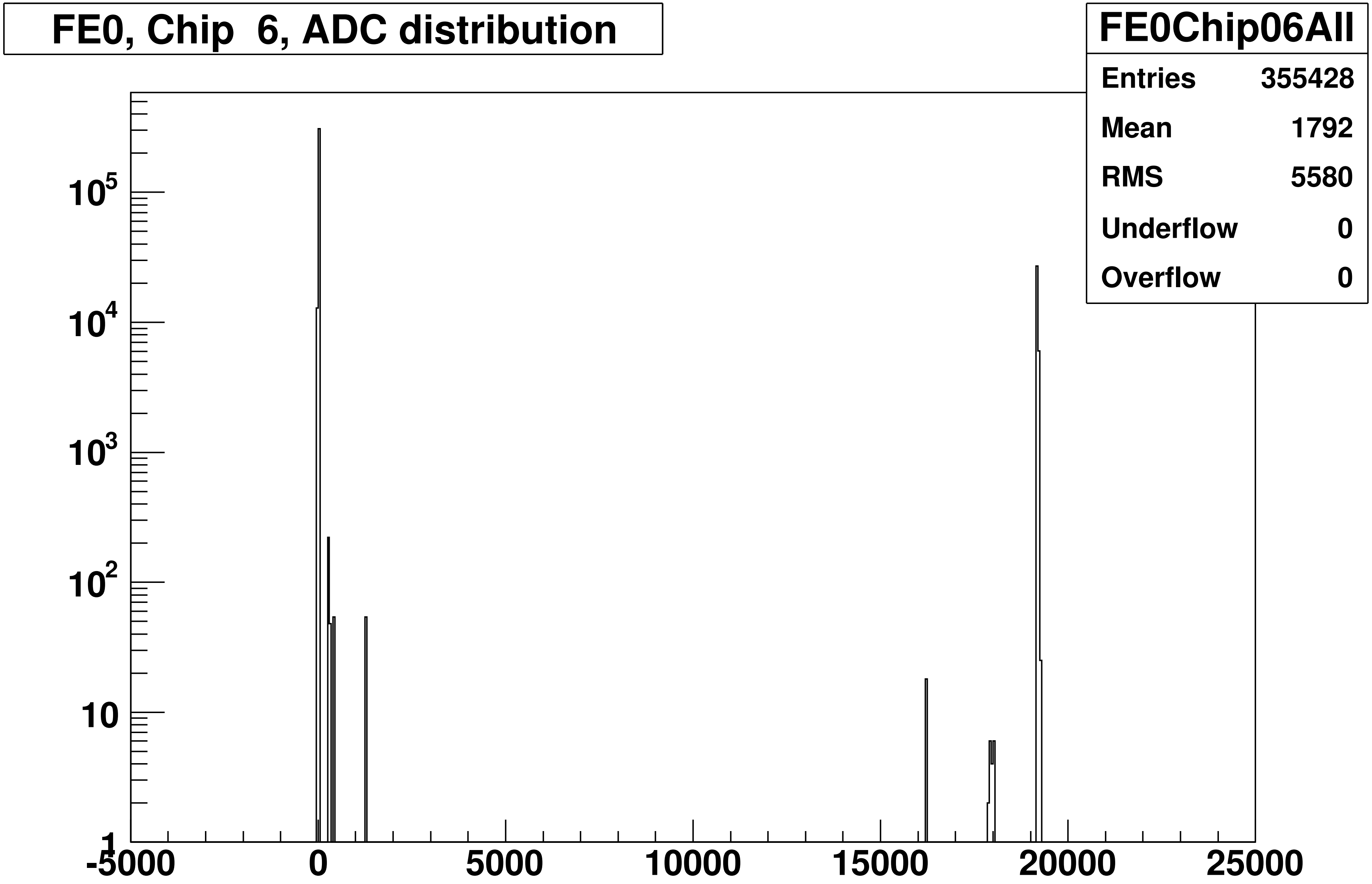
19400
19600
19800
20000


FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 355428
Mean 1970
RMS 5843
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 355428
Mean -18.27
RMS 1.352
Underflow 0
Overflow 3.69e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 355428
Mean 1.914e+04
RMS 34.68
Underflow 3.185e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 355428
Mean 1792
RMS 5580
Underflow 0
Overflow 0
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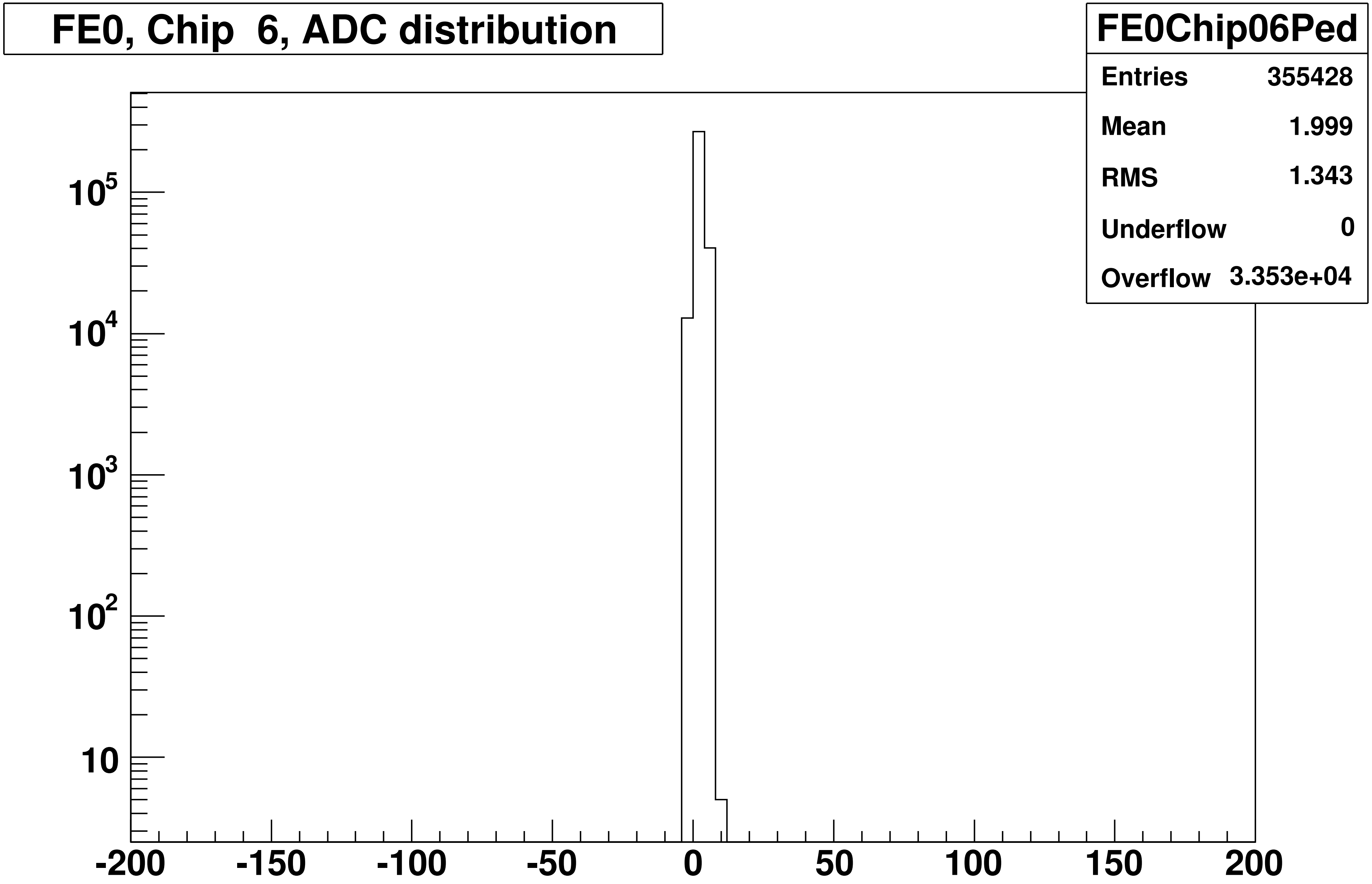
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 355428
Mean 1.999
RMS 1.343
Underflow 0
Overflow 3.353e+04
10
10^2
10^3
10^4
10^5
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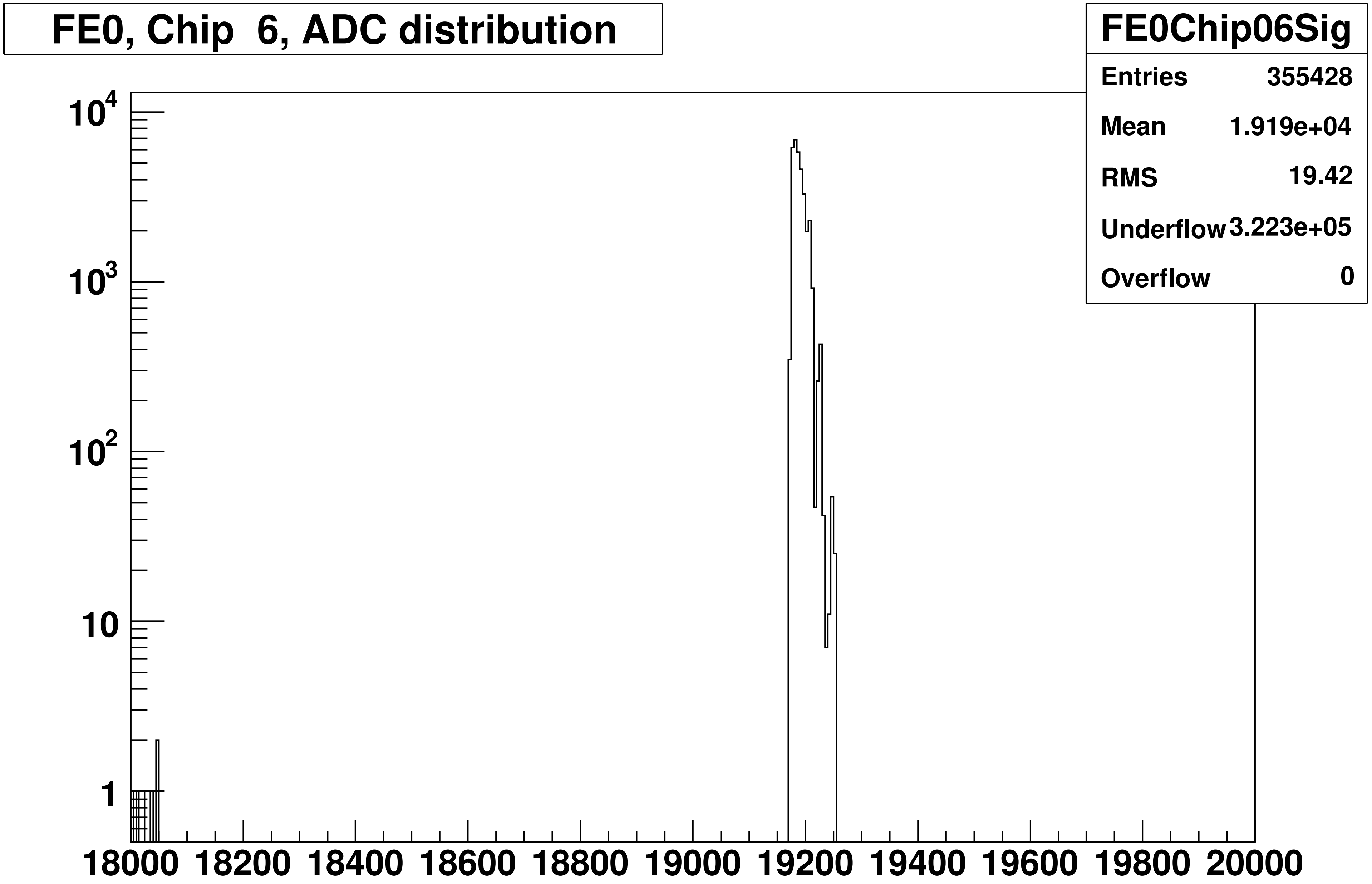
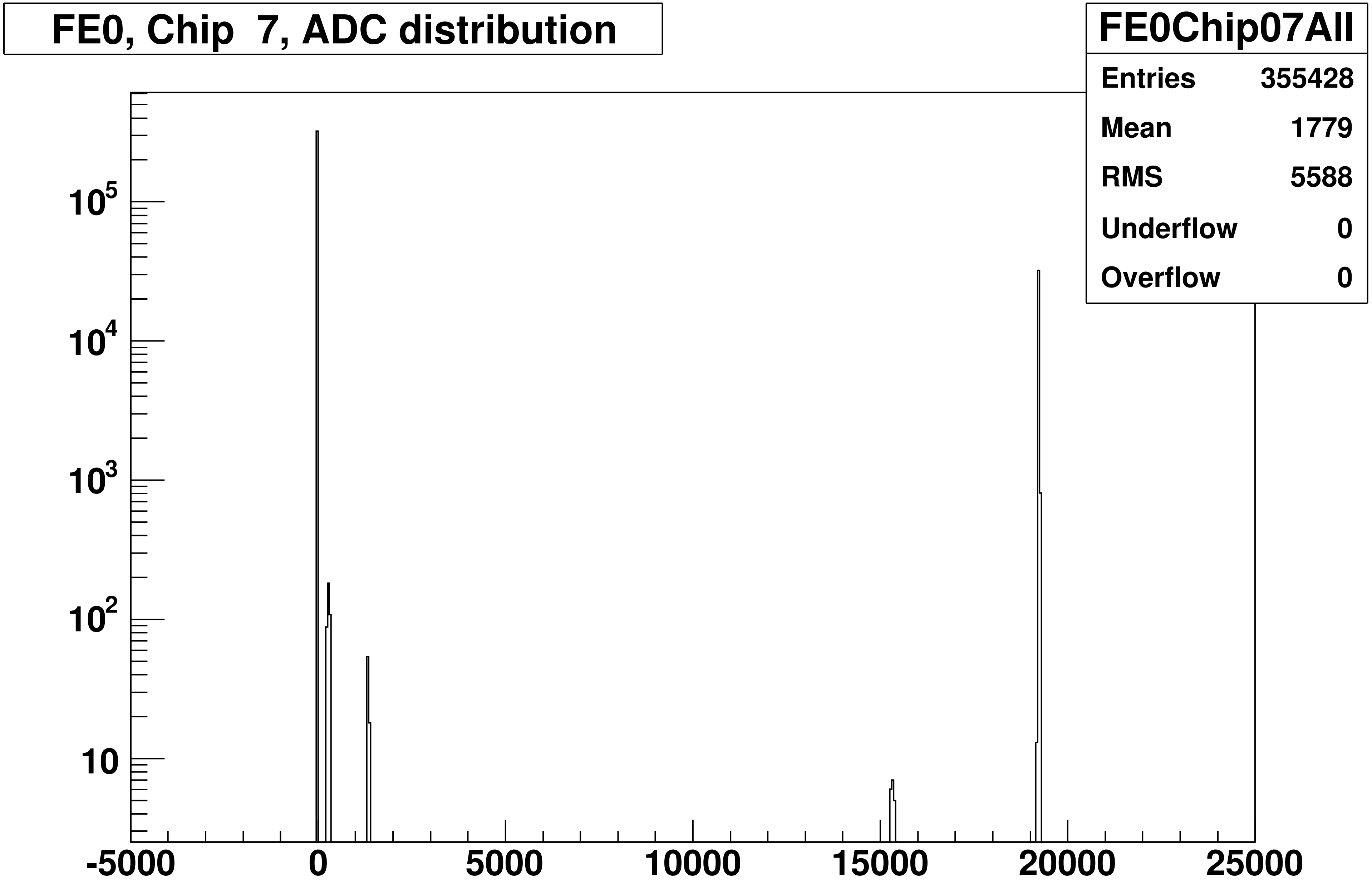
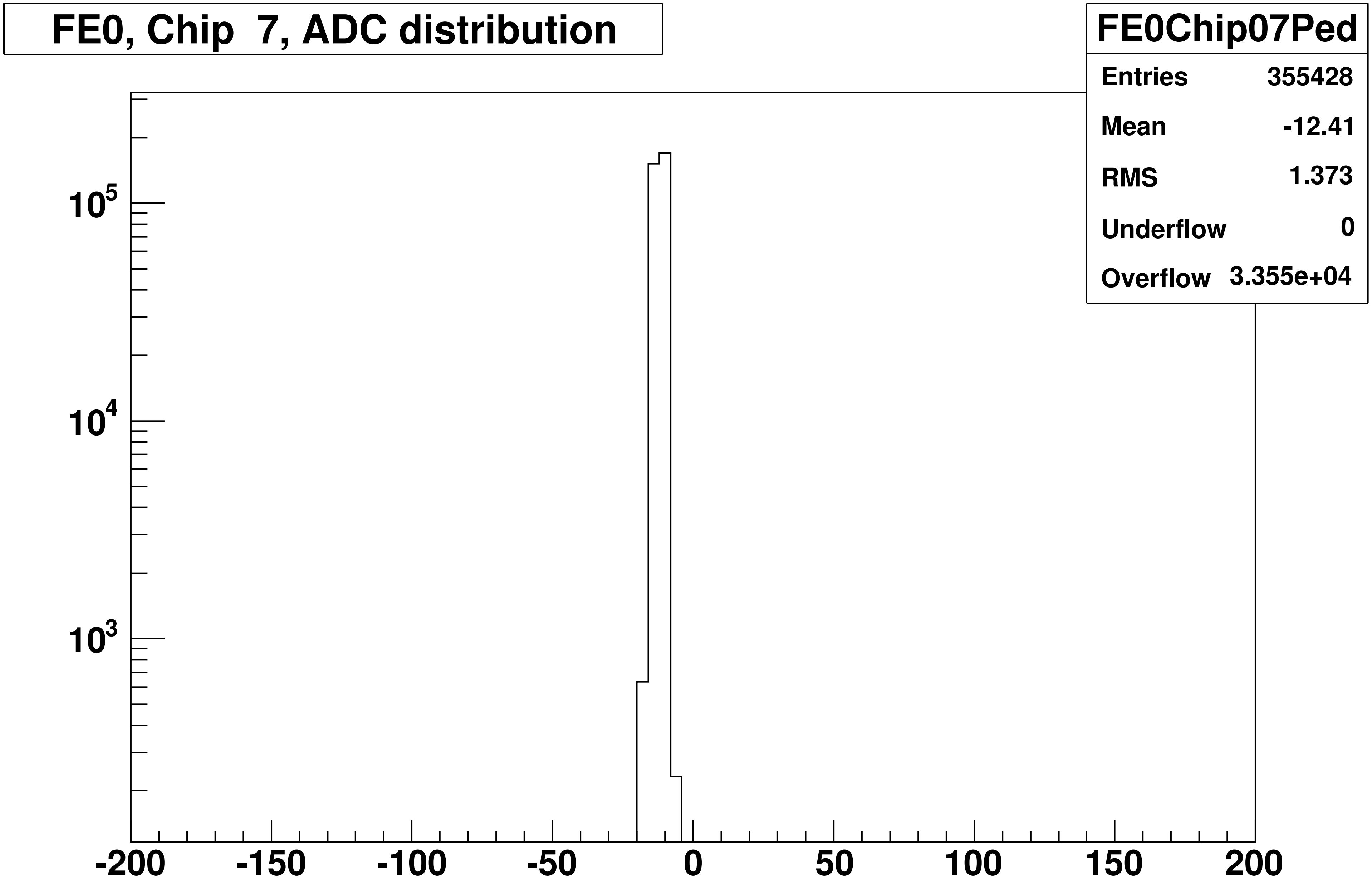
-150
-100
-50
0
50
100
150
200



FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 355428
Mean 1.919e+04
RMS 19.42
Underflow 3.223e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 355428
Mean 1779
RMS 5588
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 355428
Mean -12.41
RMS 1.373
Underflow 0
Overflow 3.355e+04
10^5
10^4
10^3
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150
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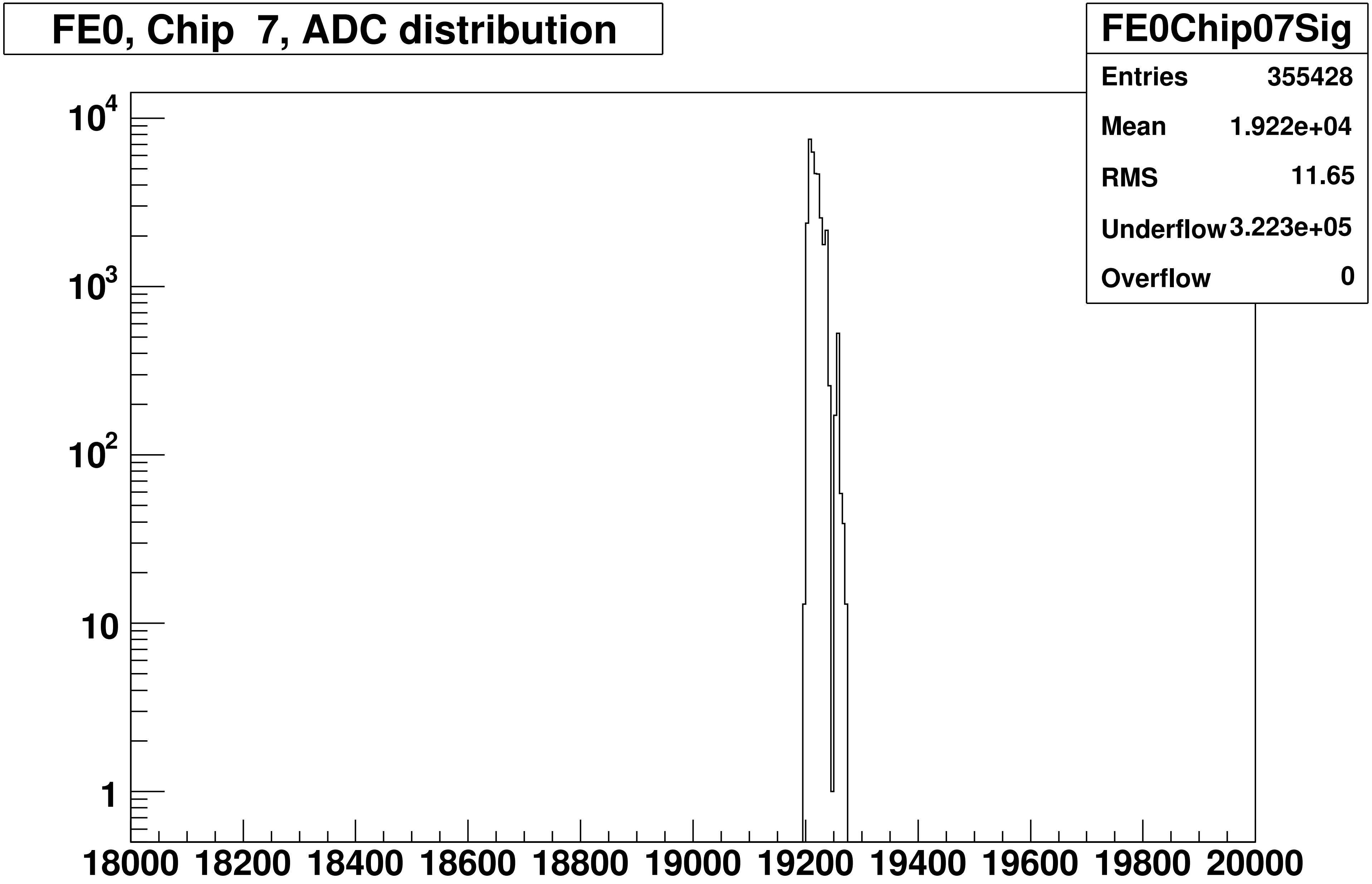
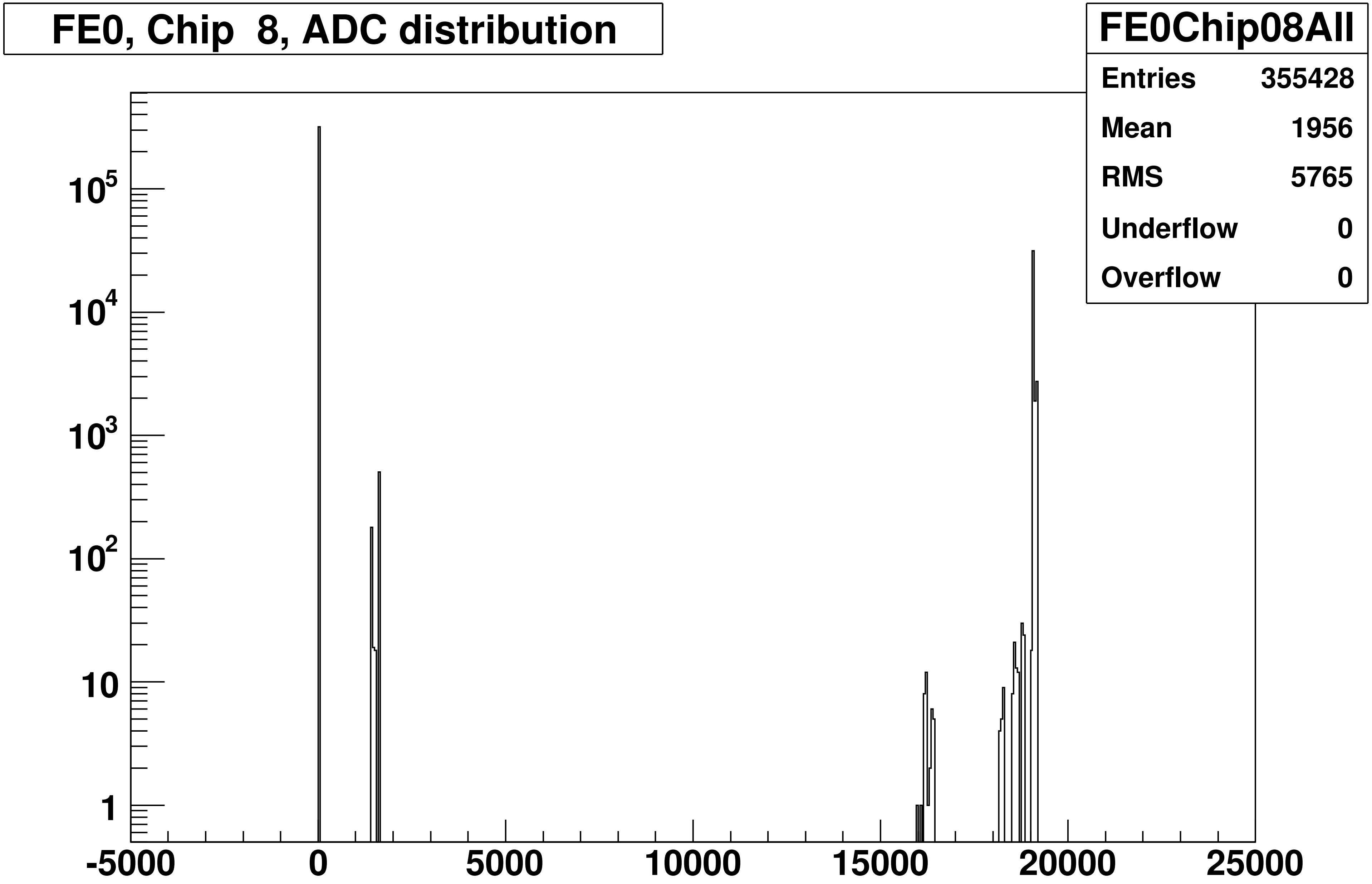


FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 355428
Mean 1.922e+04
RMS 11.65
Underflow 3.223e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 355428
Mean 1956
RMS 5765
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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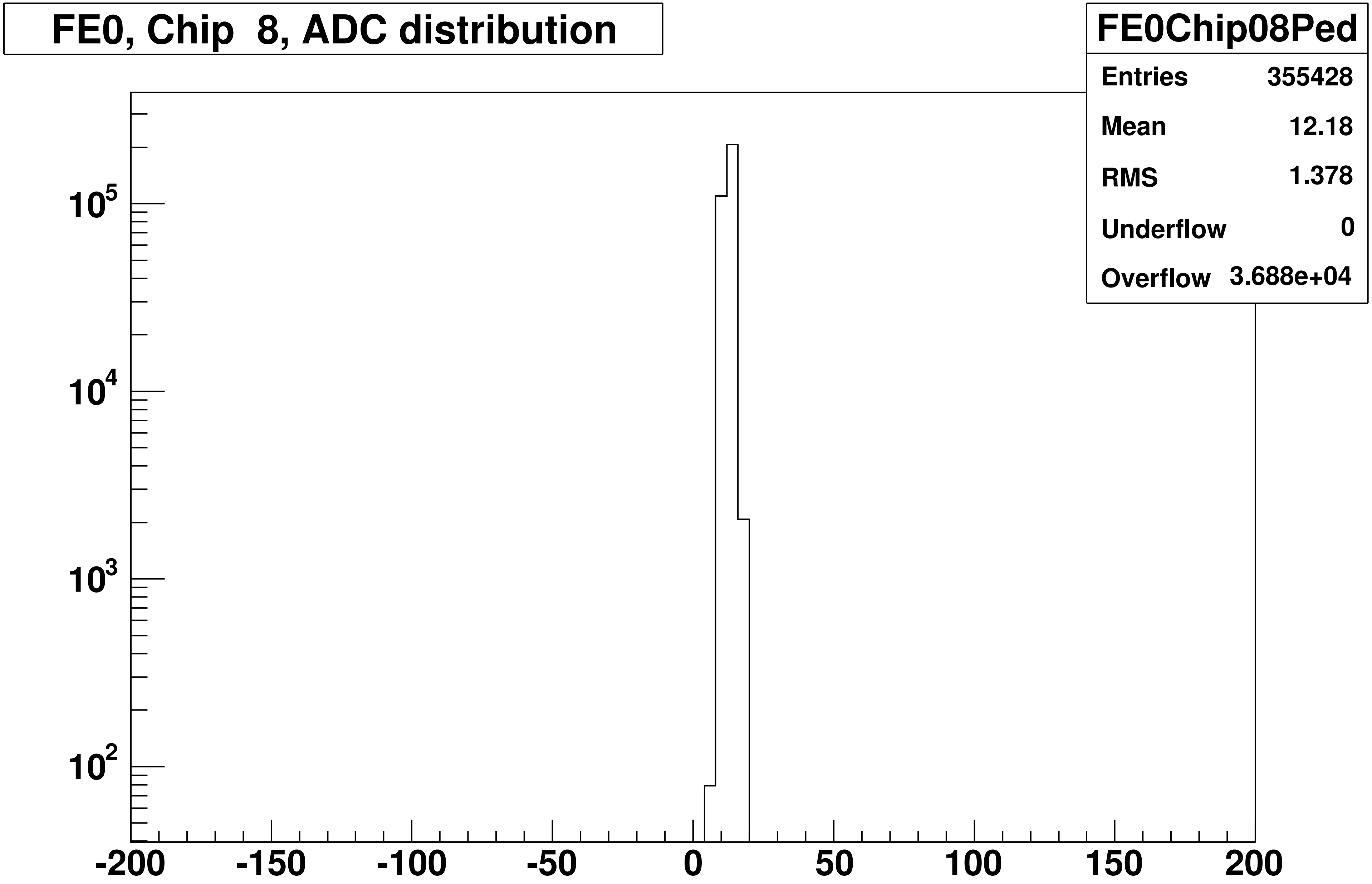
10000
15000
20000
25000


FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 355428
Mean 12.18
RMS 1.378
Underflow 0
Overflow 3.688e+04
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10^4
10^3
10^2
-200
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-100
-50
0
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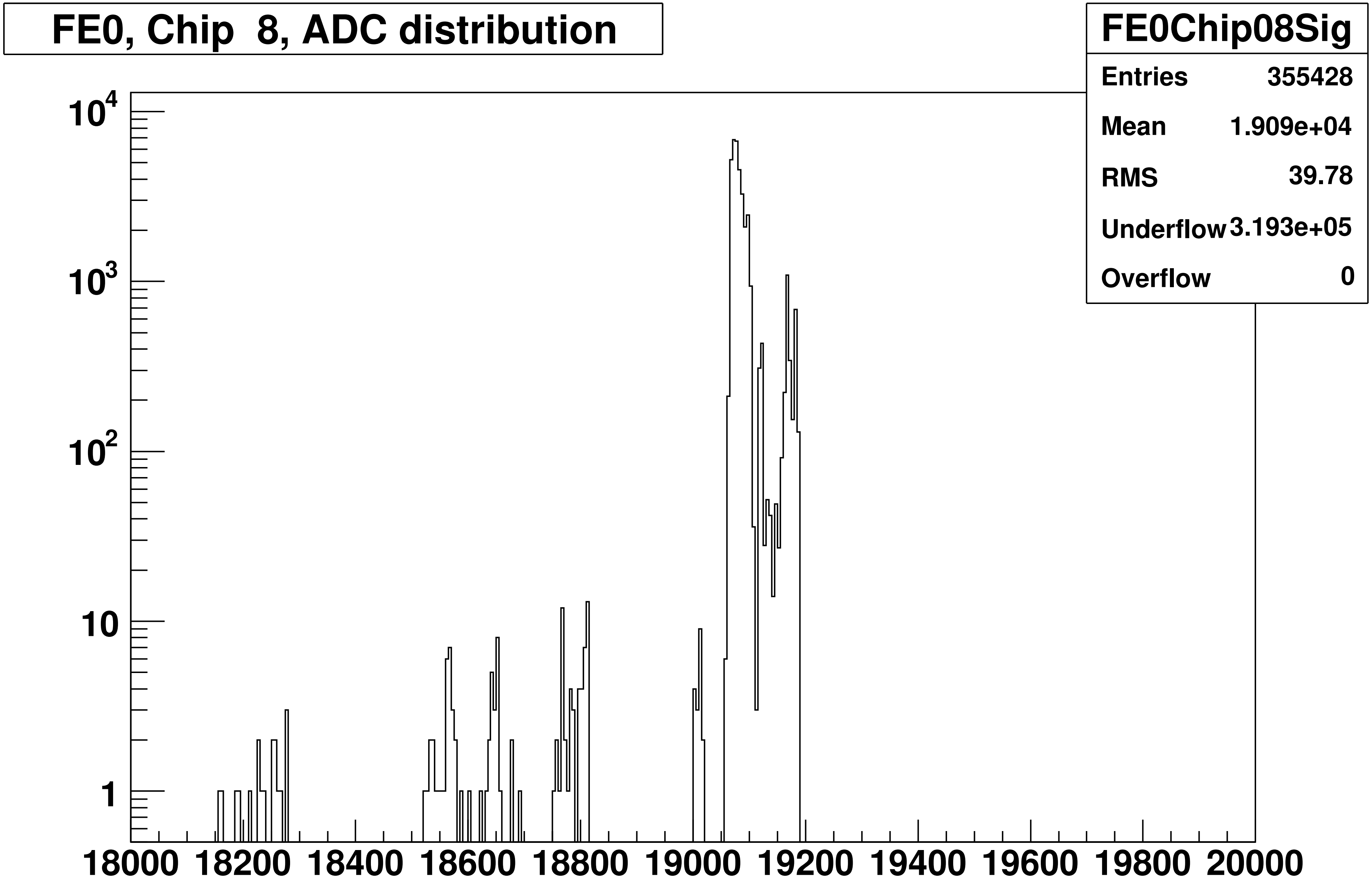
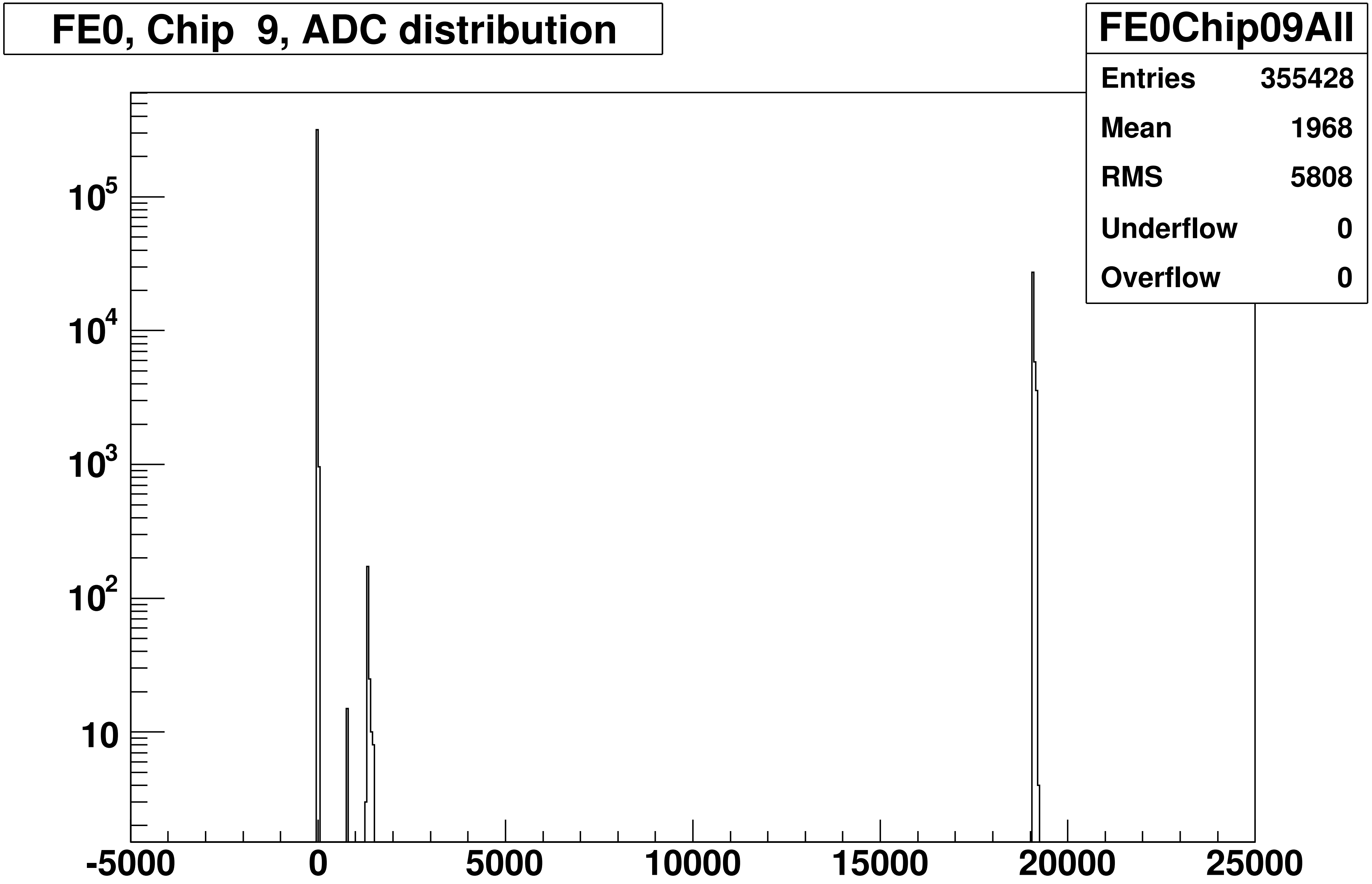
150
200



FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 355428
Mean 1.909e+04
RMS 39.78
Underflow 3.193e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 355428
Mean 1968
RMS 5808
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
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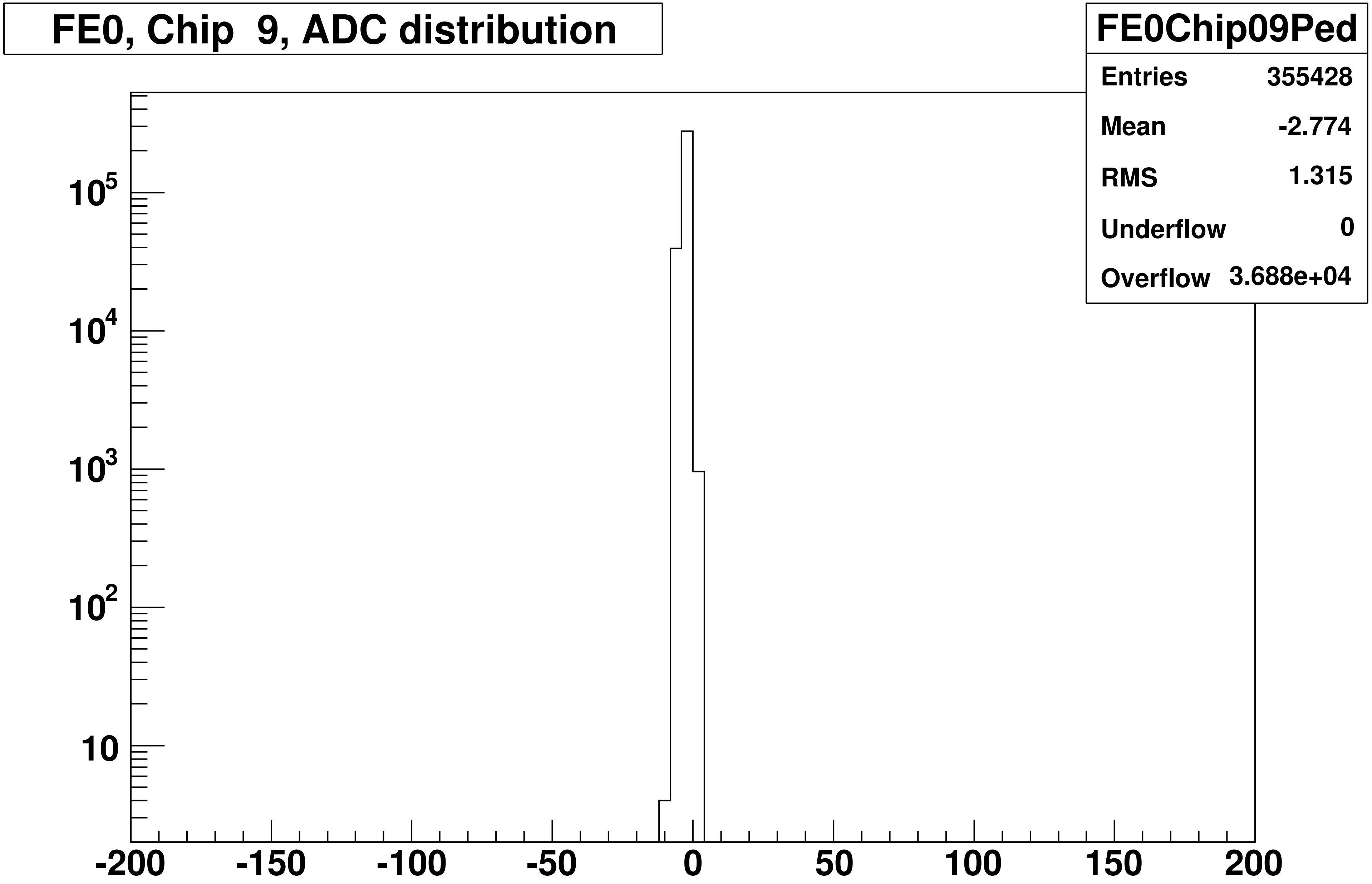
10000
15000
20000
25000


FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 355428
Mean -2.774
RMS 1.315
Underflow 0
Overflow 3.688e+04
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10^4
10^3
10^2
10
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-100
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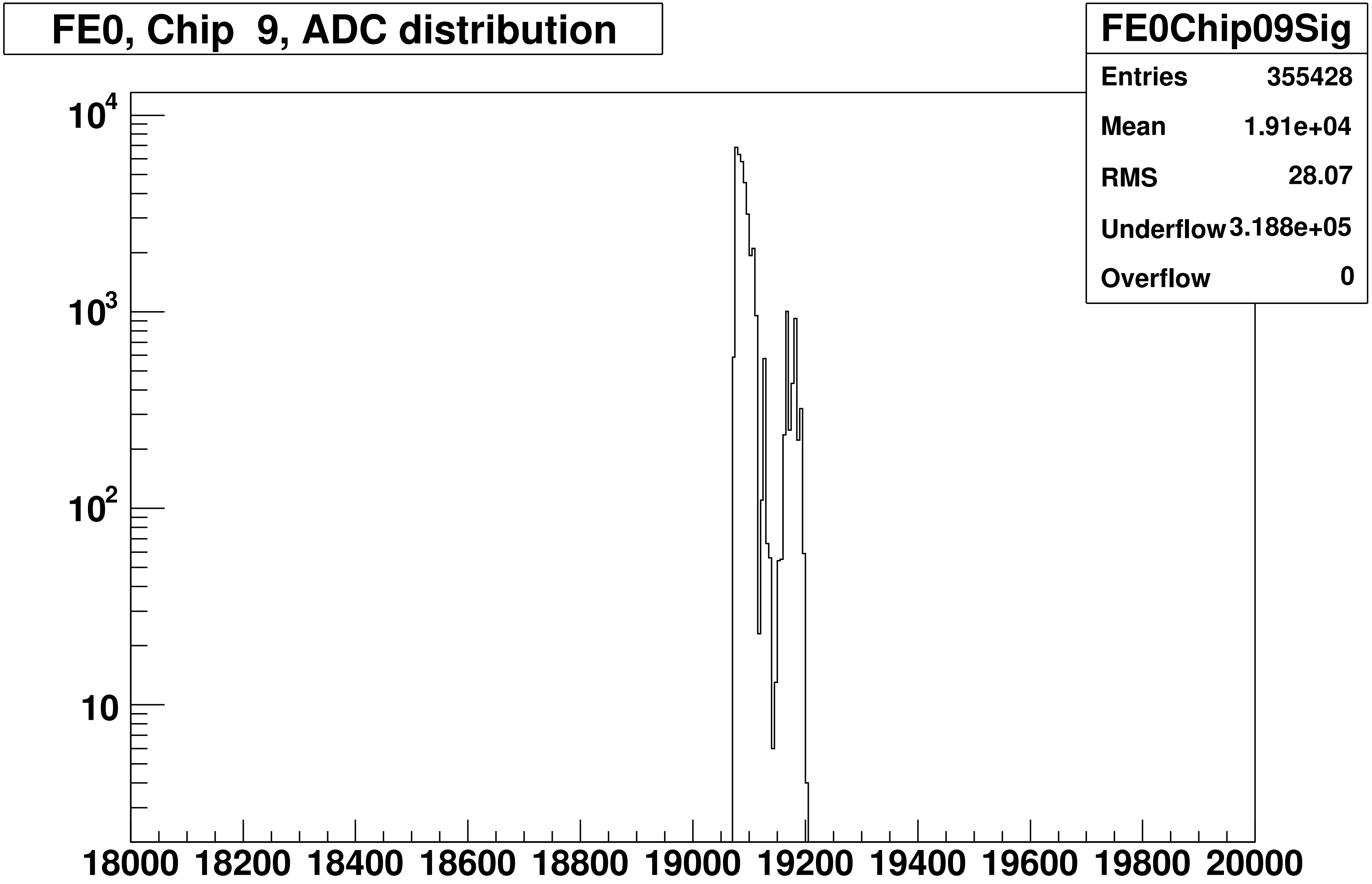
150
200


FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 355428
Mean 1.91e+04
RMS 28.07
Underflow 3.188e+05
Overflow 0
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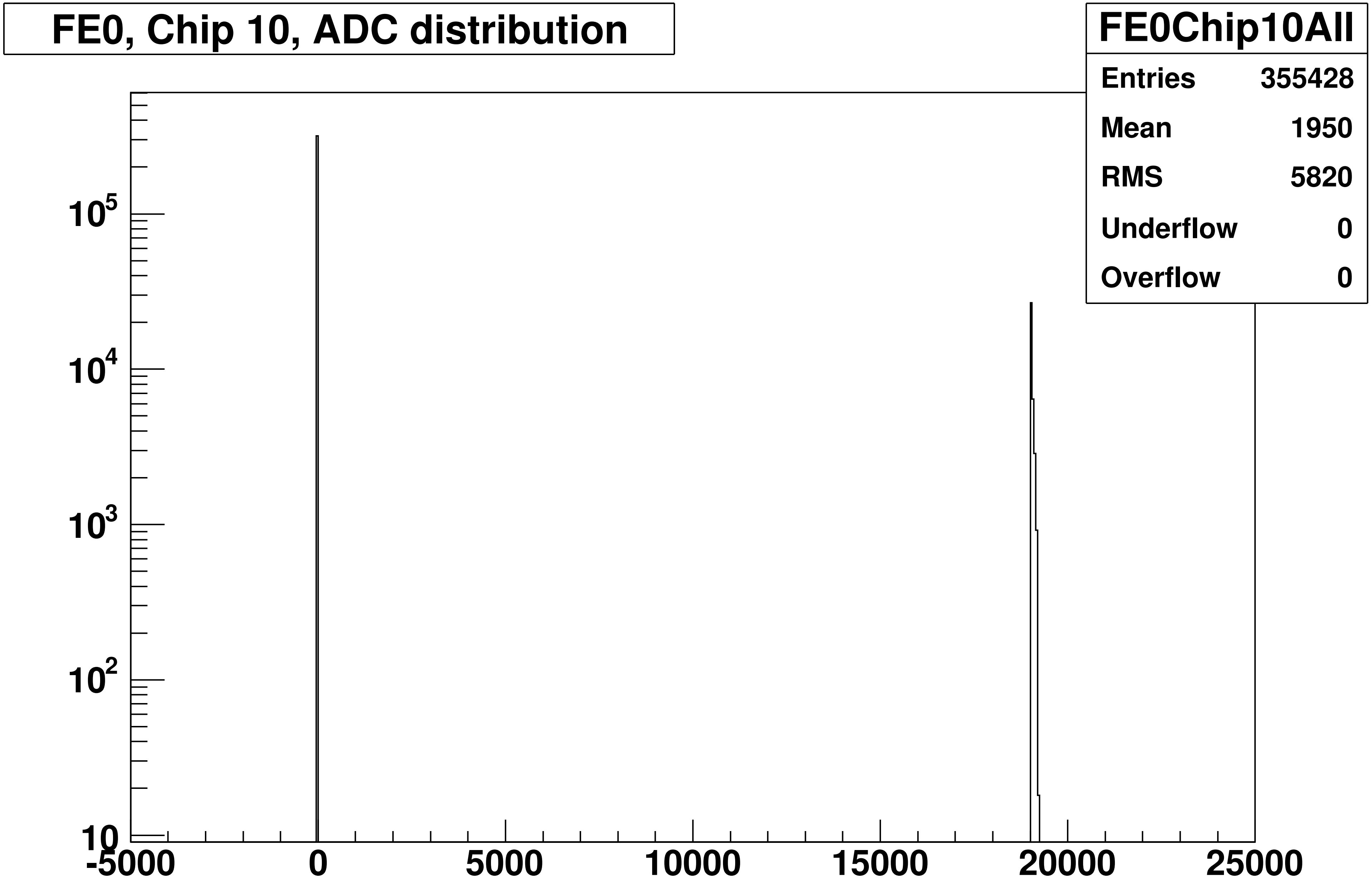
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 355428
Mean 1950
RMS 5820
Underflow 0
Overflow 0
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10^4
10^3
10^2
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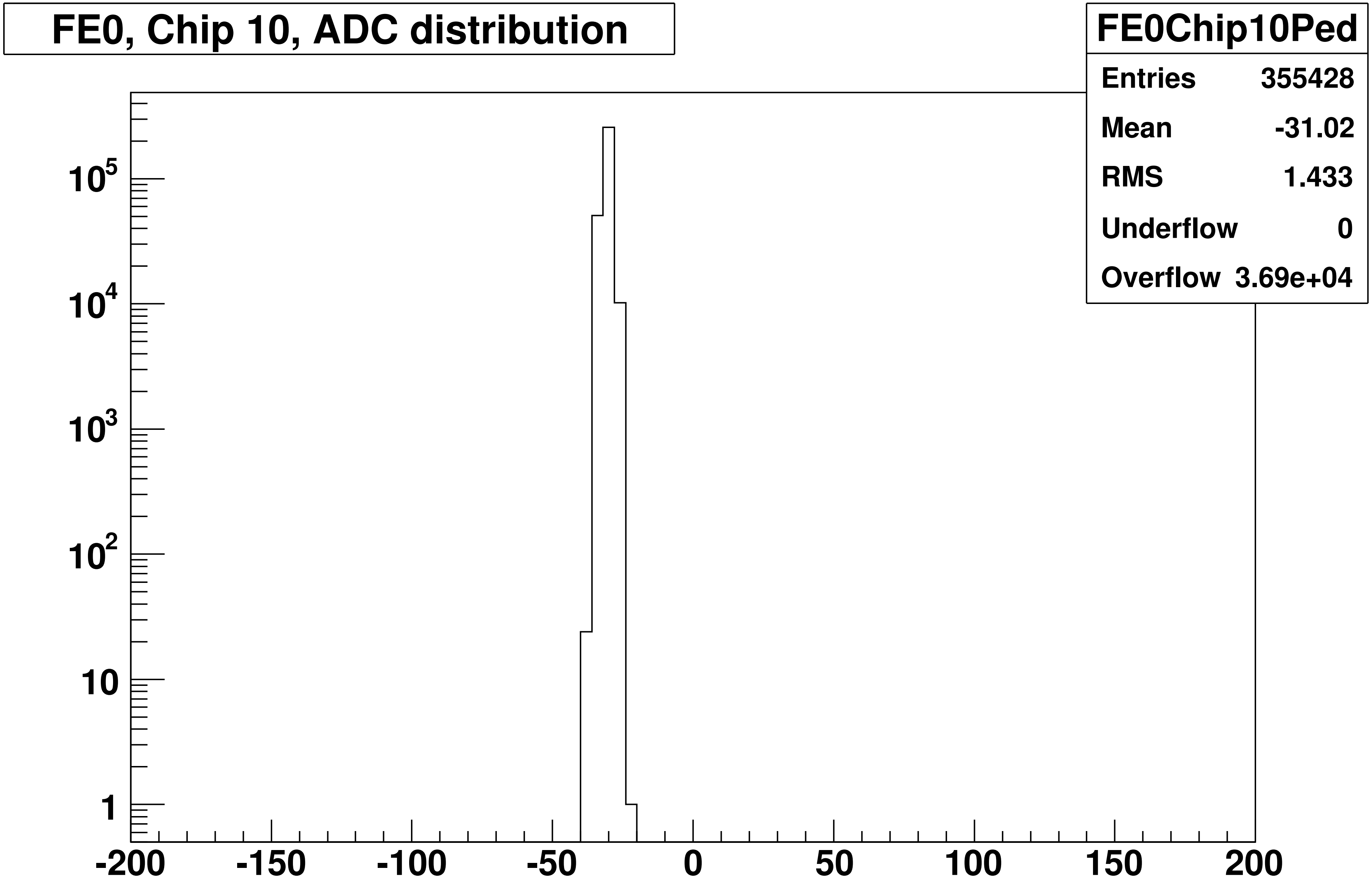
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 355428
Mean -31.02
RMS 1.433
Underflow 0
Overflow 3.69e+04
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10^3
10^2
10
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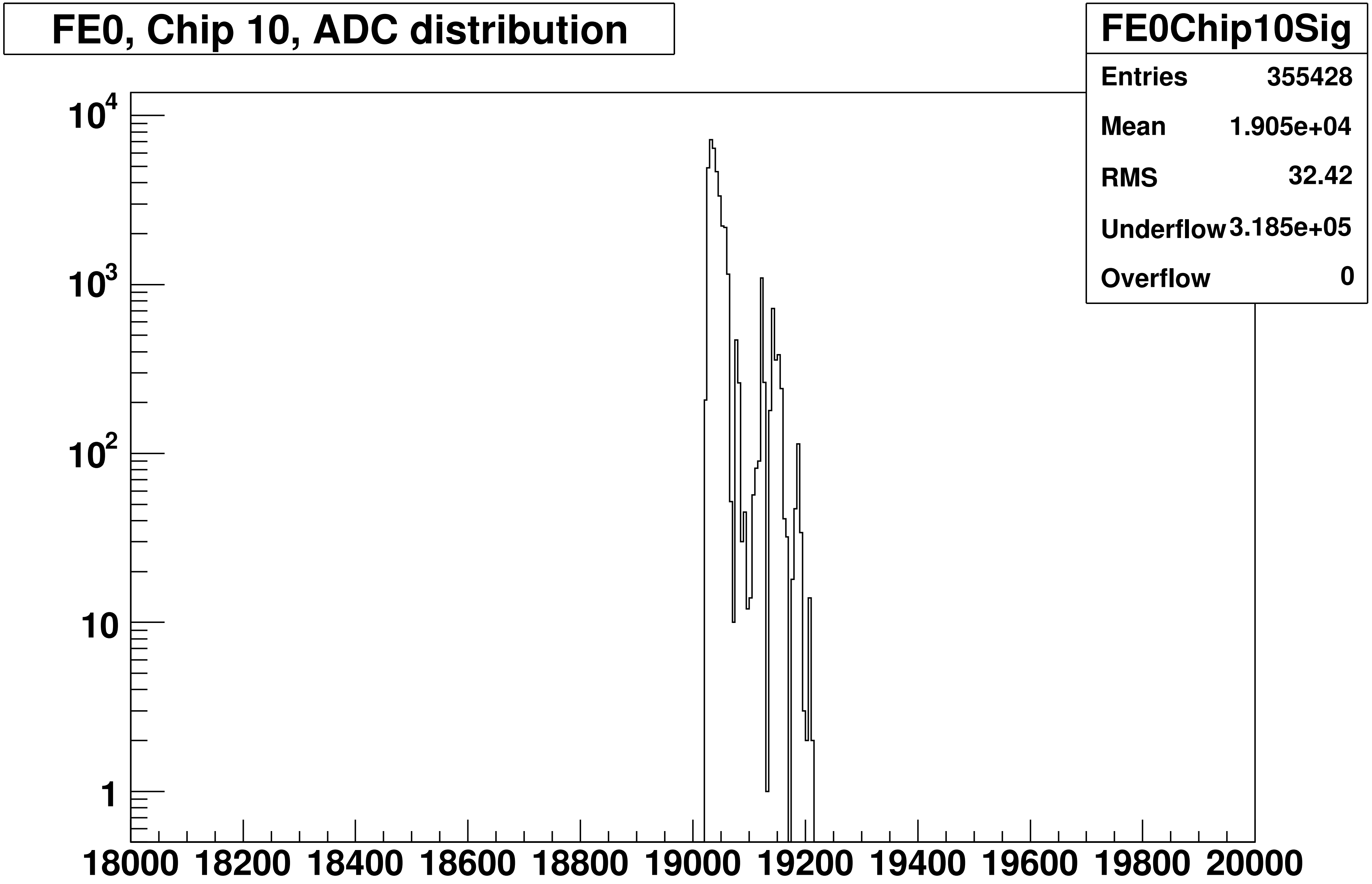
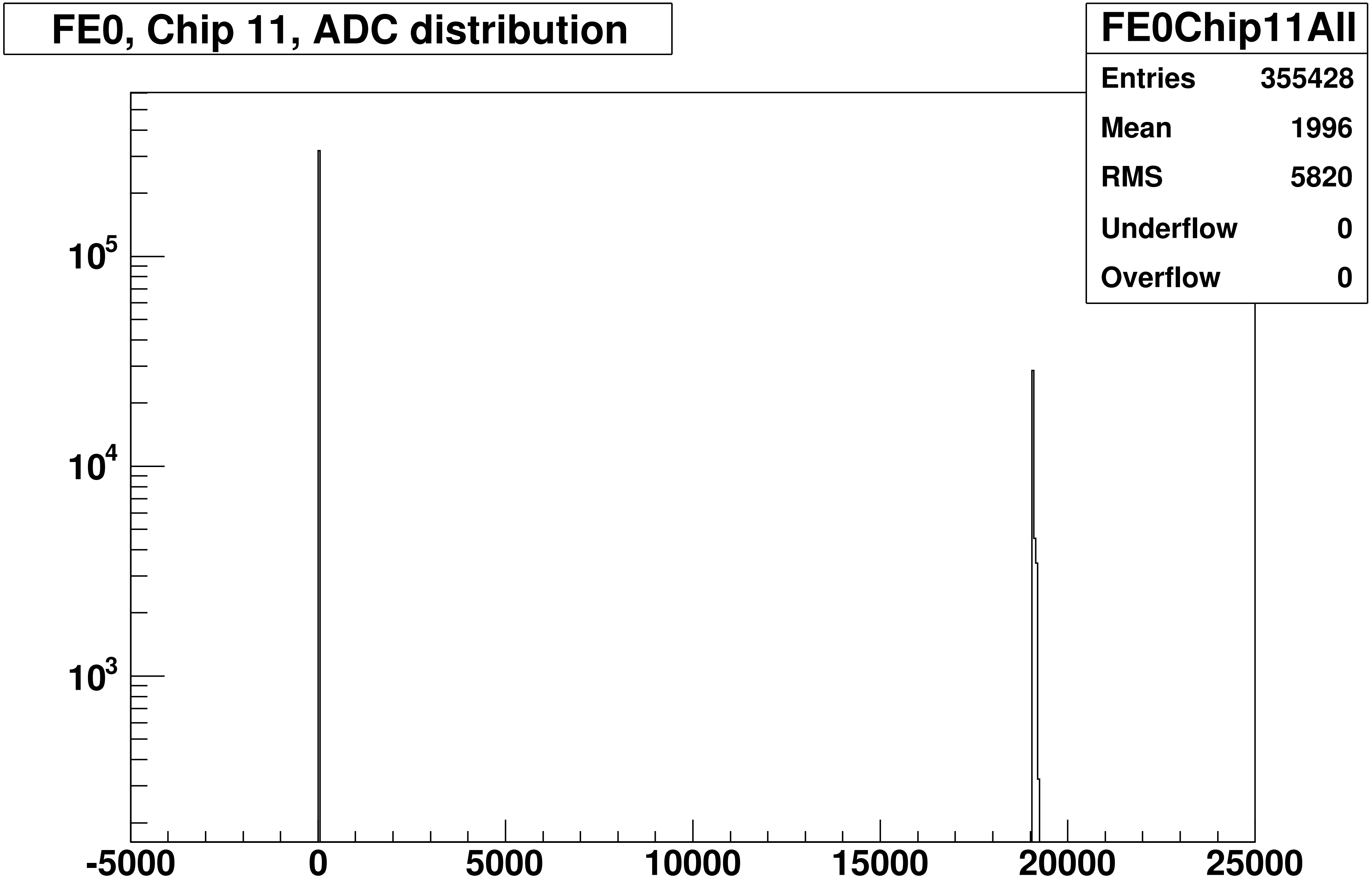
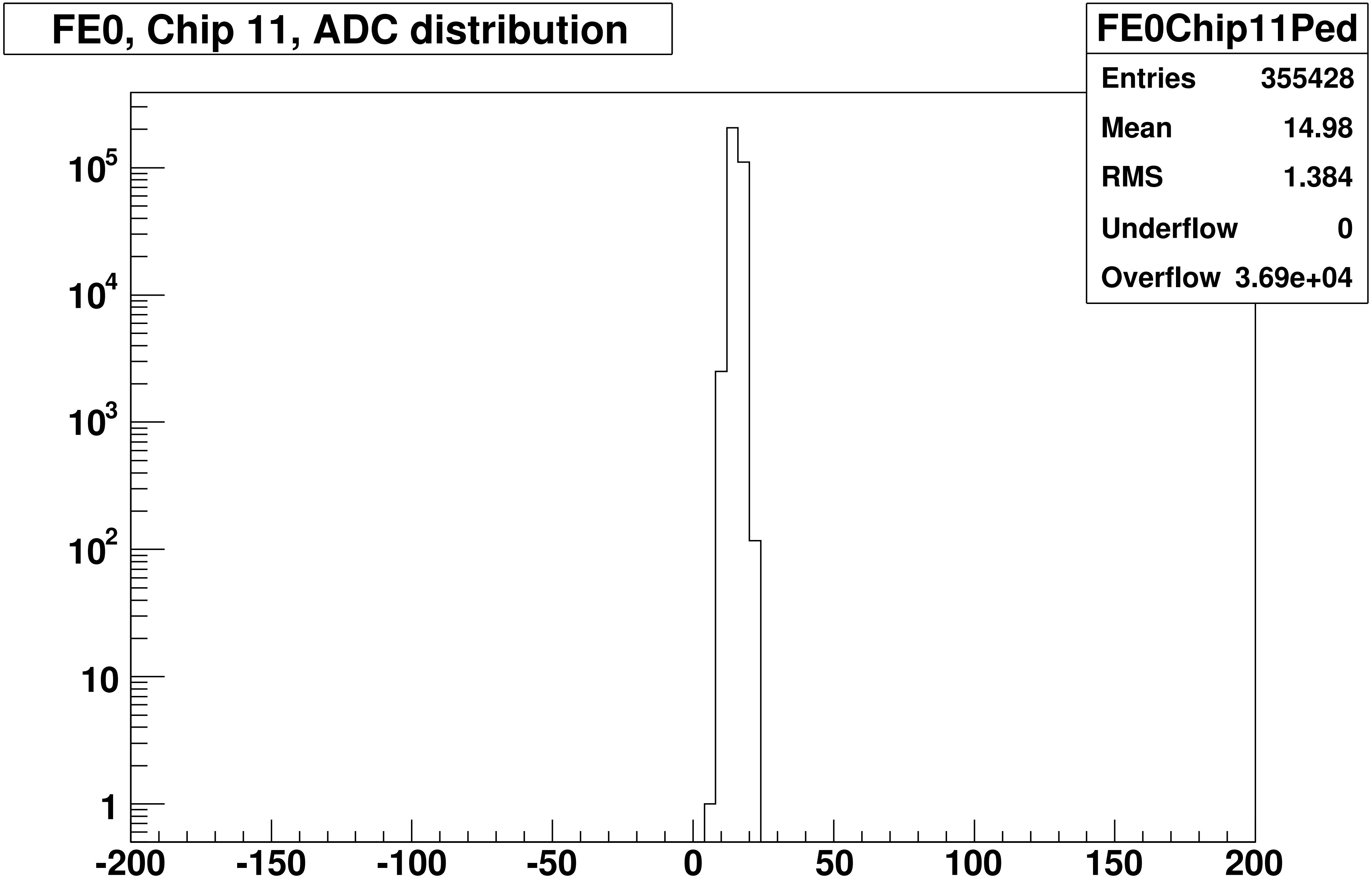
100
150
200


FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 355428
Mean 1.905e+04
RMS 32.42
Underflow 3.185e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 355428
Mean 1996
RMS 5820
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 355428
Mean 14.98
RMS 1.384
Underflow 0
Overflow 3.69e+04
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-100
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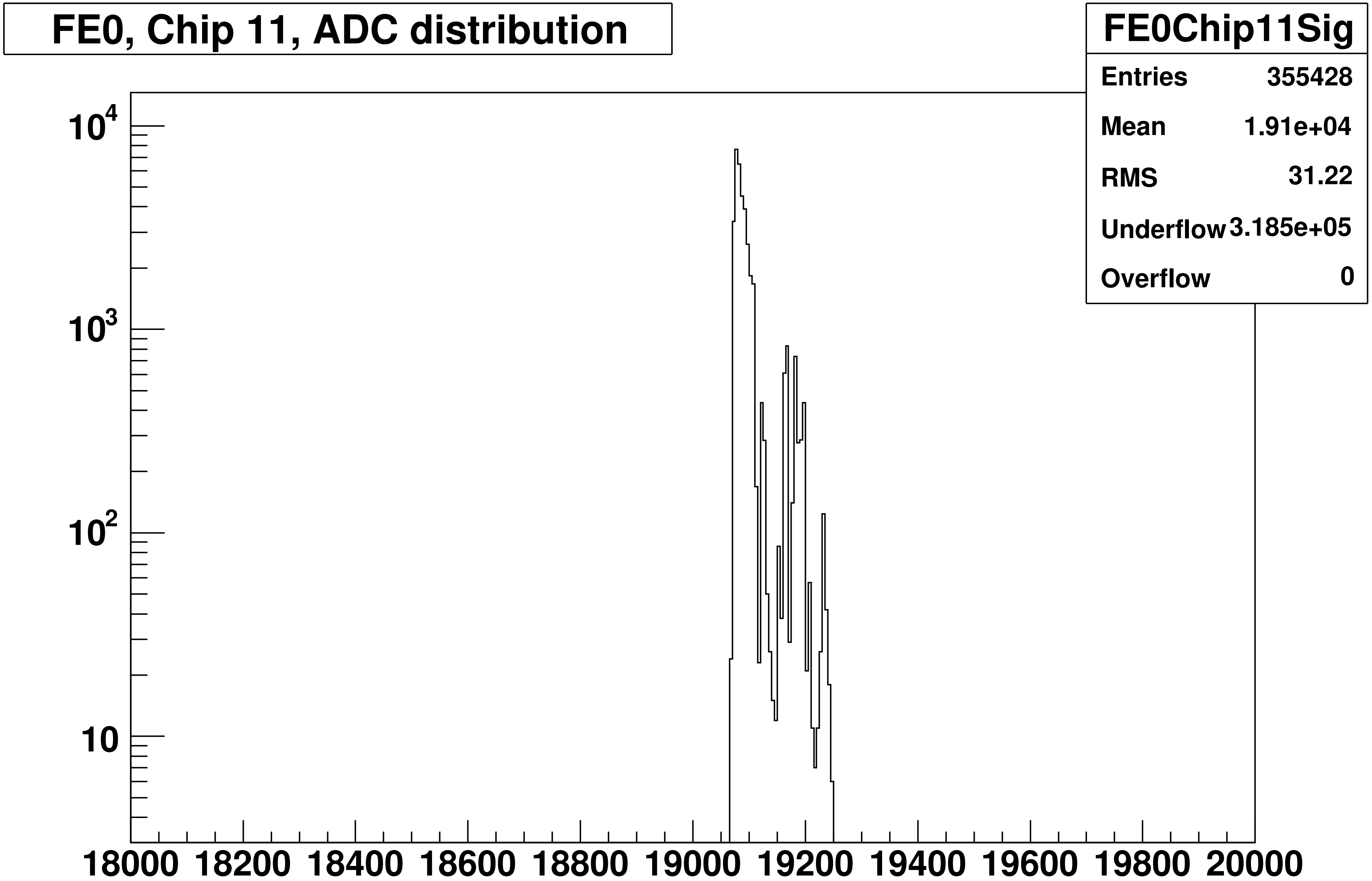
0
50
100
150
200



FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 355428
Mean 1.91e+04
RMS 31.22
Underflow 3.185e+05
Overflow 0
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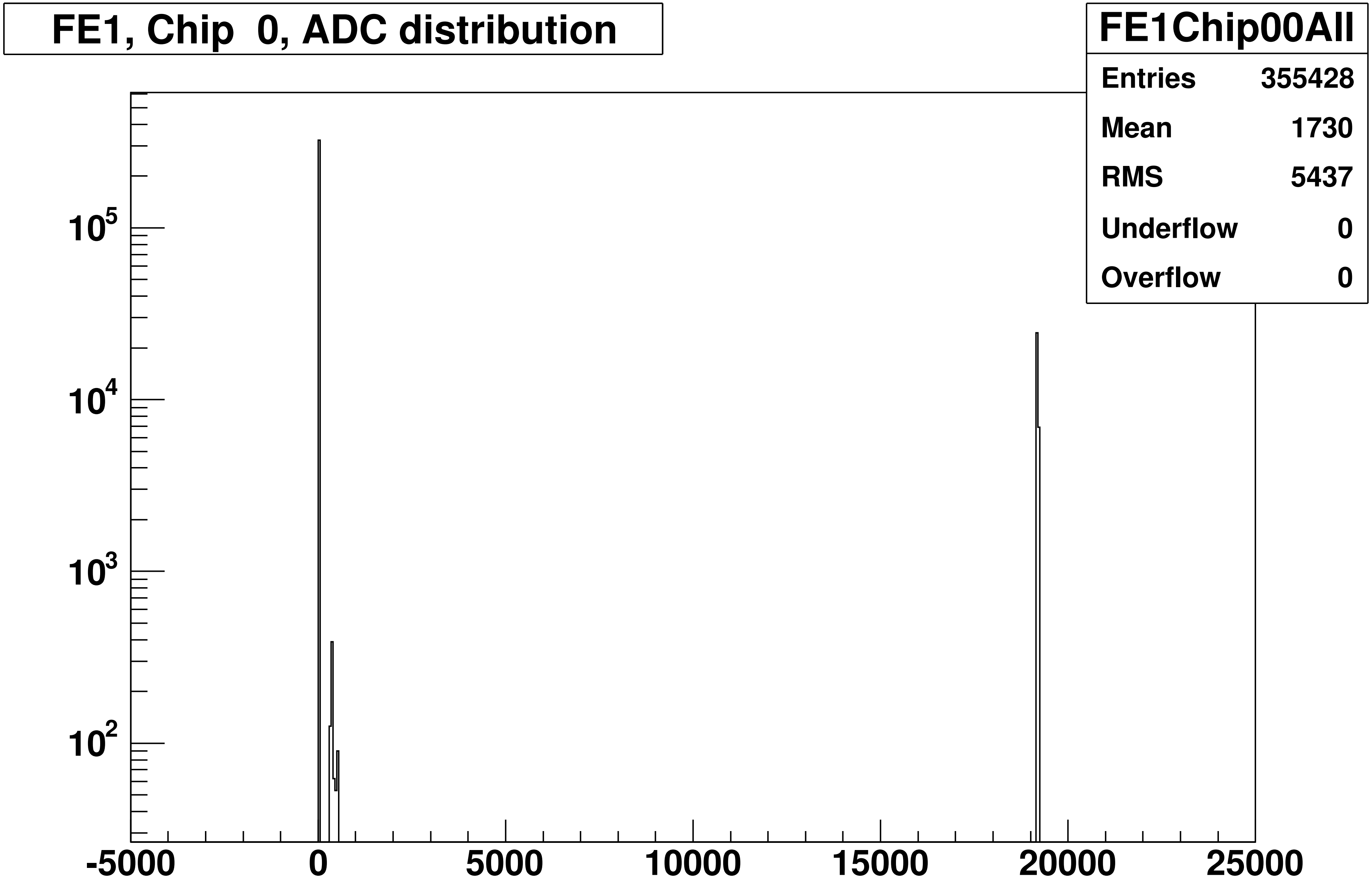
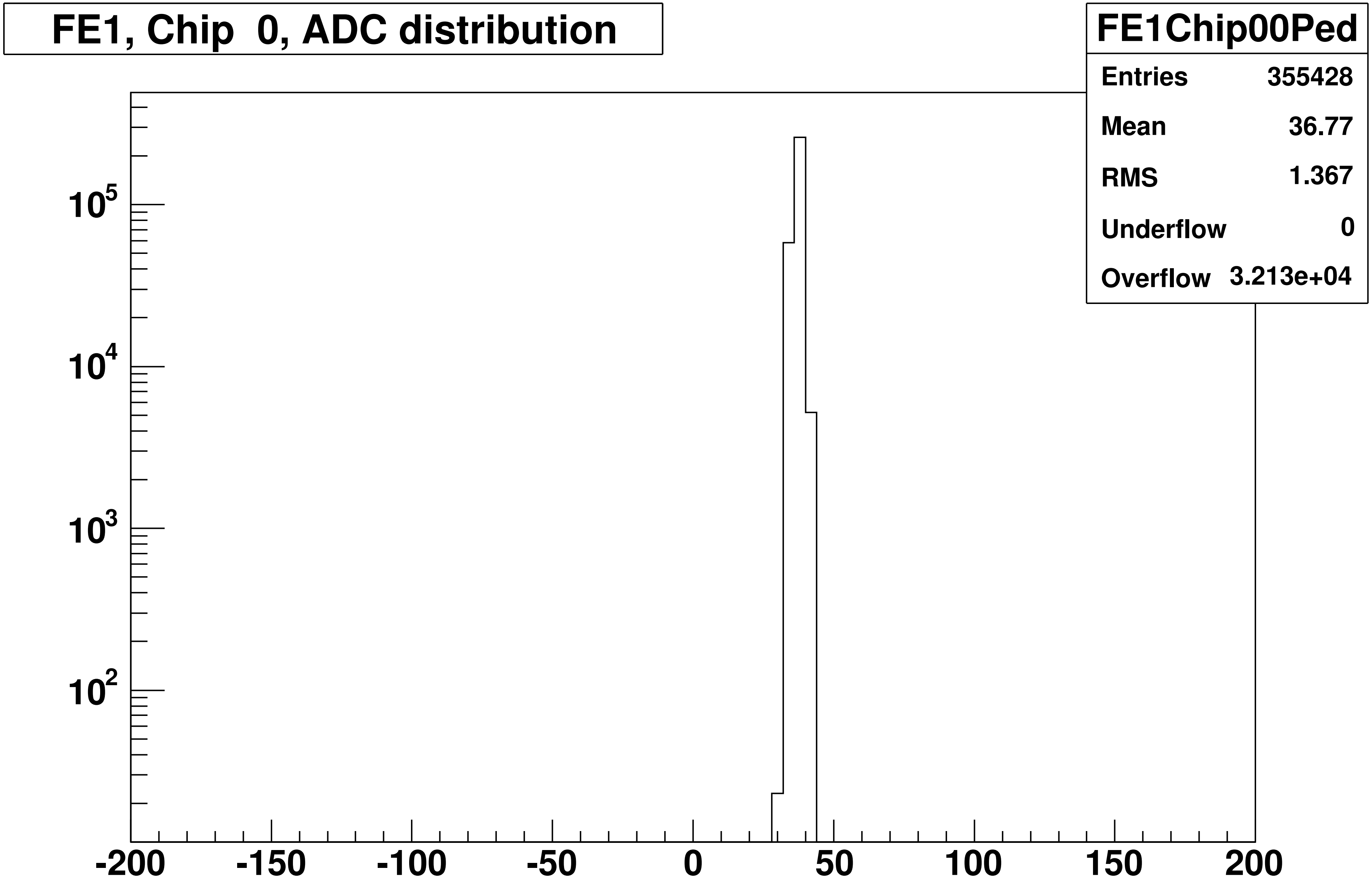
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 355428
Mean 1730
RMS 5437
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 355428
Mean 36.77
RMS 1.367
Underflow 0
Overflow 3.213e+04
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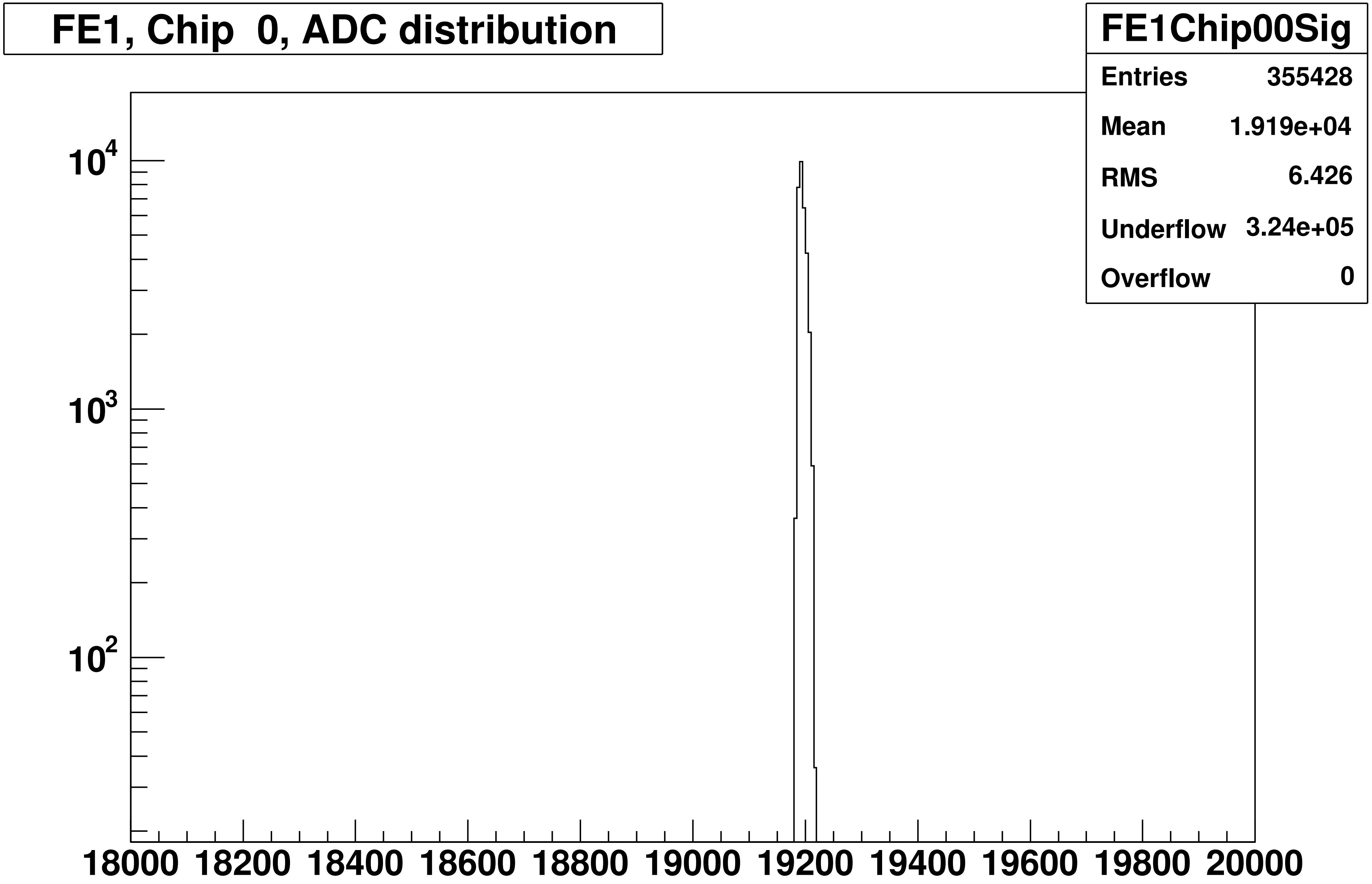
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 355428
Mean 1.919e+04
RMS 6.426
Underflow 3.24e+05
Overflow 0
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10^3
10^2
18000
18200
18400
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19000
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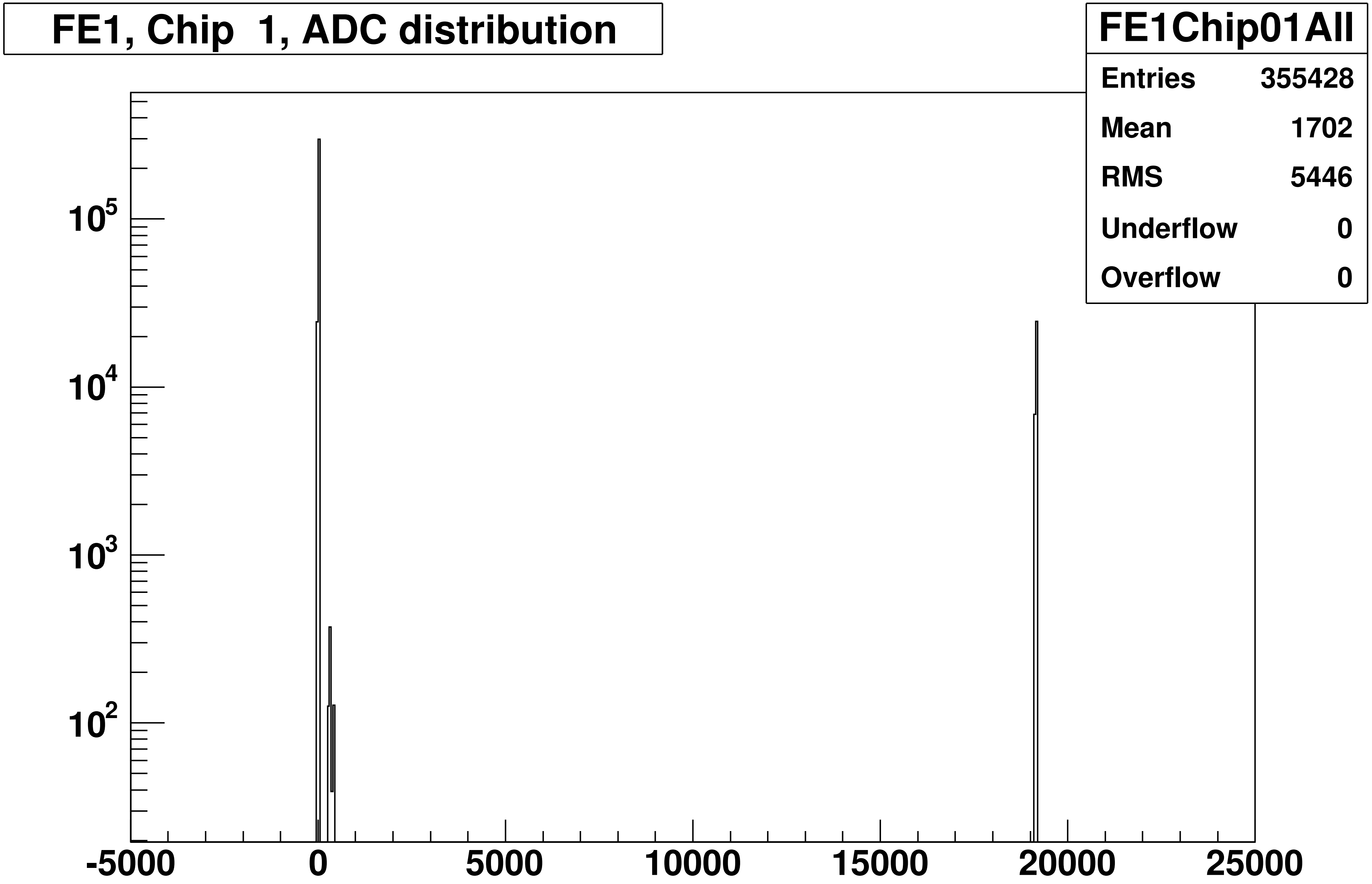
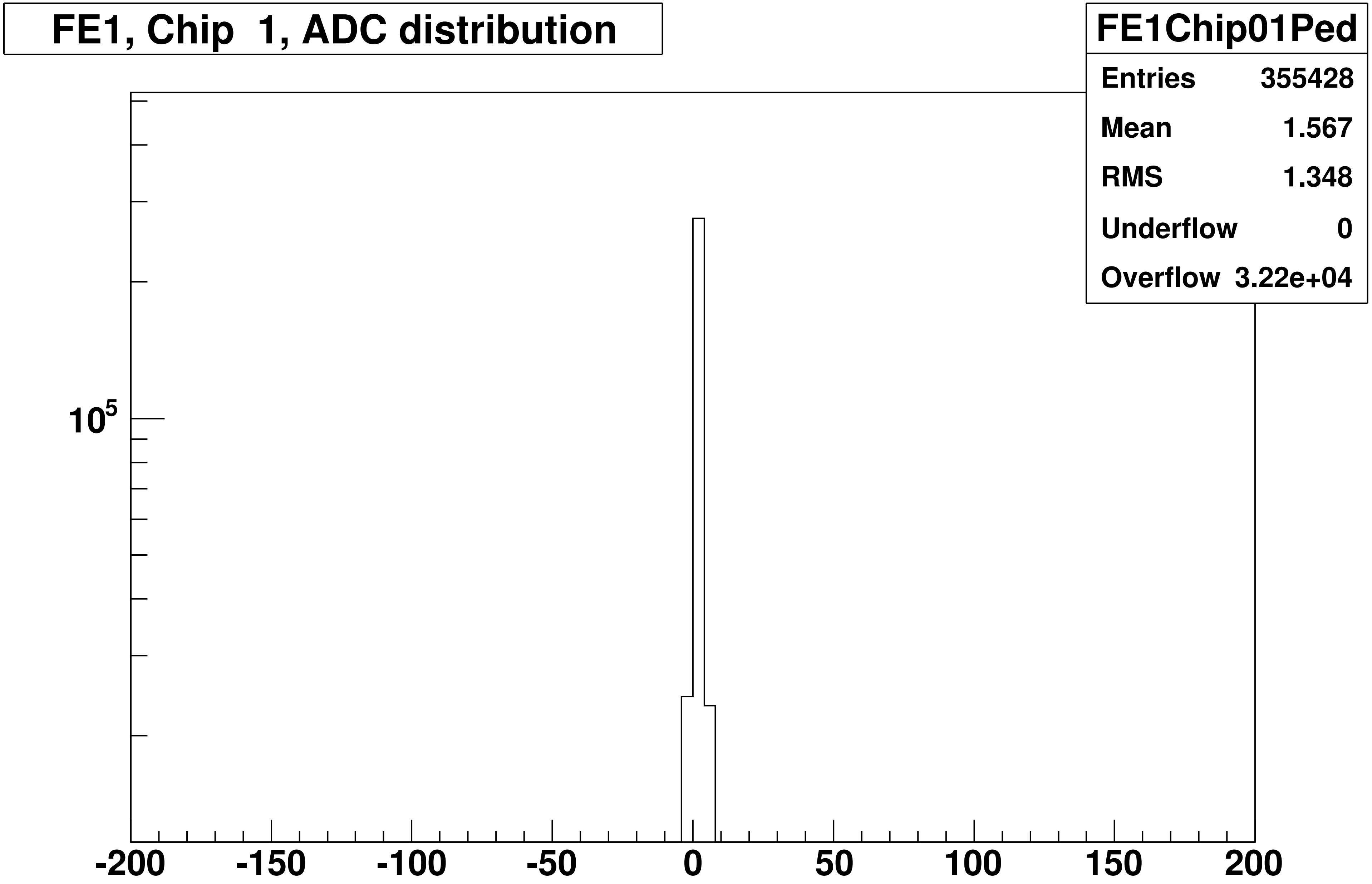
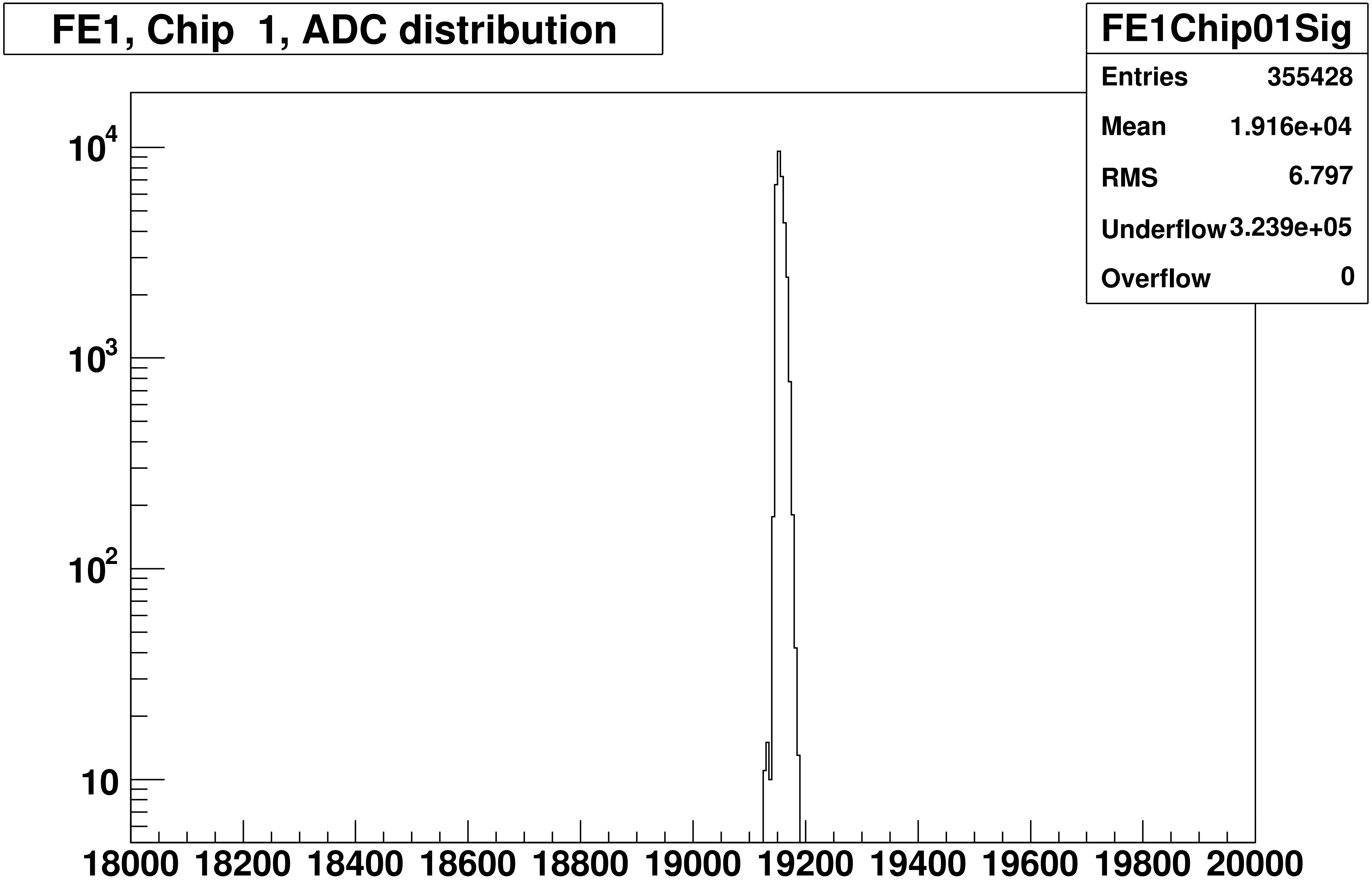
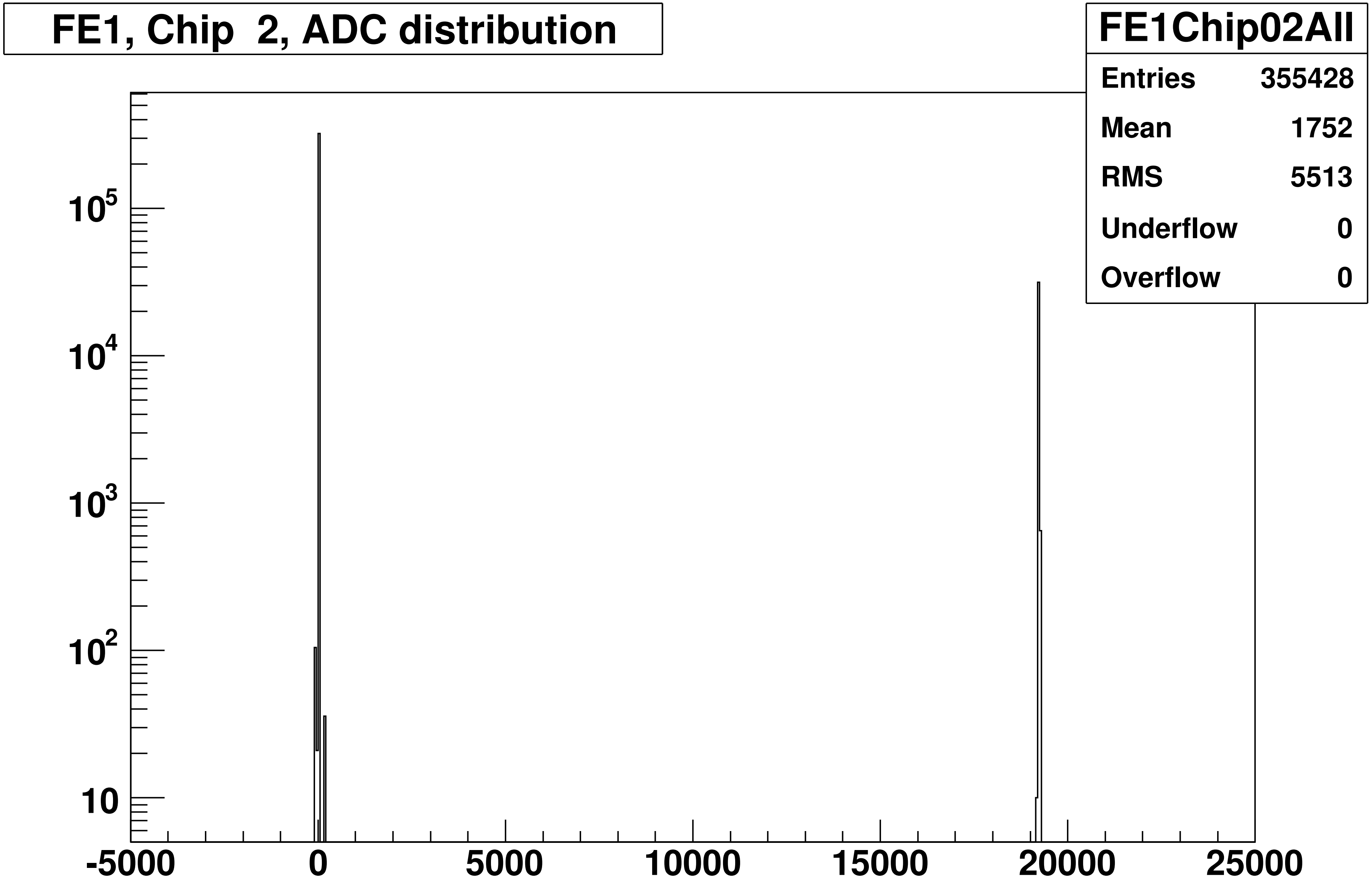
19400
19600
19800
20000



FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 355428
Mean 1702
RMS 5446
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 355428
Mean 1.567
RMS 1.348
Underflow 0
Overflow 3.22e+04
10^5
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 355428
Mean 1.916e+04
RMS 6.797
Underflow 3.239e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 355428
Mean 1752
RMS 5513
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
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15000
20000
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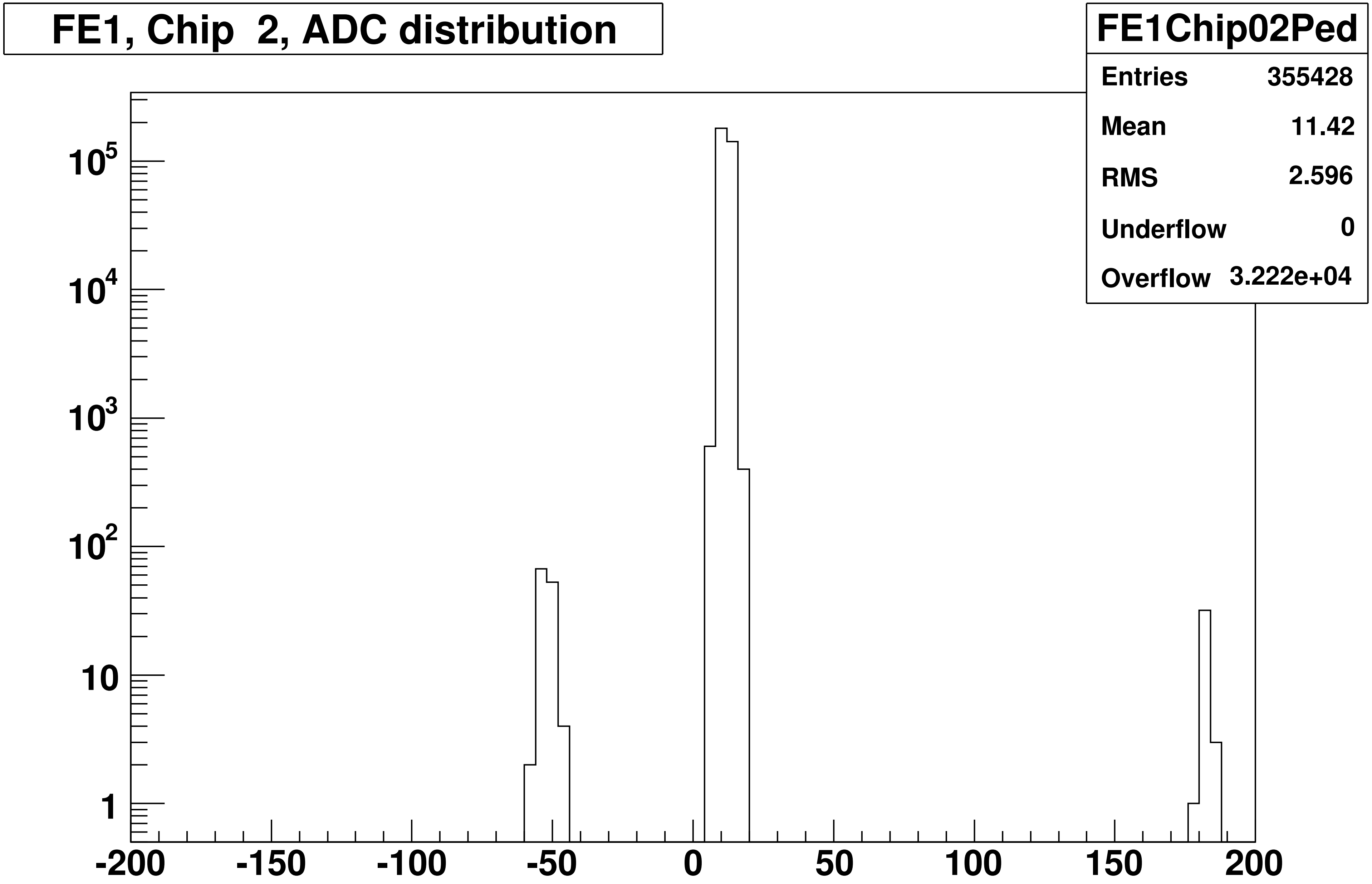
FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 355428
Mean 11.42
RMS 2.596
Underflow 0
Overflow 3.222e+04
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10^4
10^3
10^2
10
1
-200
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-100
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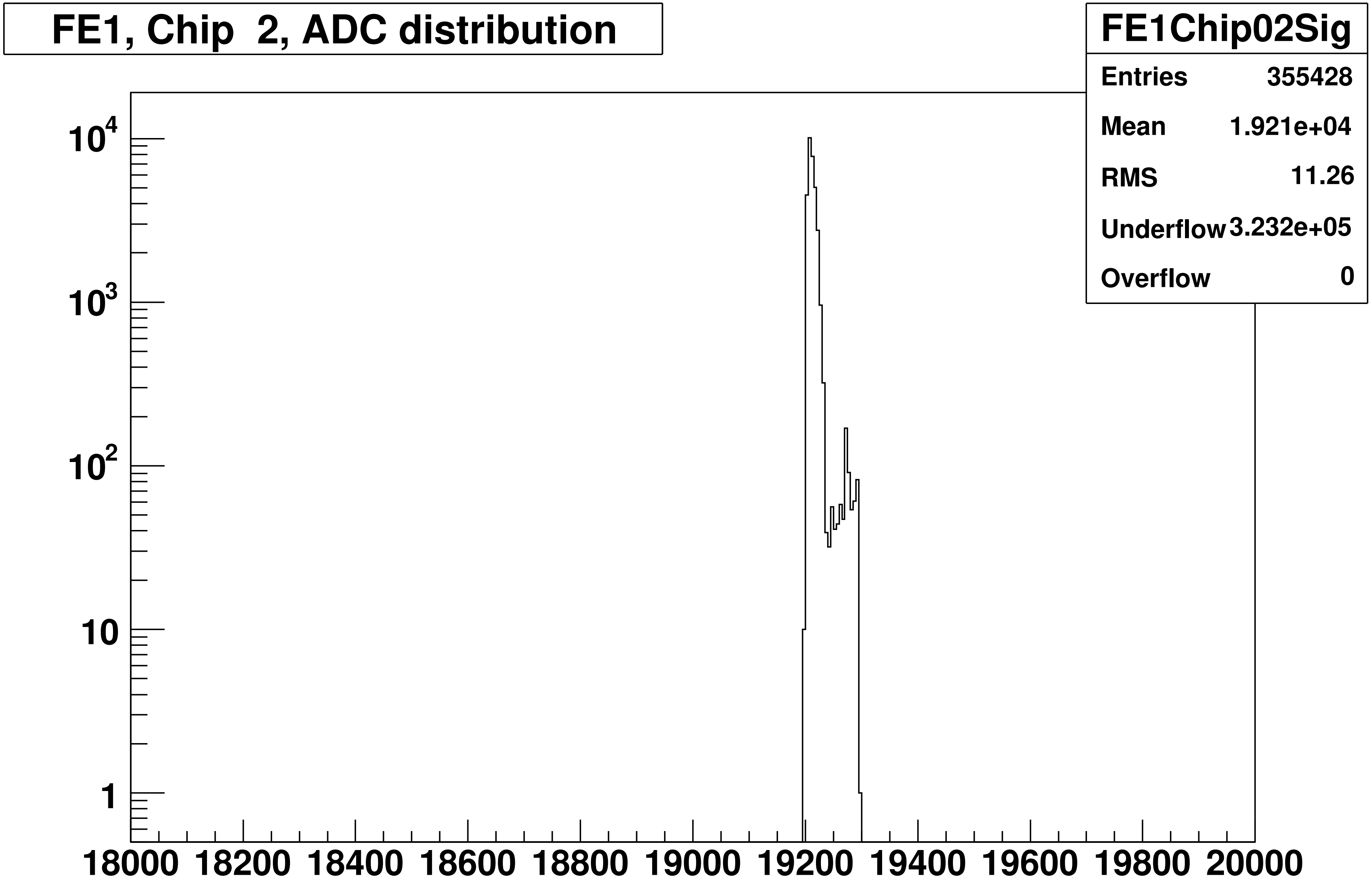
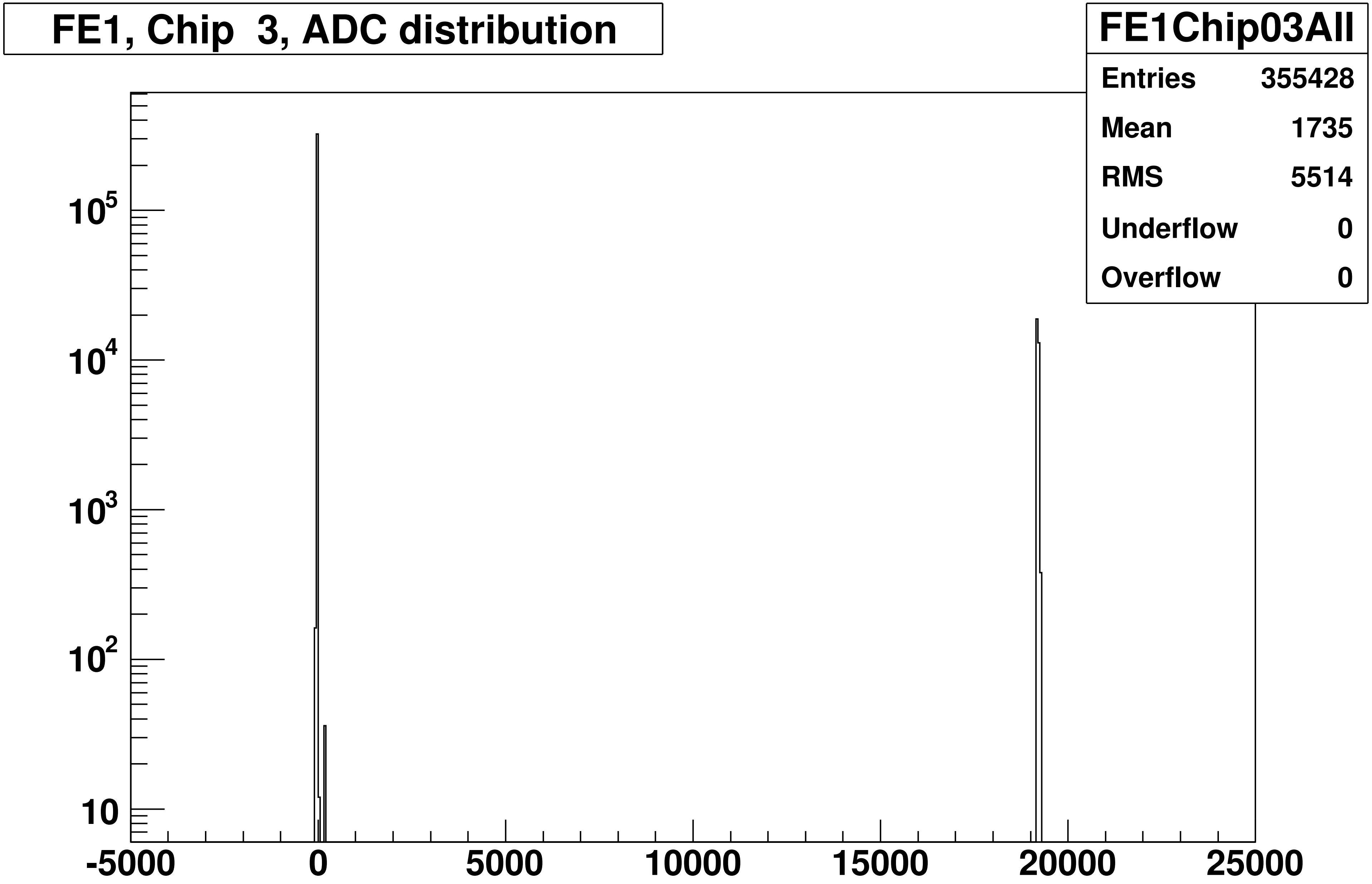
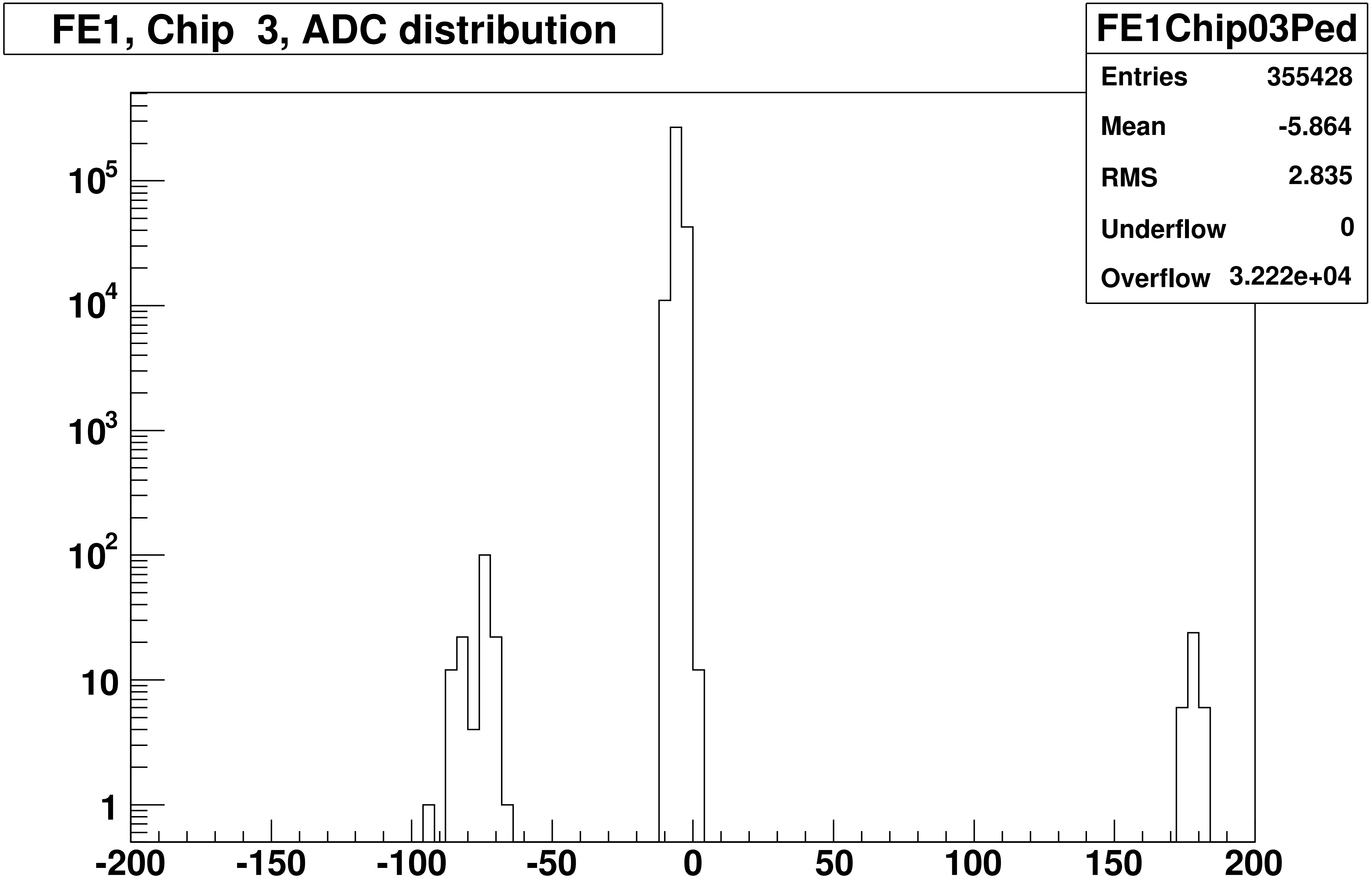
100
150
200


FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 355428
Mean 1.921e+04
RMS 11.26
Underflow 3.232e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 355428
Mean 1735
RMS 5514
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 355428
Mean -5.864
RMS 2.835
Underflow 0
Overflow 3.222e+04
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10^4
10^3
10^2
10
1
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150
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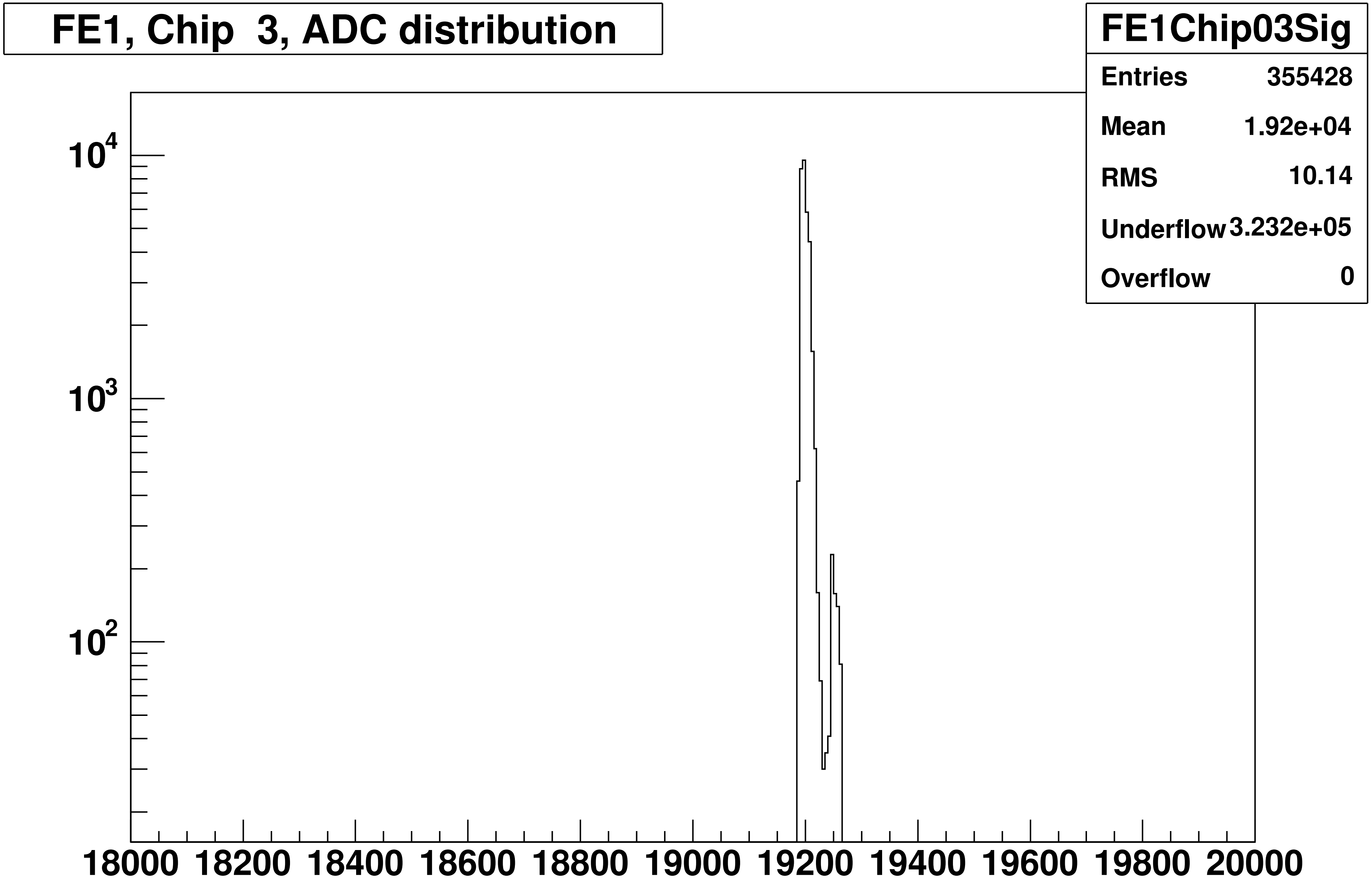
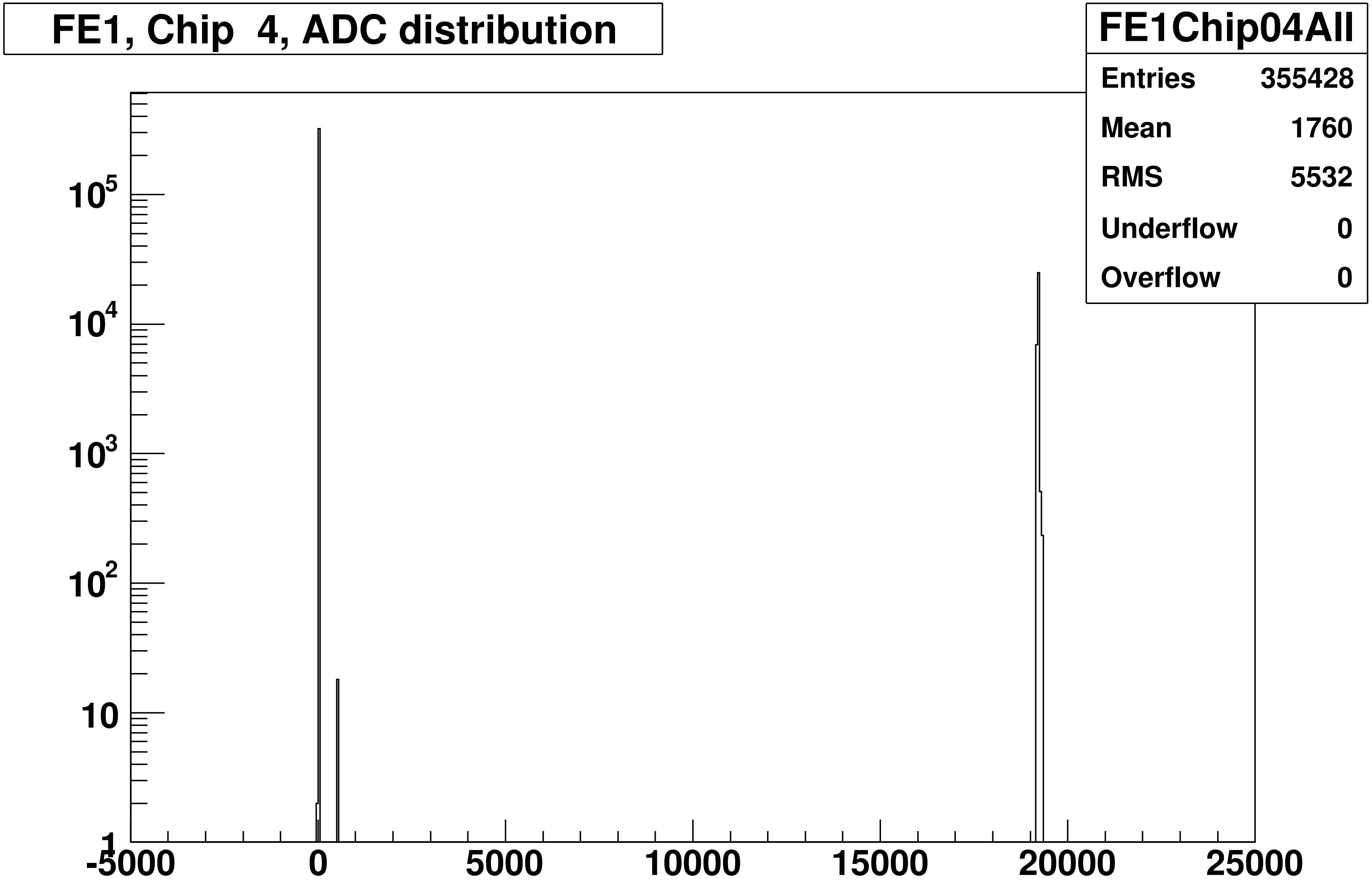

FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 355428
Mean 1.92e+04
RMS 10.14
Underflow 3.232e+05
Overflow 0
10^4
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 355428
Mean 1760
RMS 5532
Underflow 0
Overflow 0
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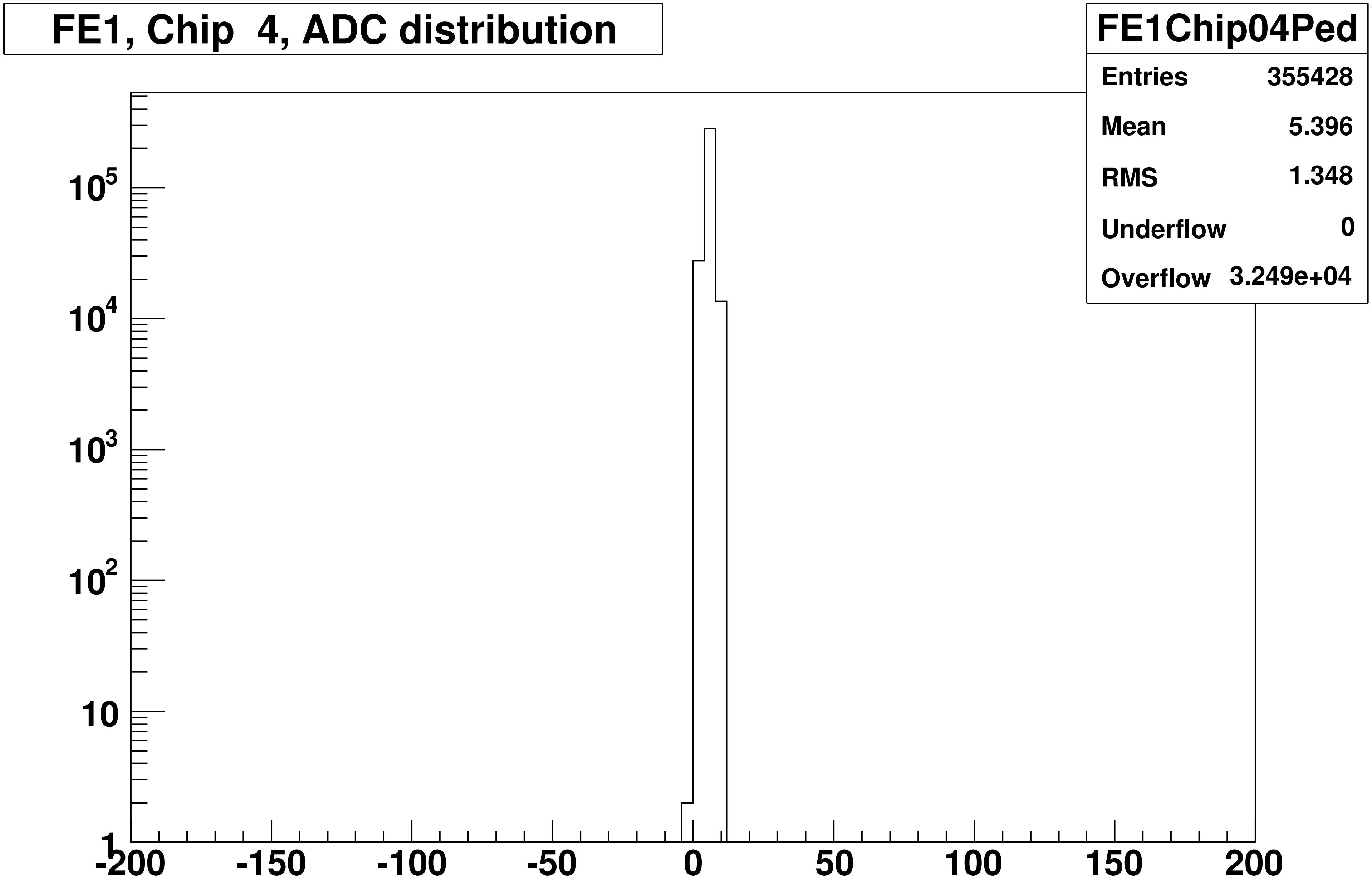
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 355428
Mean 5.396
RMS 1.348
Underflow 0
Overflow 3.249e+04
10^5
10^4
10^3
10^2
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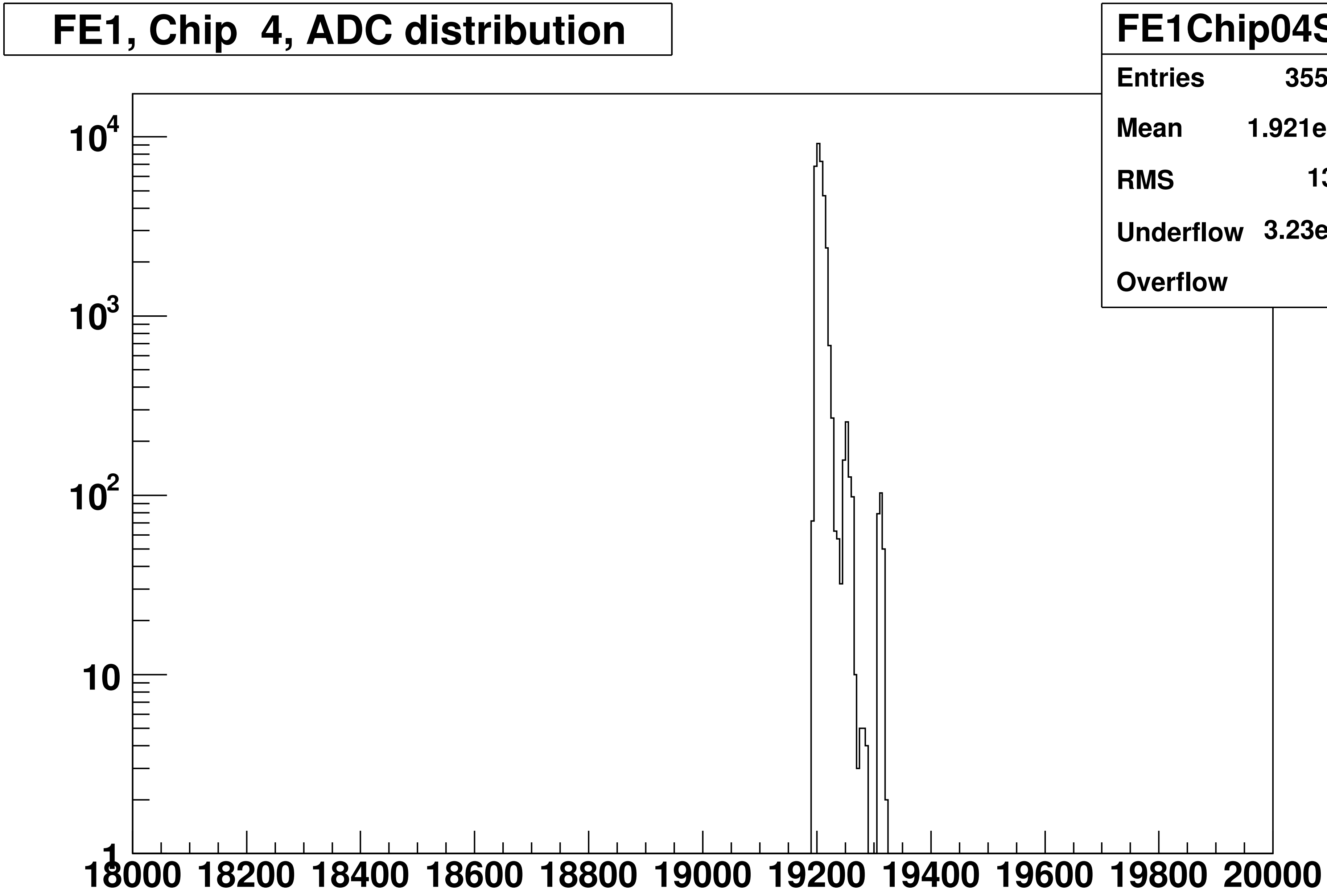
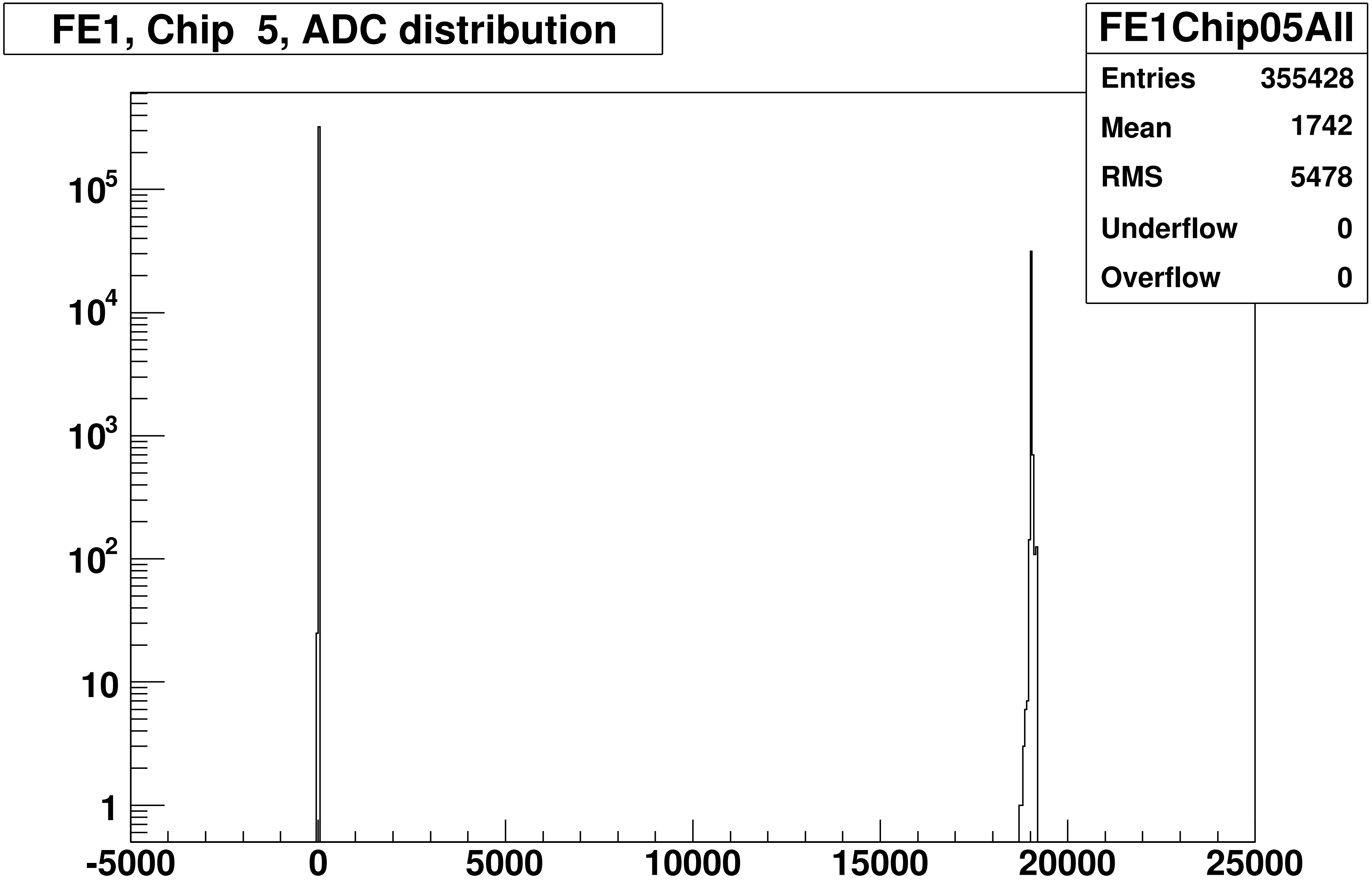
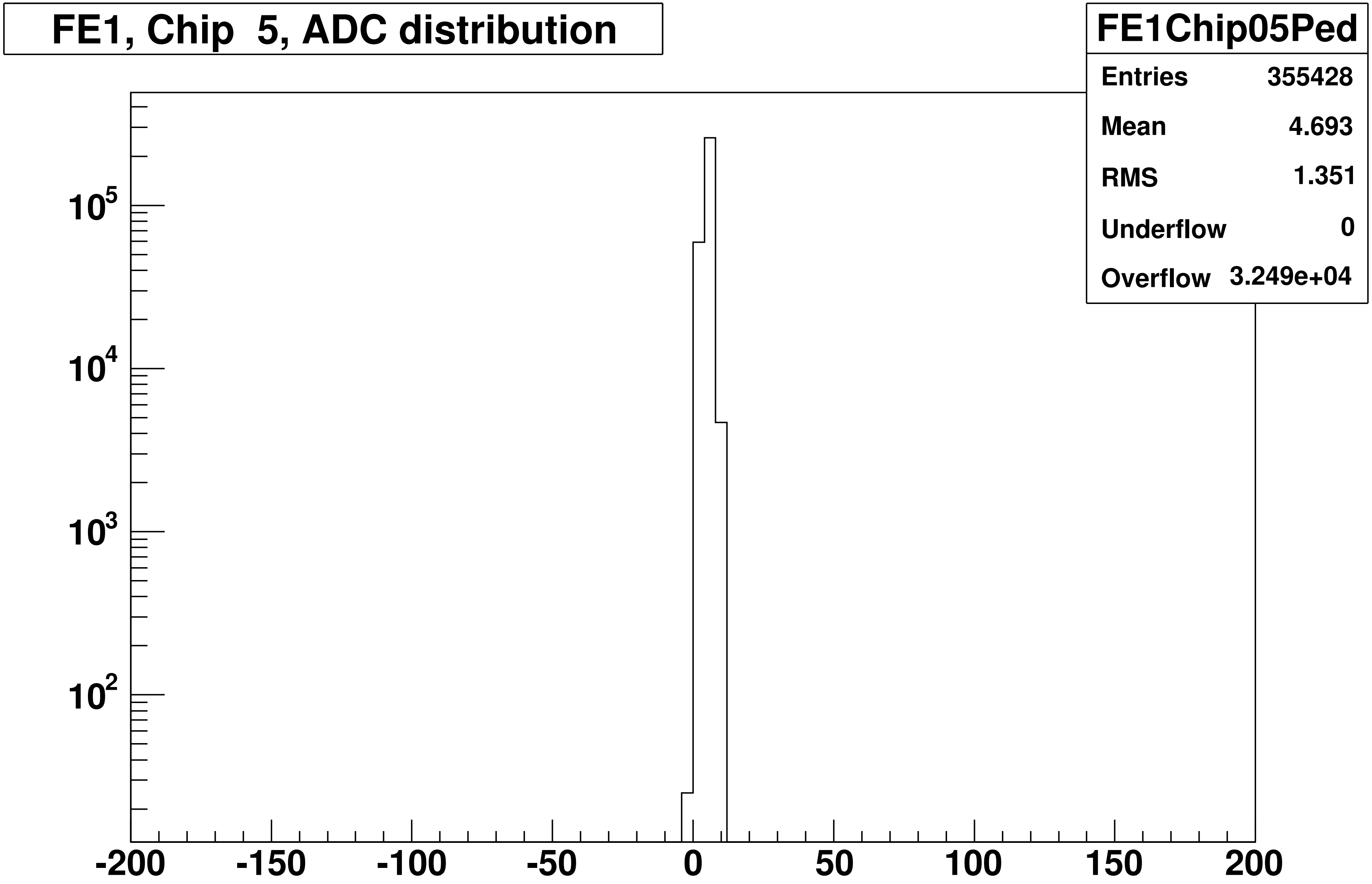
1
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 355428
Mean 1.921e+04
RMS 13.18
Underflow 3.23e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 355428
Mean 1742
RMS 5478
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 355428
Mean 4.693
RMS 1.351
Underflow 0
Overflow 3.249e+04
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10^4
10^3
10^2
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-100
-50
0
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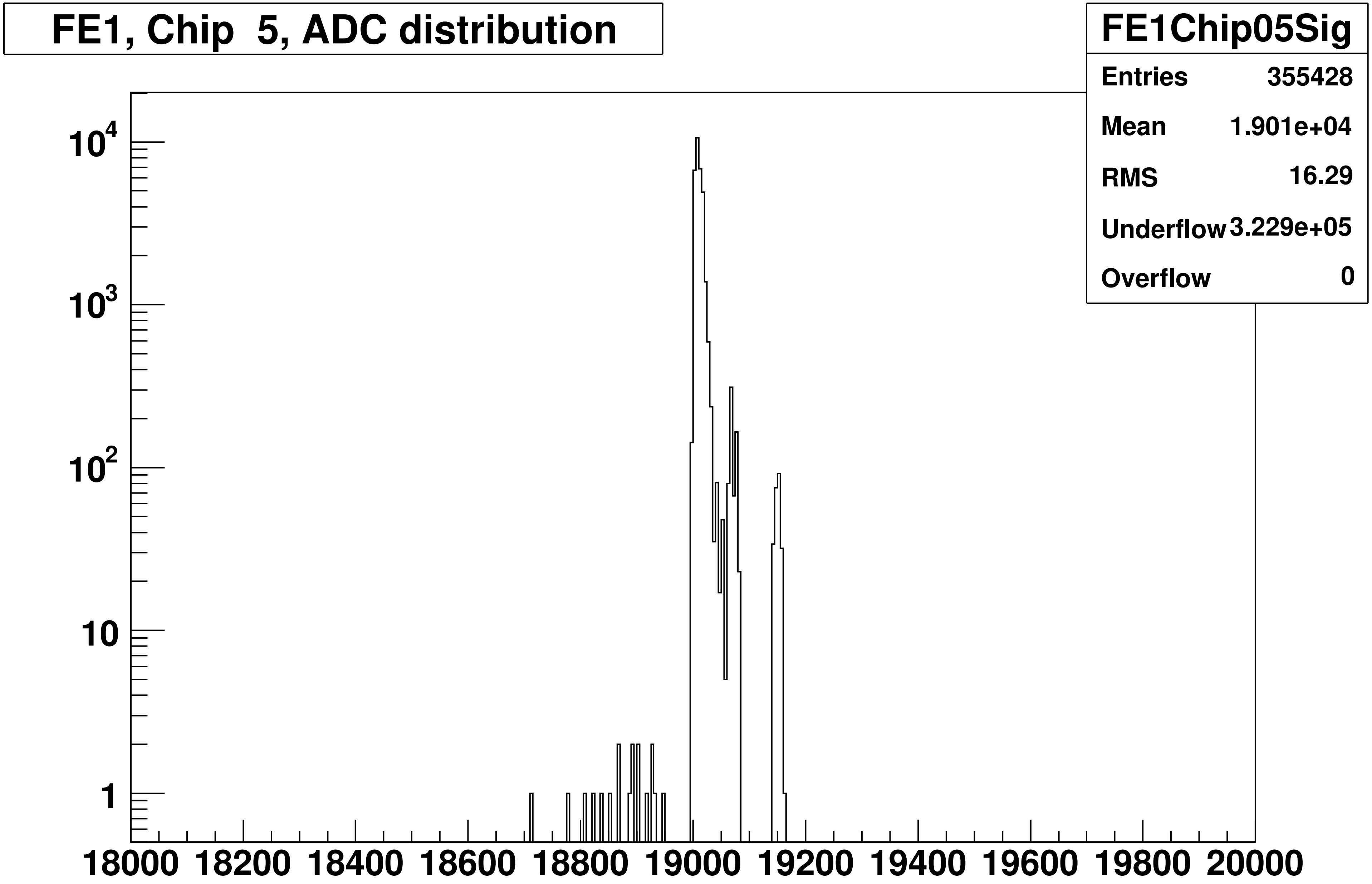
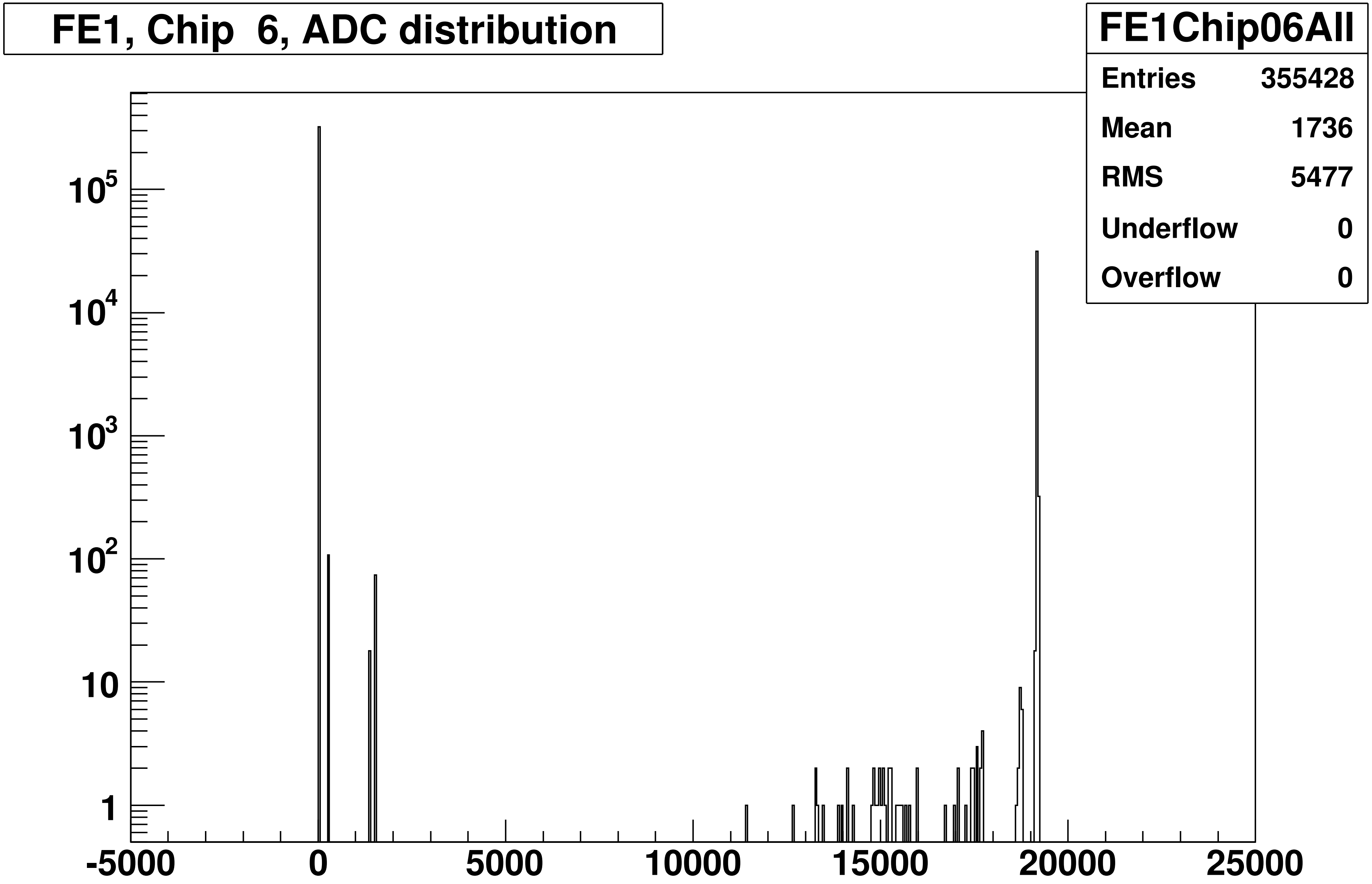
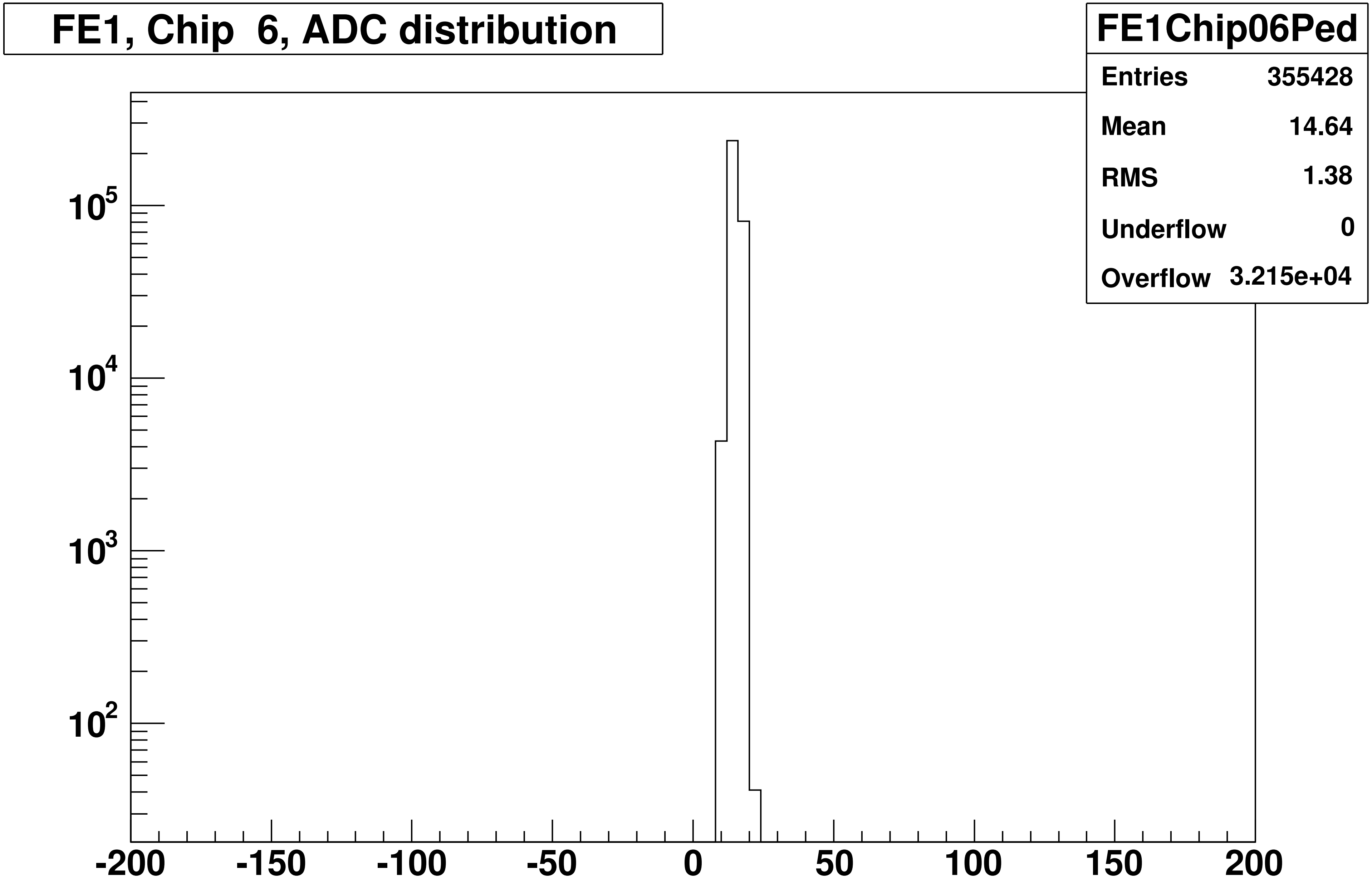
100
150
200



FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 355428
Mean 1.901e+04
RMS 16.29
Underflow 3.229e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 355428
Mean 1736
RMS 5477
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 355428
Mean 14.64
RMS 1.38
Underflow 0
Overflow 3.215e+04
10^5
10^4
10^3
10^2
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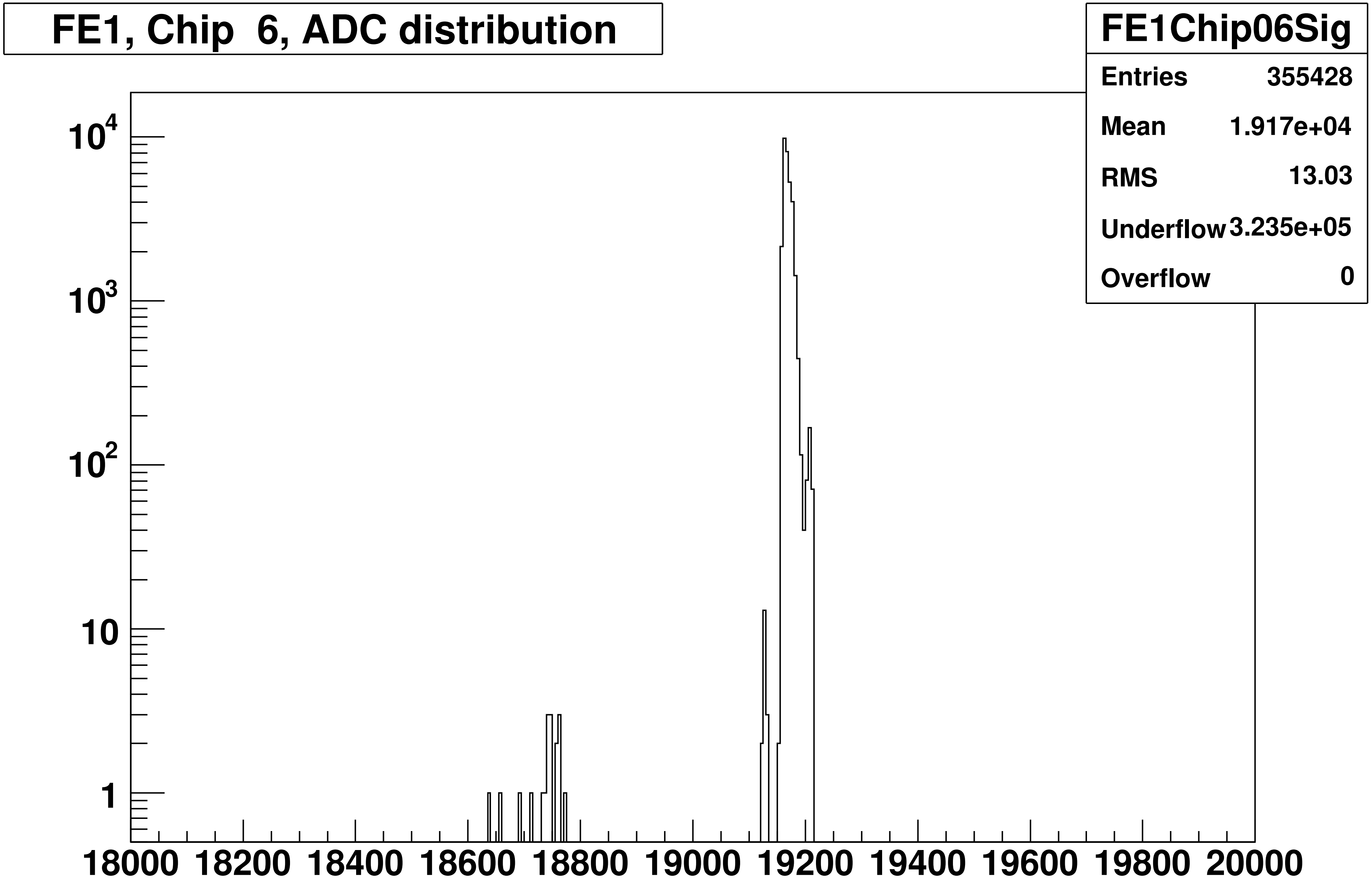
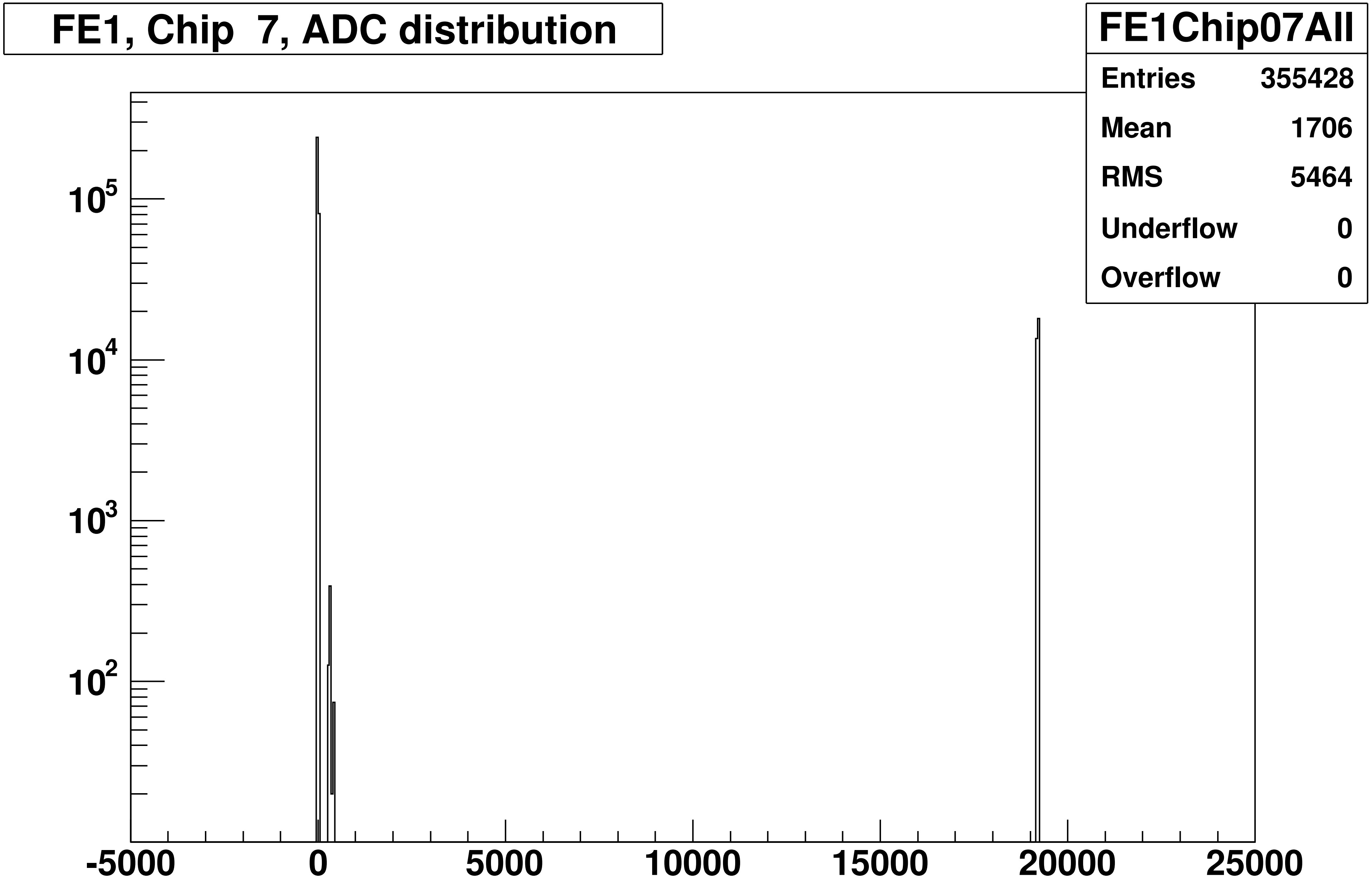
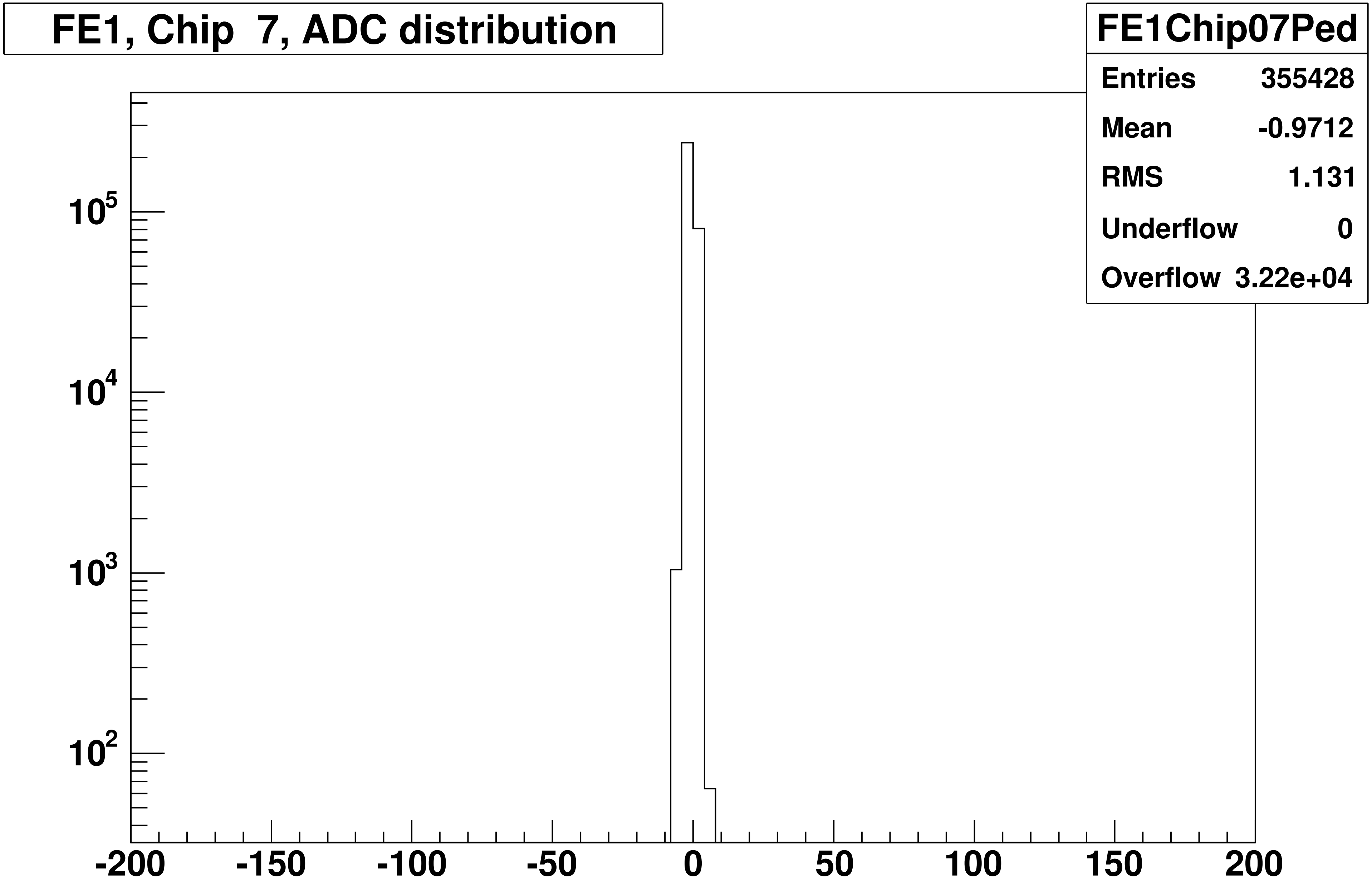
-150
-100
-50
0
50
100
150
200


FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 355428
Mean 1.917e+04
RMS 13.03
Underflow 3.235e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 355428
Mean 1706
RMS 5464
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 355428
Mean -0.9712
RMS 1.131
Underflow 0
Overflow 3.22e+04
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10^4
10^3
10^2
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-100
-50
0
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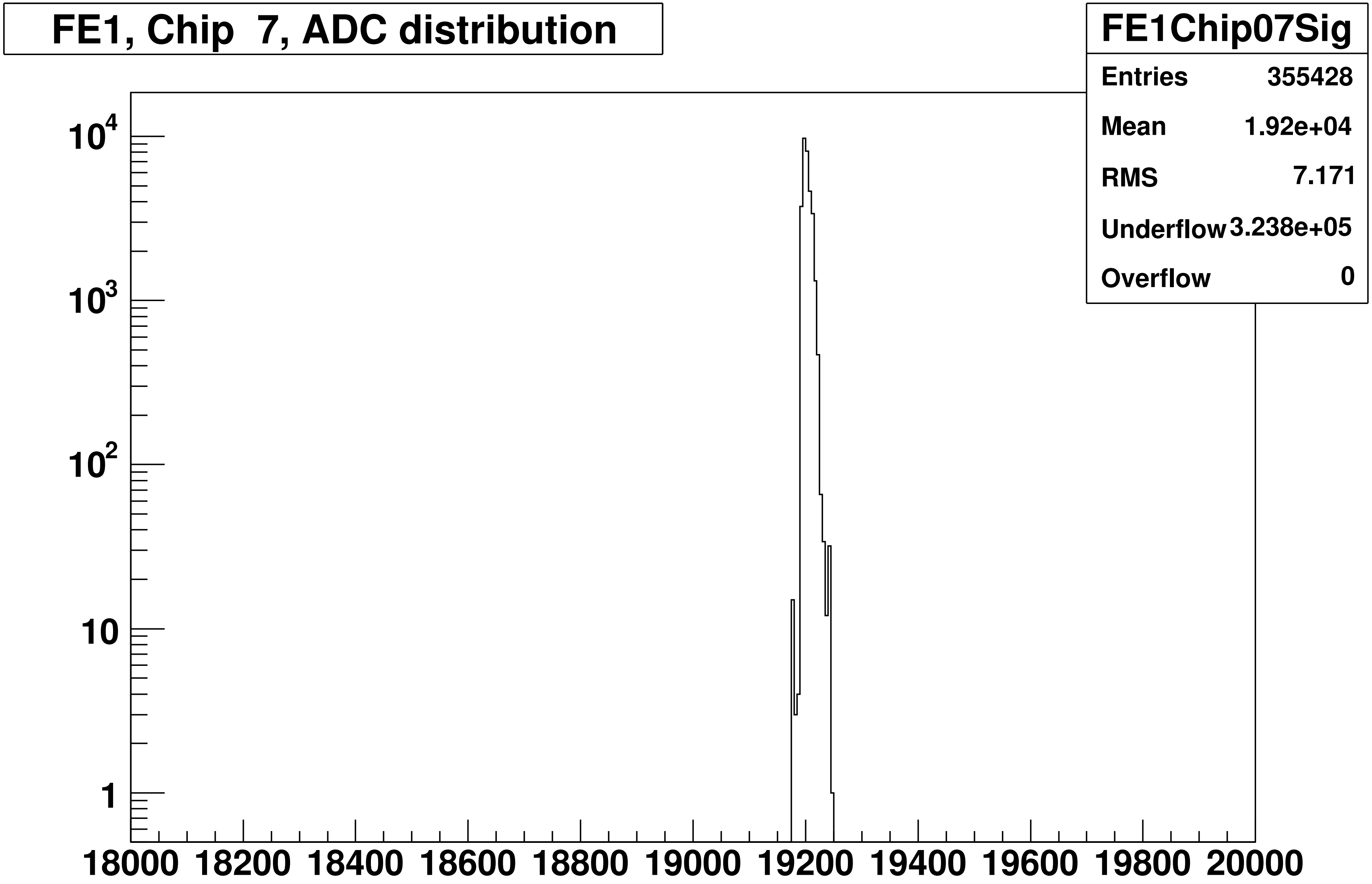
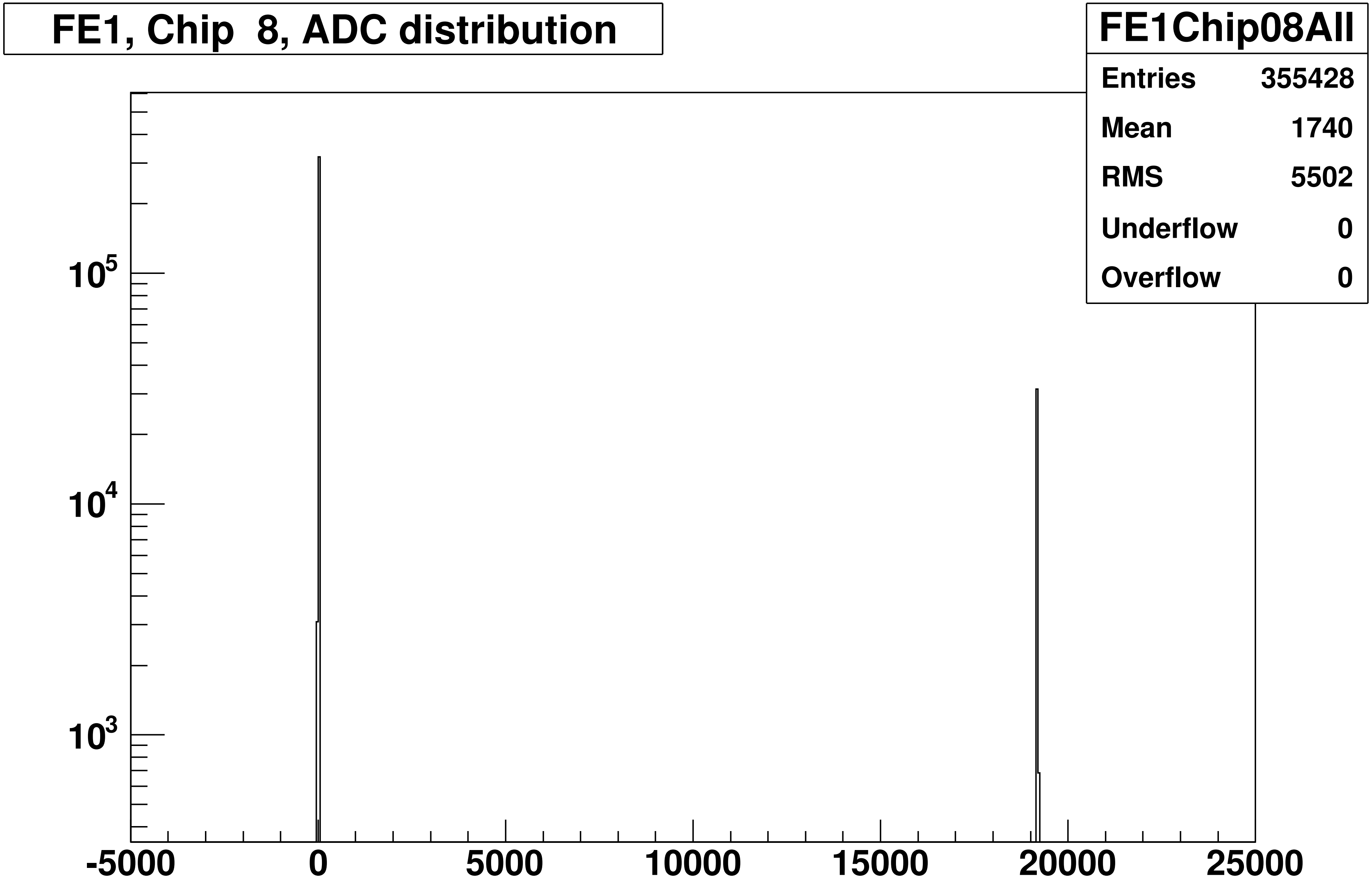
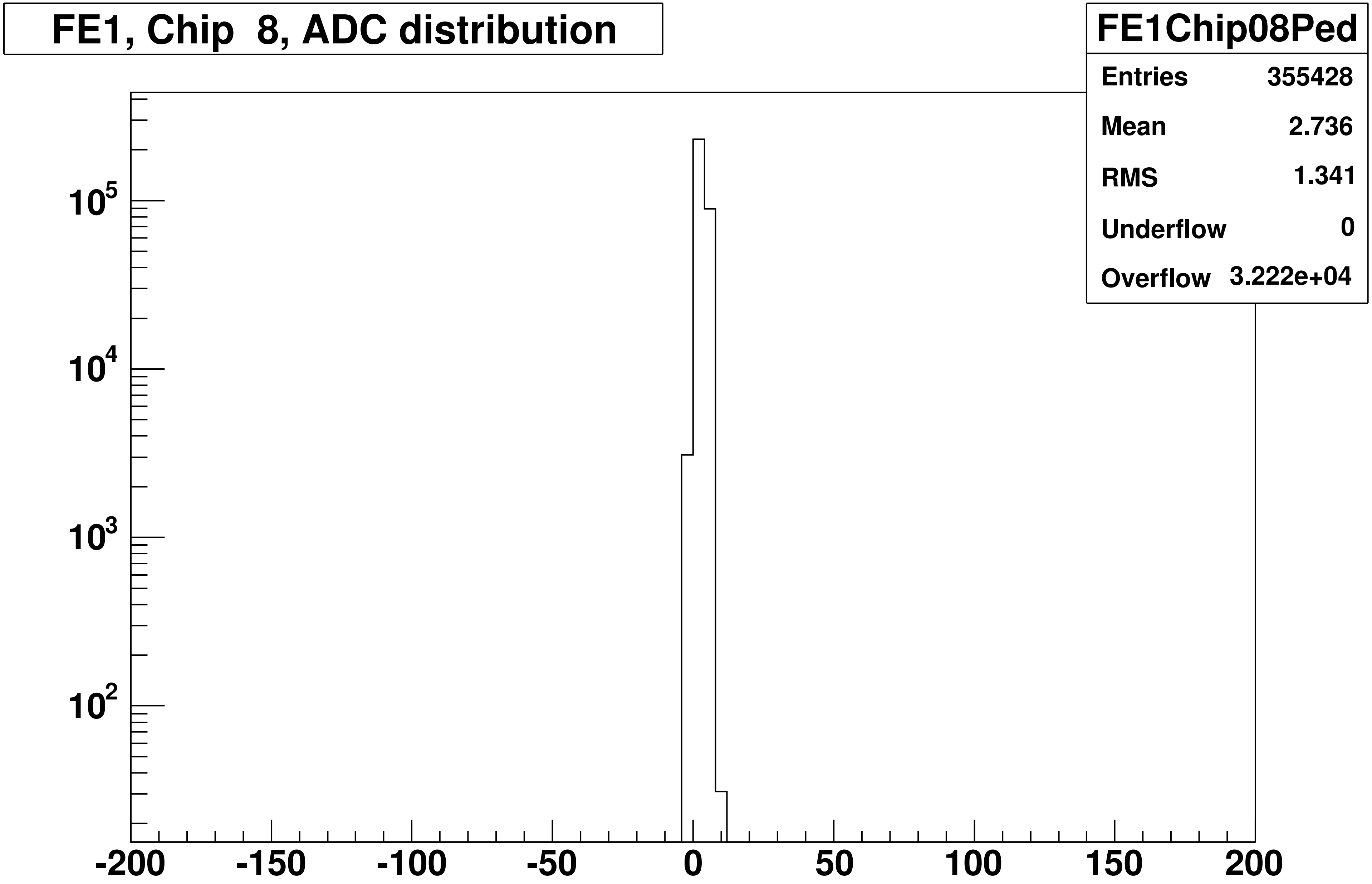
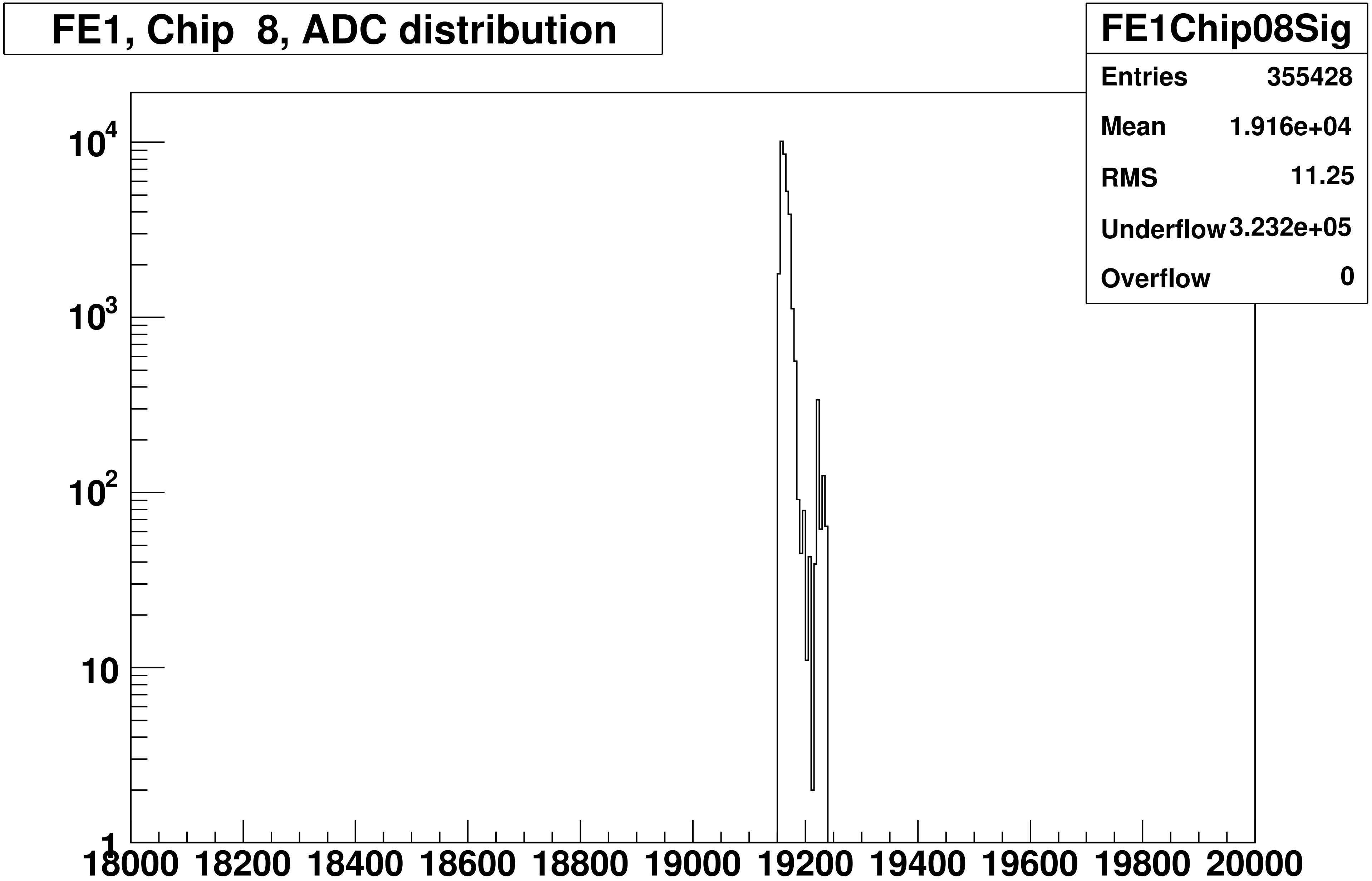
100
150
200


FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 355428
Mean 1.92e+04
RMS 7.171
Underflow 3.238e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

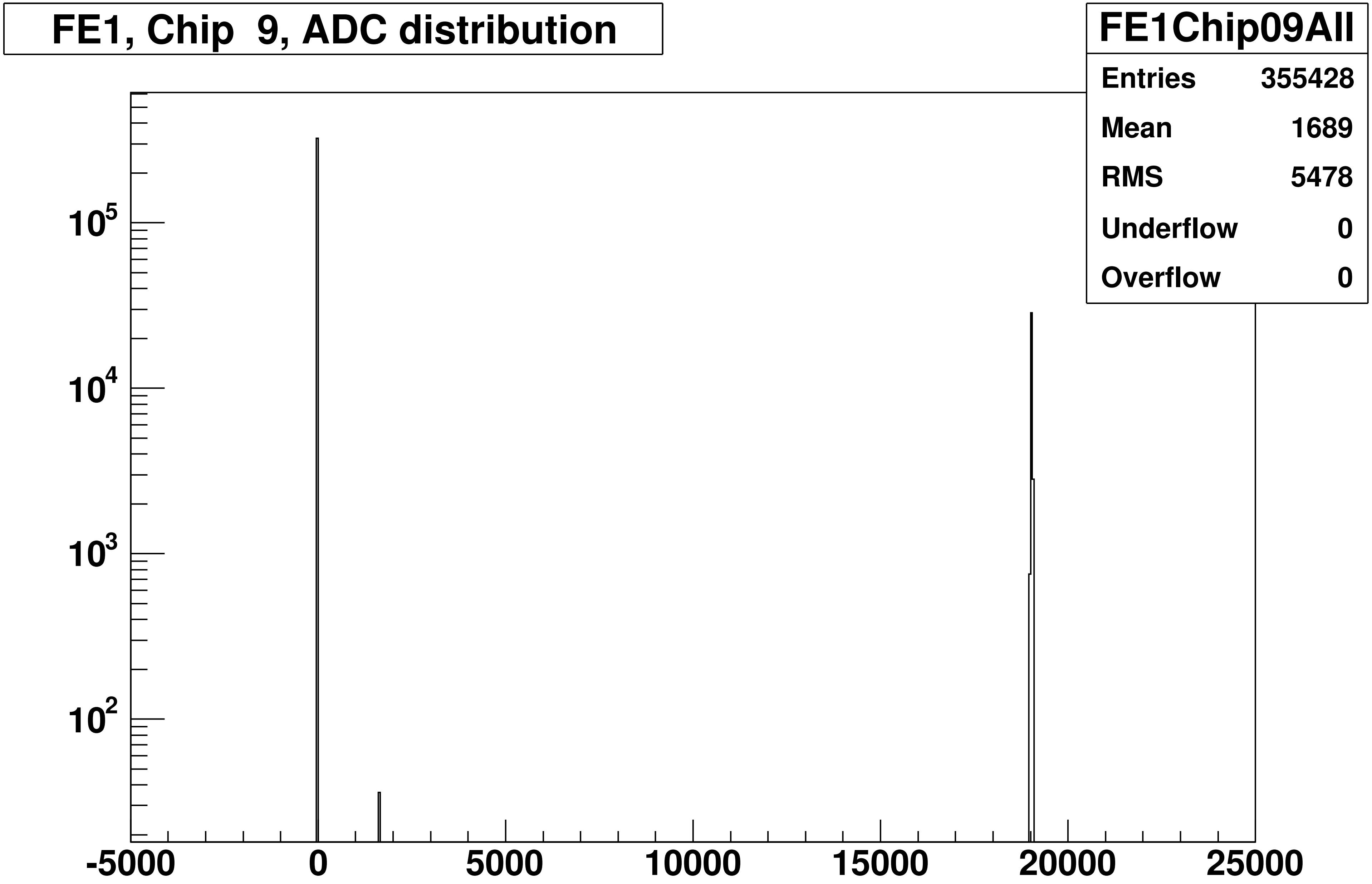


FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 355428
Mean 1740
RMS 5502
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 355428
Mean 2.736
RMS 1.341
Underflow 0
Overflow 3.222e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 355428
Mean 1.916e+04
RMS 11.25
Underflow 3.232e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 355428
Mean 1689
RMS 5478
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
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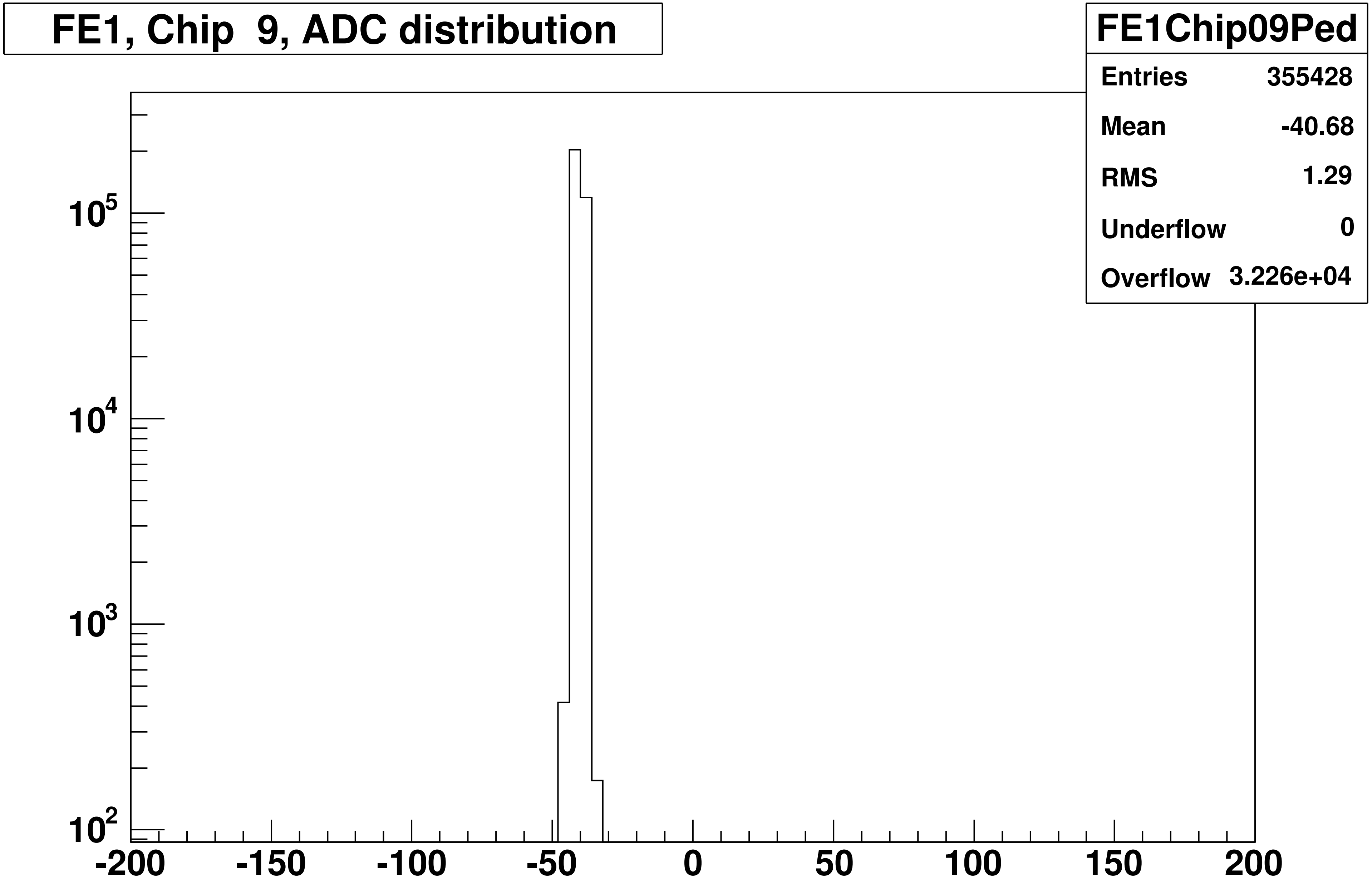
10000
15000
20000
25000


FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 355428
Mean -40.68
RMS 1.29
Underflow 0
Overflow 3.226e+04
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10^4
10^3
10^2
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-100
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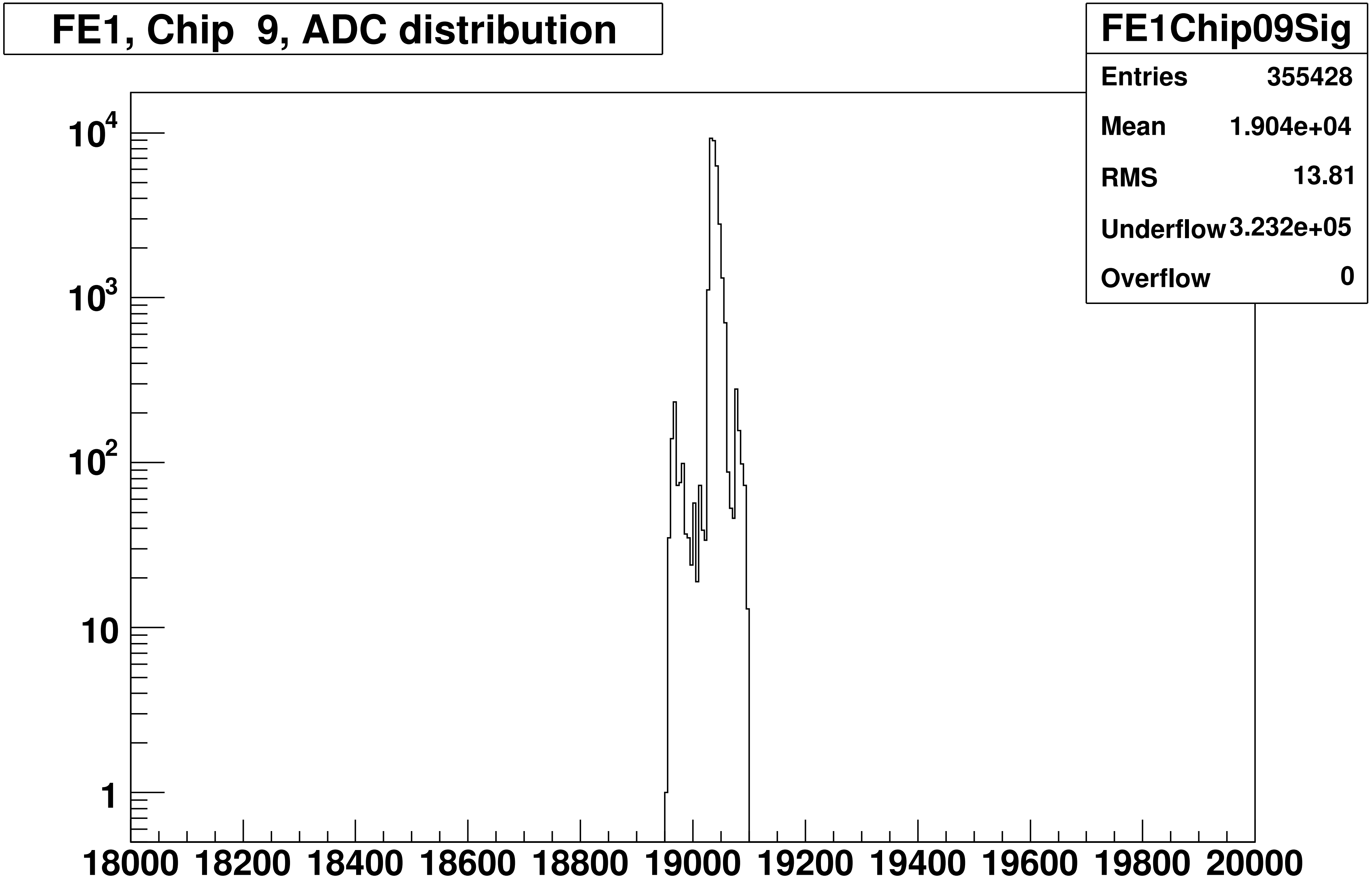
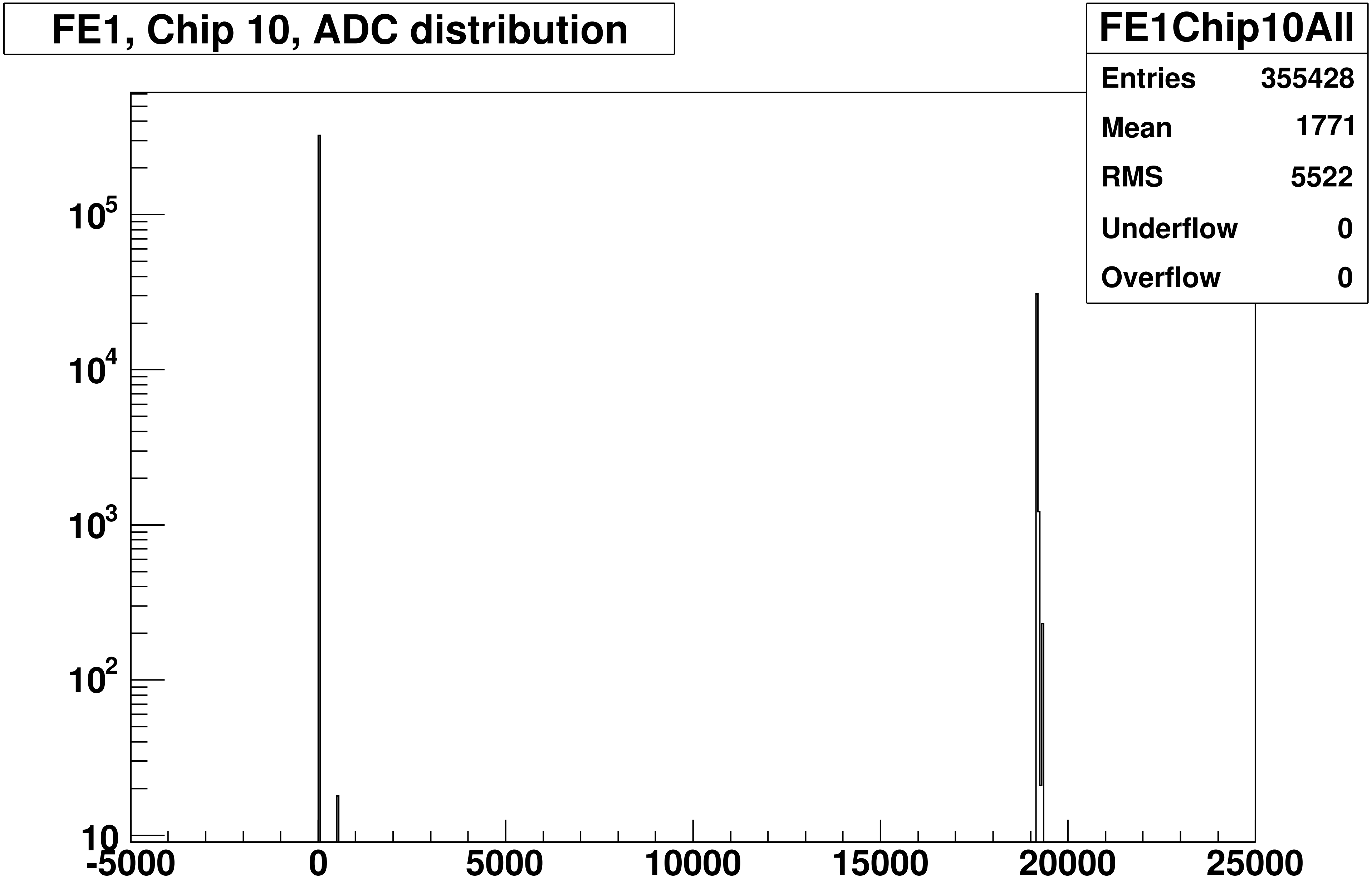
100
150
200



FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 355428
Mean 1.904e+04
RMS 13.81
Underflow 3.232e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 355428
Mean 1771
RMS 5522
Underflow 0
Overflow 0
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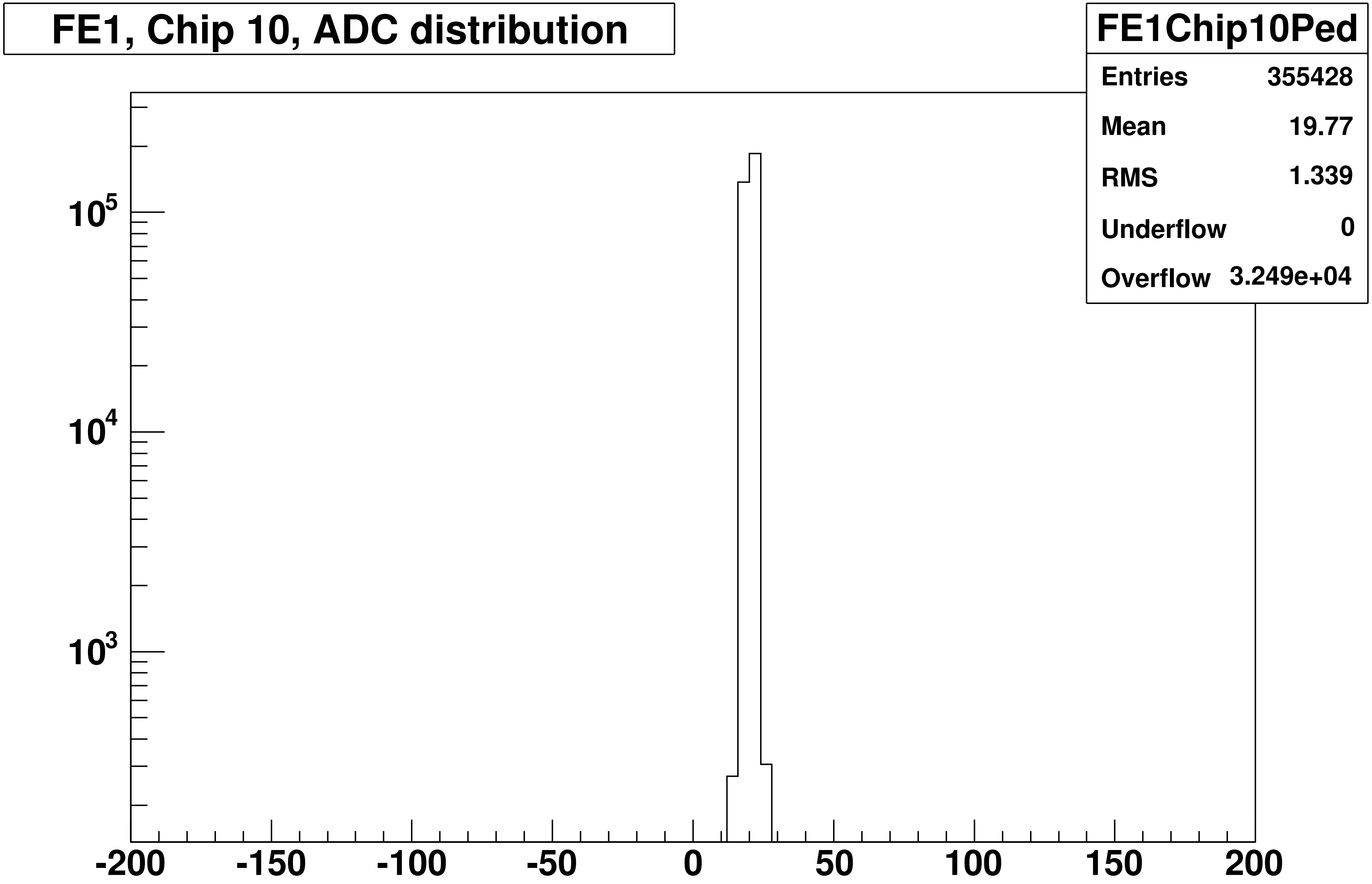
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 355428
Mean 19.77
RMS 1.339
Underflow 0
Overflow 3.249e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
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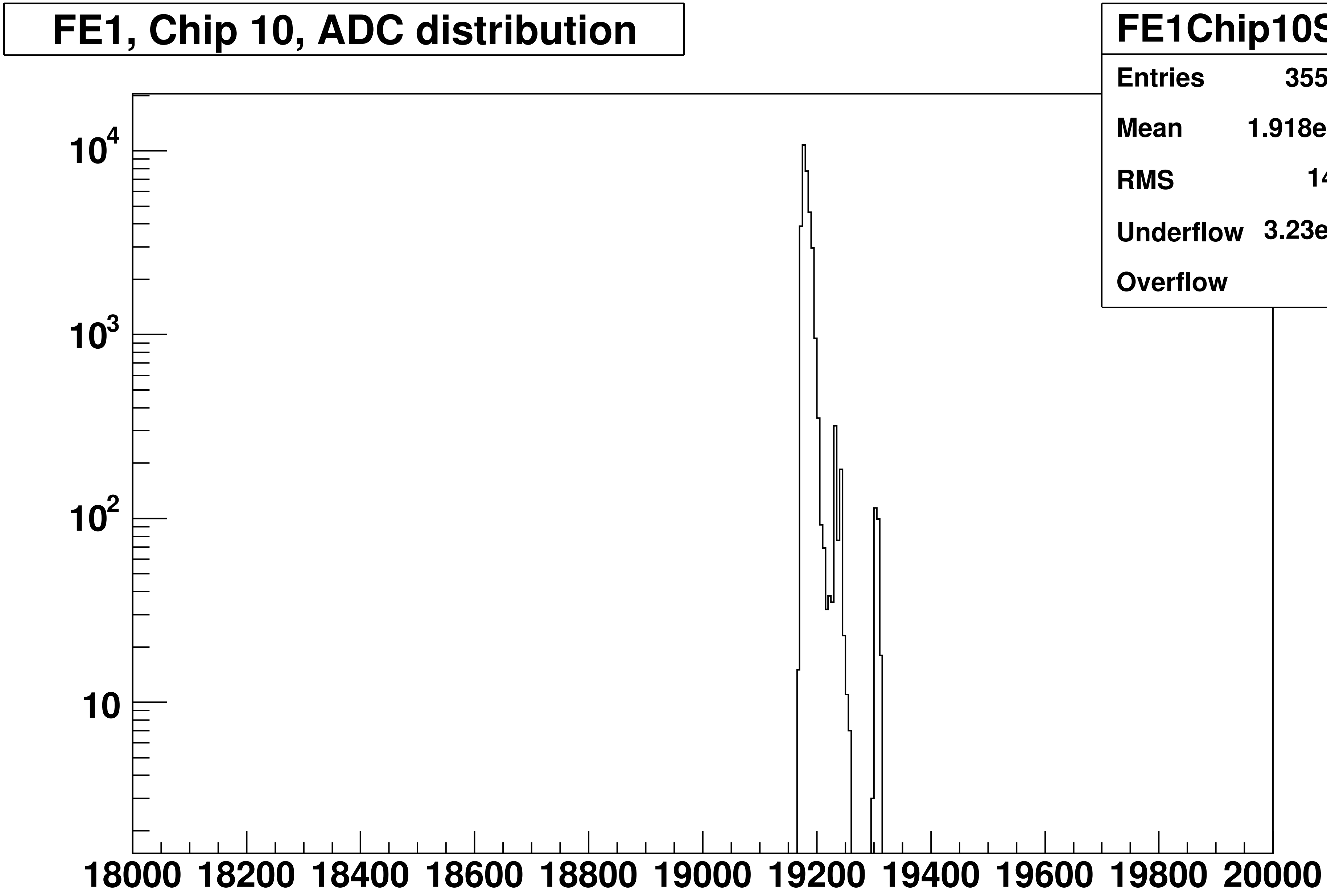
100
150
200



FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 355428
Mean 1.918e+04
RMS 14.69
Underflow 3.23e+05
Overflow 0
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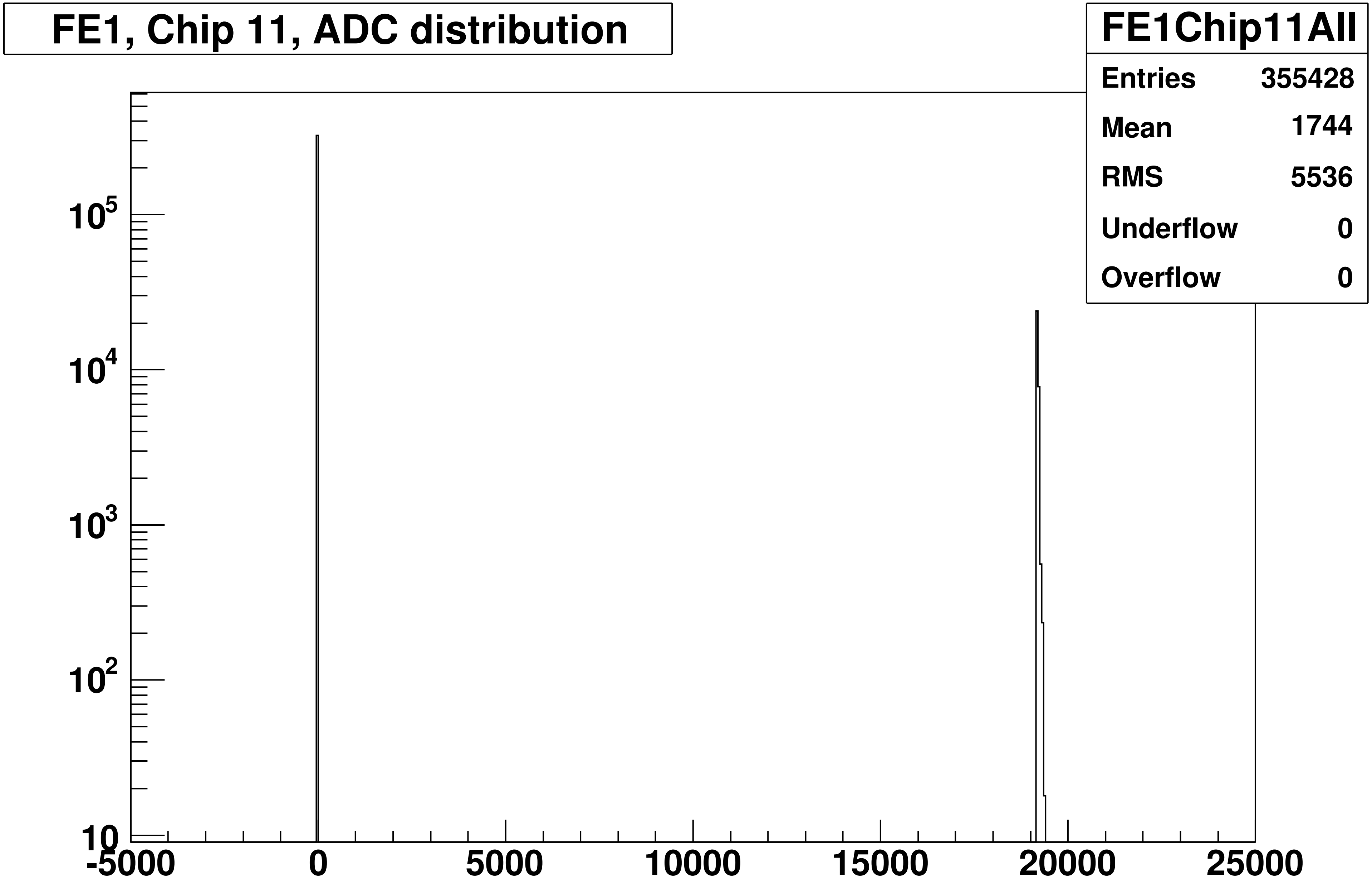
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 355428
Mean 1744
RMS 5536
Underflow 0
Overflow 0
10
10^2
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10^4
10^5
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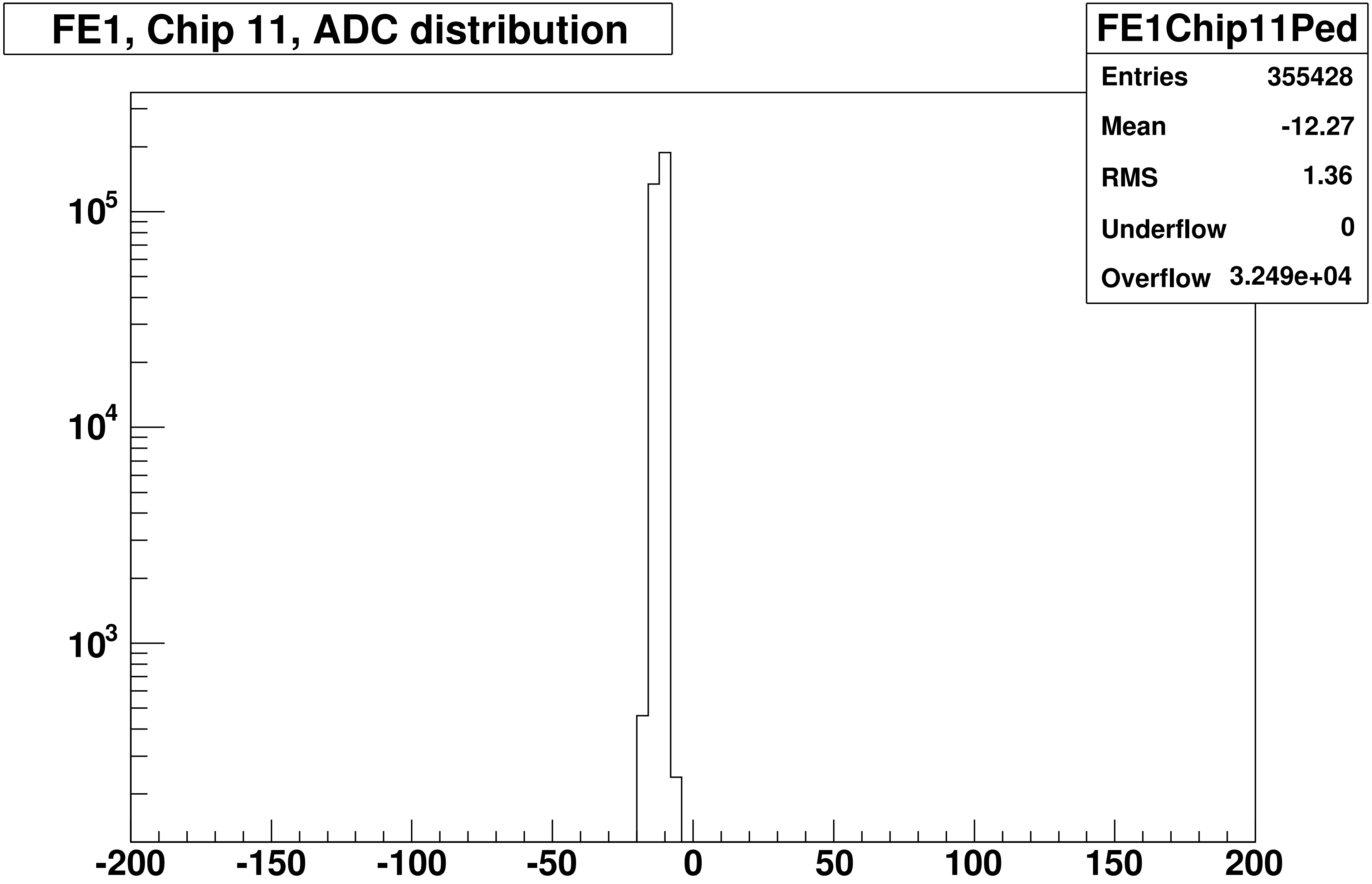
15000
20000
25000



FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 355428
Mean -12.27
RMS 1.36
Underflow 0
Overflow 3.249e+04
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10^4
10^3
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-100
-50
0
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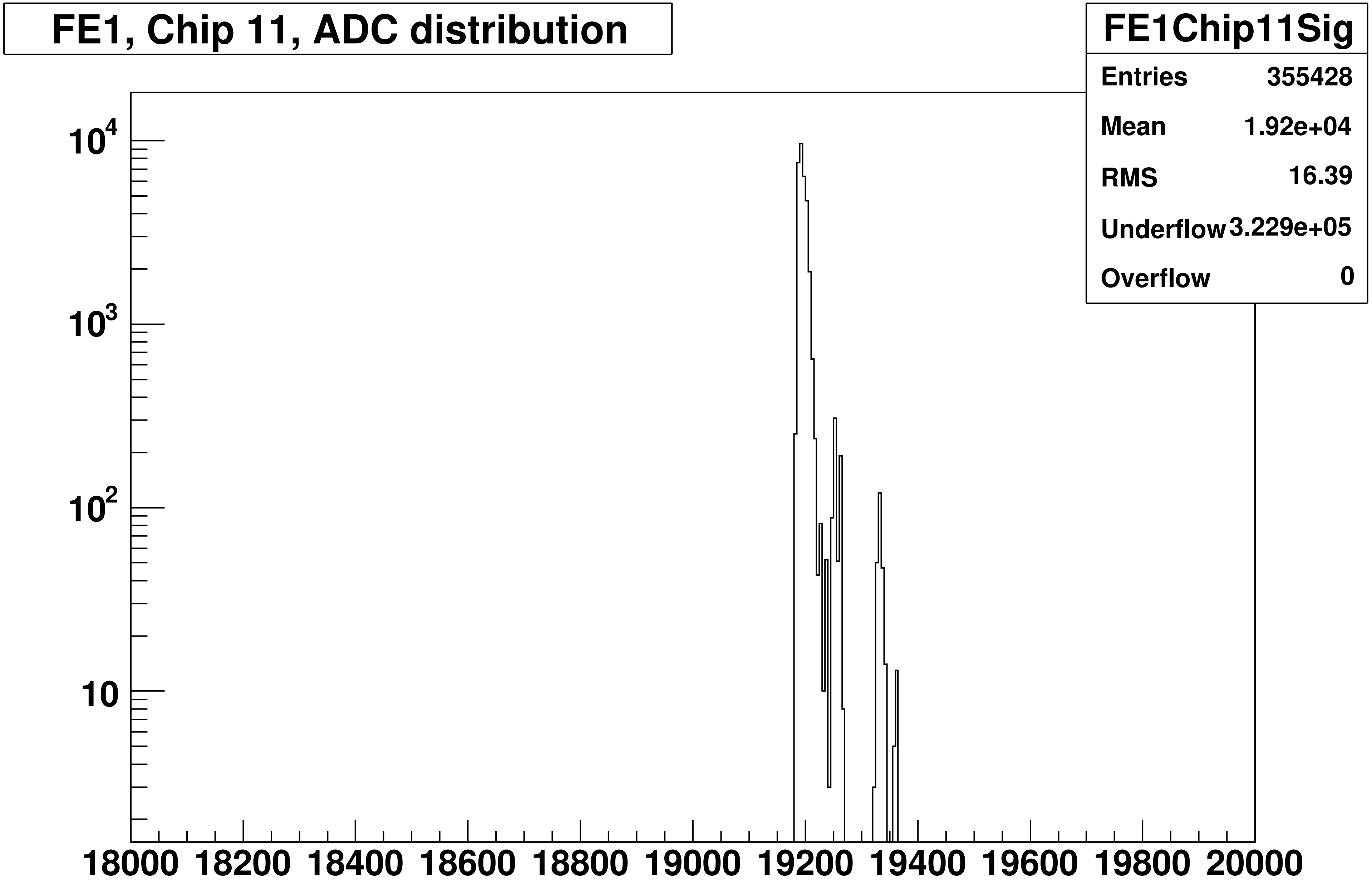
200



FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 355428
Mean 1.92e+04
RMS 16.39
Underflow 3.229e+05
Overflow 0
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10^3
10^2
10
18000
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18400
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19000
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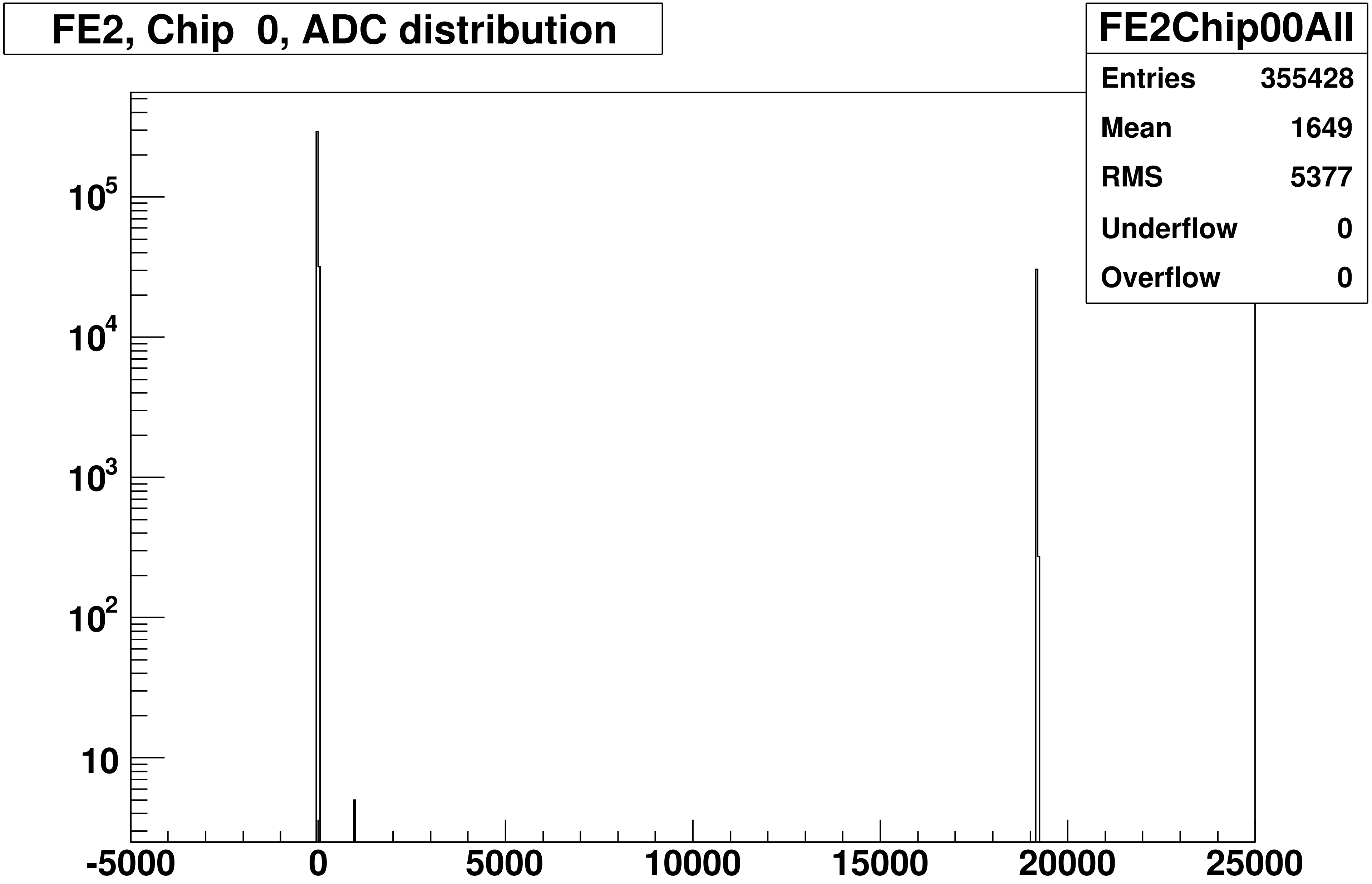
19800
20000


FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 355428
Mean 1649
RMS 5377
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
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0
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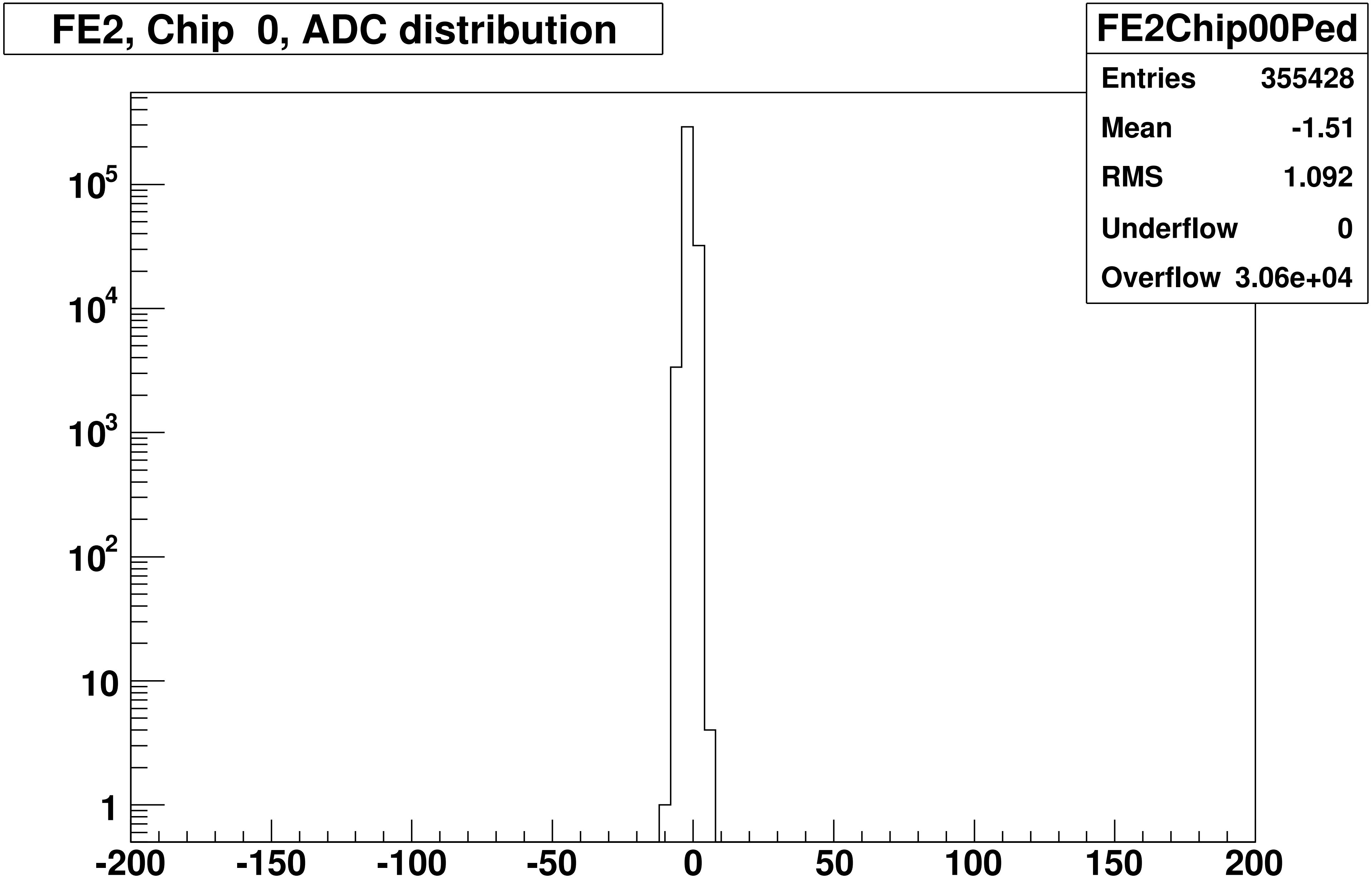
10000
15000
20000
25000



FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 355428
Mean -1.51
RMS 1.092
Underflow 0
Overflow 3.06e+04
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10^3
10^2
10
1
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-100
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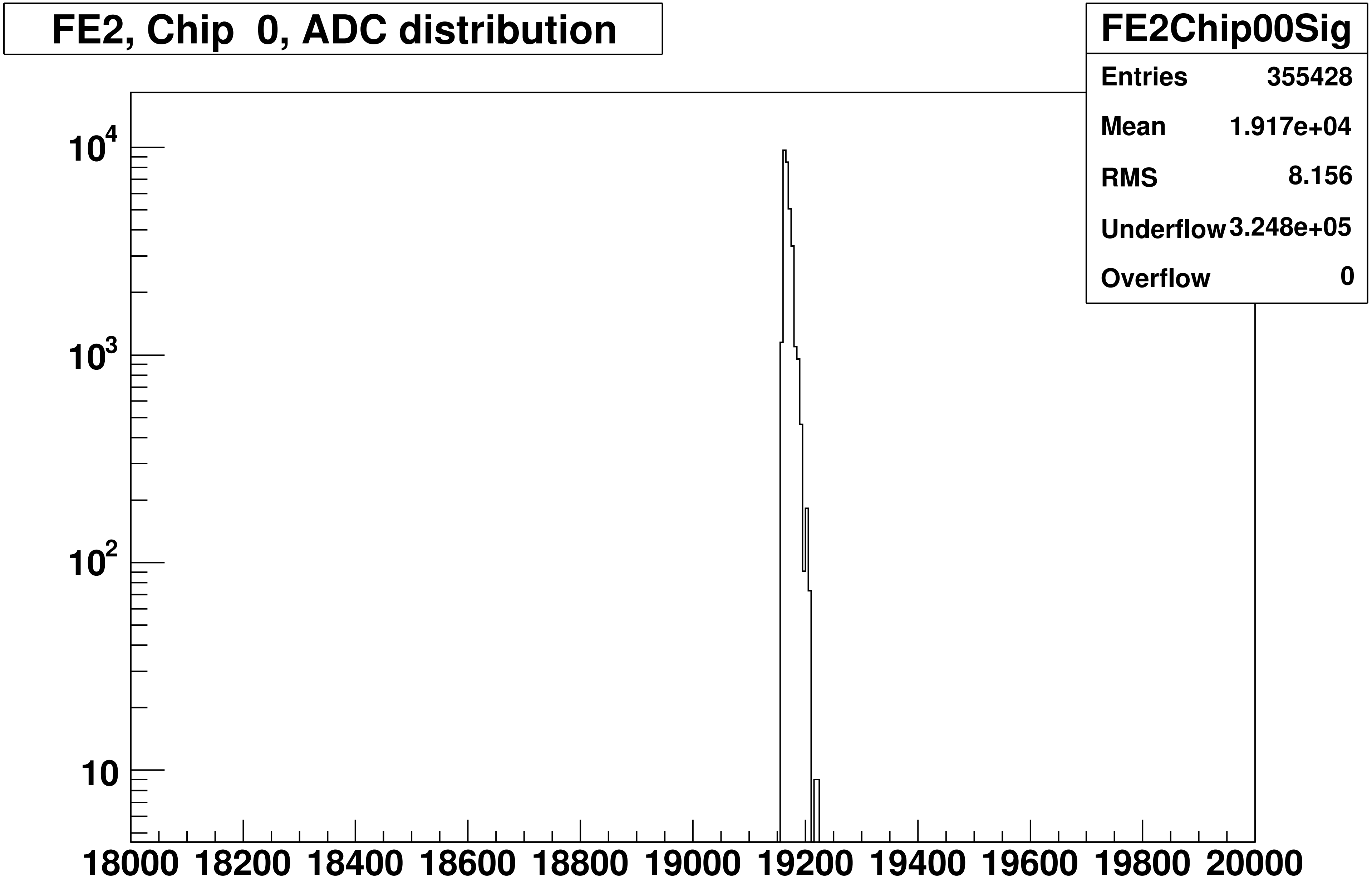
100
150
200


FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 355428
Mean 1.917e+04
RMS 8.156
Underflow 3.248e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
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19000
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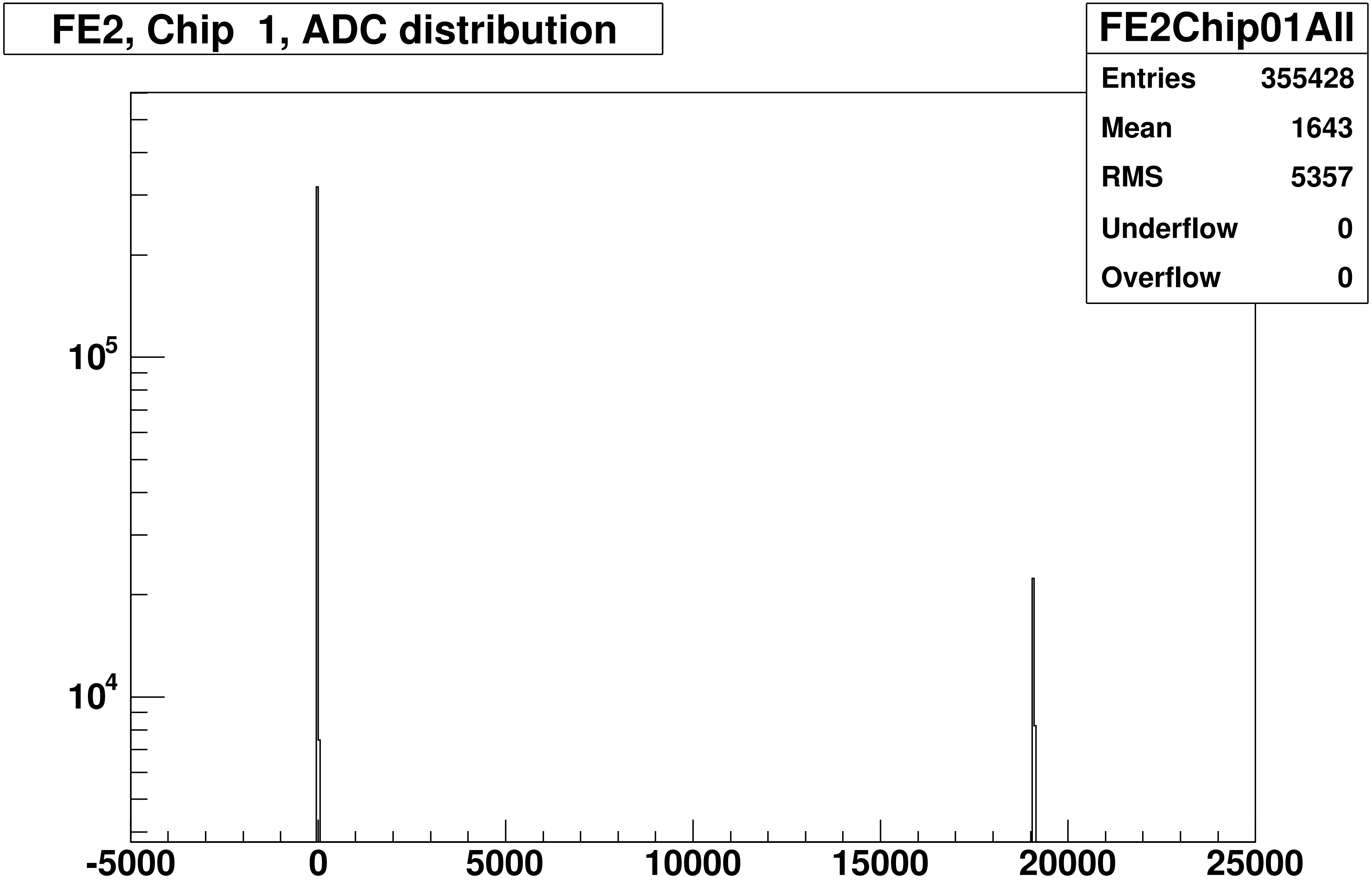
19600
19800
20000


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 355428
Mean 1643
RMS 5357
Underflow 0
Overflow 0
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10^4
-5000
0
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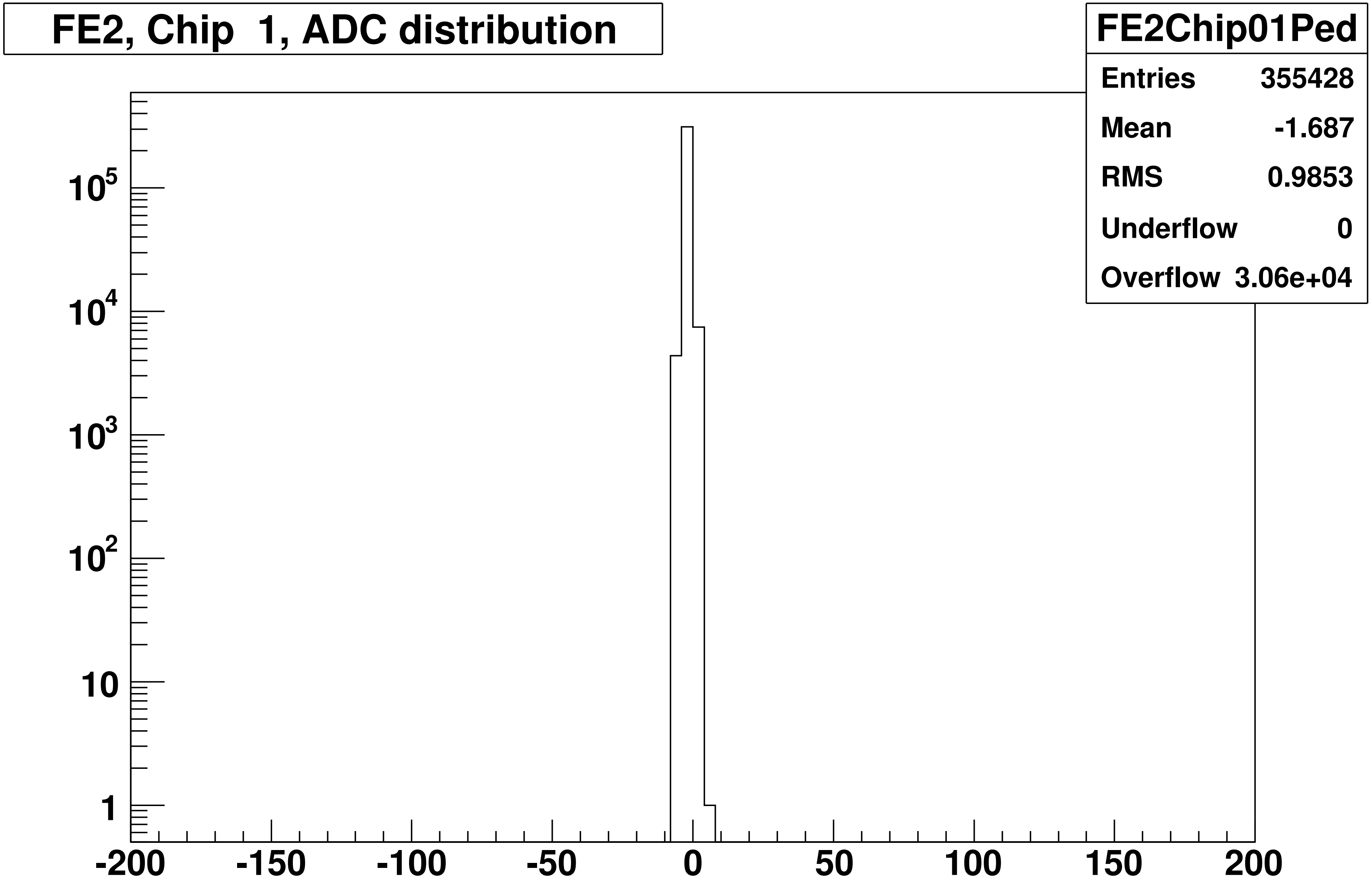
10000
15000
20000
25000


FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 355428
Mean -1.687
RMS 0.9853
Underflow 0
Overflow 3.06e+04
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10^3
10^2
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1
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-100
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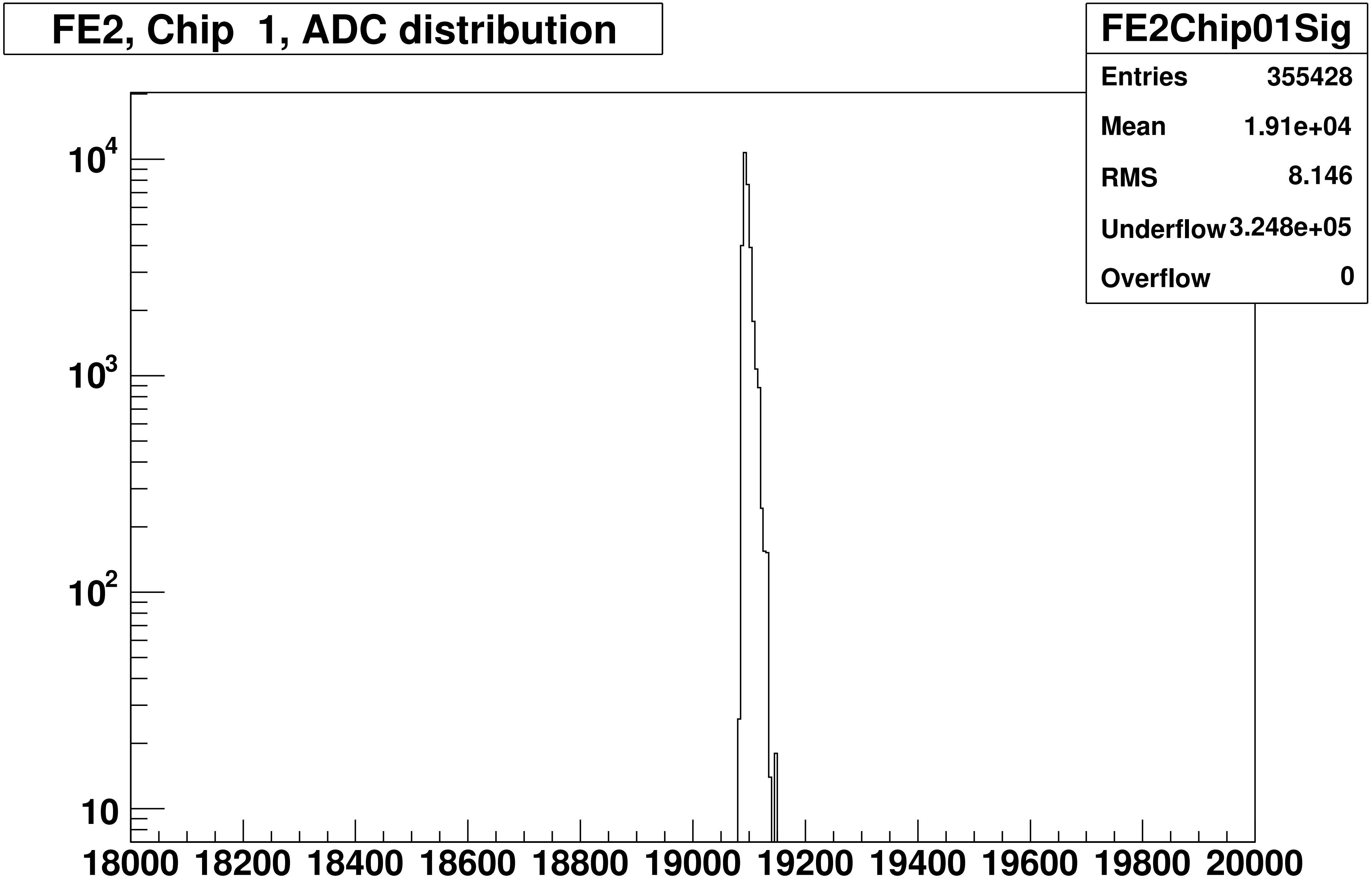
150
200



FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 355428
Mean 1.91e+04
RMS 8.146
Underflow 3.248e+05
Overflow 0
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10^2
10^3
10^4
18000
18200
18400
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19000
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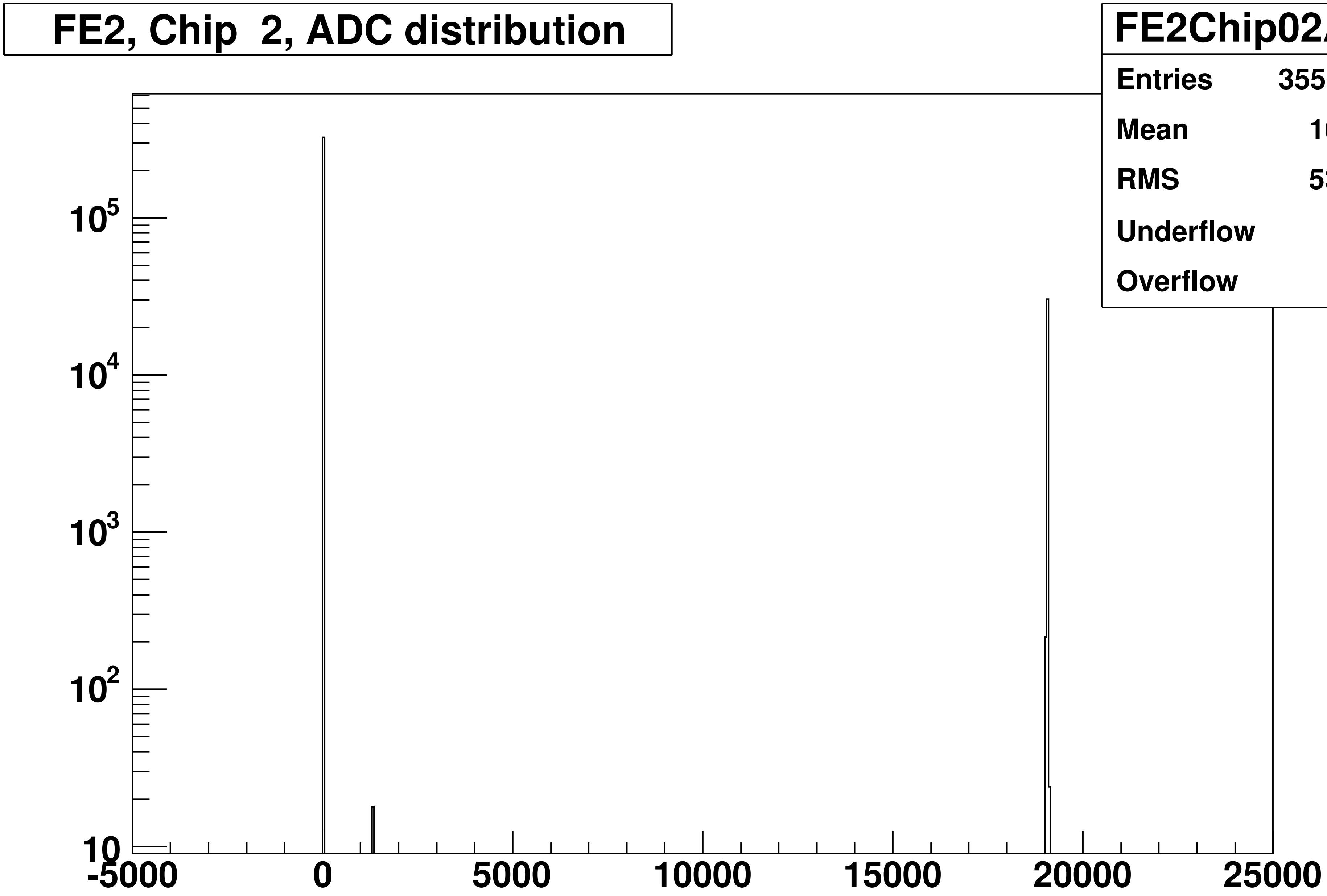
19600
19800
20000


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 355428
Mean 1652
RMS 5343
Underflow 0
Overflow 0
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10^3
10^4
10^5
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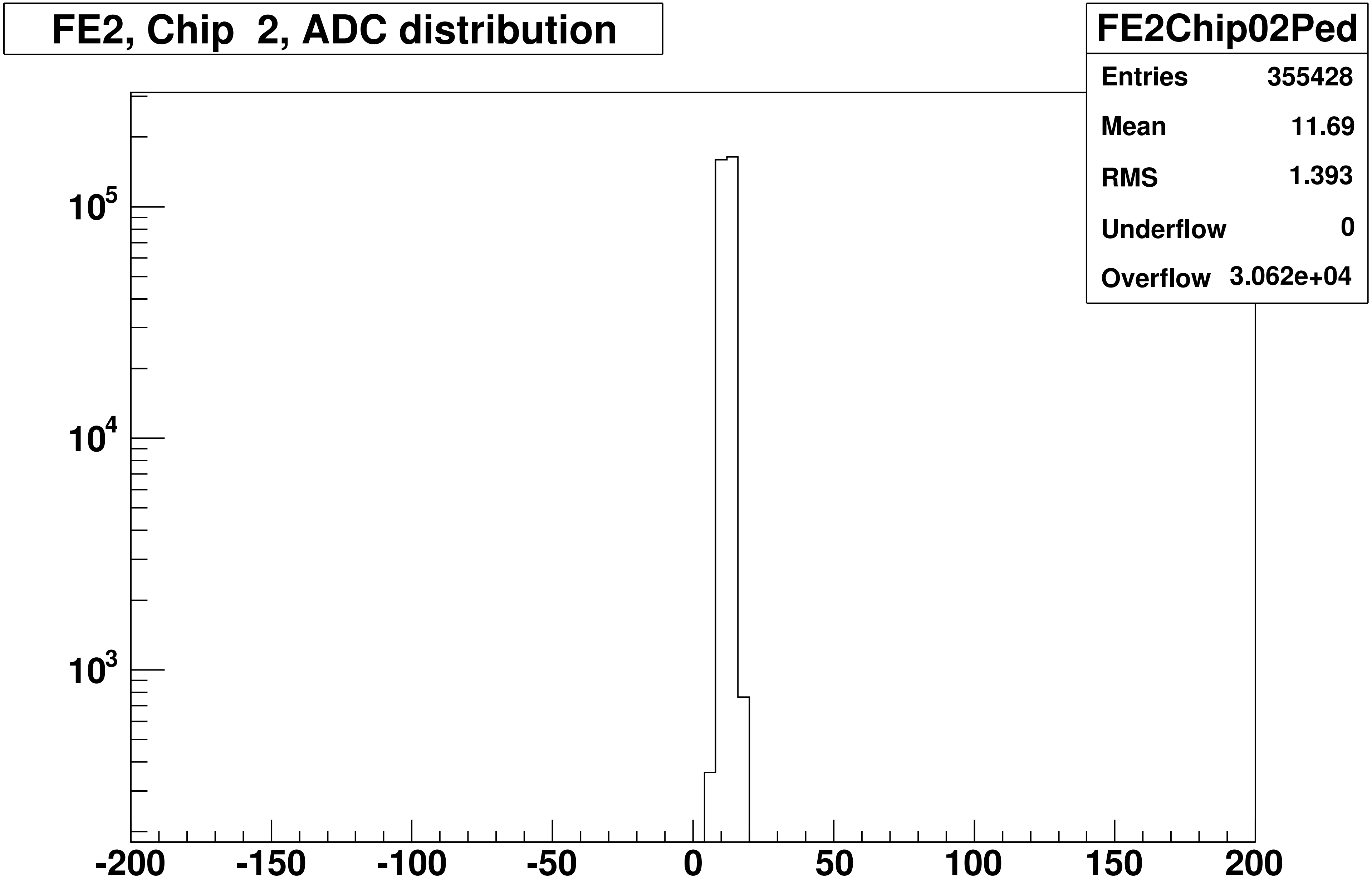
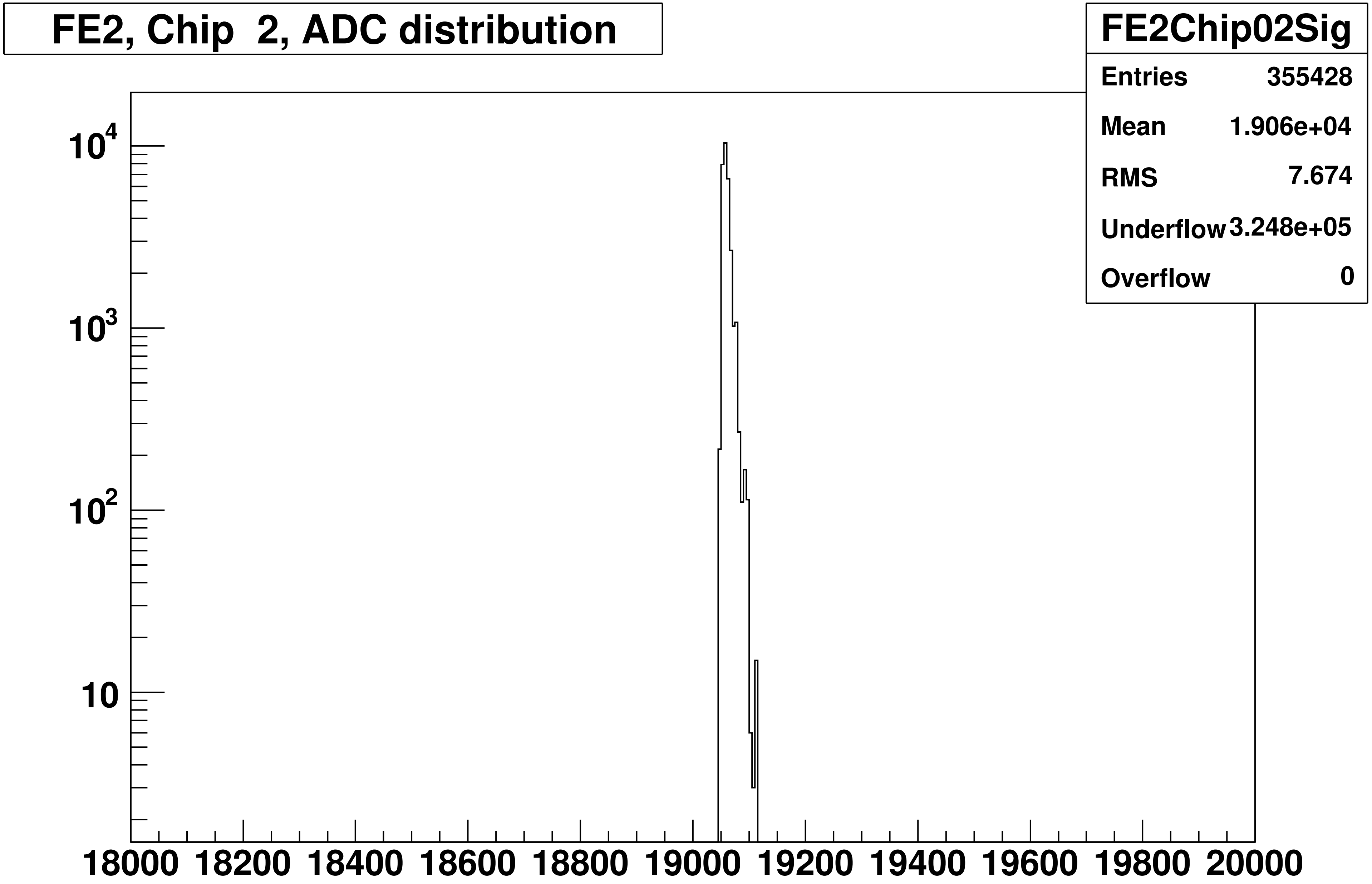
10000
15000
20000
25000



FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 355428
Mean 11.69
RMS 1.393
Underflow 0
Overflow 3.062e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 355428
Mean 1.906e+04
RMS 7.674
Underflow 3.248e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
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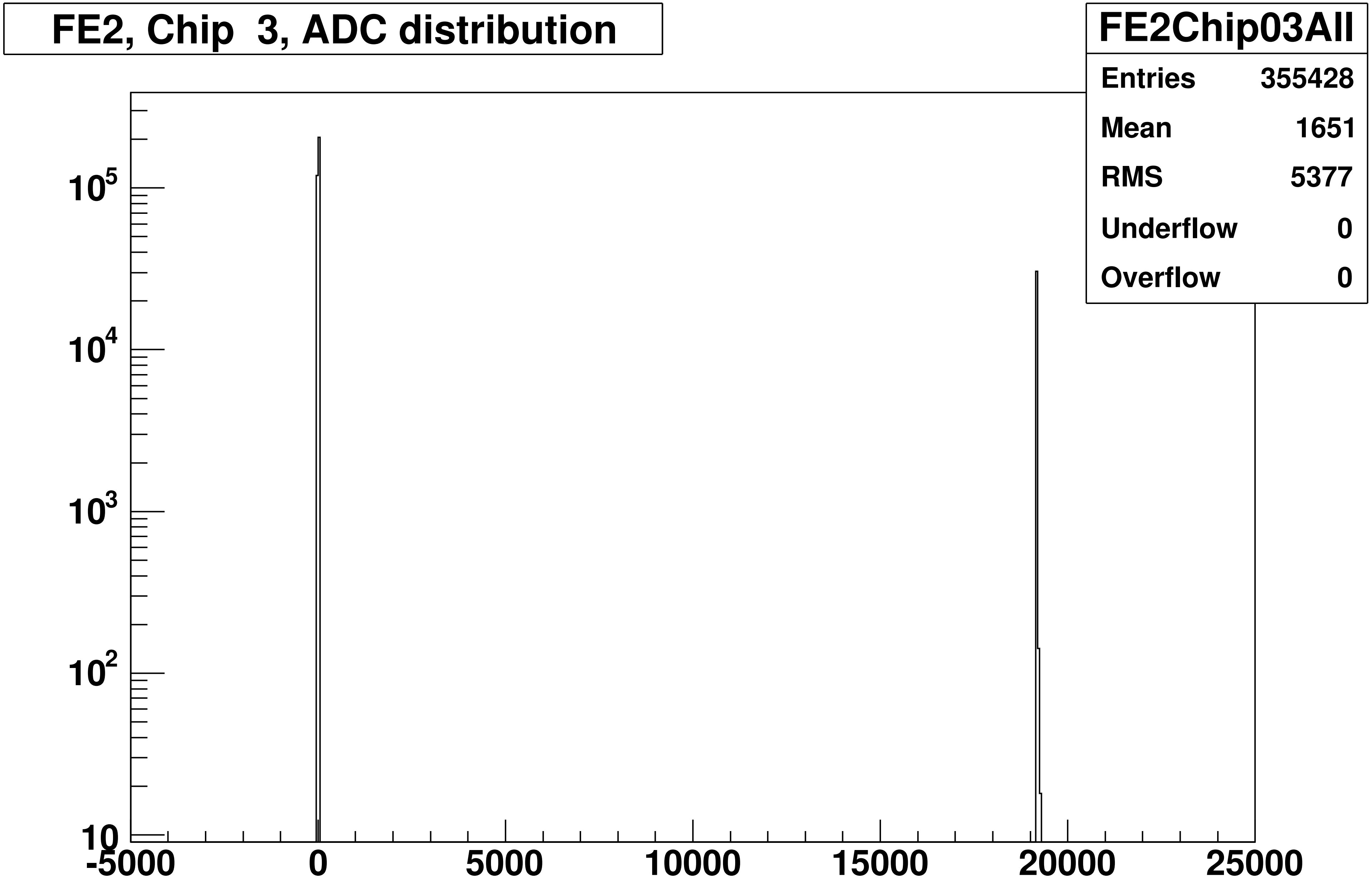
19600
19800
20000


FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 355428
Mean 1651
RMS 5377
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
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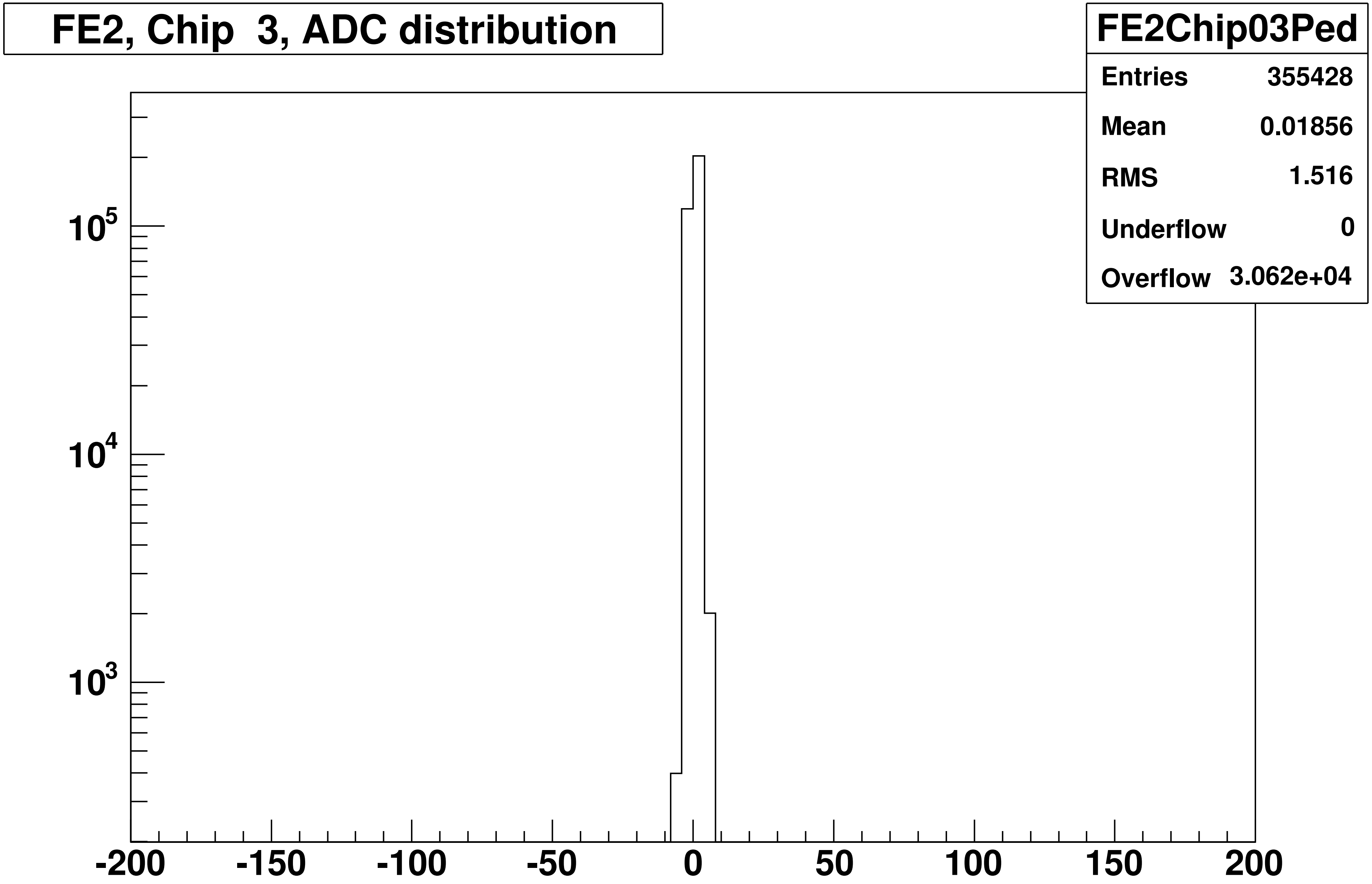
10000
15000
20000
25000


FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 355428
Mean 0.01856
RMS 1.516
Underflow 0
Overflow 3.062e+04
10^5
10^4
10^3
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-100
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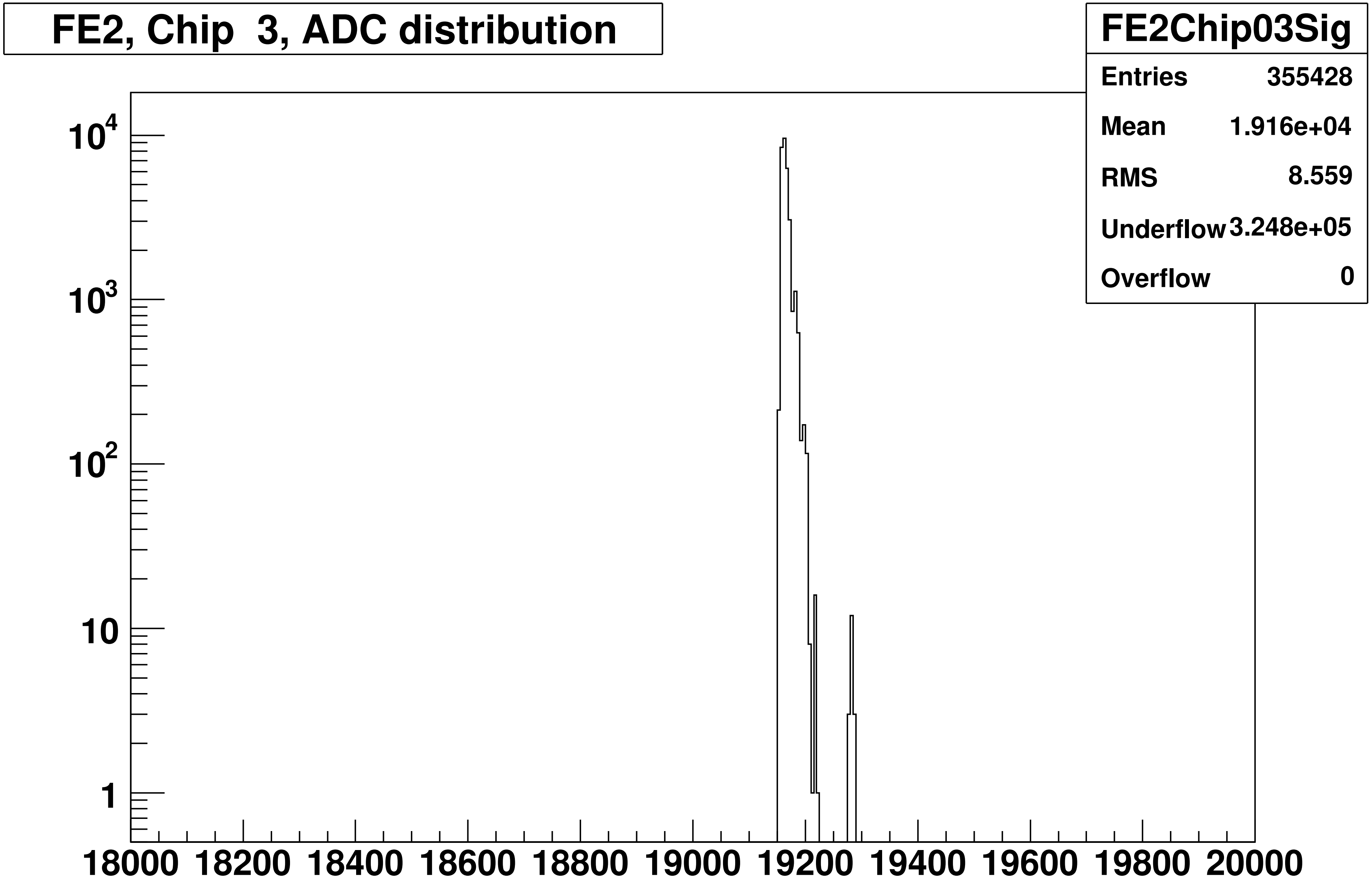
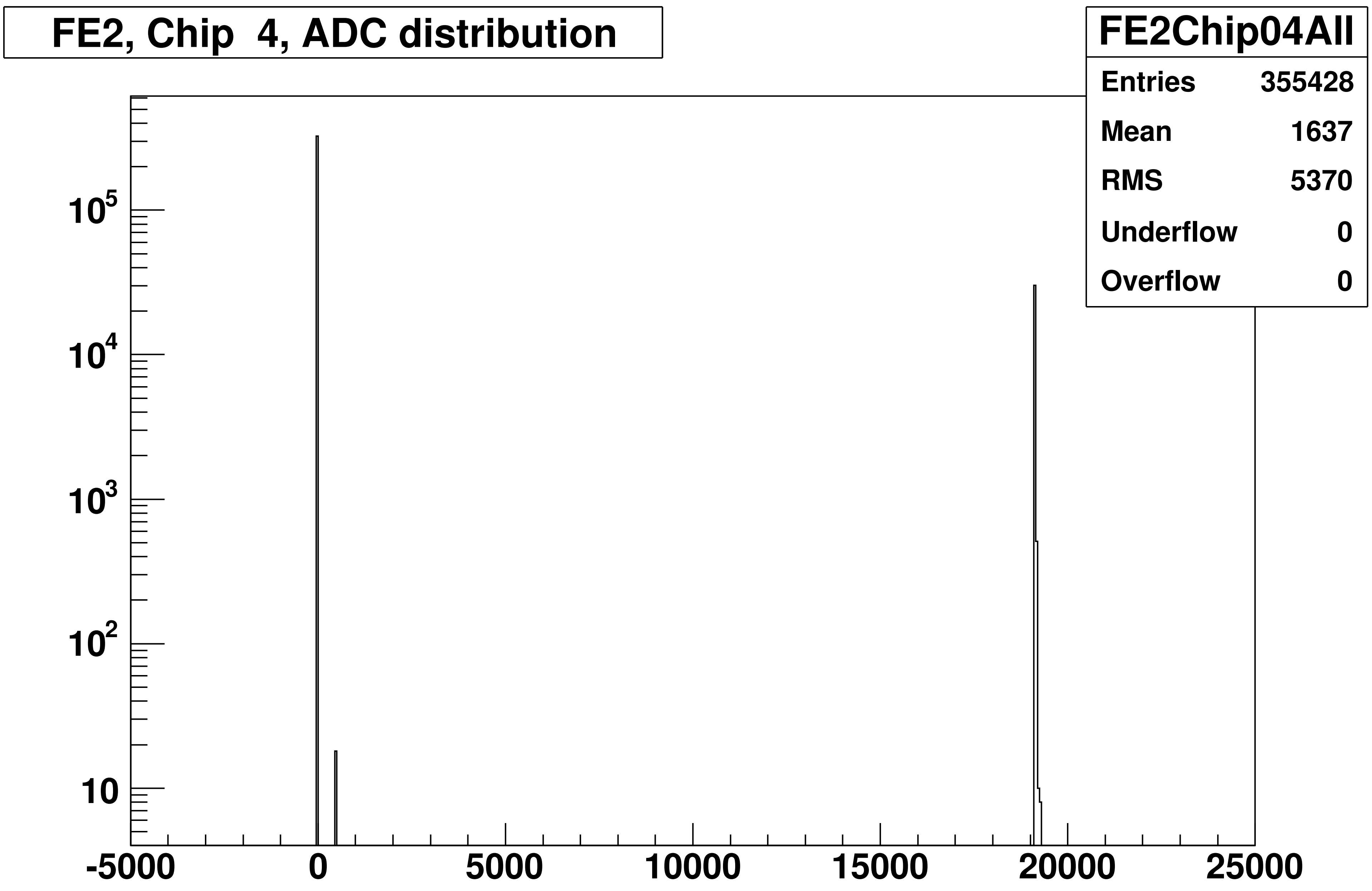
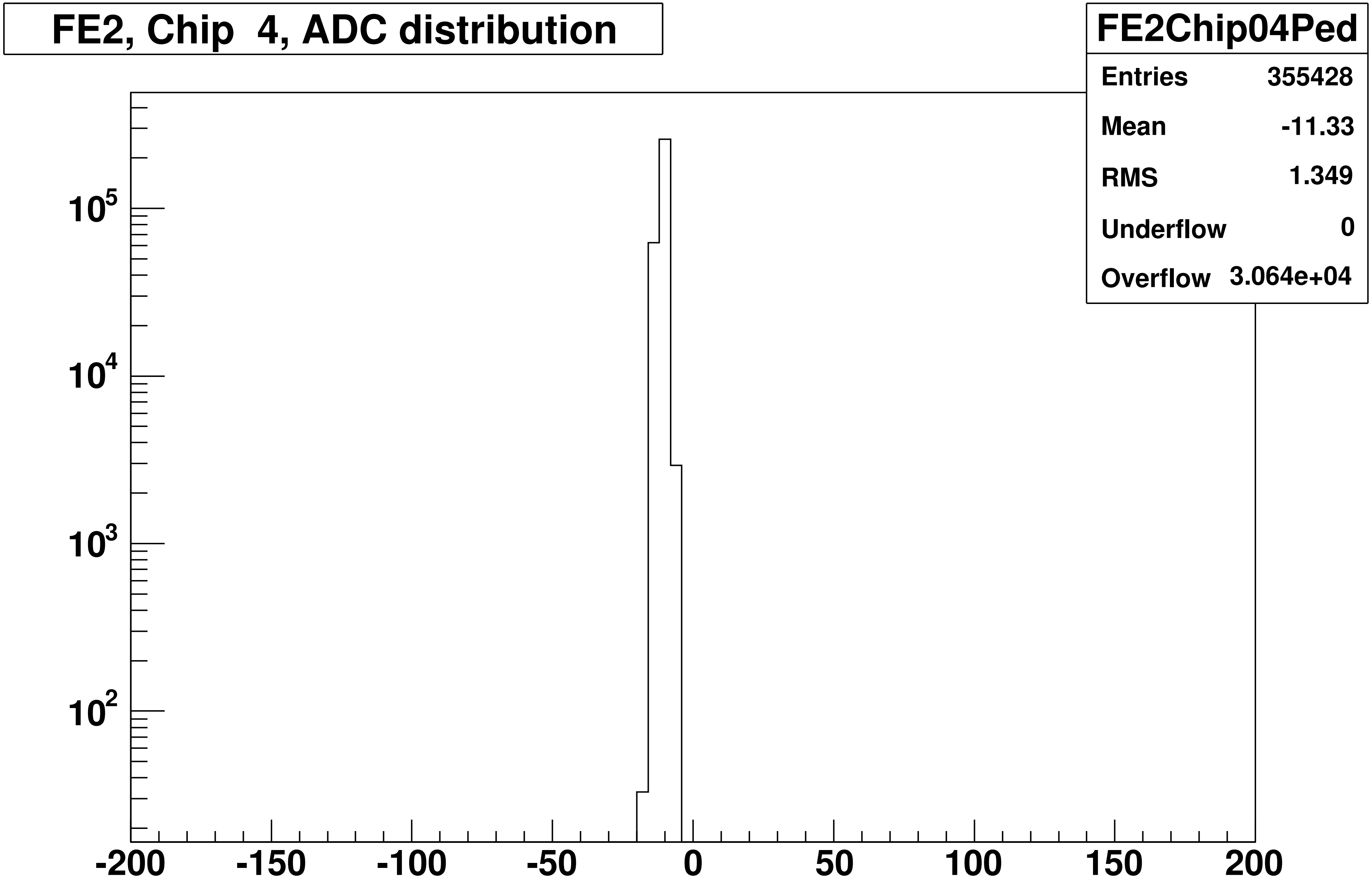
0
50
100
150
200


FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 355428
Mean 1.916e+04
RMS 8.559
Underflow 3.248e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 355428
Mean 1637
RMS 5370
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 355428
Mean -11.33
RMS 1.349
Underflow 0
Overflow 3.064e+04
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10^4
10^3
10^2
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-100
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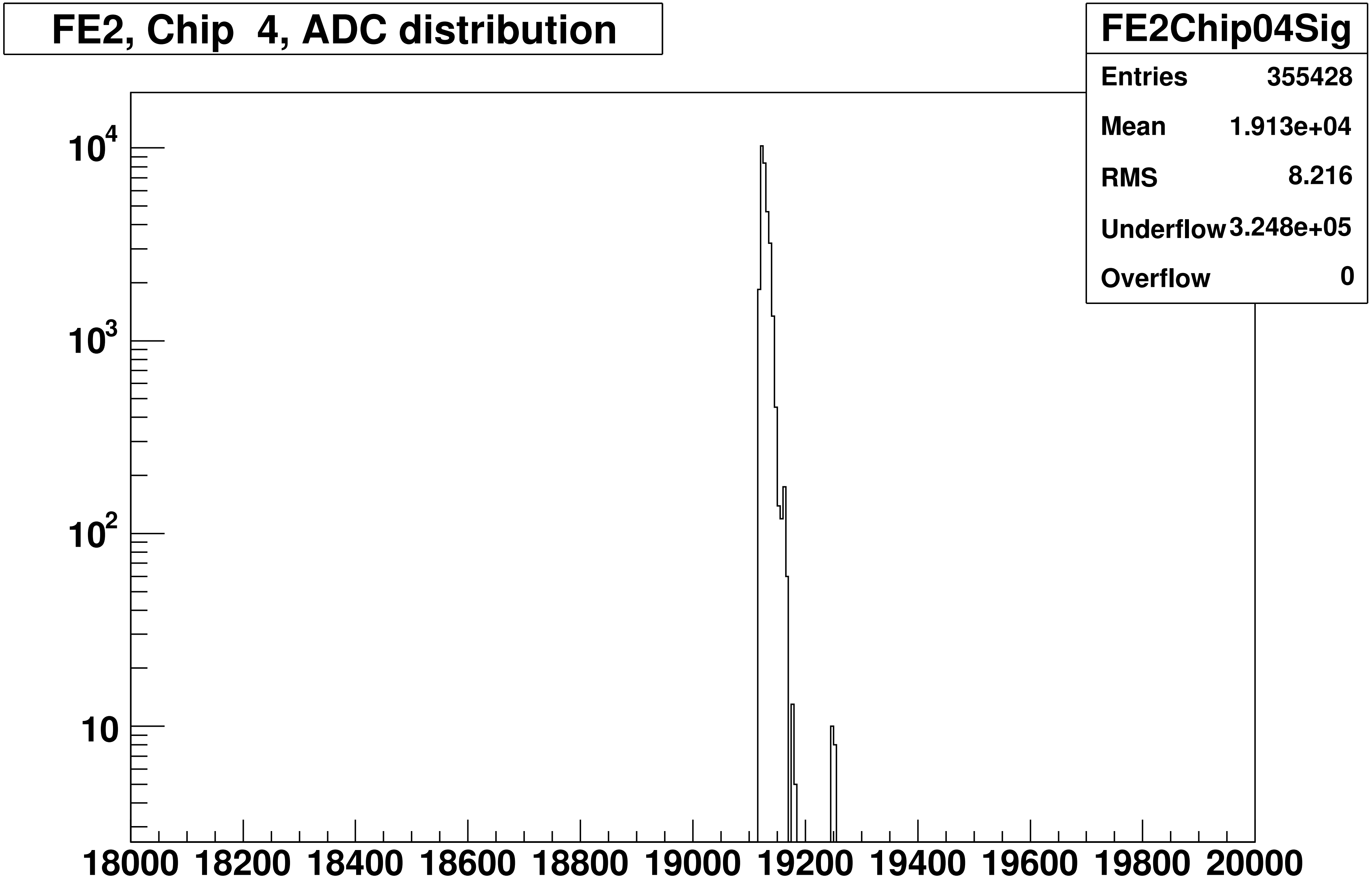
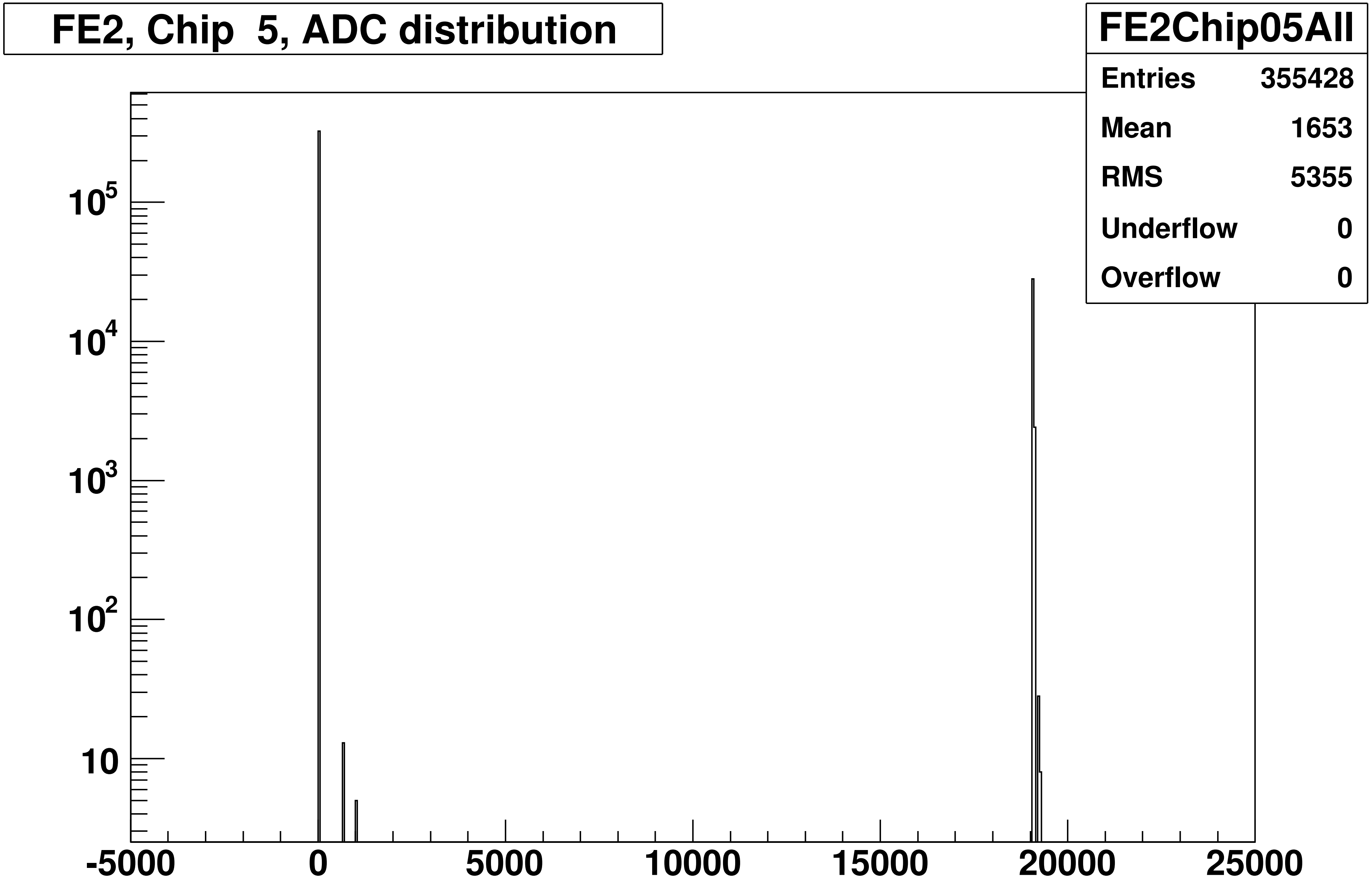
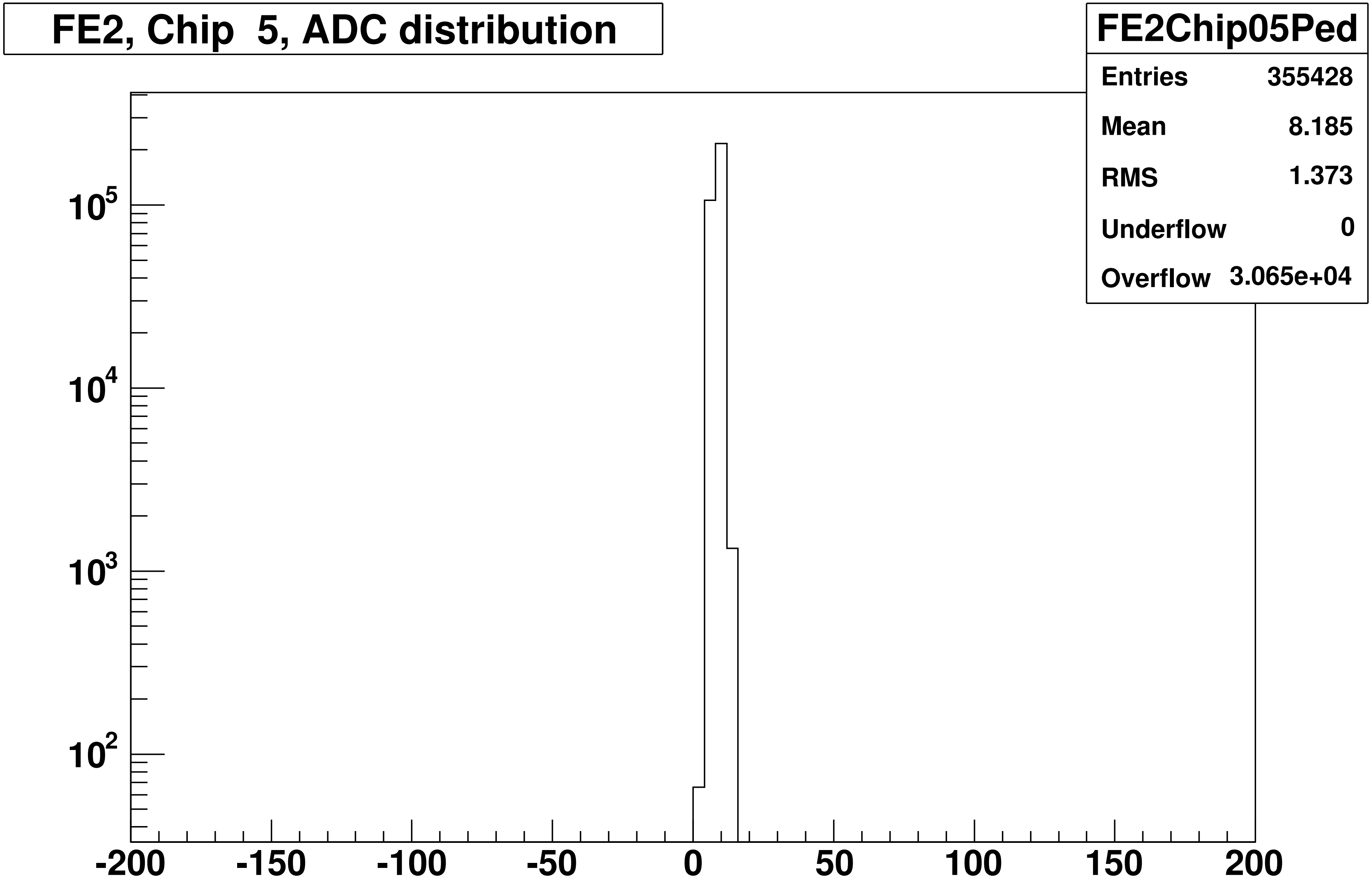
150
200



FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 355428
Mean 1.913e+04
RMS 8.216
Underflow 3.248e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 355428
Mean 1653
RMS 5355
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 355428
Mean 8.185
RMS 1.373
Underflow 0
Overflow 3.065e+04
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10^4
10^3
10^2
-200
-150
-100
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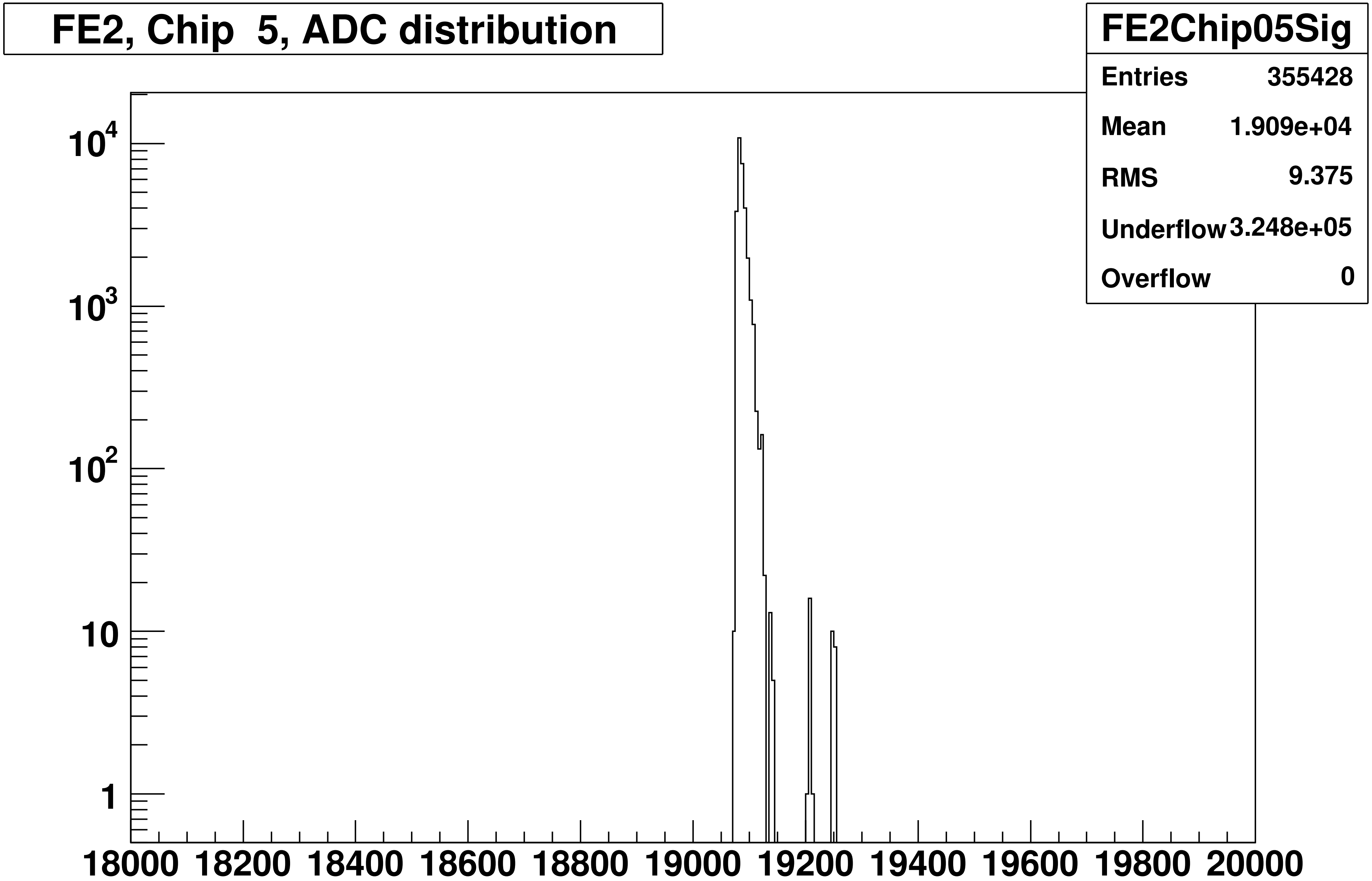
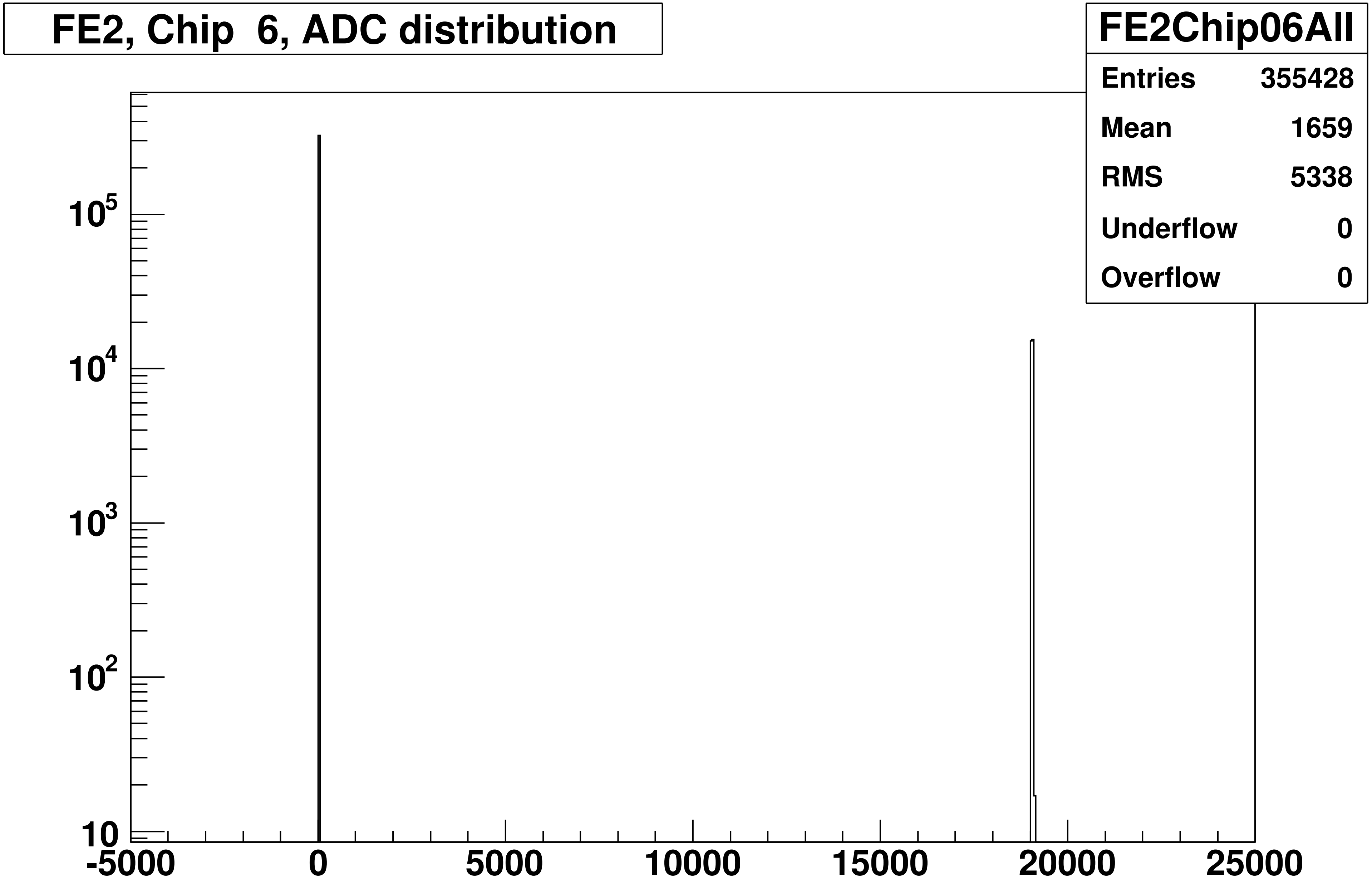
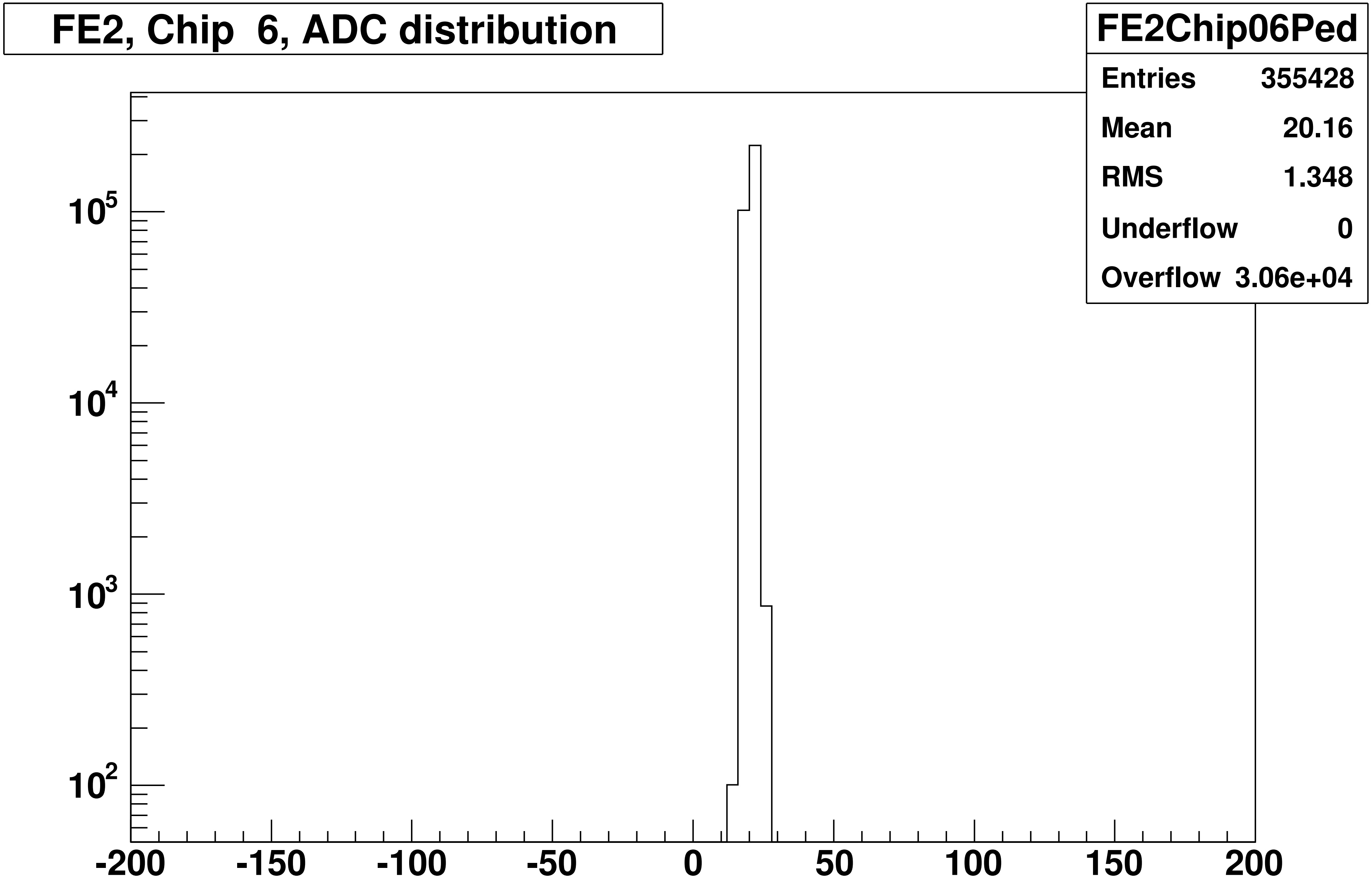
0
50
100
150
200

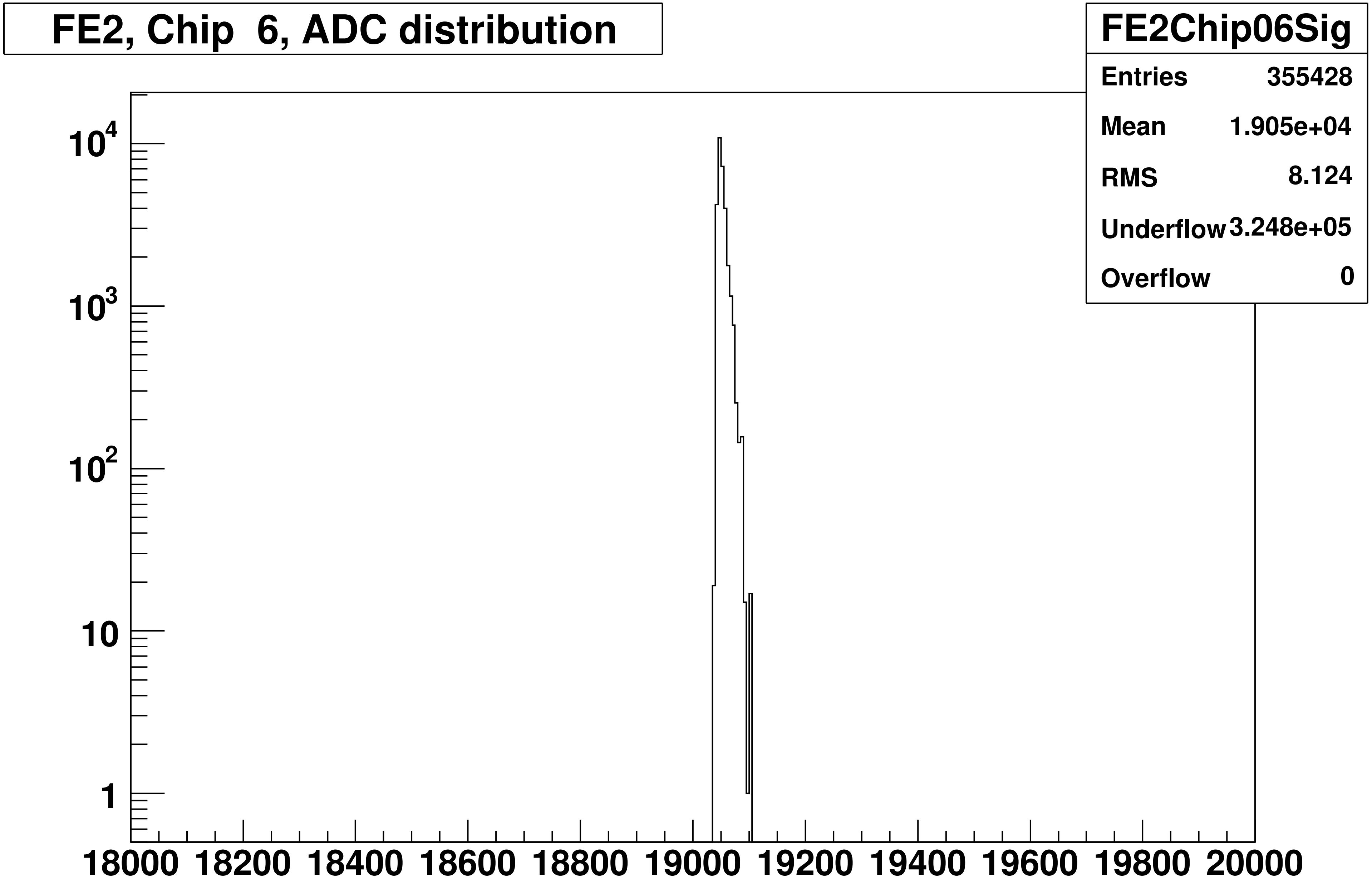


FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 355428
Mean 1.909e+04
RMS 9.375
Underflow 3.248e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 355428
Mean 1659
RMS 5338
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 355428
Mean 20.16
RMS 1.348
Underflow 0
Overflow 3.06e+04
10^5
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200


FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 355428
Mean 1.905e+04
RMS 8.124
Underflow 3.248e+05
Overflow 0
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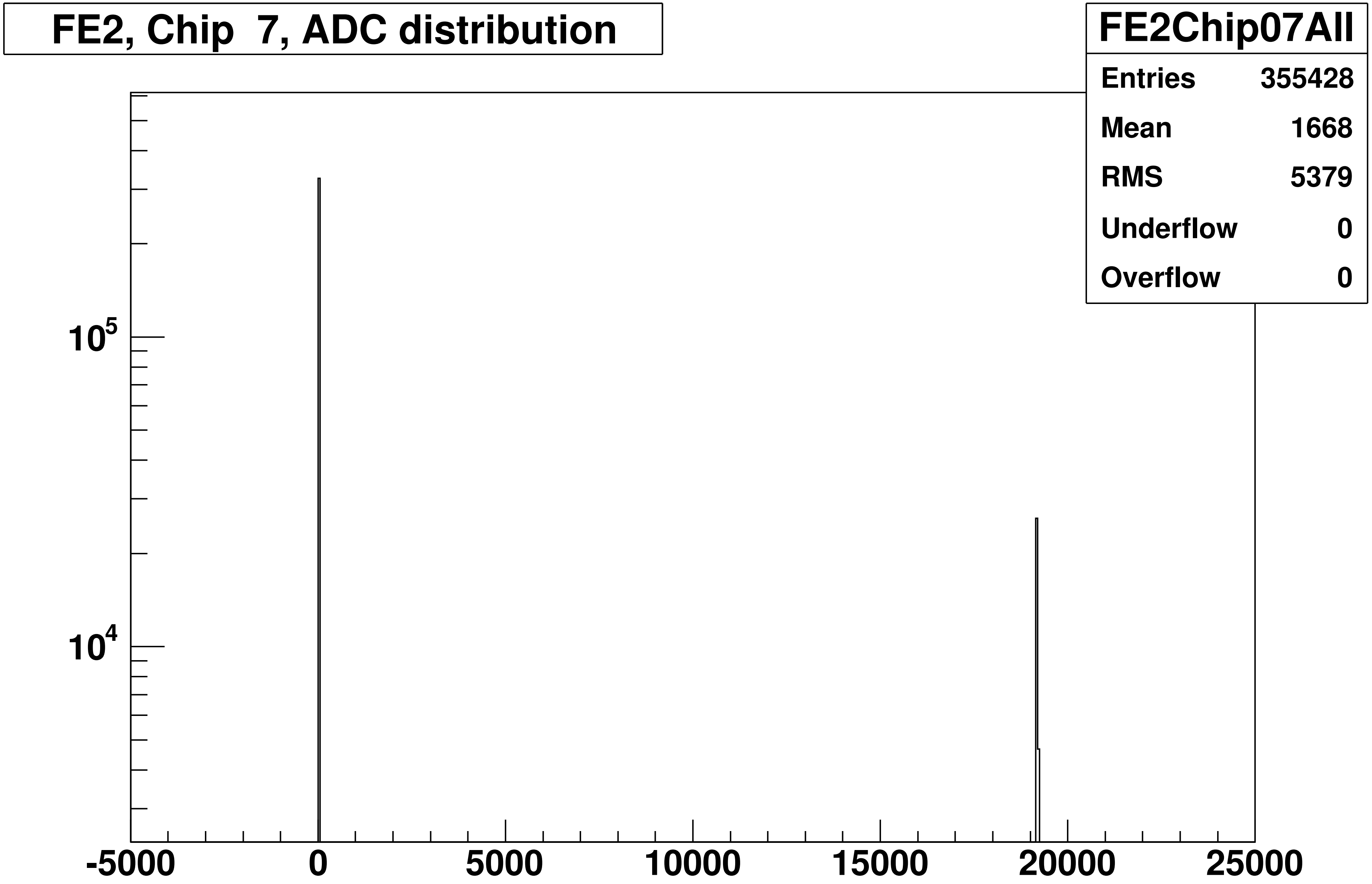
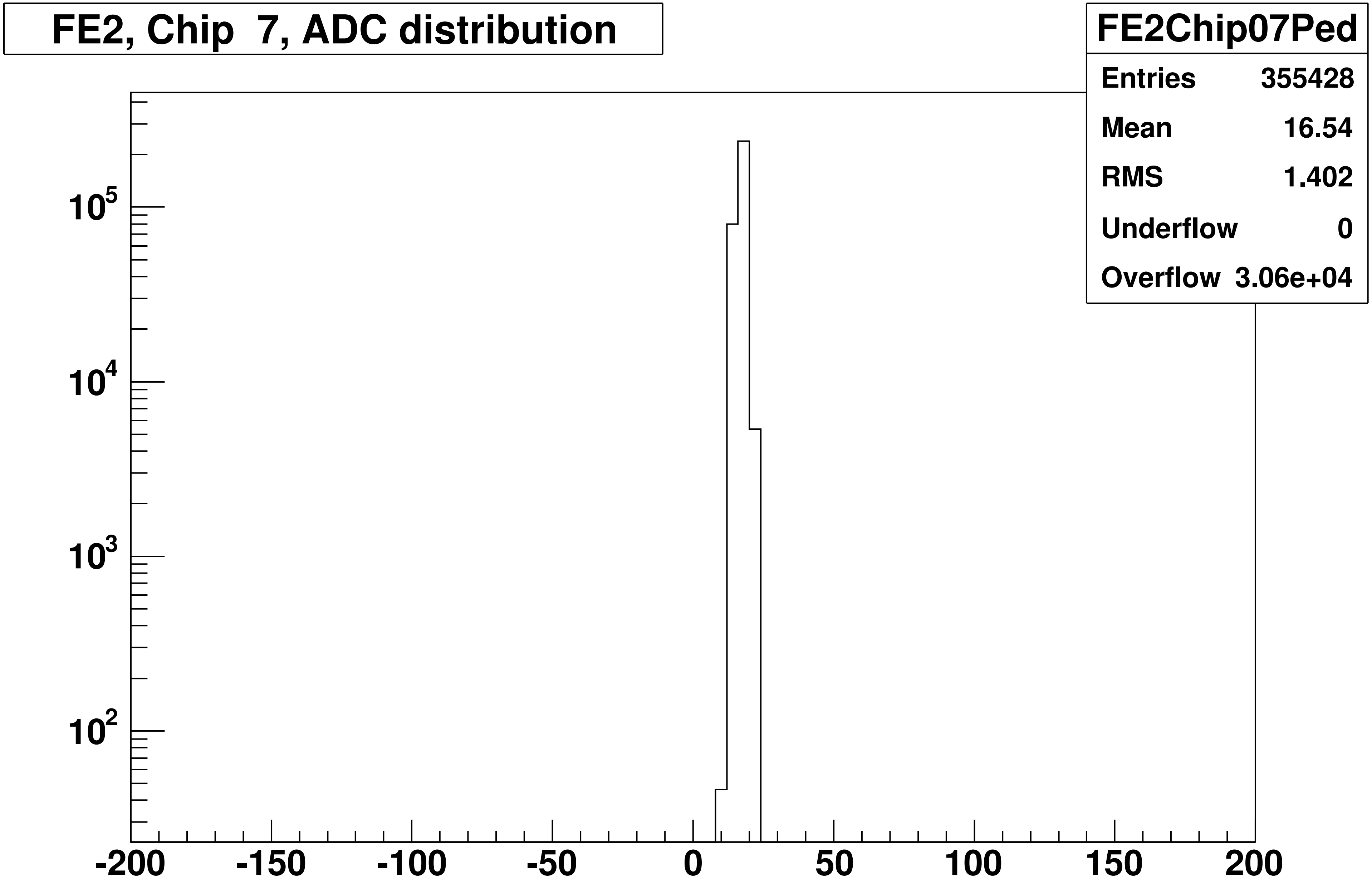
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 355428
Mean 1668
RMS 5379
Underflow 0
Overflow 0
10^5
10^4
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 355428
Mean 16.54
RMS 1.402
Underflow 0
Overflow 3.06e+04
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10^4
10^3
10^2
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-100
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0
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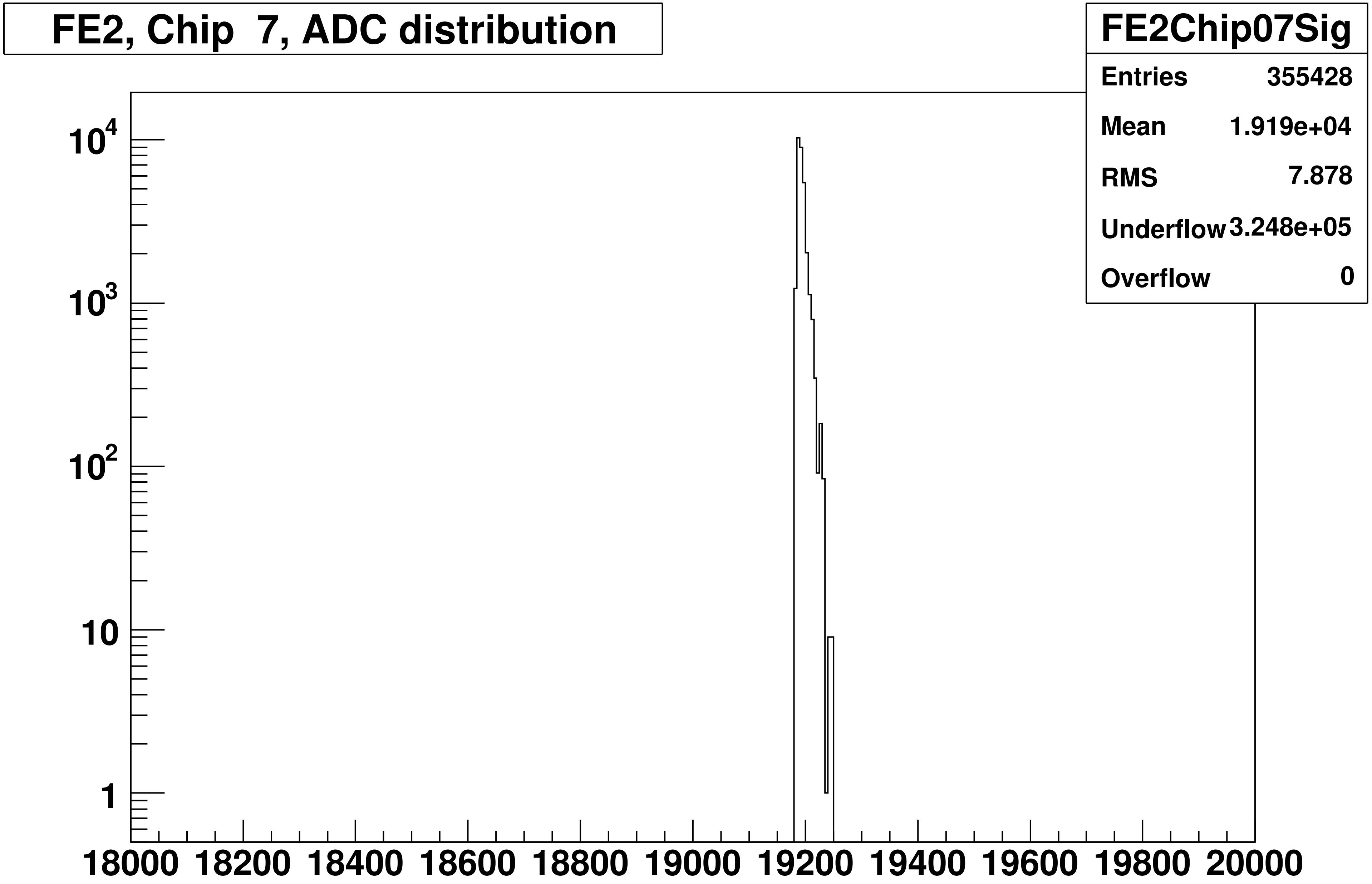
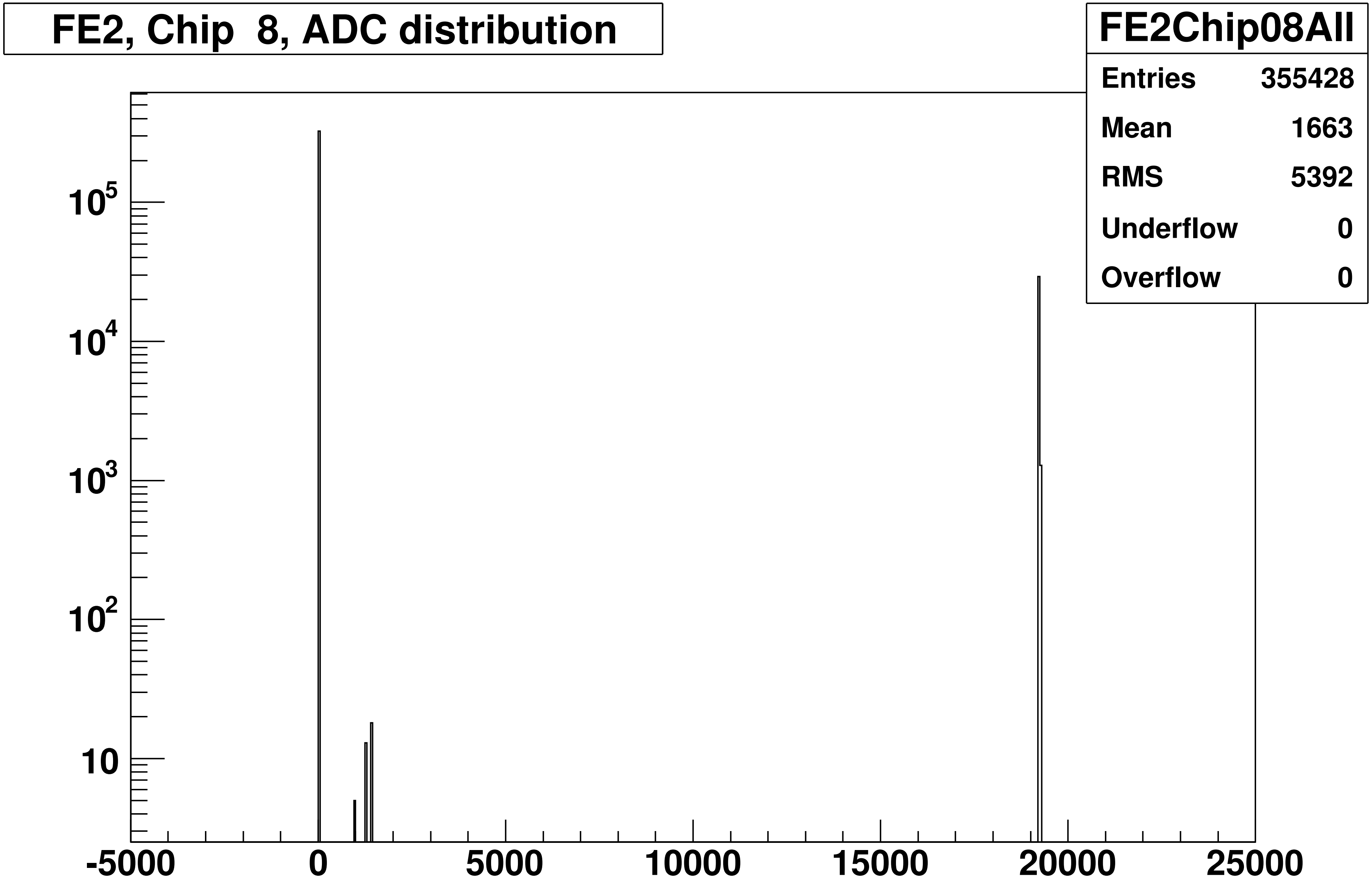
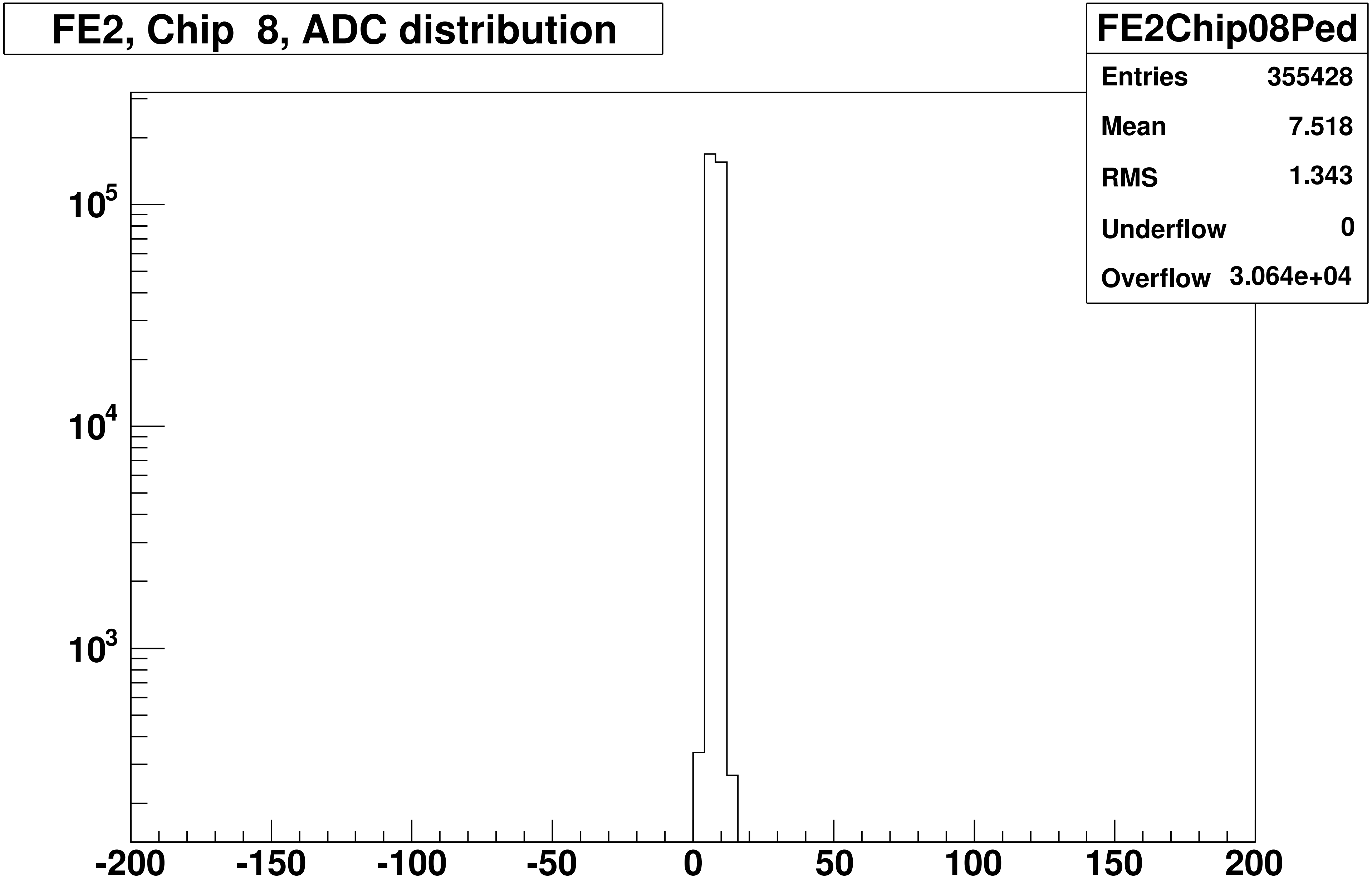
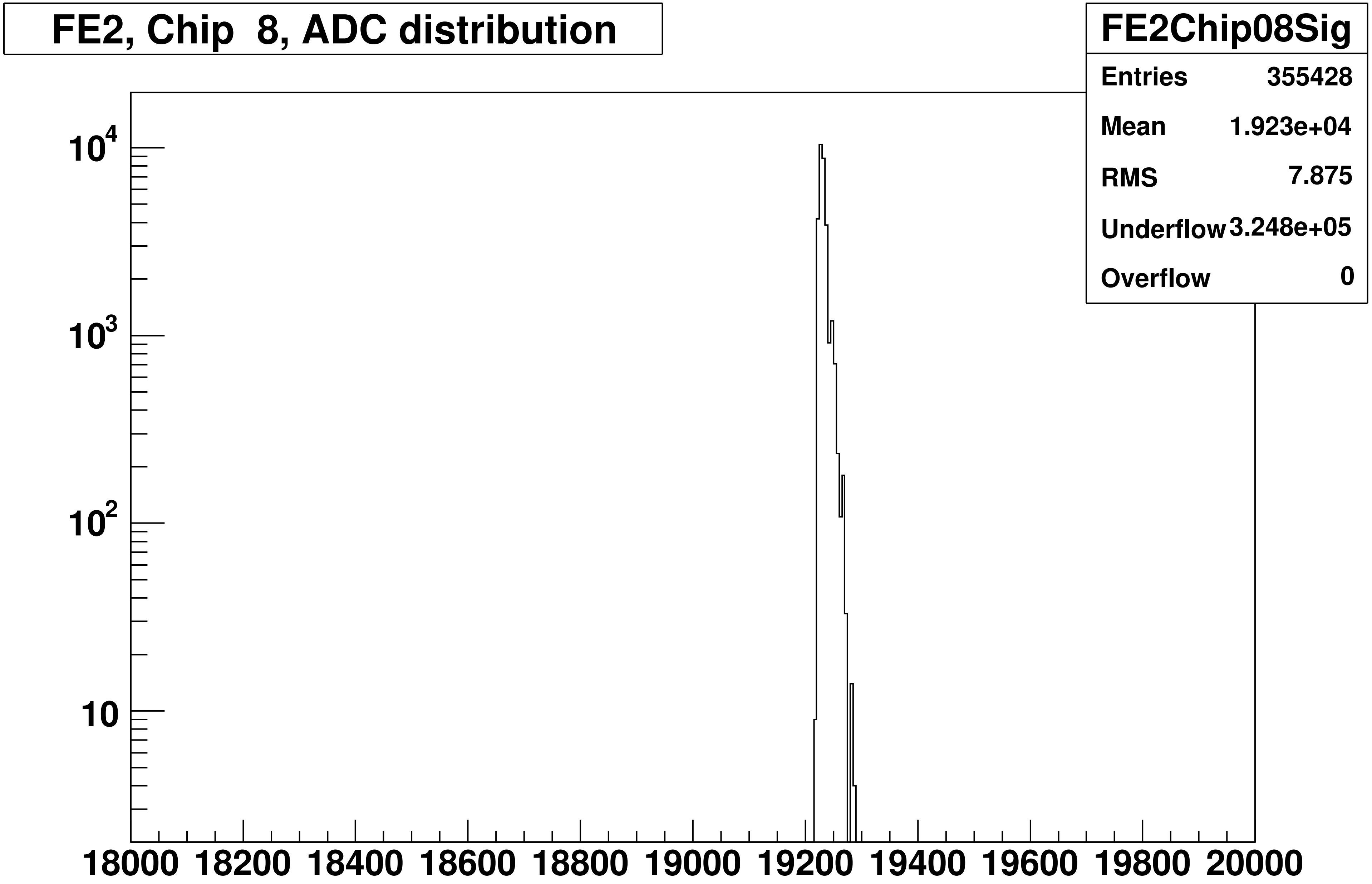
100
150
200



FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 355428
Mean 1.919e+04
RMS 7.878
Underflow 3.248e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 355428
Mean 1663
RMS 5392
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 355428
Mean 7.518
RMS 1.343
Underflow 0
Overflow 3.064e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 355428
Mean 1.923e+04
RMS 7.875
Underflow 3.248e+05
Overflow 0
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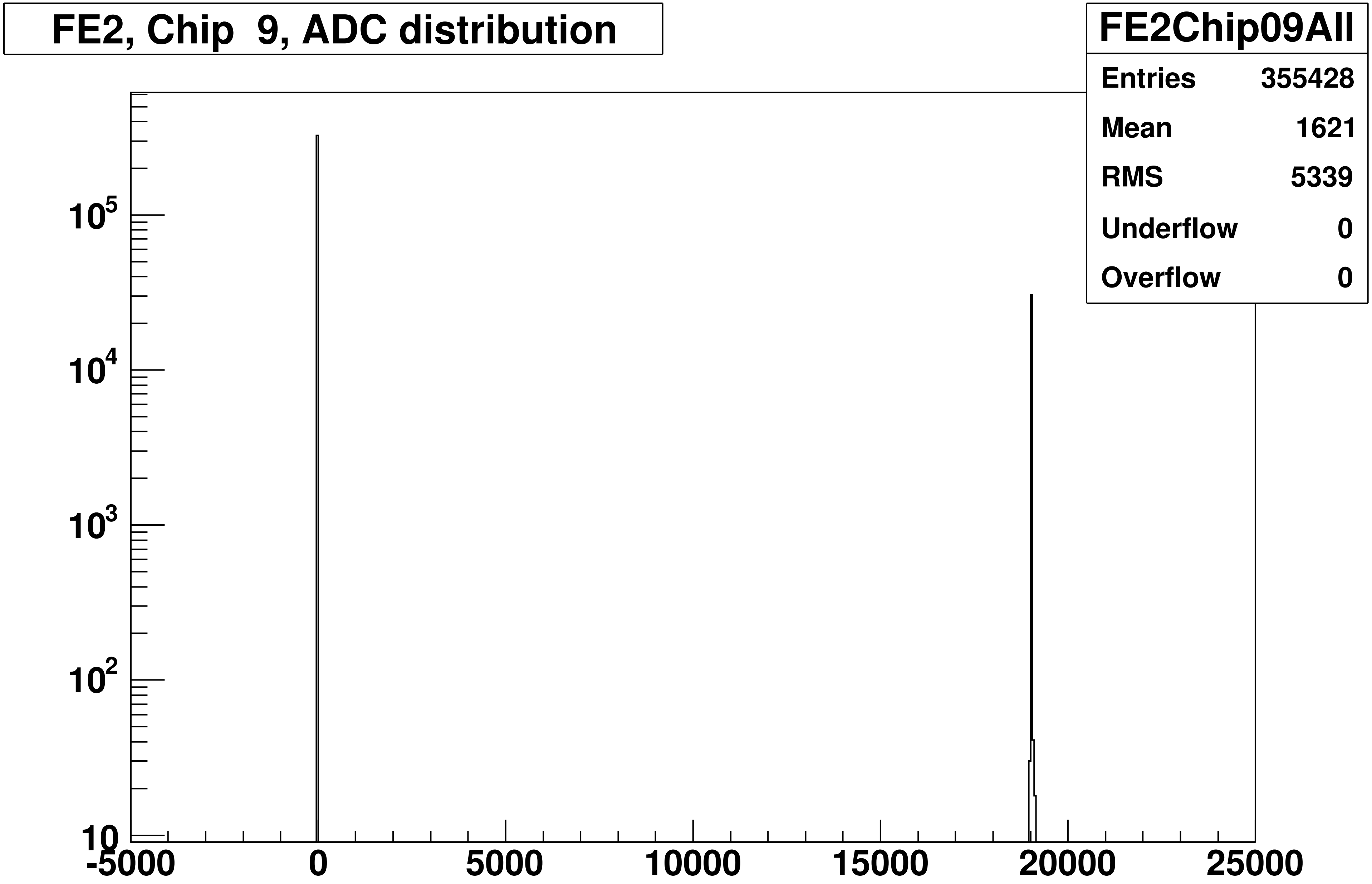
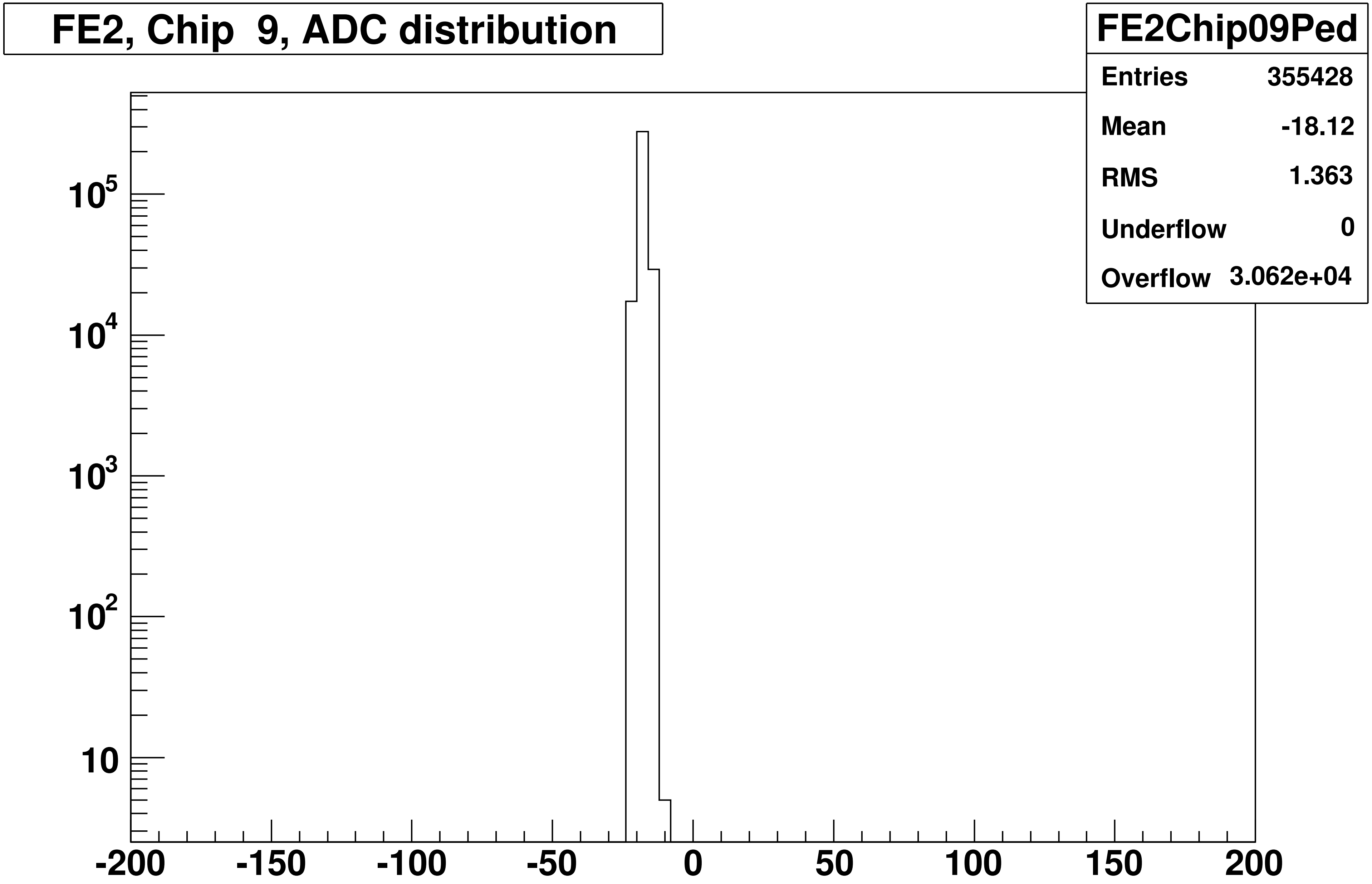
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 355428
Mean 1621
RMS 5339
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 355428
Mean -18.12
RMS 1.363
Underflow 0
Overflow 3.062e+04
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10^4
10^3
10^2
10
-200
-150
-100
-50
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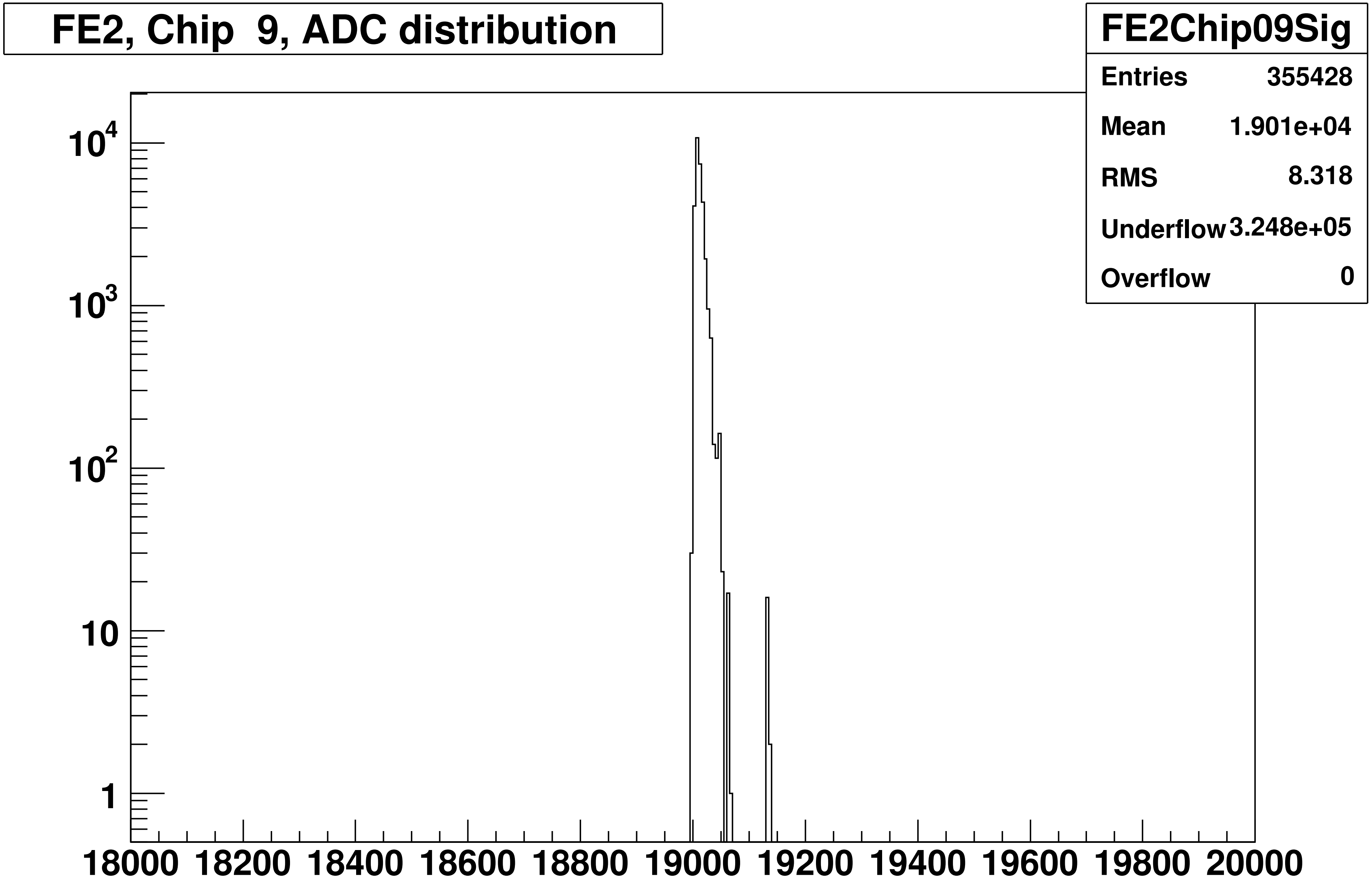
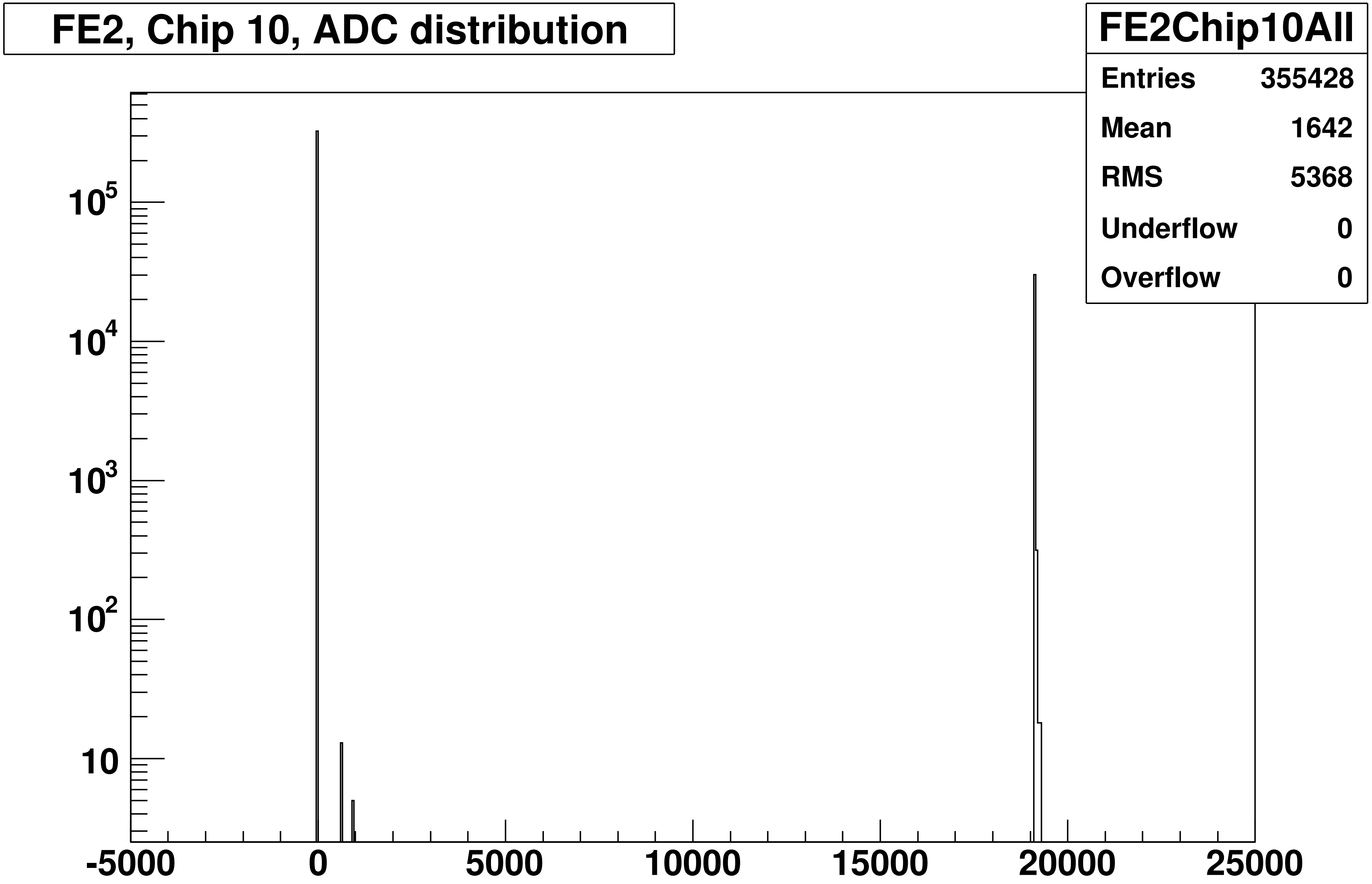
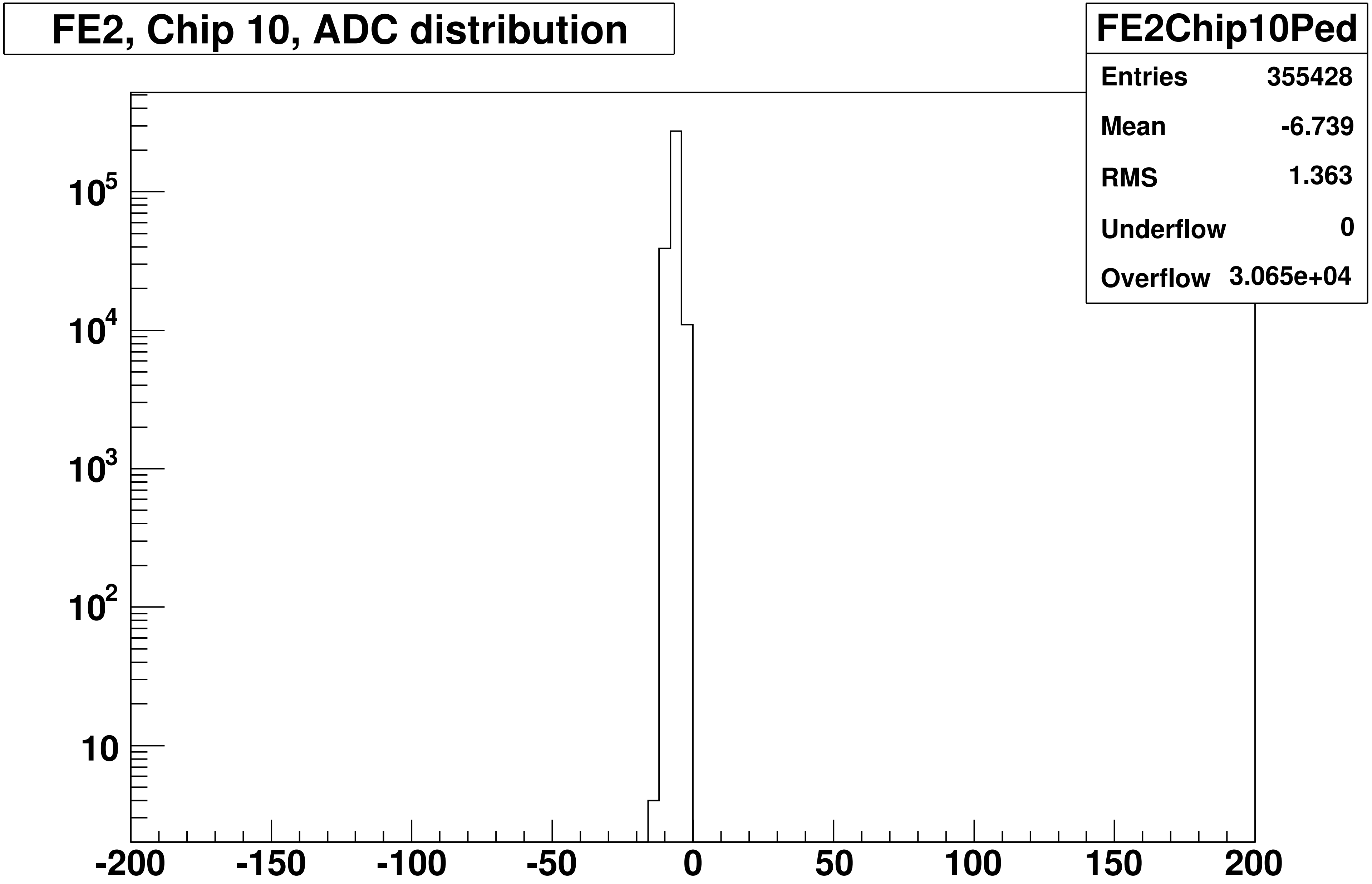
100
150
200


FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 355428
Mean 1.901e+04
RMS 8.318
Underflow 3.248e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 355428
Mean 1642
RMS 5368
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 355428
Mean -6.739
RMS 1.363
Underflow 0
Overflow 3.065e+04
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10^4
10^3
10^2
10
-200
-150
-100
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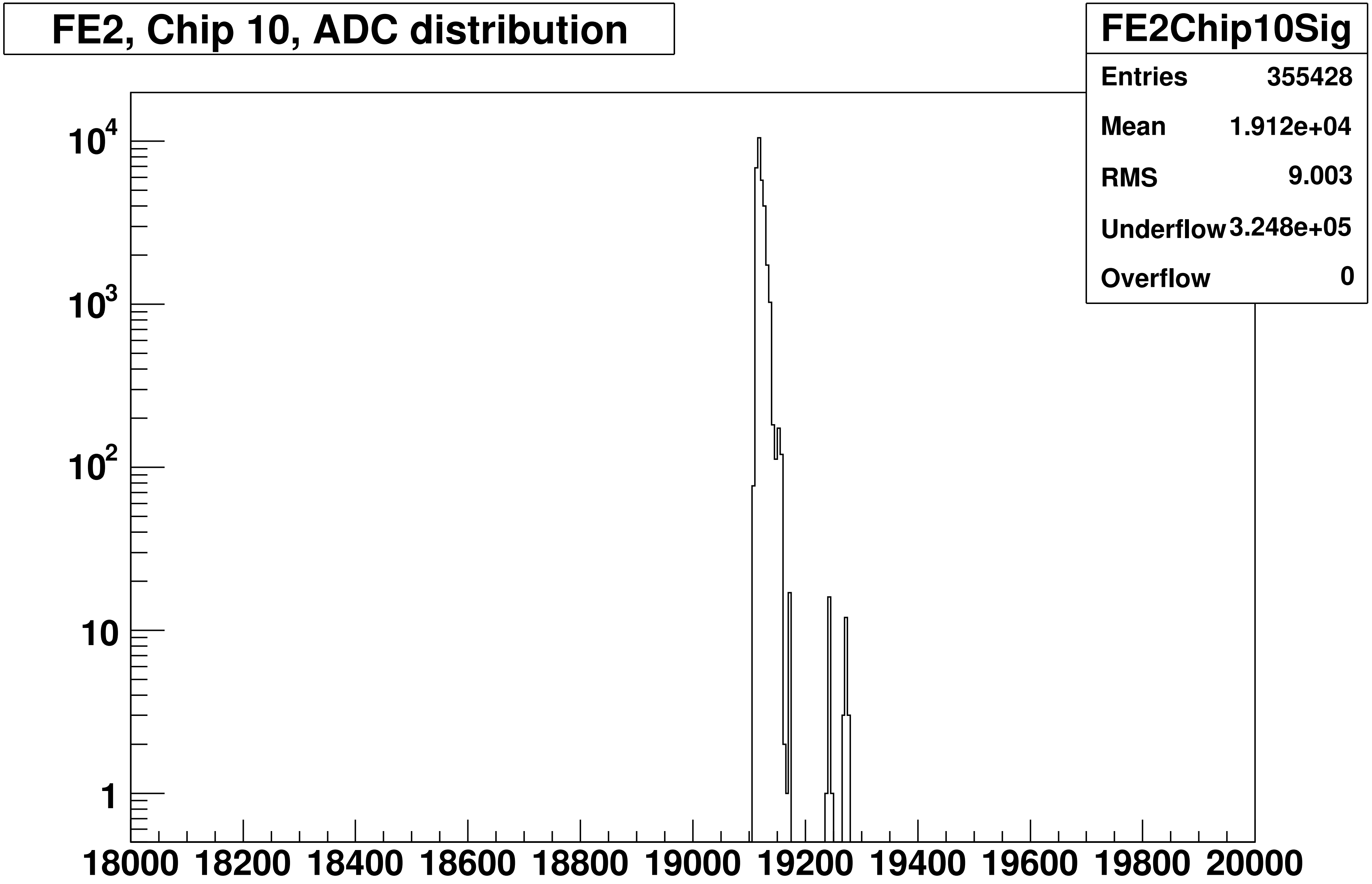
0
50
100
150
200


FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 355428
Mean 1.912e+04
RMS 9.003
Underflow 3.248e+05
Overflow 0
10^4
10^3
10^2
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1
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18400
18600
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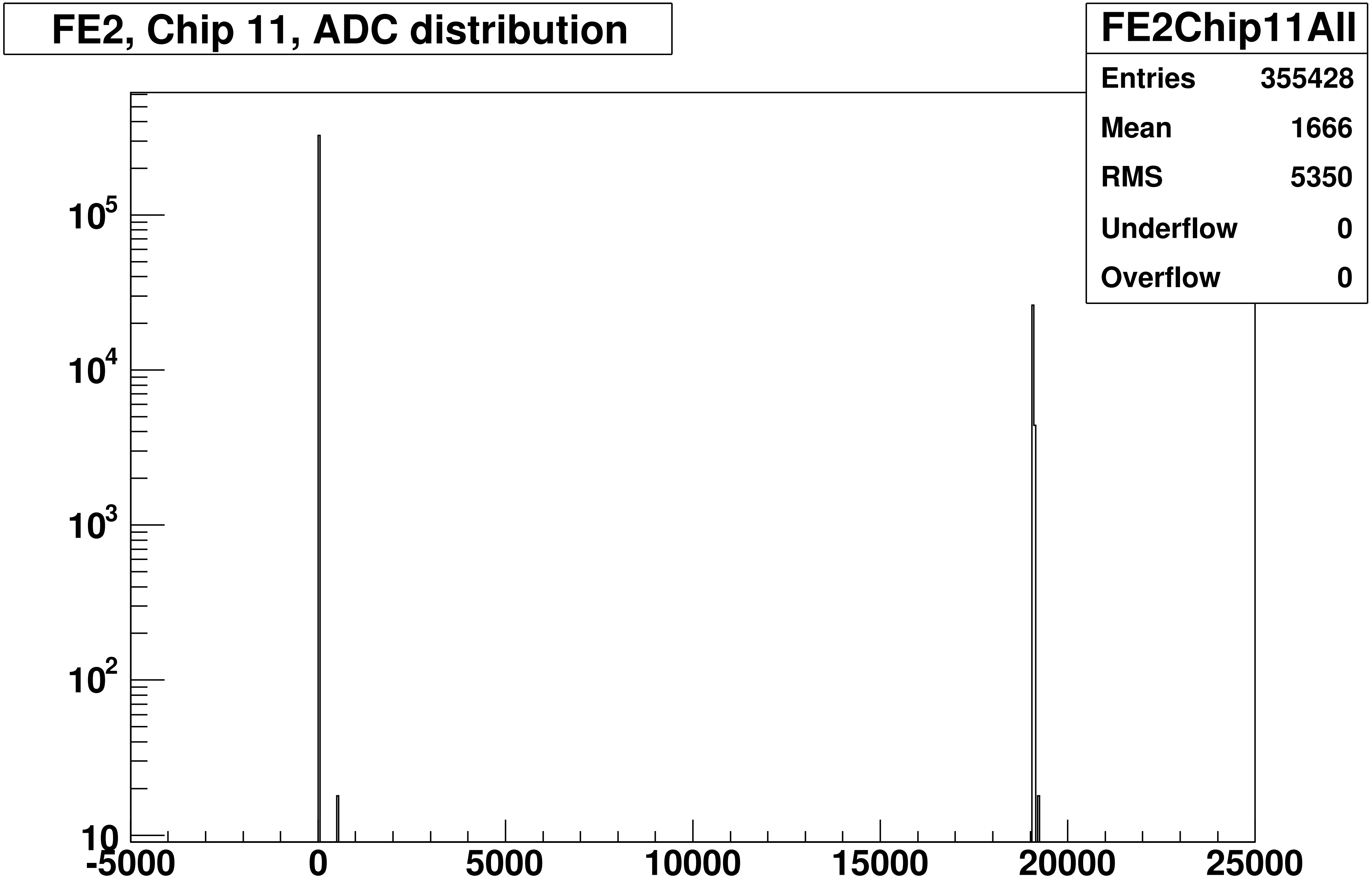
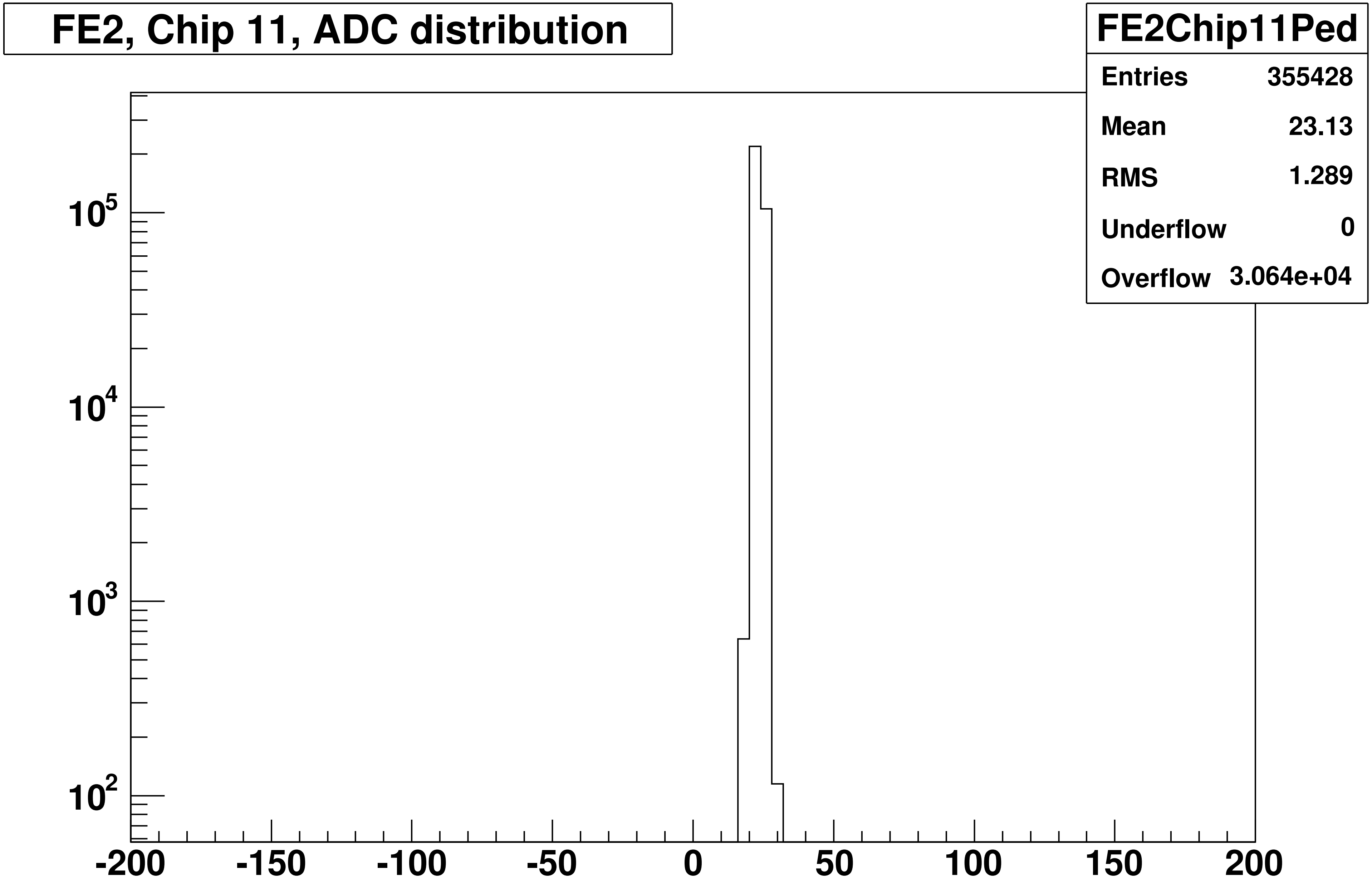
19000
19200
19400
19600
19800
20000



FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 355428
Mean 1666
RMS 5350
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 355428
Mean 23.13
RMS 1.289
Underflow 0
Overflow 3.064e+04
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10^4
10^3
10^2
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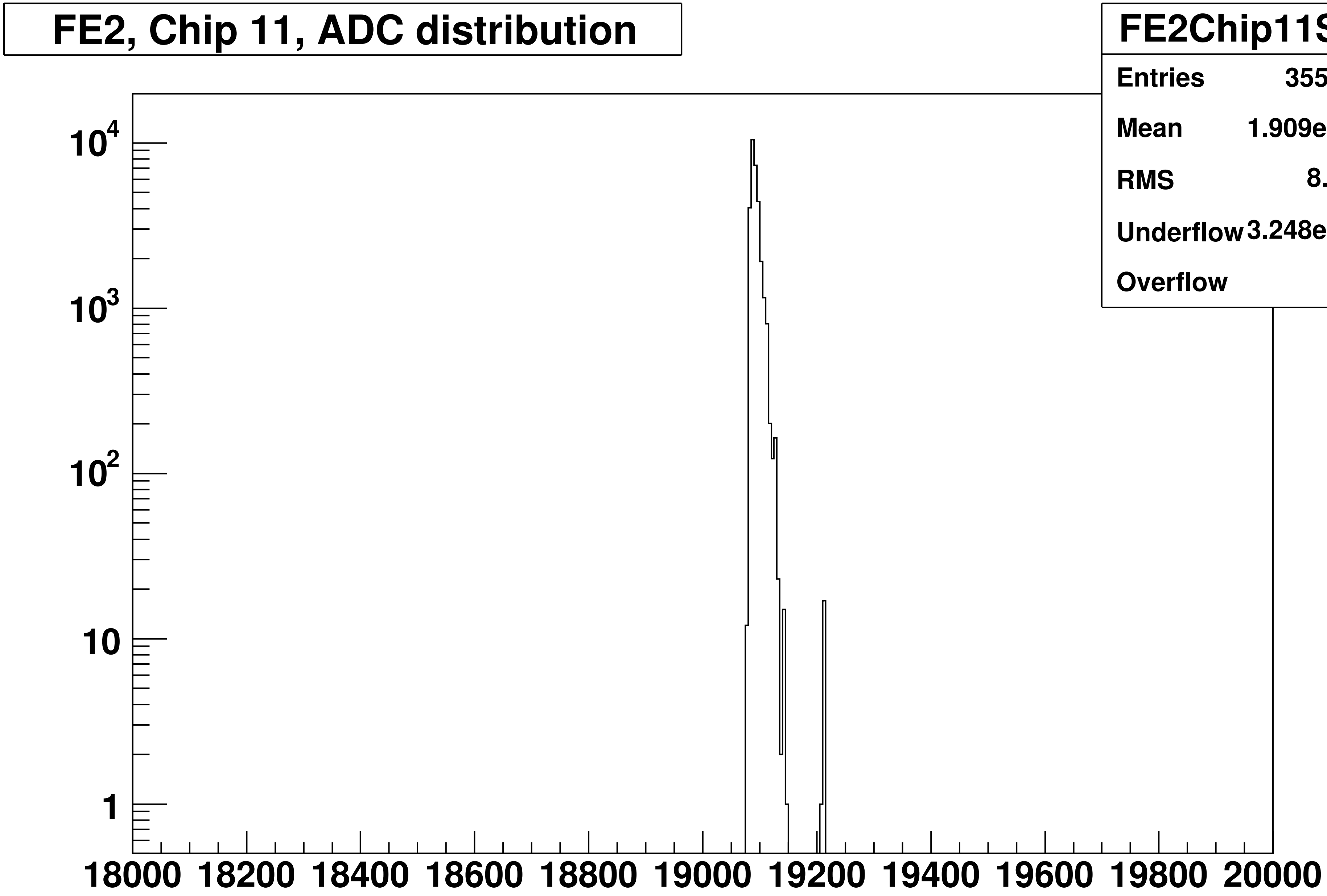
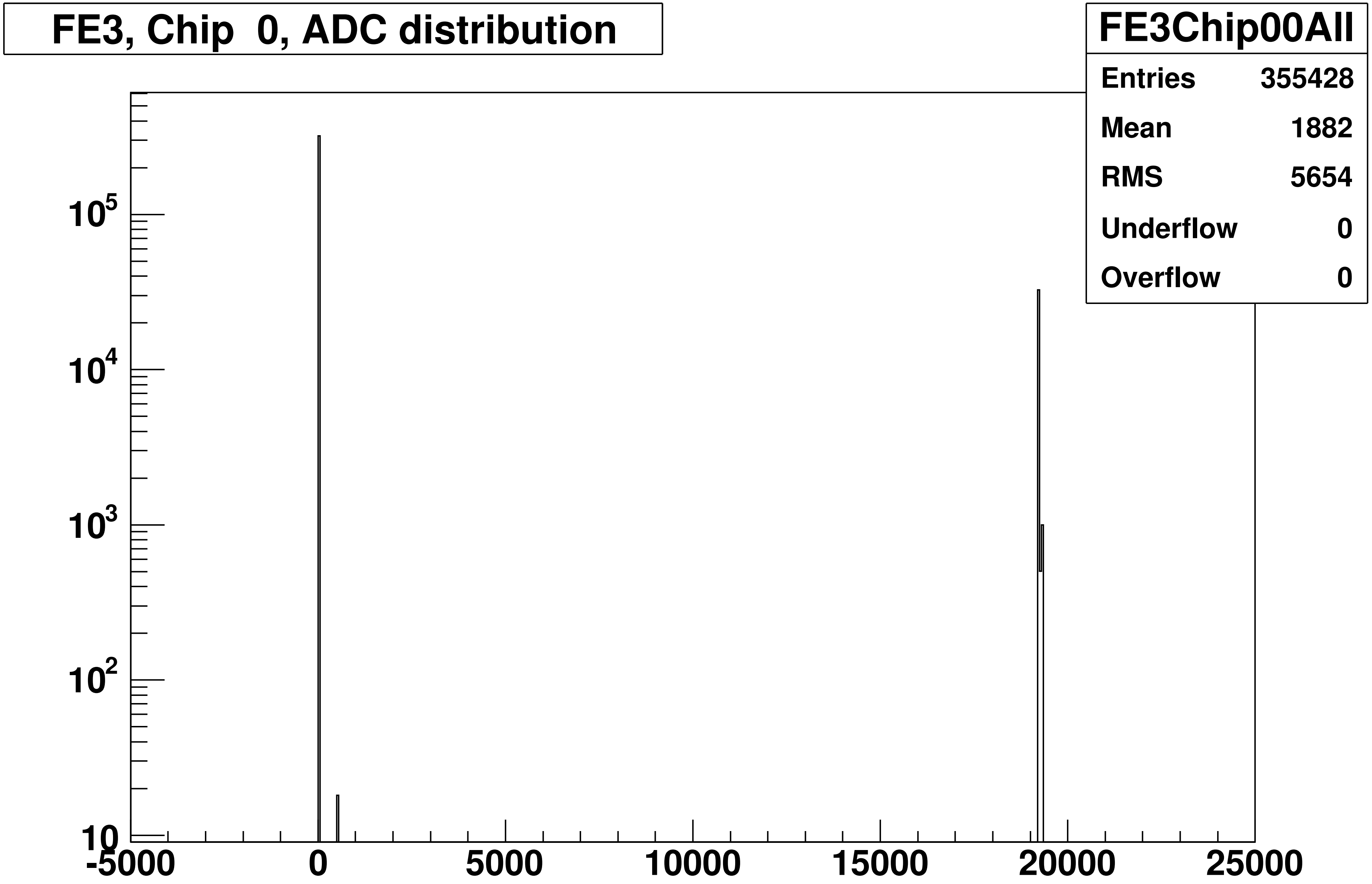
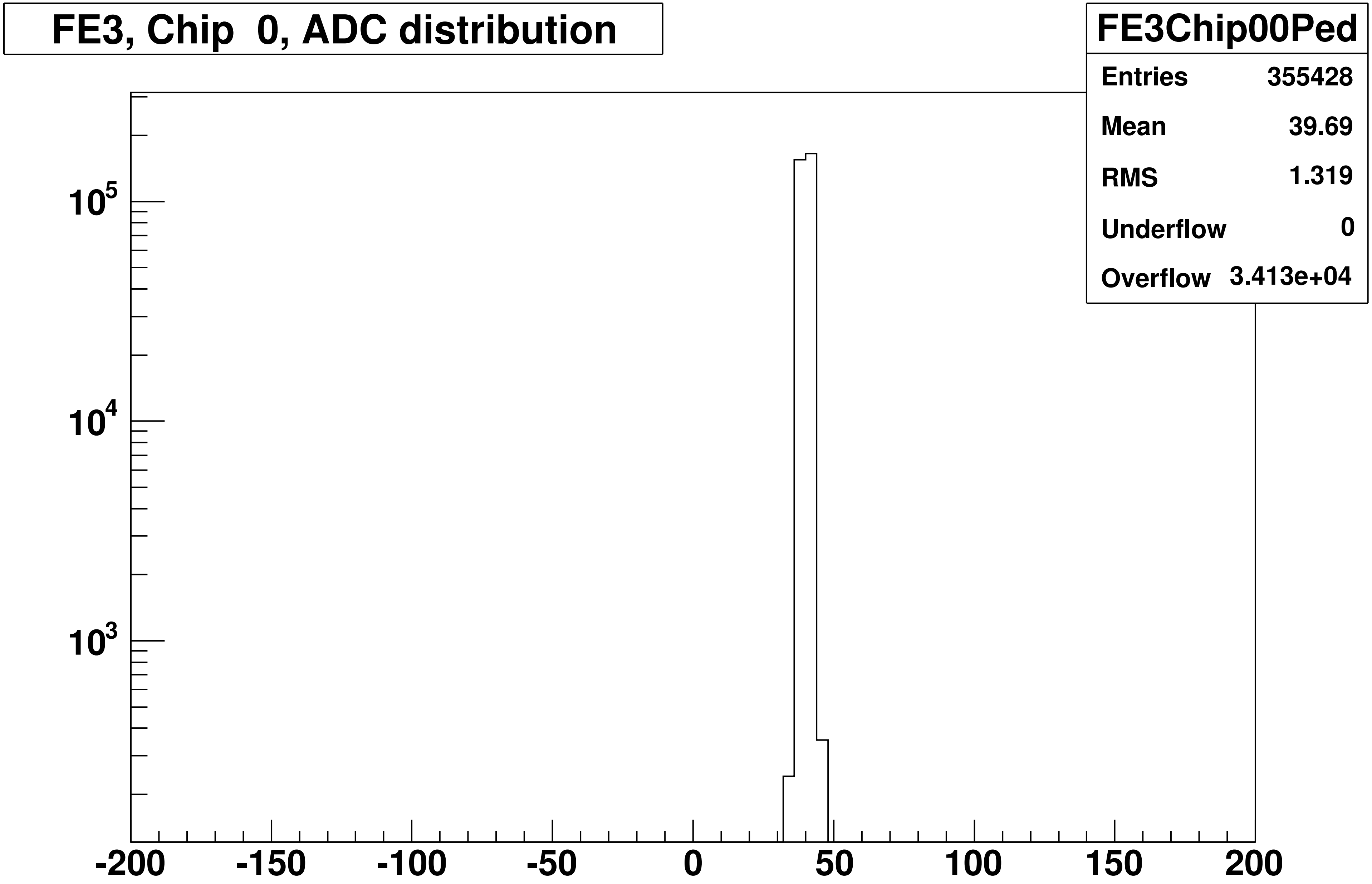
-100
-50
0
50
100
150
200



FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 355428
Mean 1.909e+04
RMS 8.568
Underflow 3.248e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 355428
Mean 1882
RMS 5654
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 355428
Mean 39.69
RMS 1.319
Underflow 0
Overflow 3.413e+04
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10^4
10^3
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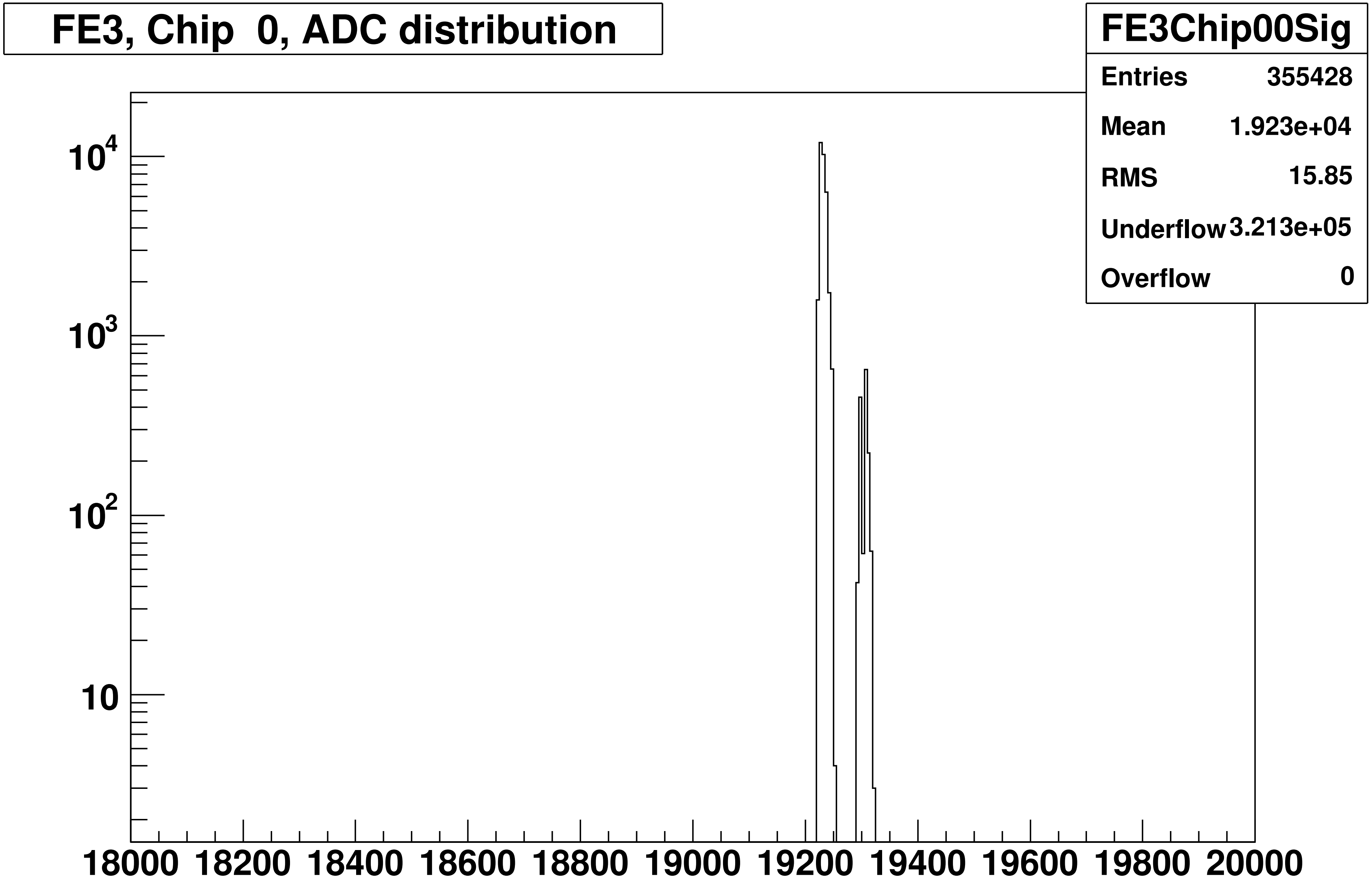
-150
-100
-50
0
50
100
150
200


FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 355428
Mean 1.923e+04
RMS 15.85
Underflow 3.213e+05
Overflow 0
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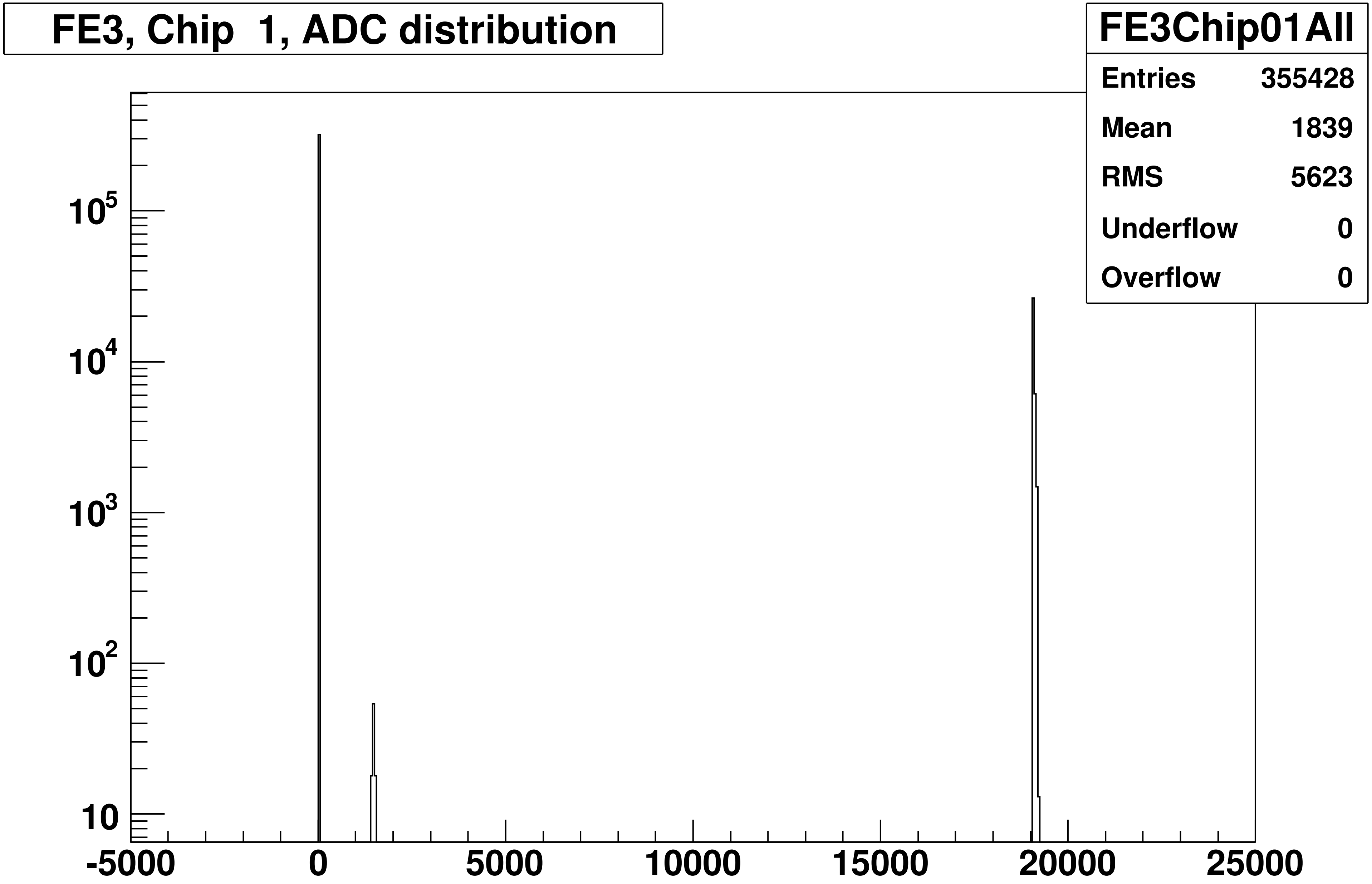
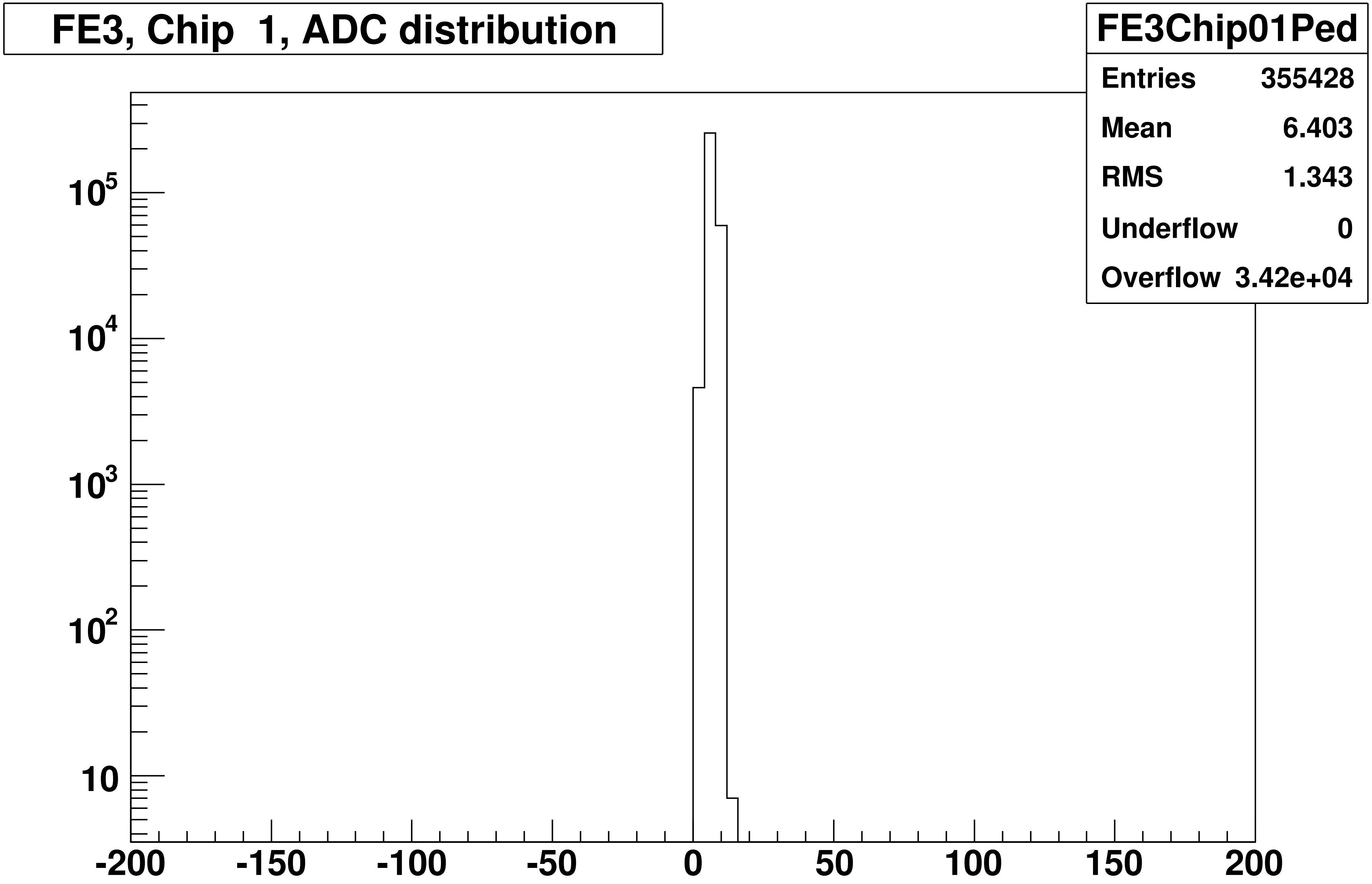
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 355428
Mean 1839
RMS 5623
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 355428
Mean 6.403
RMS 1.343
Underflow 0
Overflow 3.42e+04
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10^4
10^3
10^2
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-100
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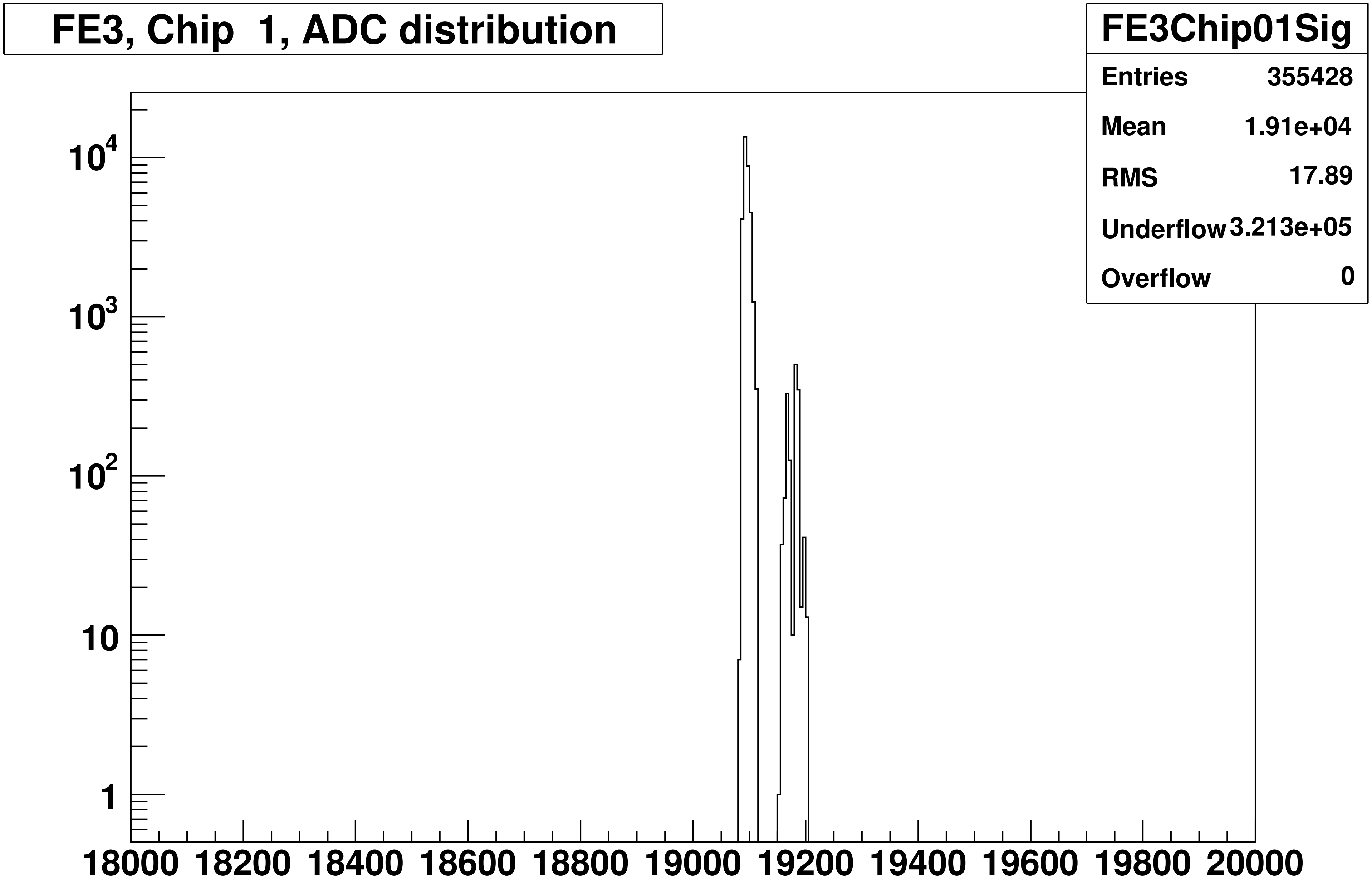
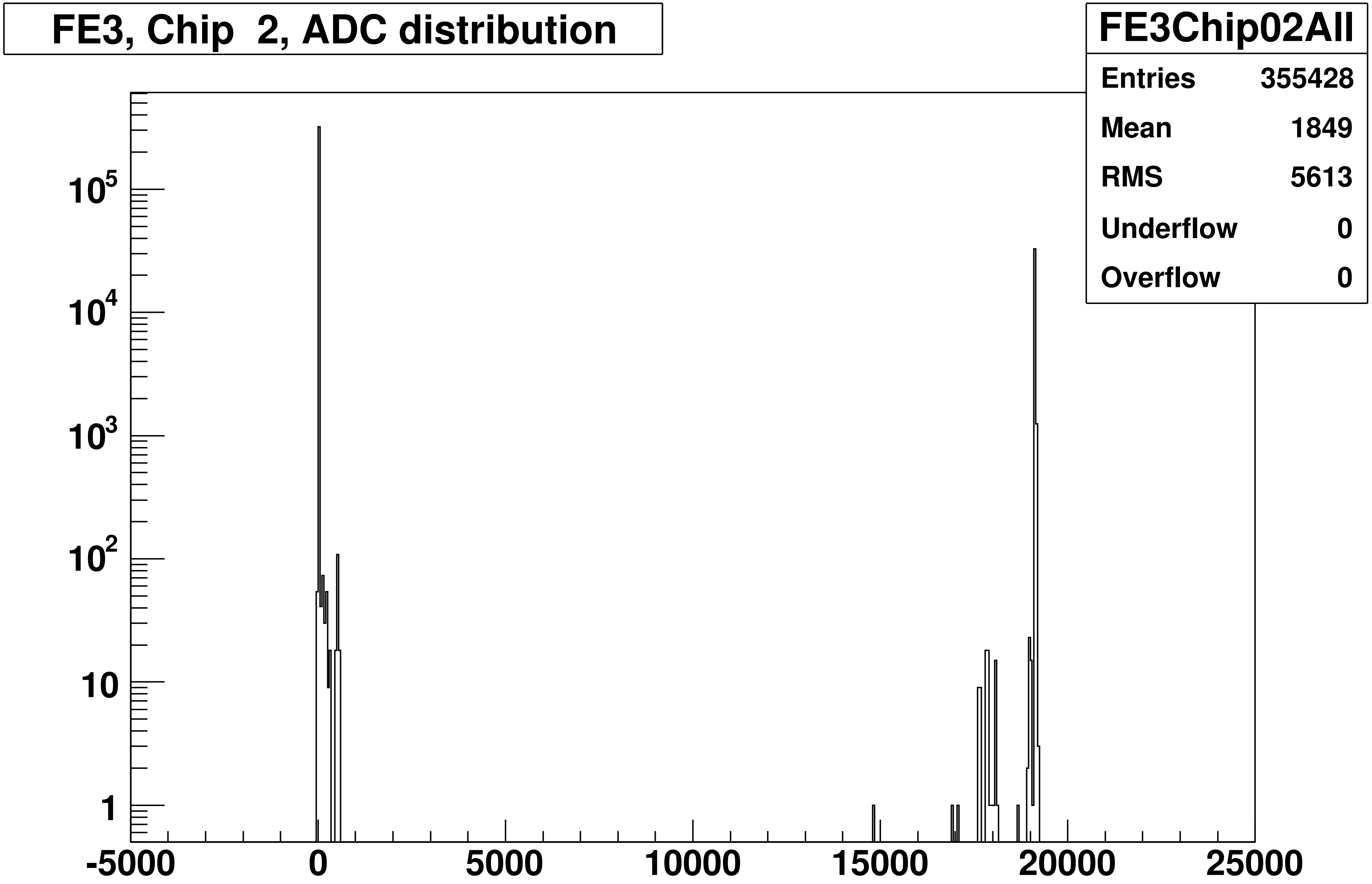
150
200



FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 355428
Mean 1.91e+04
RMS 17.89
Underflow 3.213e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 355428
Mean 1849
RMS 5613
Underflow 0
Overflow 0
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10^4
10^3
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10
1
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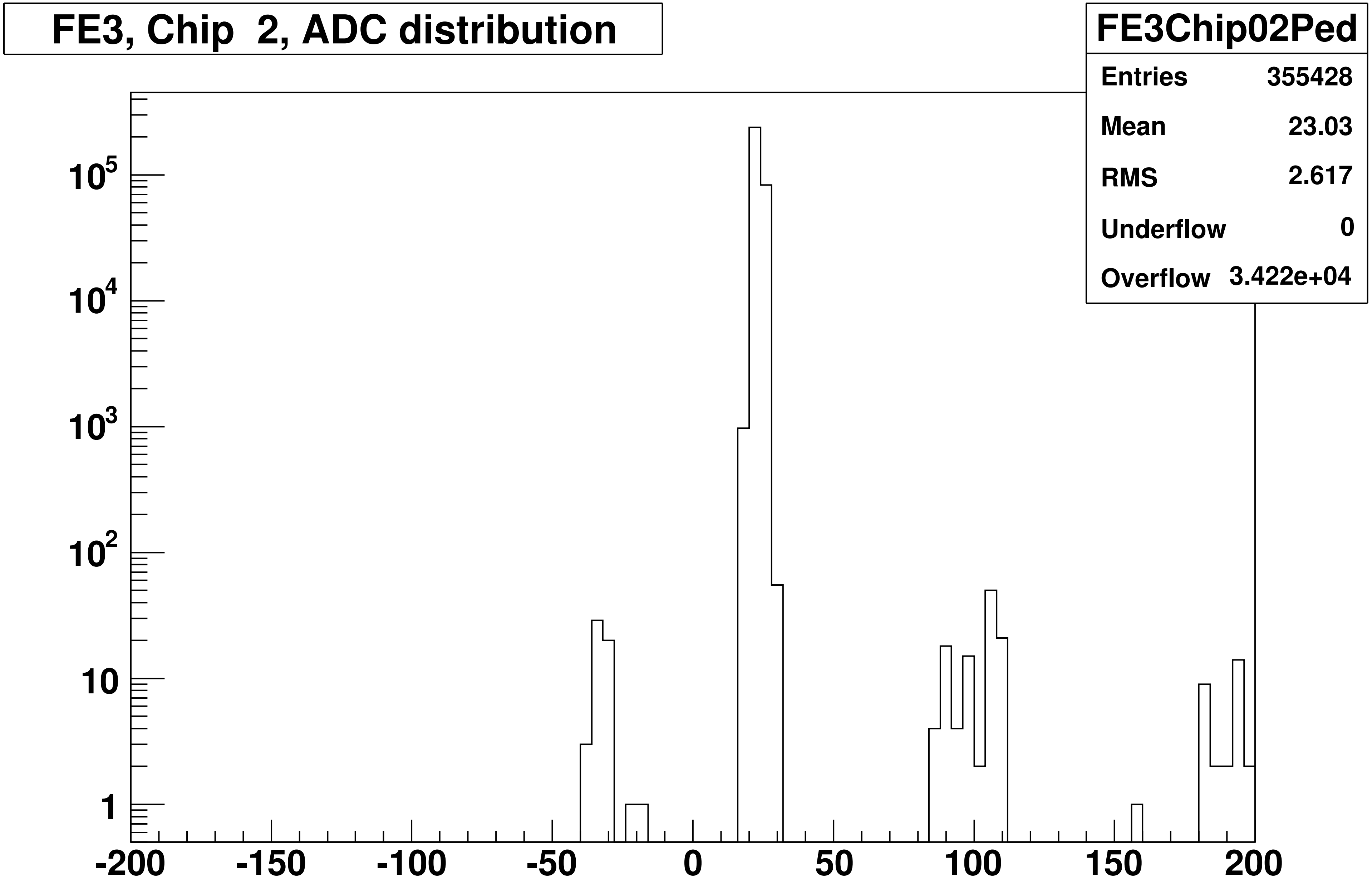
25000


FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 355428
Mean 23.03
RMS 2.617
Underflow 0
Overflow 3.422e+04
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10^3
10^2
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-100
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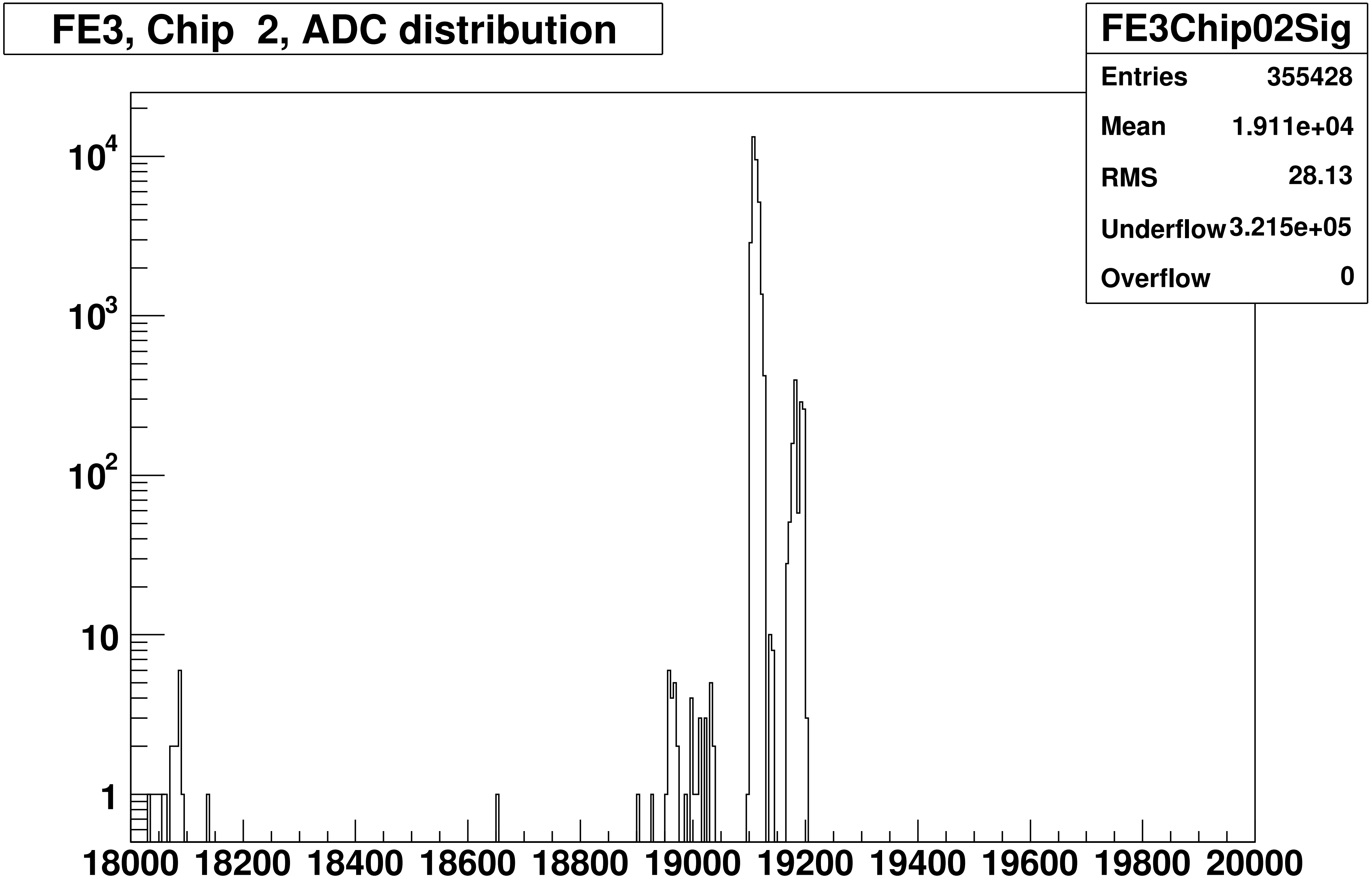
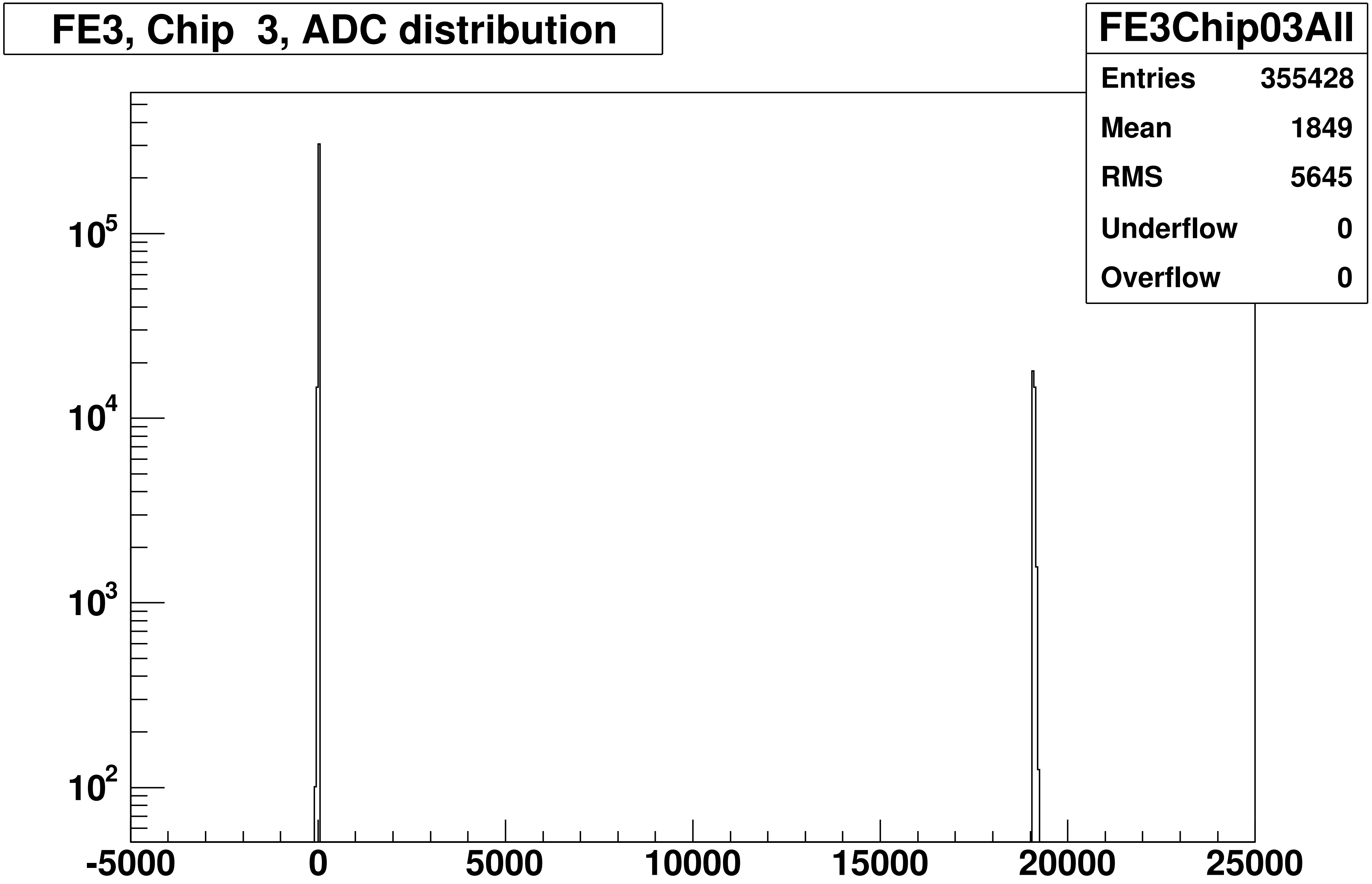
150
200


FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 355428
Mean 1.911e+04
RMS 28.13
Underflow 3.215e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 355428
Mean 1849
RMS 5645
Underflow 0
Overflow 0
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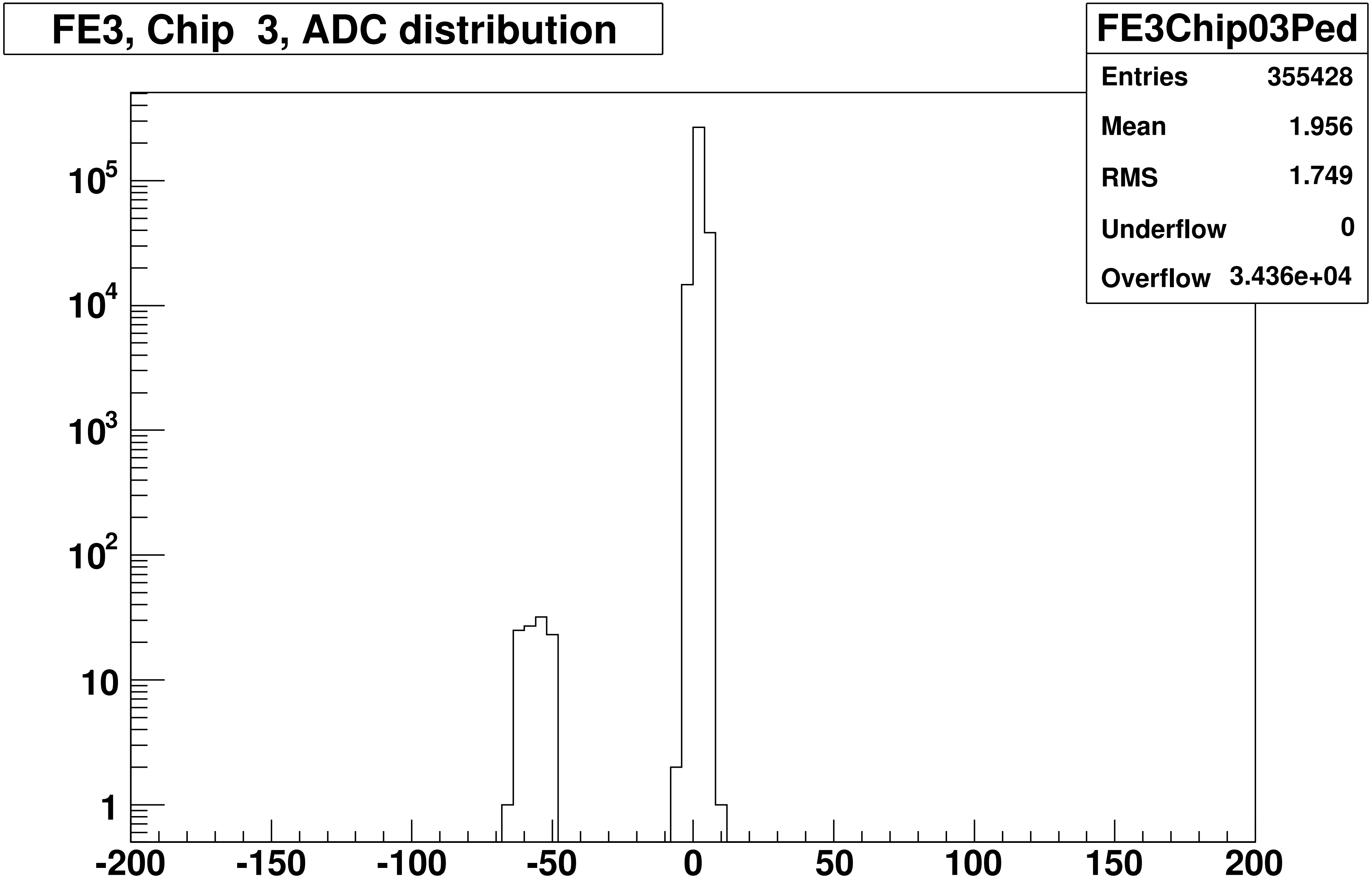
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 355428
Mean 1.956
RMS 1.749
Underflow 0
Overflow 3.436e+04
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10^4
10^3
10^2
10
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-100
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50
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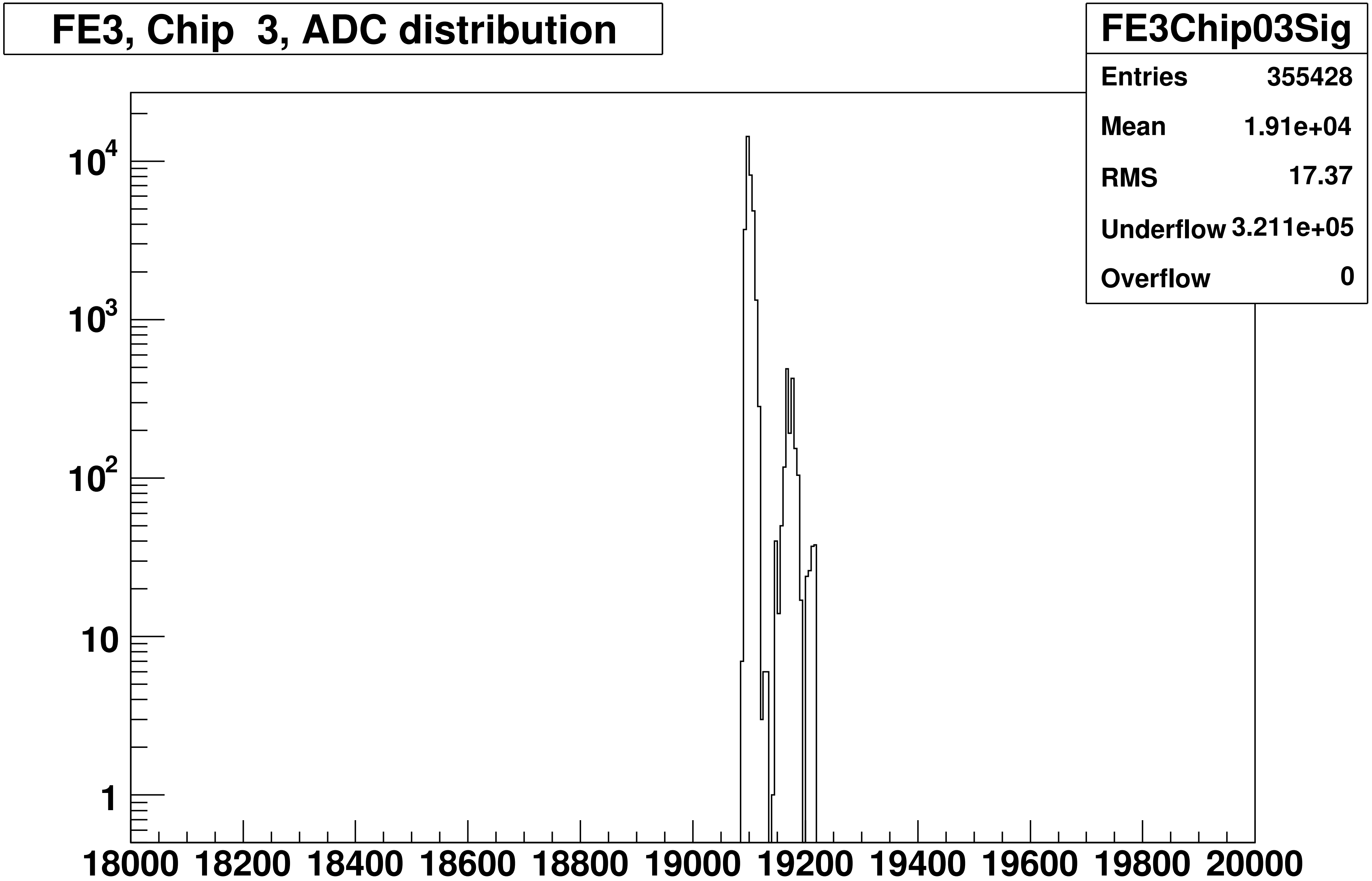
150
200


FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 355428
Mean 1.91e+04
RMS 17.37
Underflow 3.211e+05
Overflow 0
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10^3
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18000
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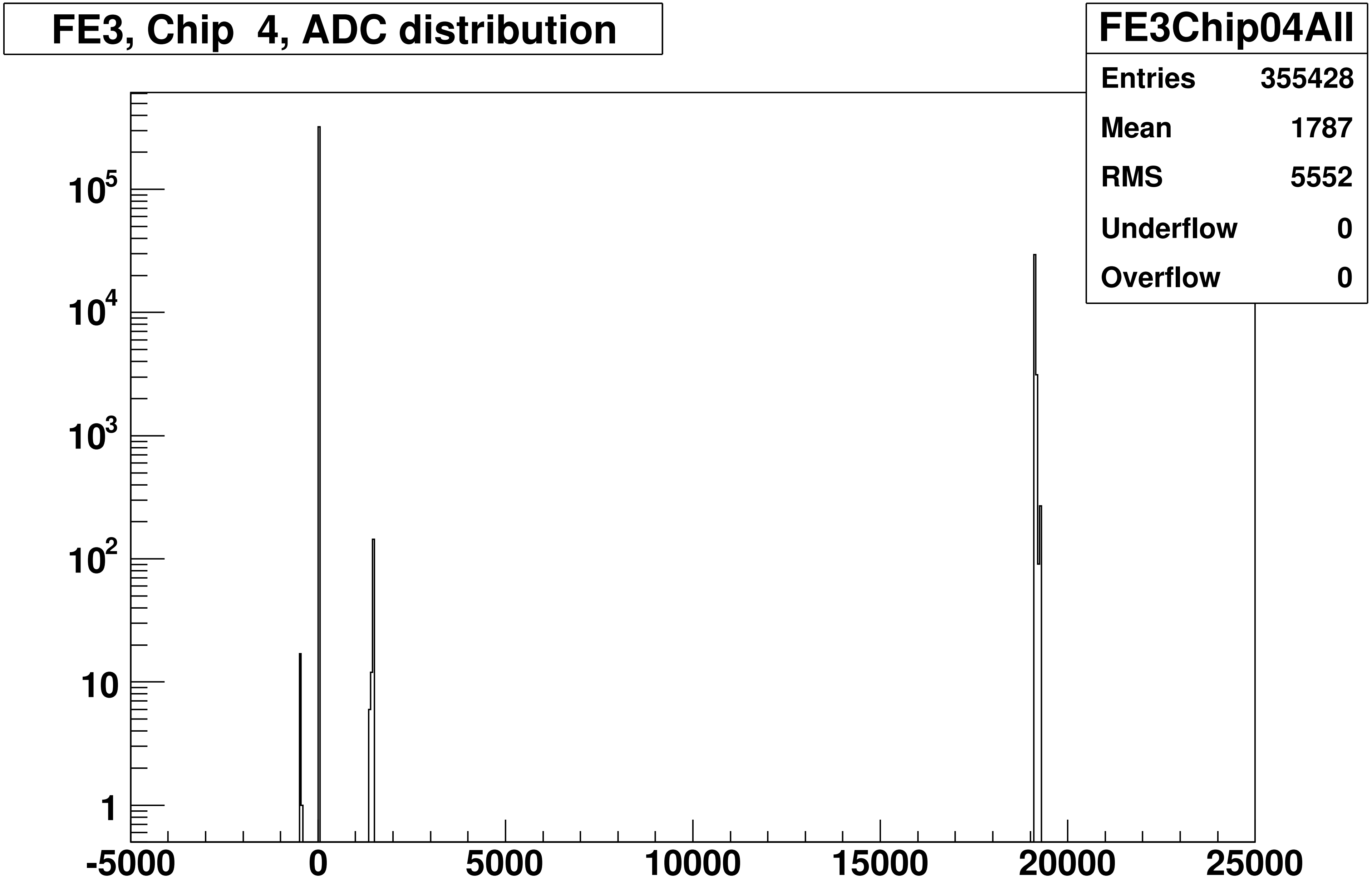
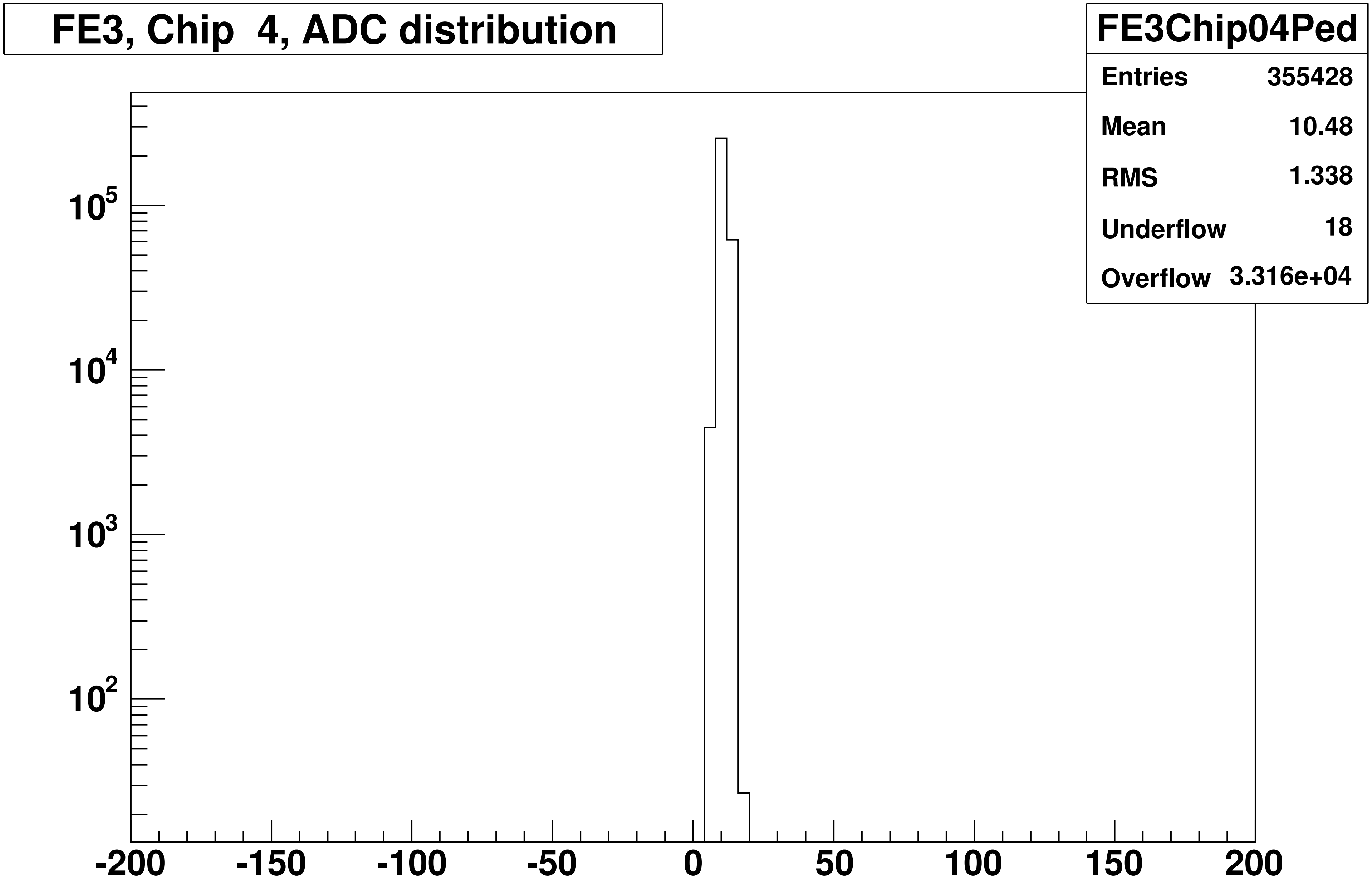
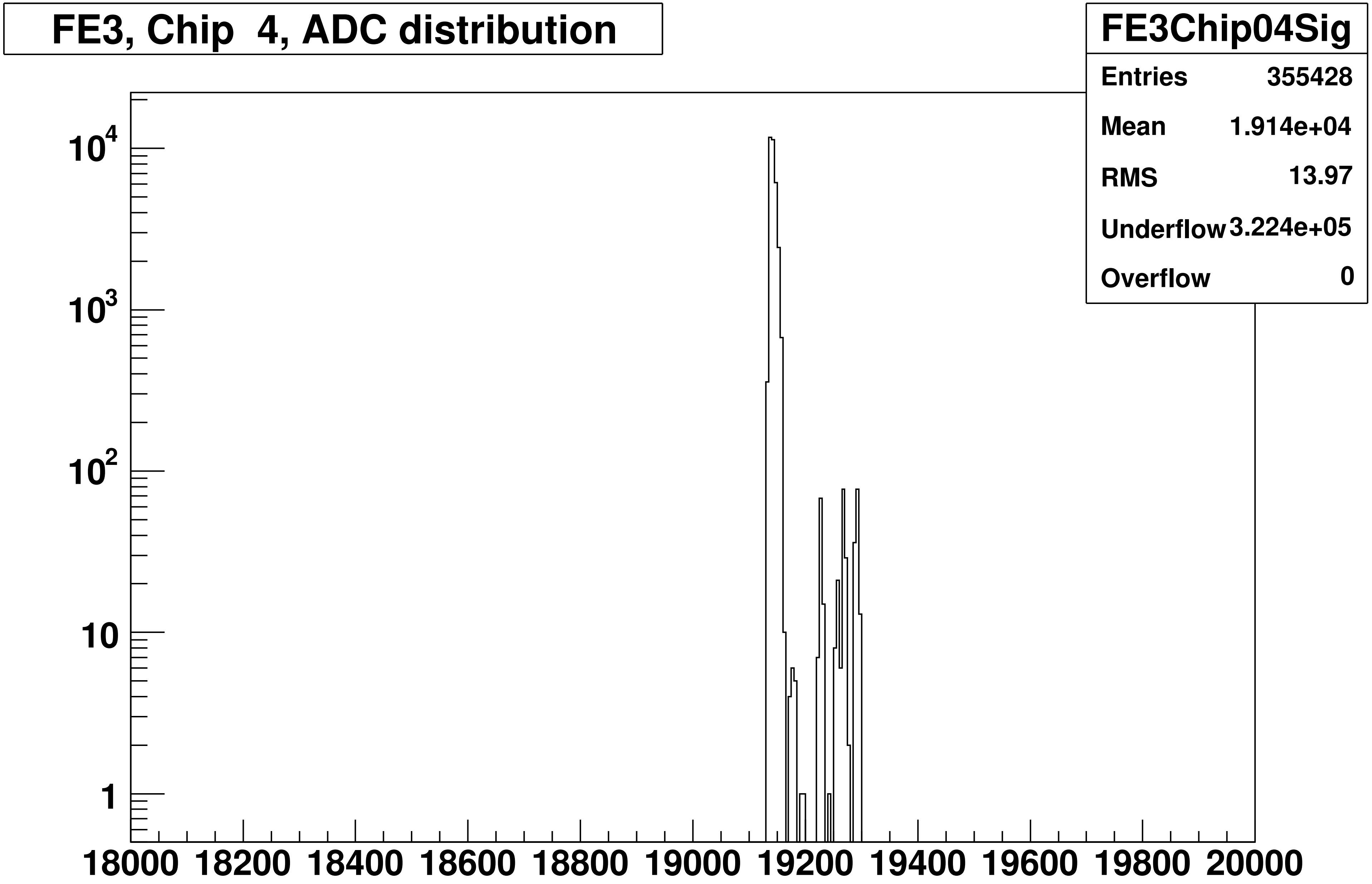
19800
20000

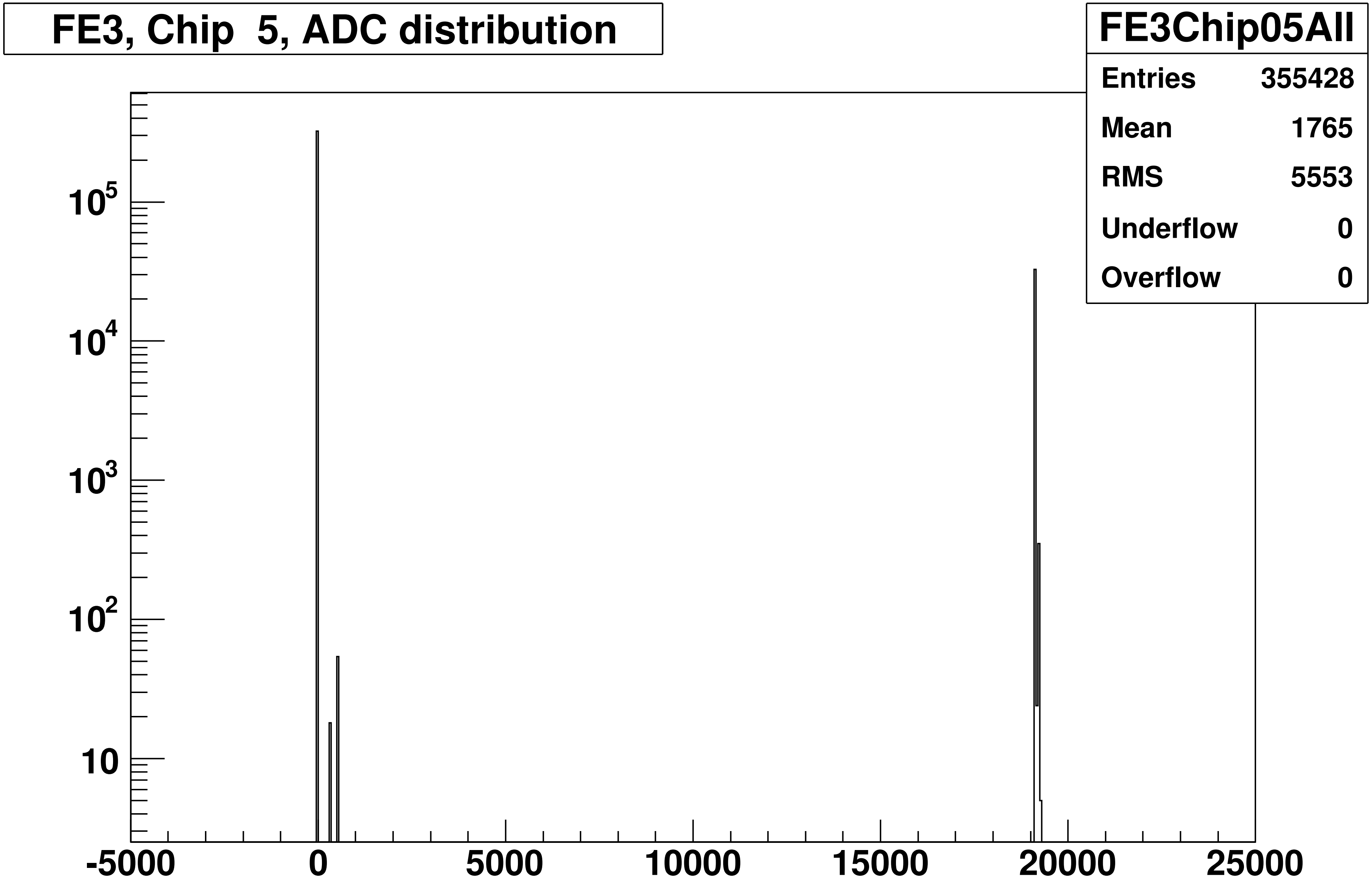


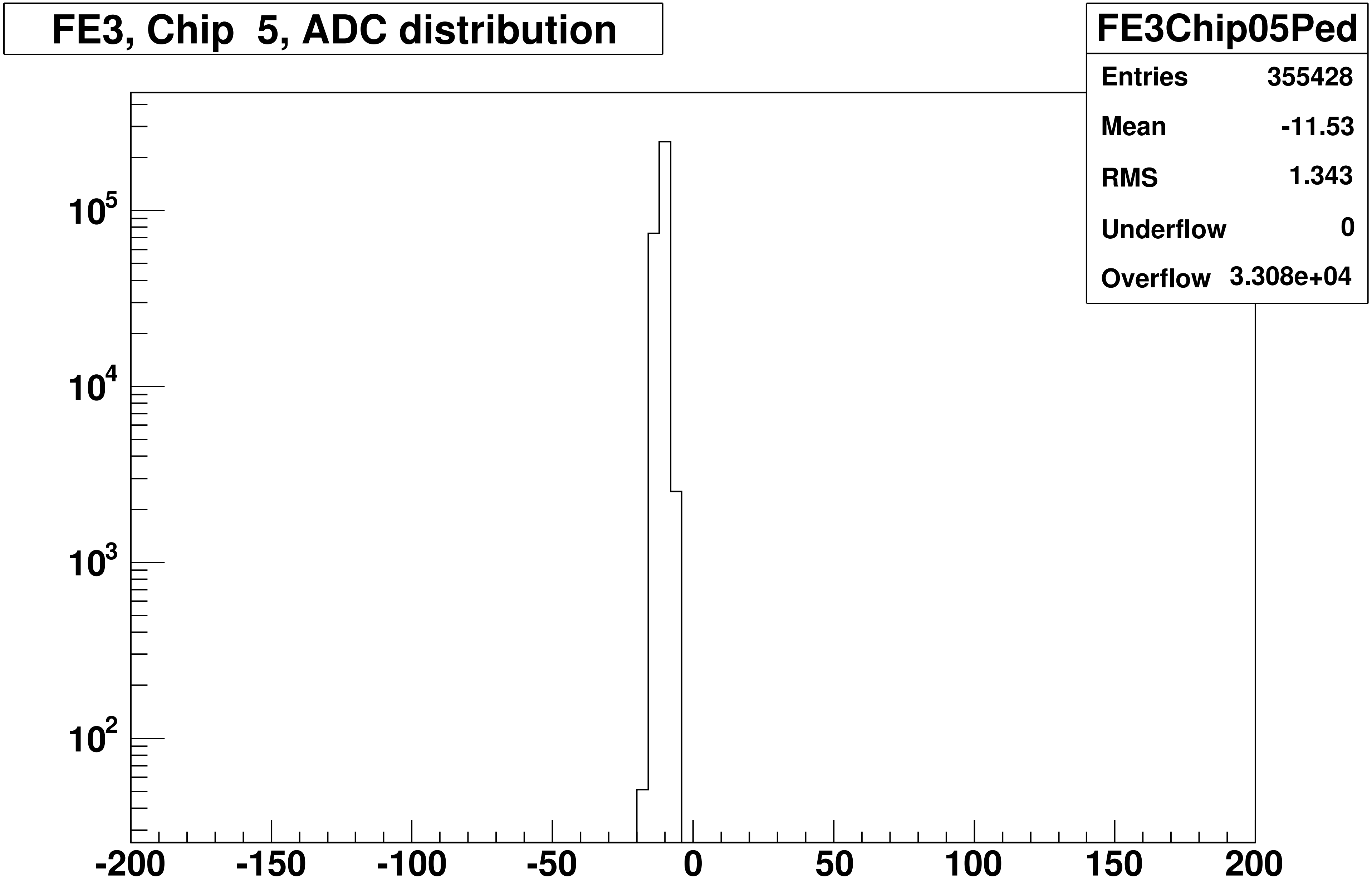
FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 355428
Mean 1787
RMS 5552
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 355428
Mean 10.48
RMS 1.338
Underflow 18
Overflow 3.316e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

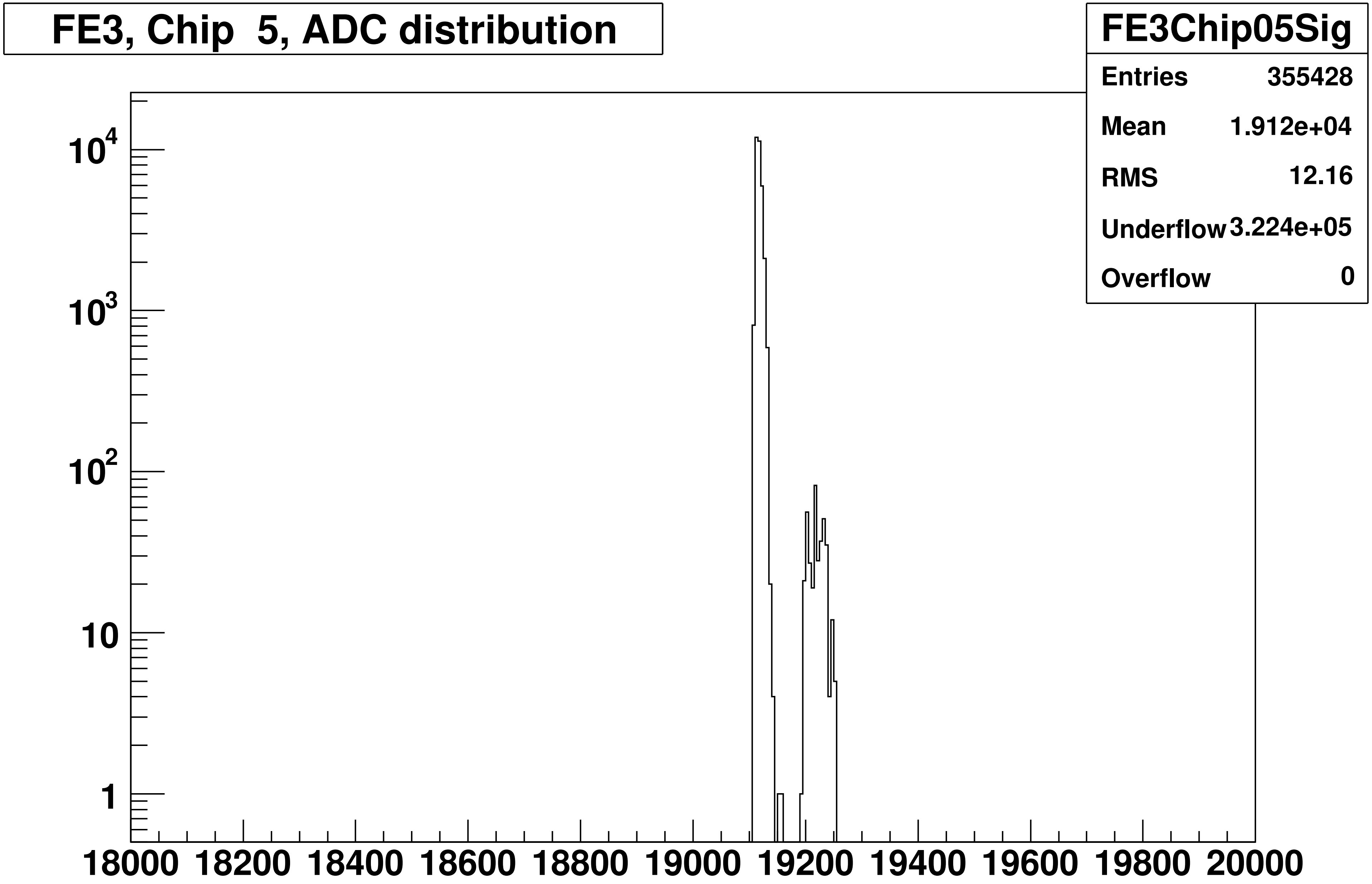


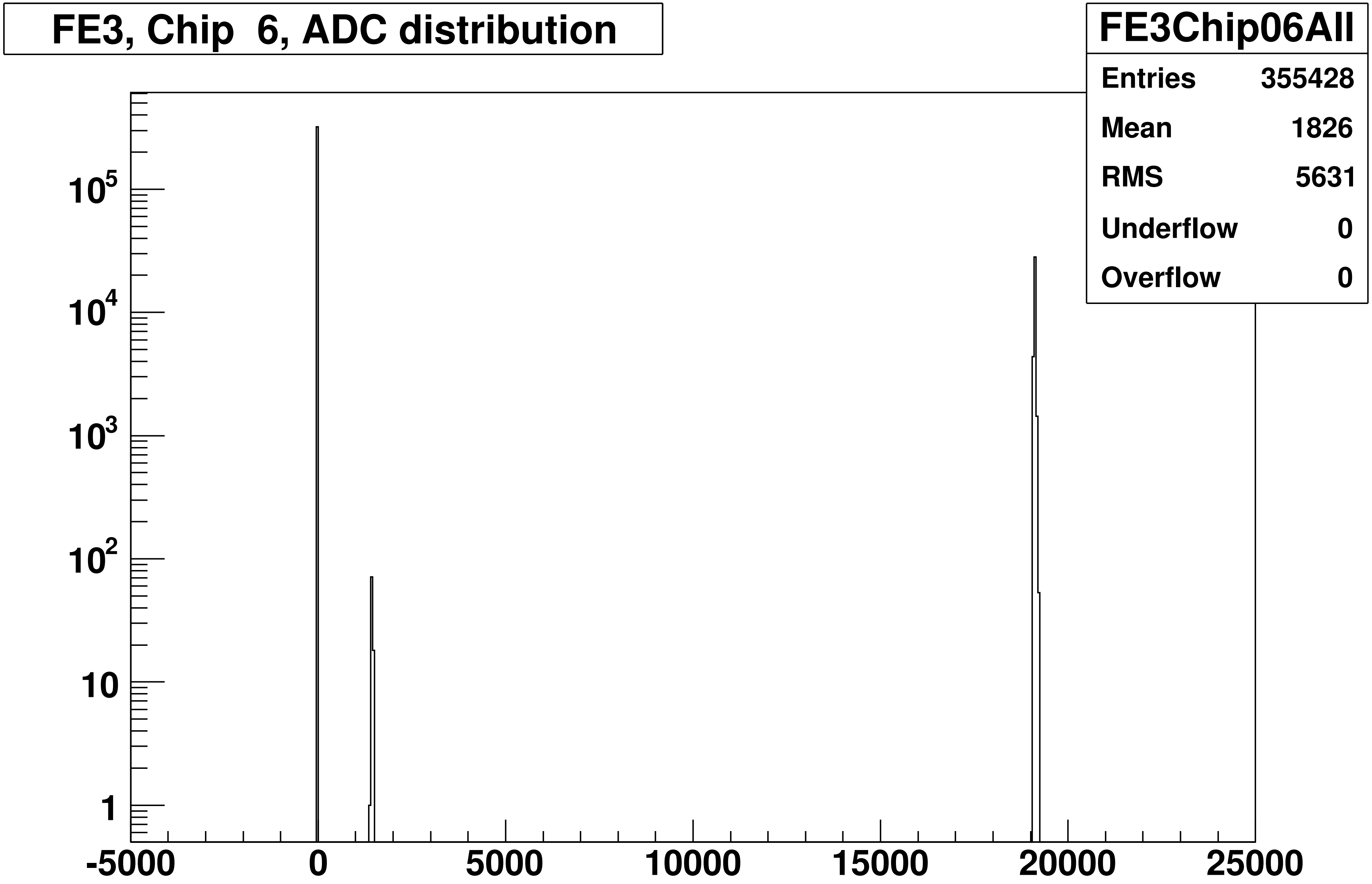
FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 355428
Mean 1.914e+04
RMS 13.97
Underflow 3.224e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

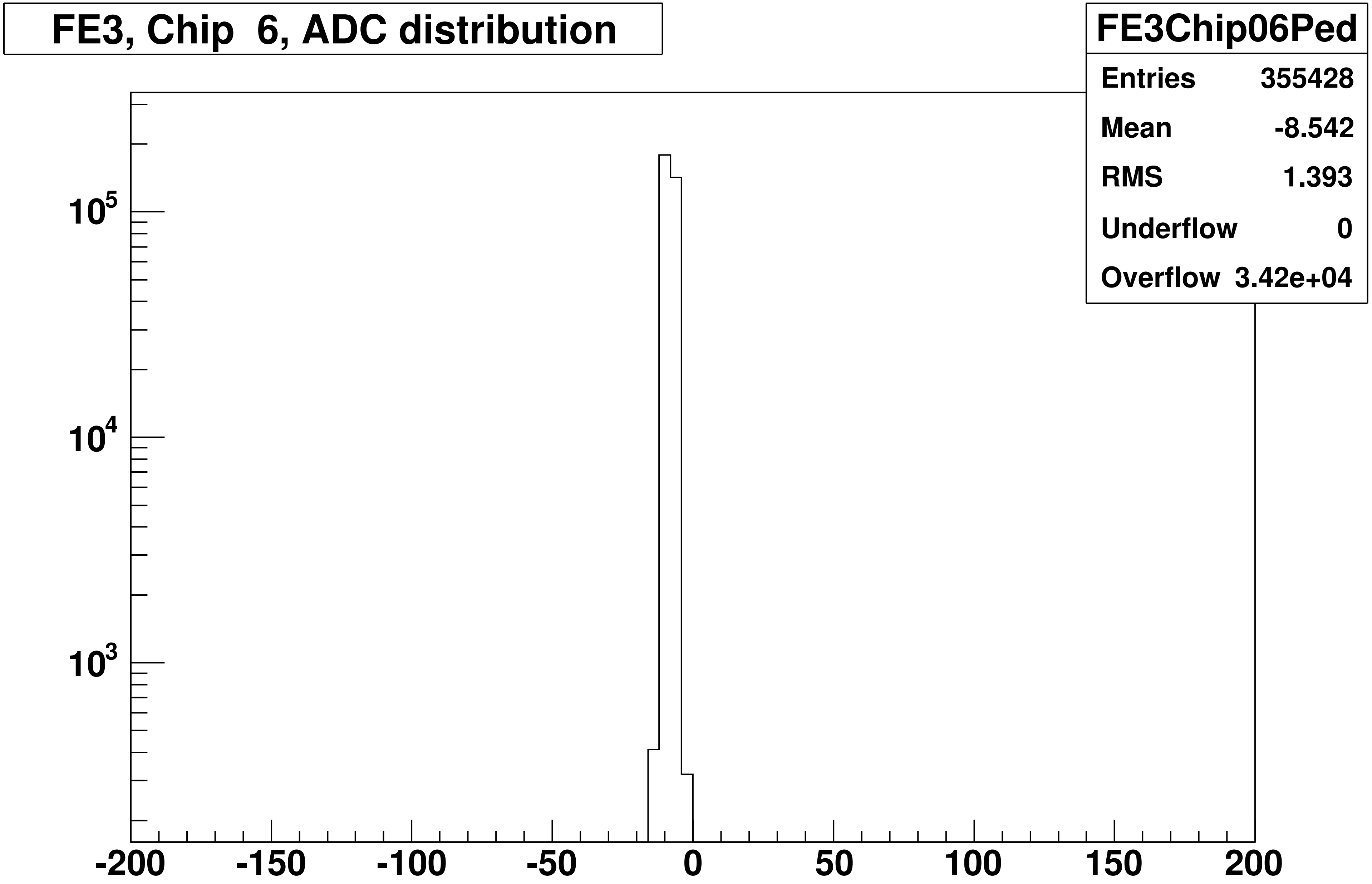

FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 355428
Mean 1765
RMS 5553
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

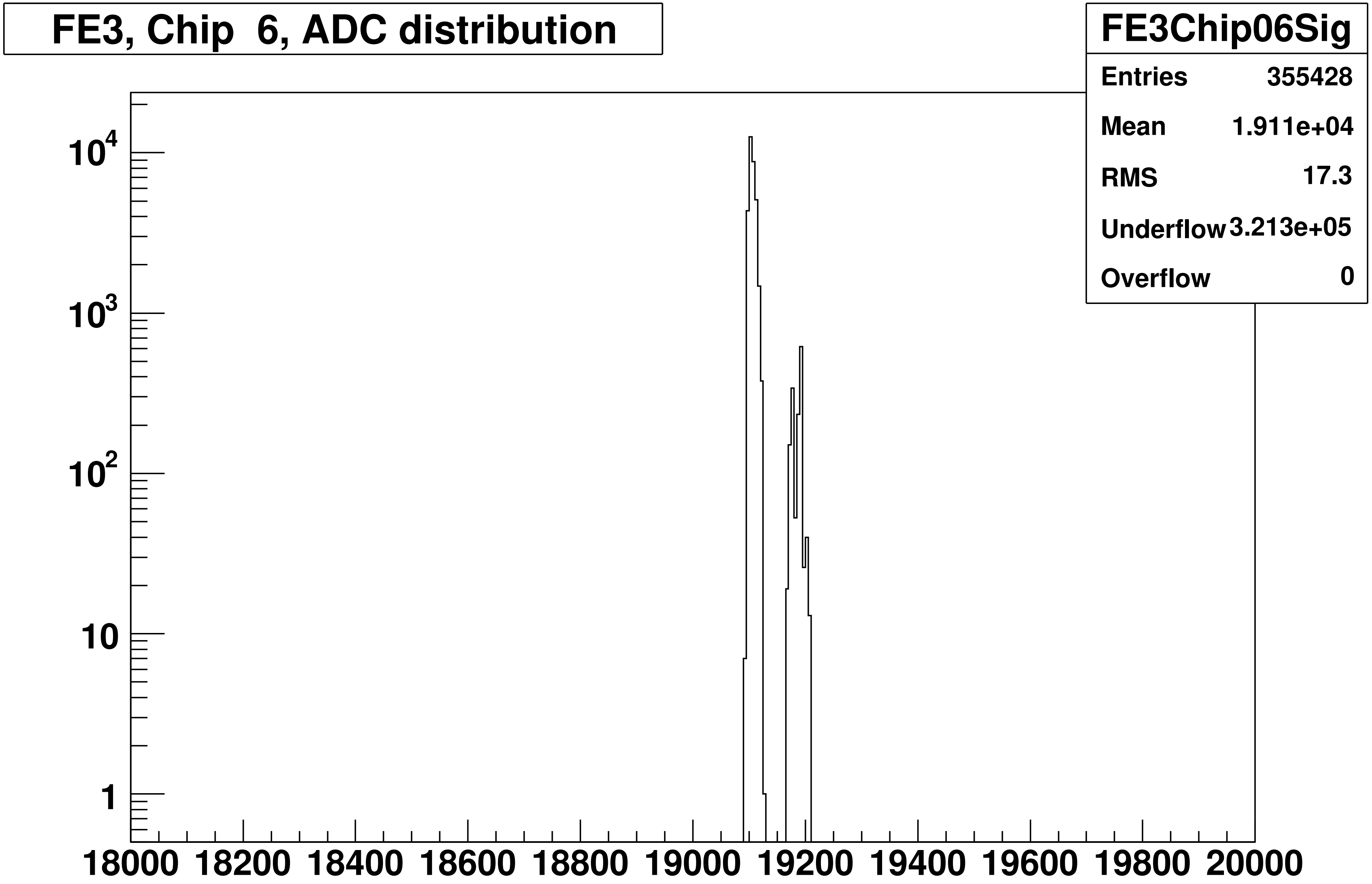


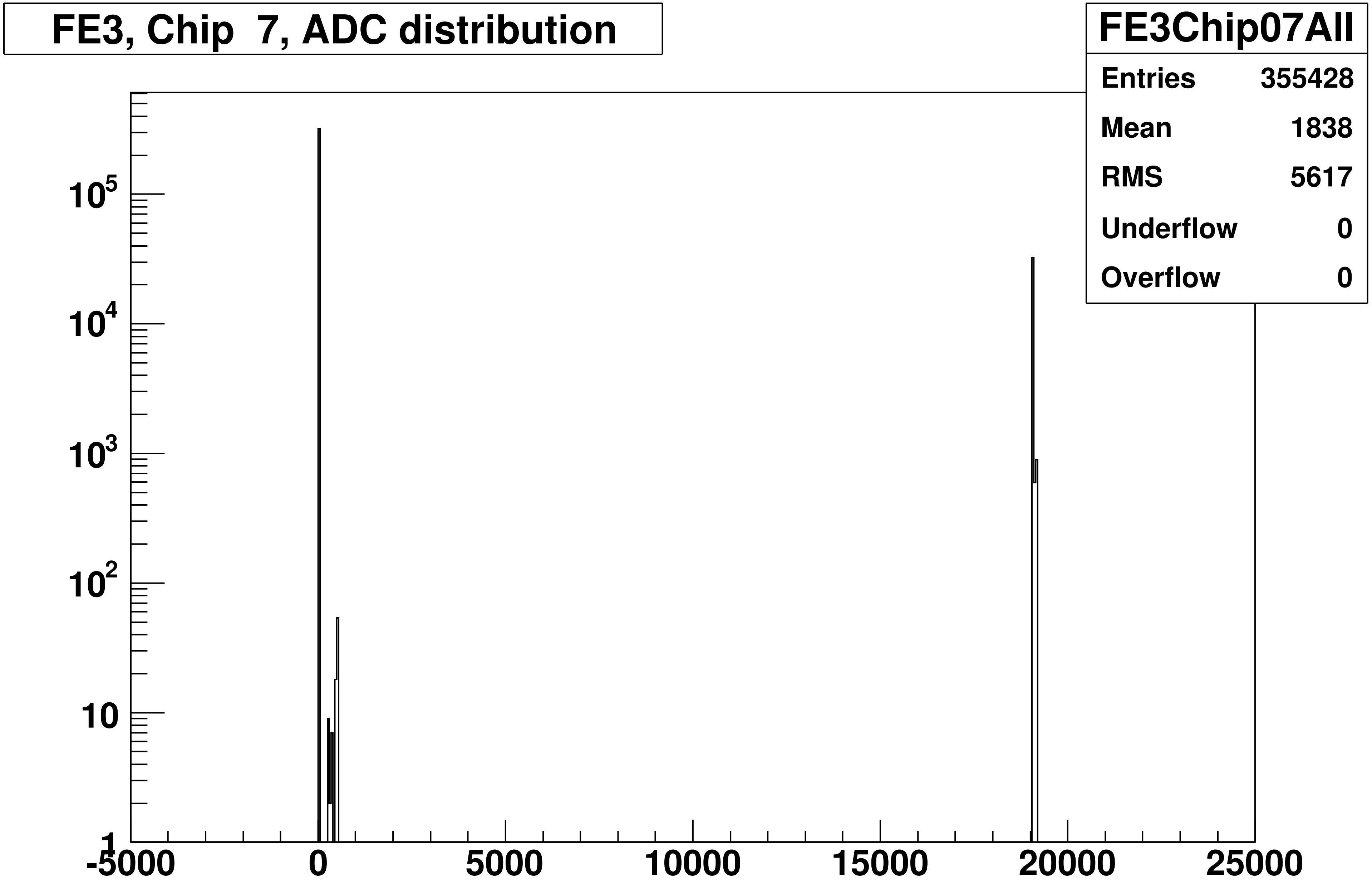
FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 355428
Mean -11.53
RMS 1.343
Underflow 0
Overflow 3.308e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 355428
Mean 1.912e+04
RMS 12.16
Underflow 3.224e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

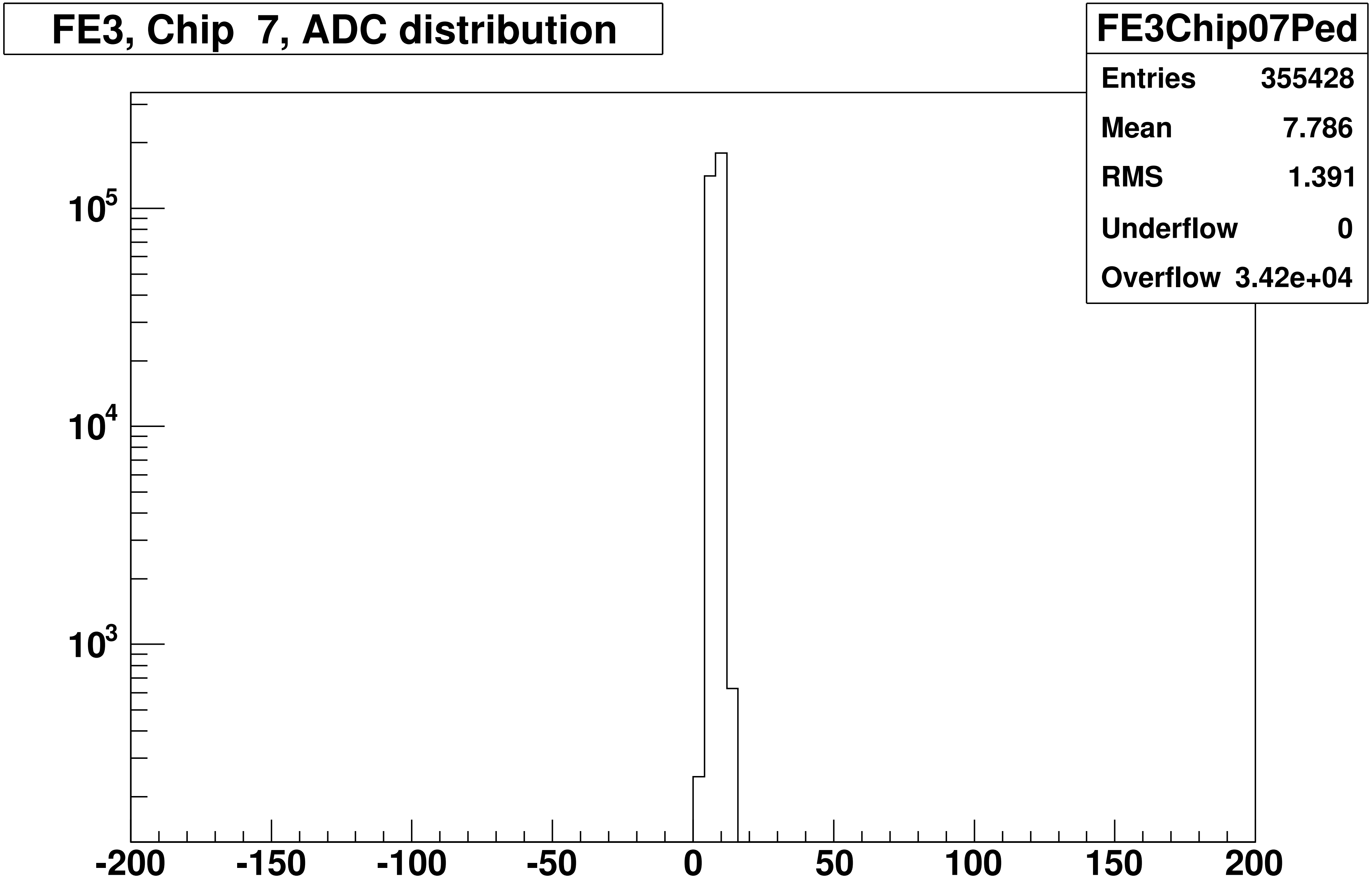

FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 355428
Mean 1826
RMS 5631
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

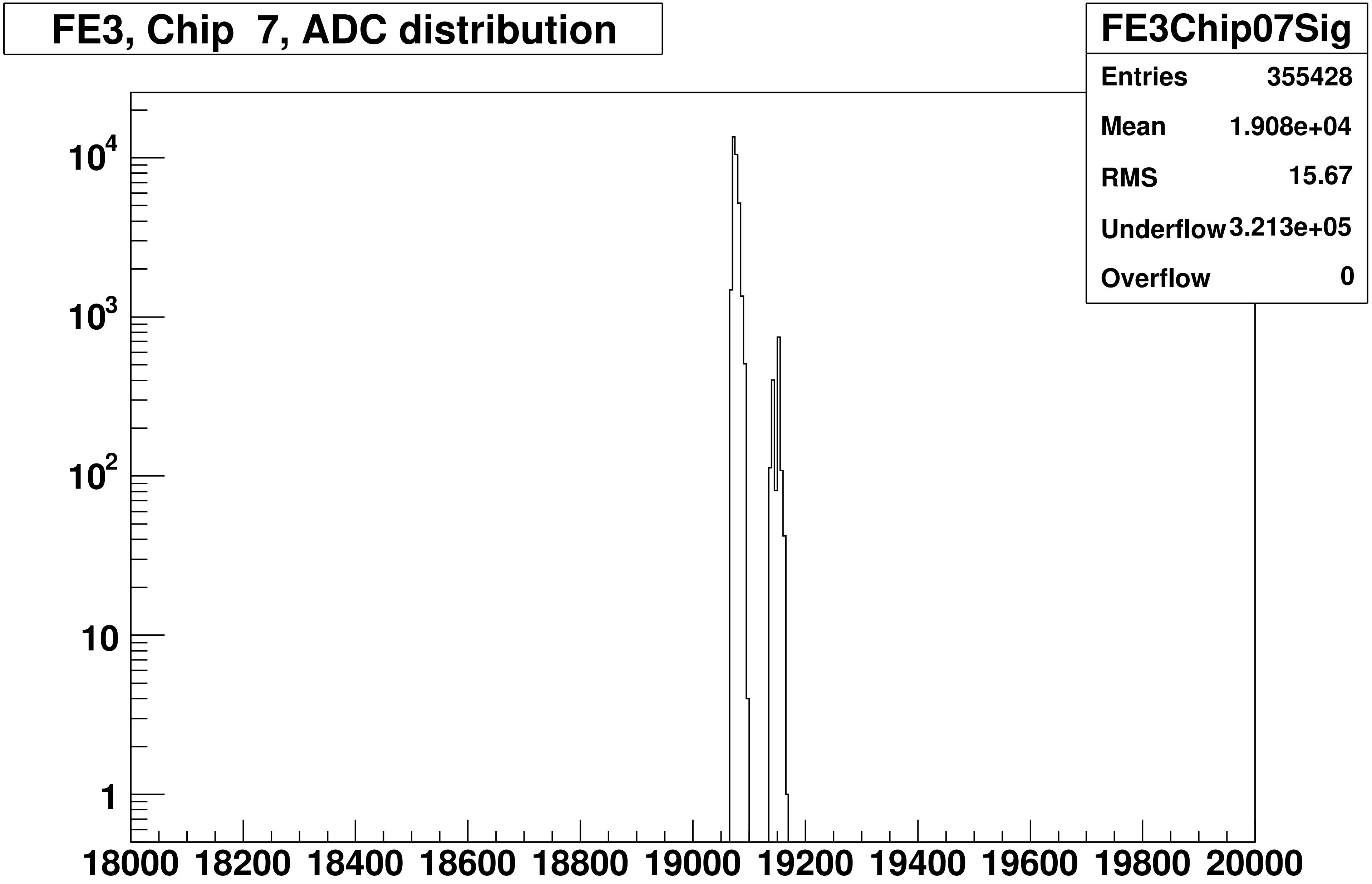

FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 355428
Mean -8.542
RMS 1.393
Underflow 0
Overflow 3.42e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 355428
Mean 1.911e+04
RMS 17.3
Underflow 3.213e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 355428
Mean 1838
RMS 5617
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 355428
Mean 7.786
RMS 1.391
Underflow 0
Overflow 3.42e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 355428
Mean 1.908e+04
RMS 15.67
Underflow 3.213e+05
Overflow 0
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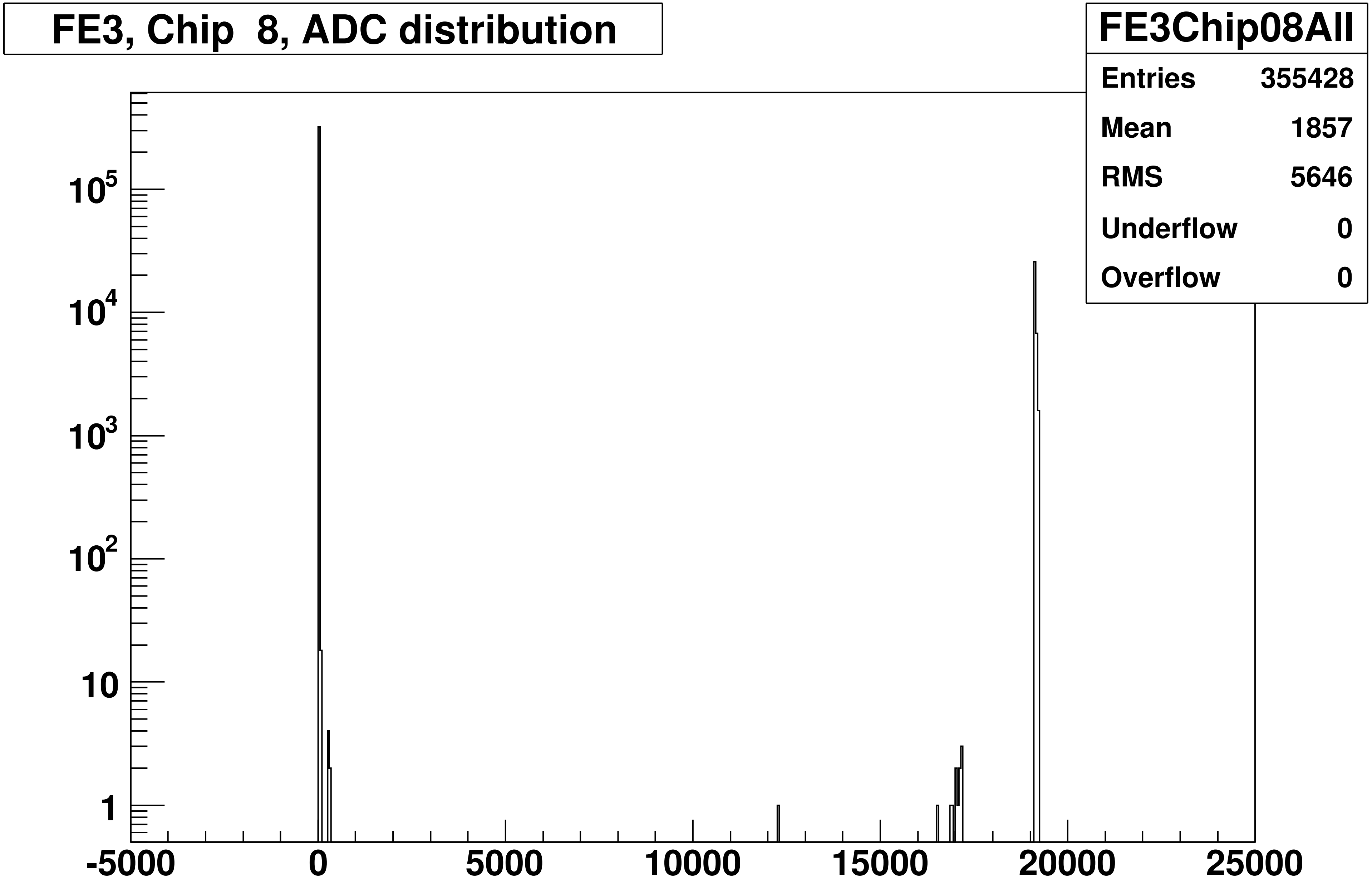
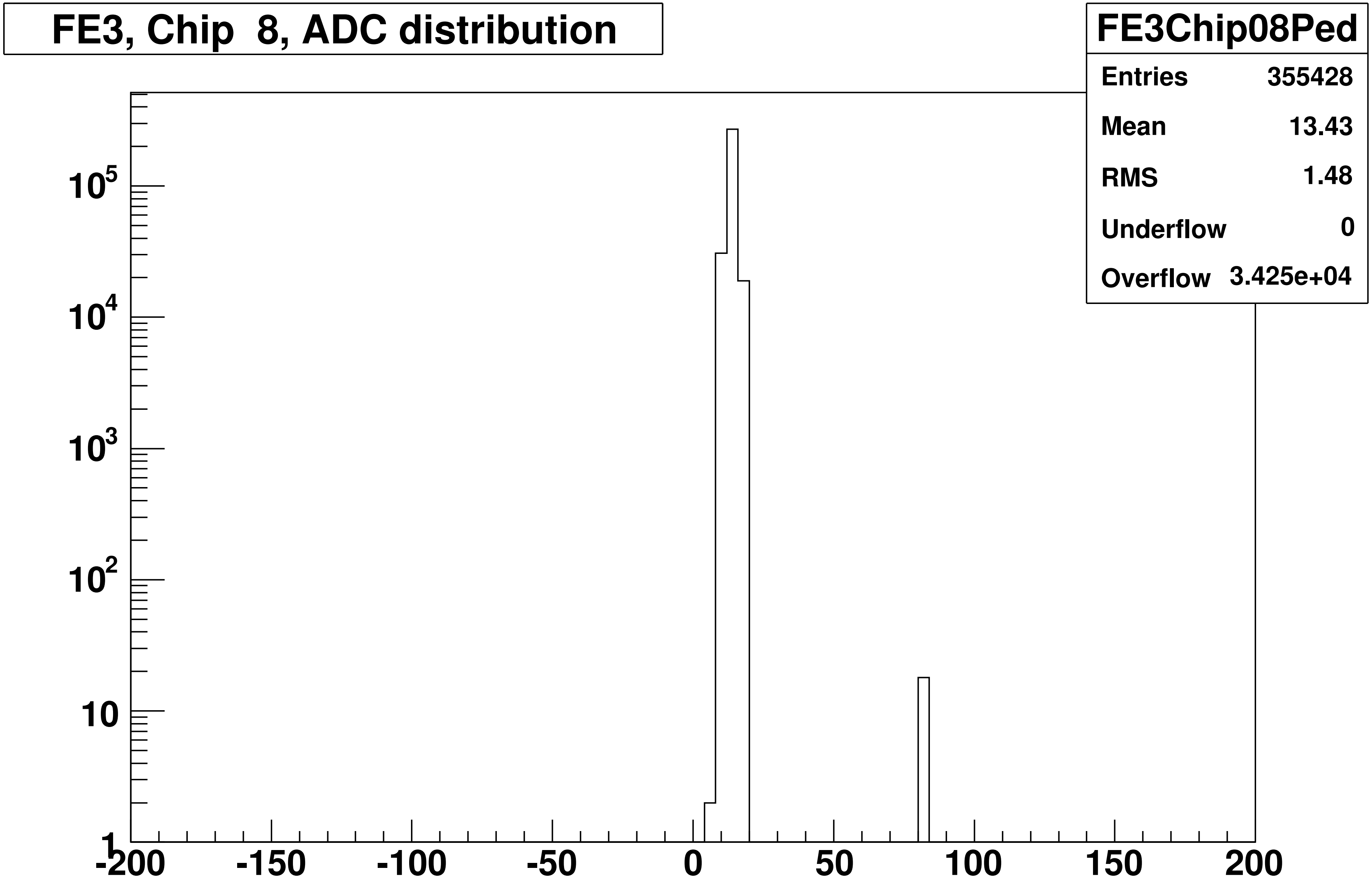
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 355428
Mean 1857
RMS 5646
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 355428
Mean 13.43
RMS 1.48
Underflow 0
Overflow 3.425e+04
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10^4
10^3
10^2
10
1
-200
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-100
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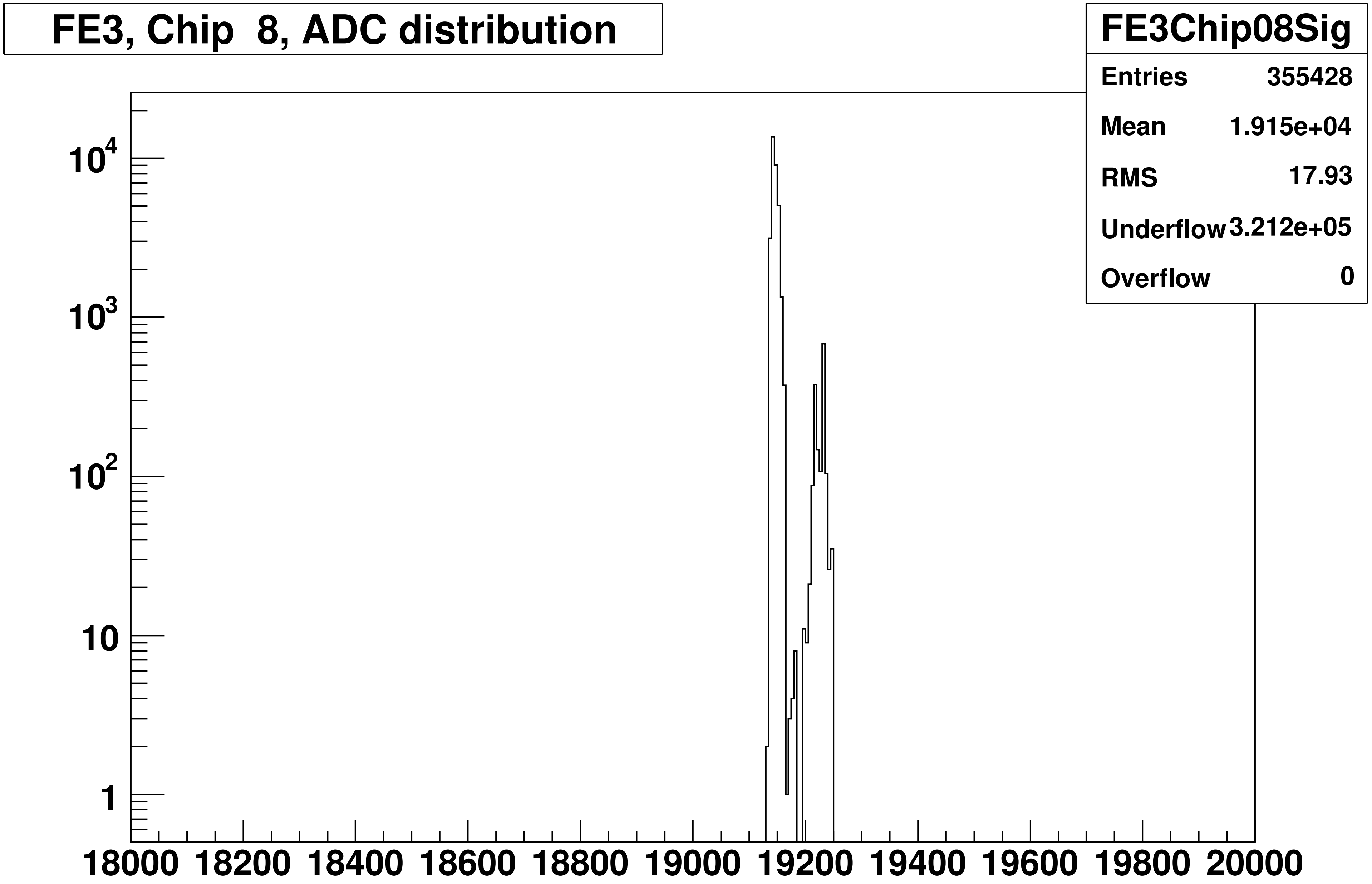
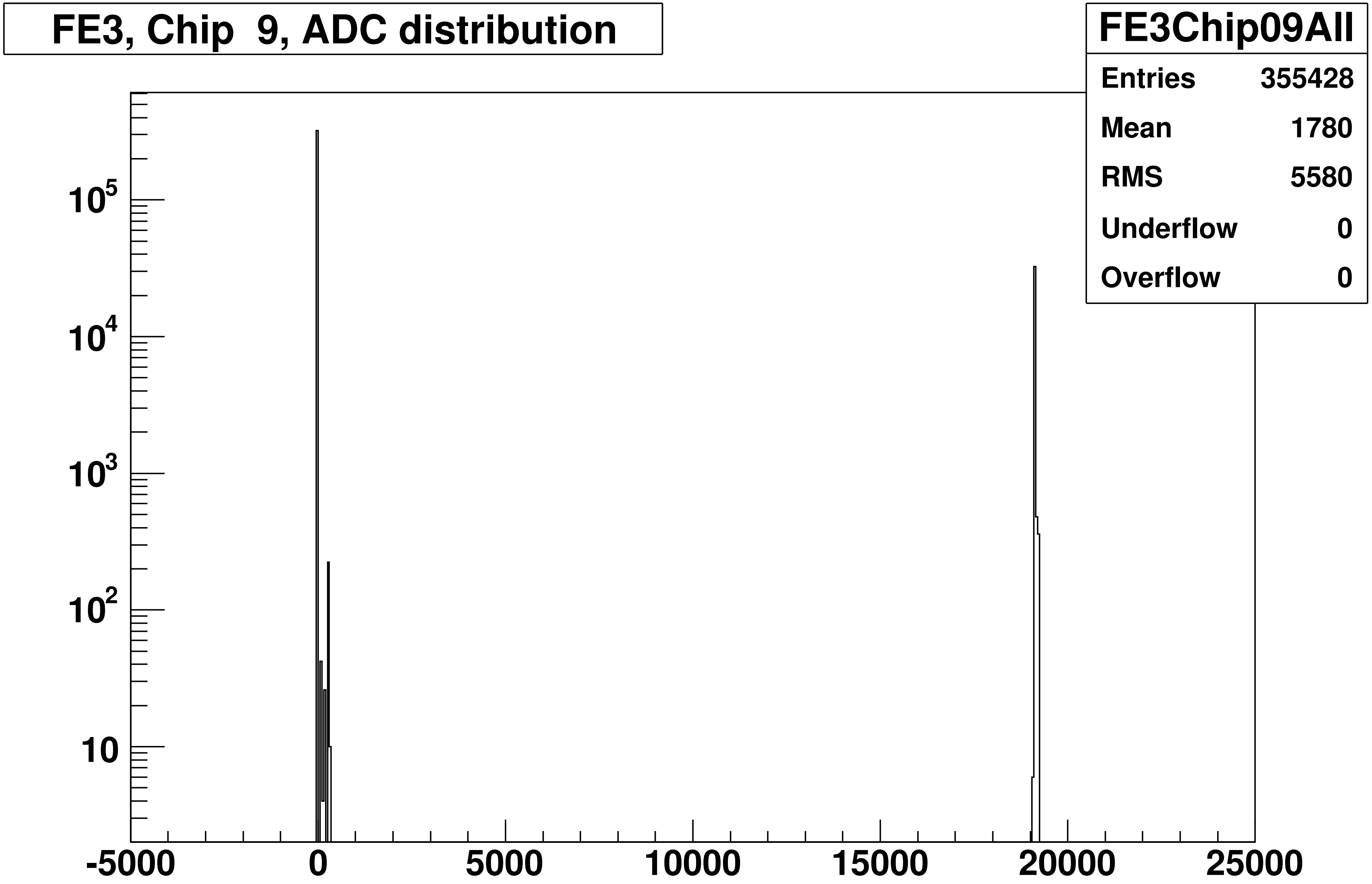
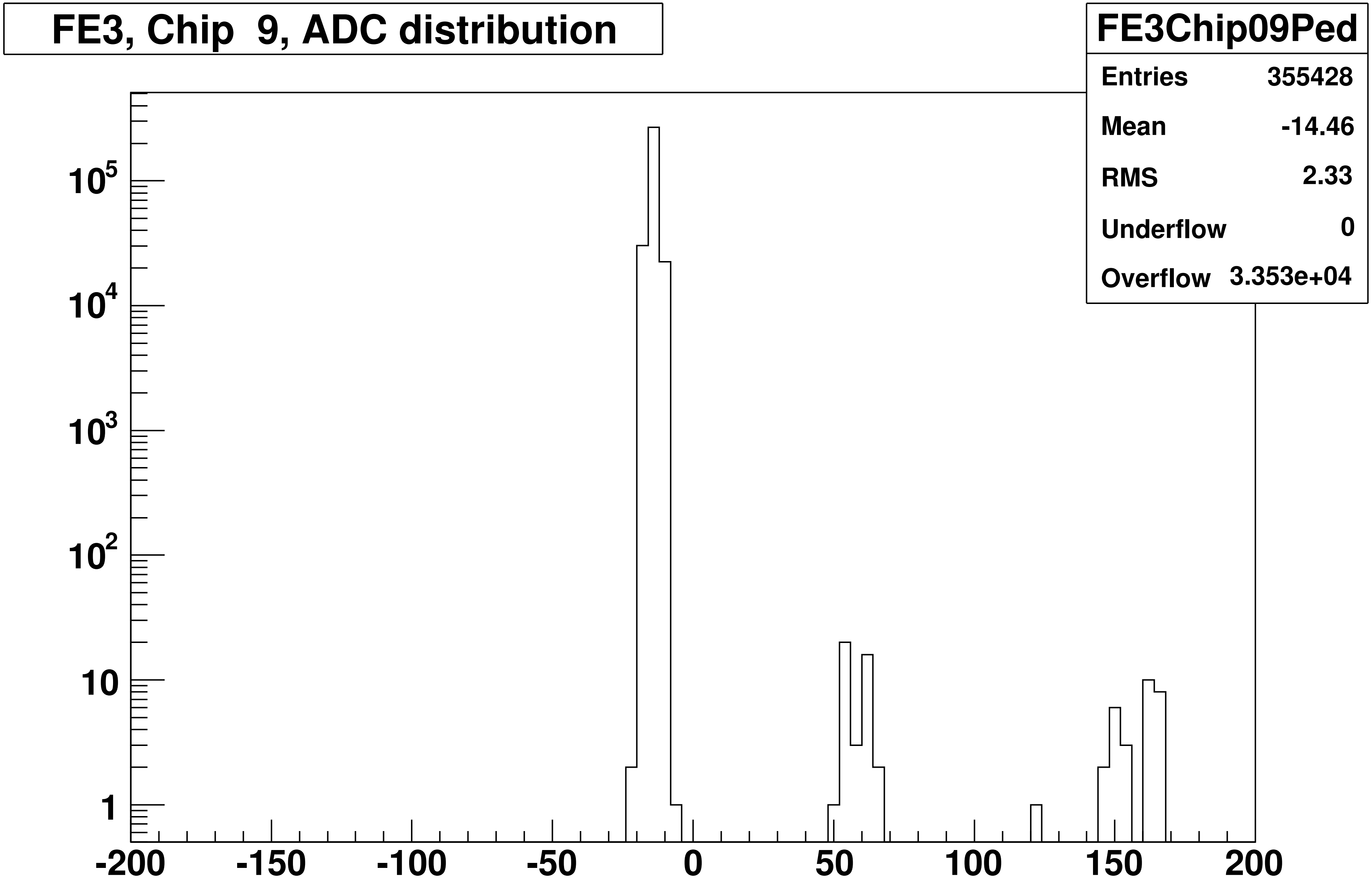
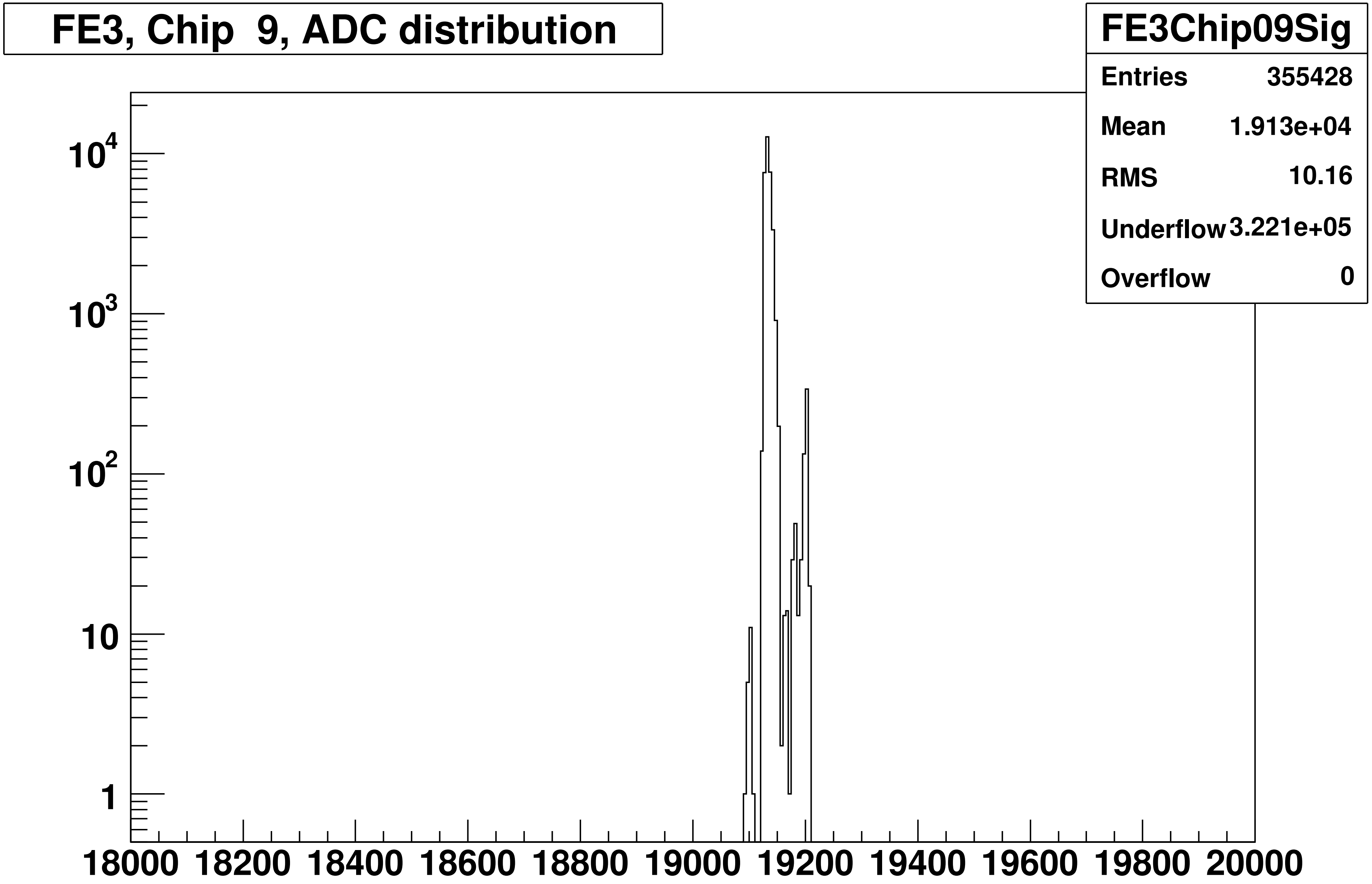
0
50
100
150
200


FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 355428
Mean 1.915e+04
RMS 17.93
Underflow 3.212e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

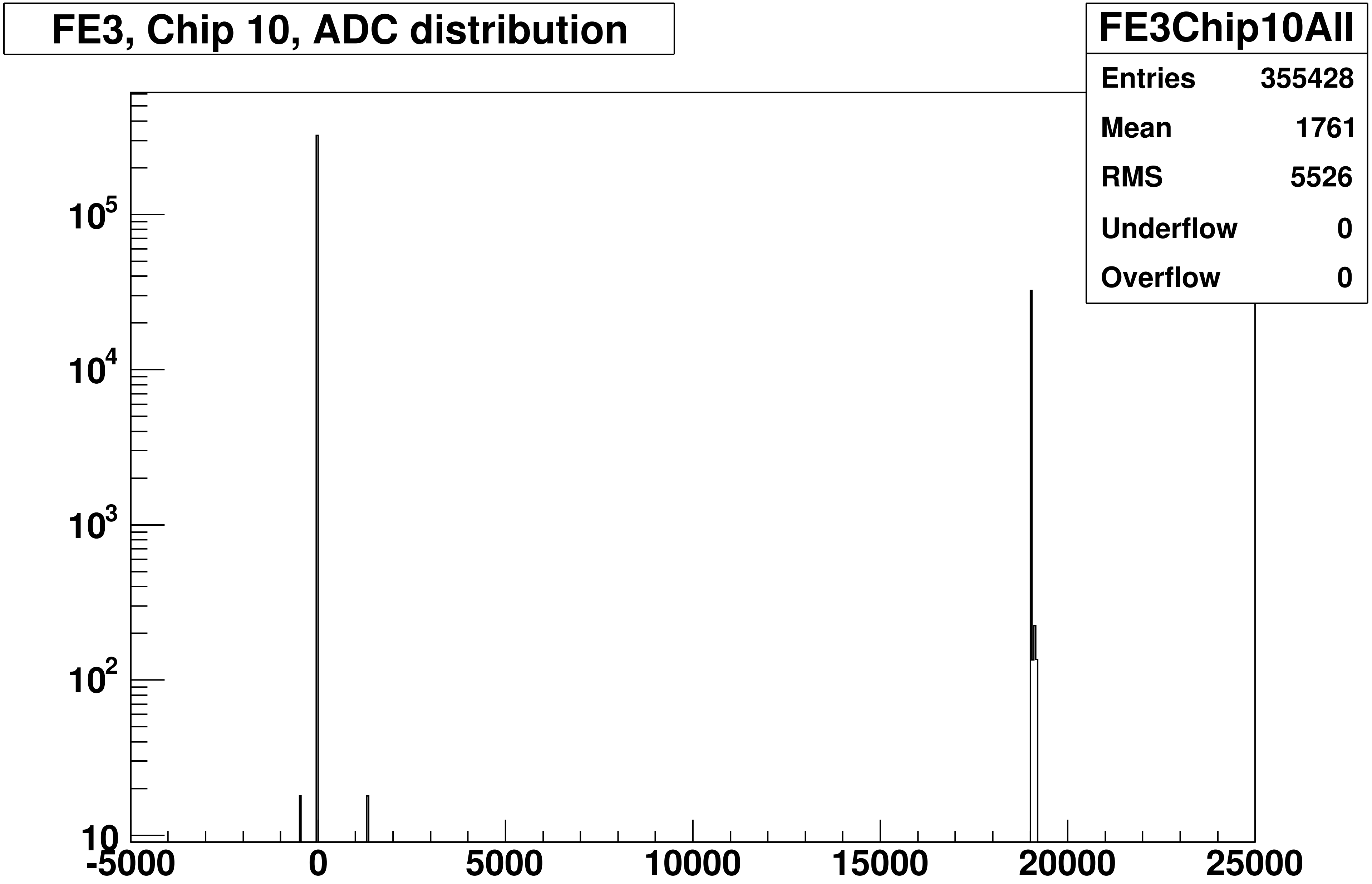

FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 355428
Mean 1780
RMS 5580
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



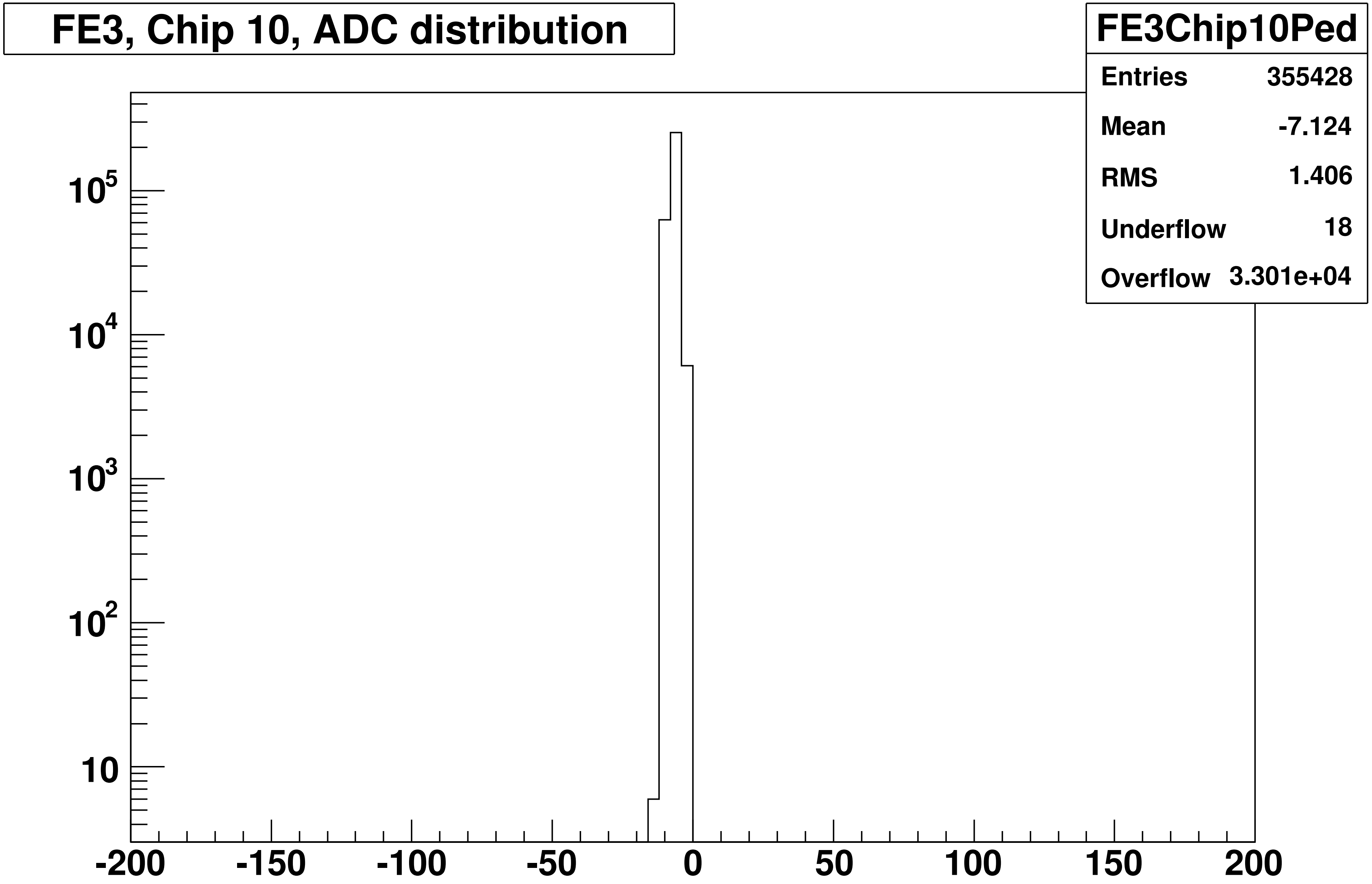
FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 355428
Mean -14.46
RMS 2.33
Underflow 0
Overflow 3.353e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 355428
Mean 1.913e+04
RMS 10.16
Underflow 3.221e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 355428
Mean 1761
RMS 5526
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 355428
Mean -7.124
RMS 1.406
Underflow 18
Overflow 3.301e+04
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10^4
10^3
10^2
10
-200
-150
-100
-50
0
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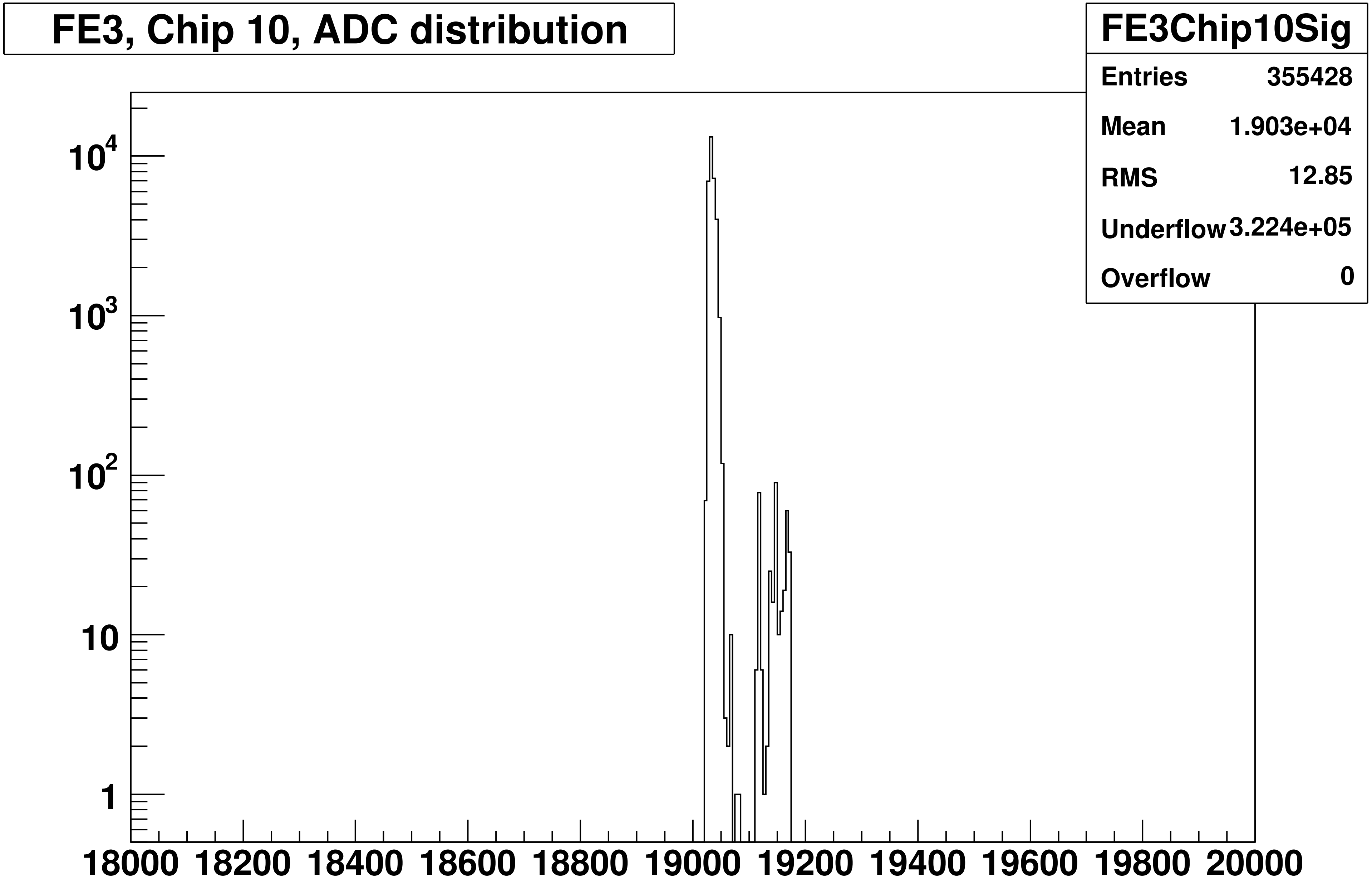
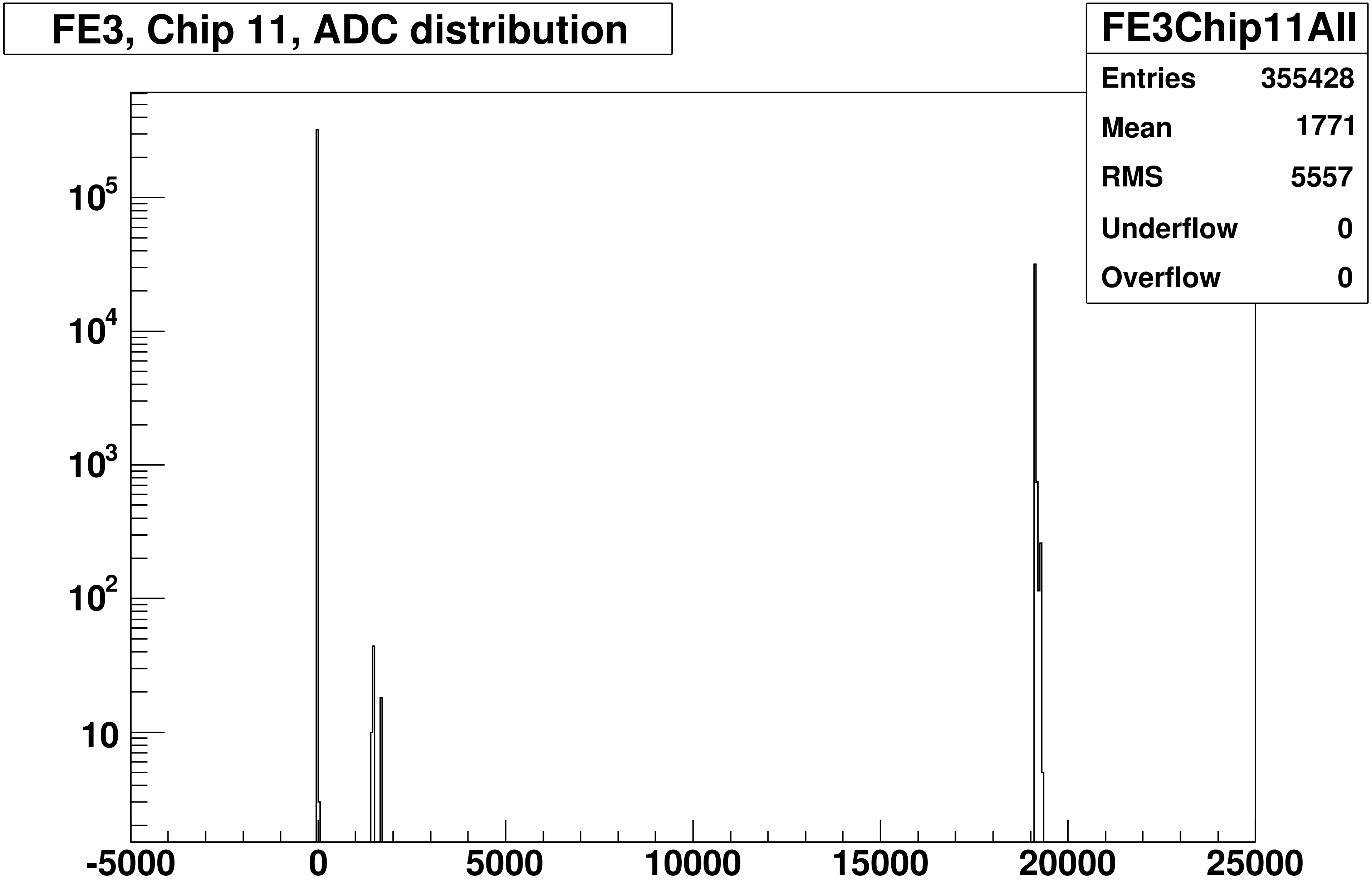
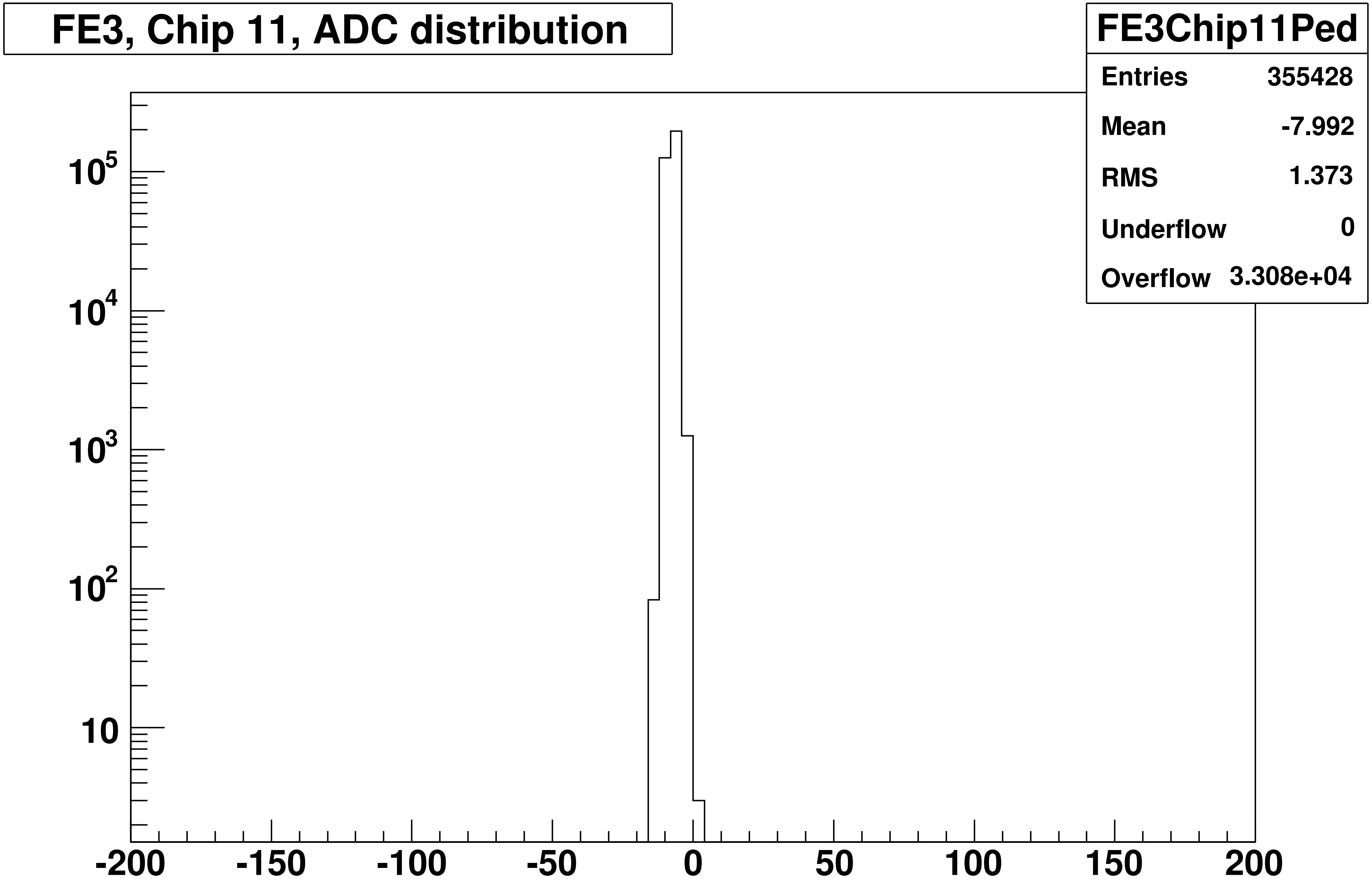
100
150
200


FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 355428
Mean 1.903e+04
RMS 12.85
Underflow 3.224e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 355428
Mean 1771
RMS 5557
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 355428
Mean -7.992
RMS 1.373
Underflow 0
Overflow 3.308e+04
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10^4
10^3
10^2
10
-200
-150
-100
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0
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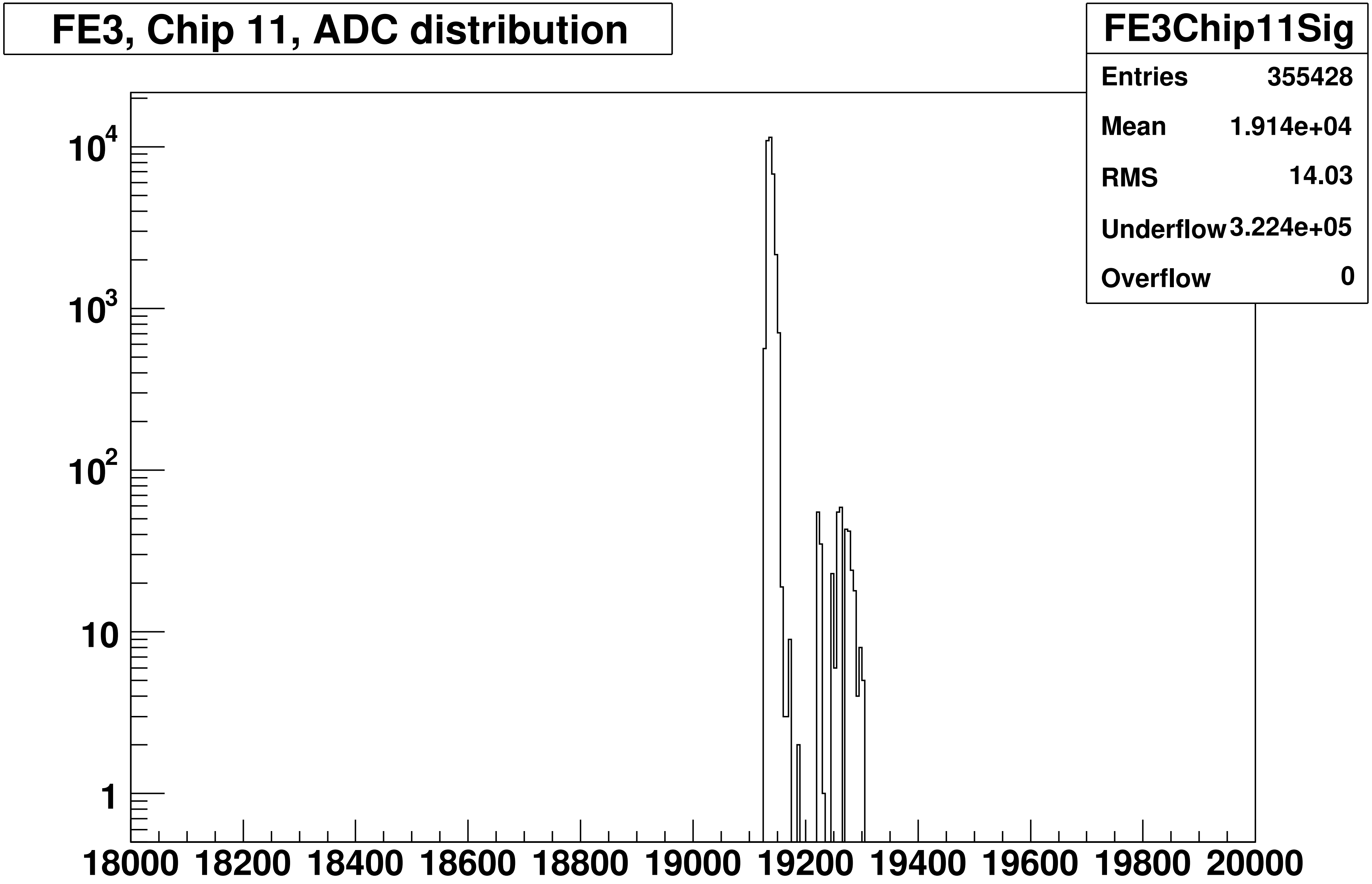
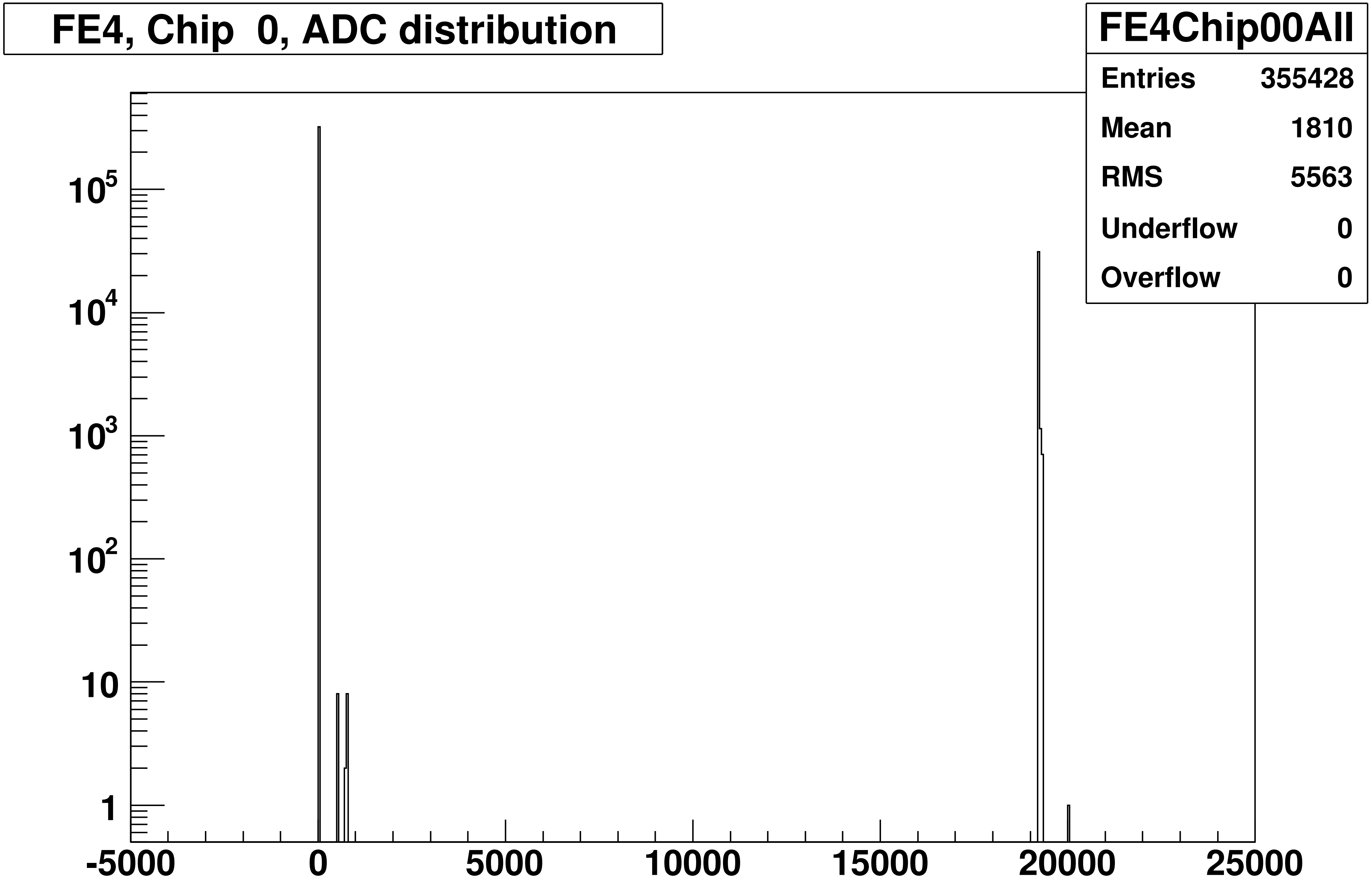
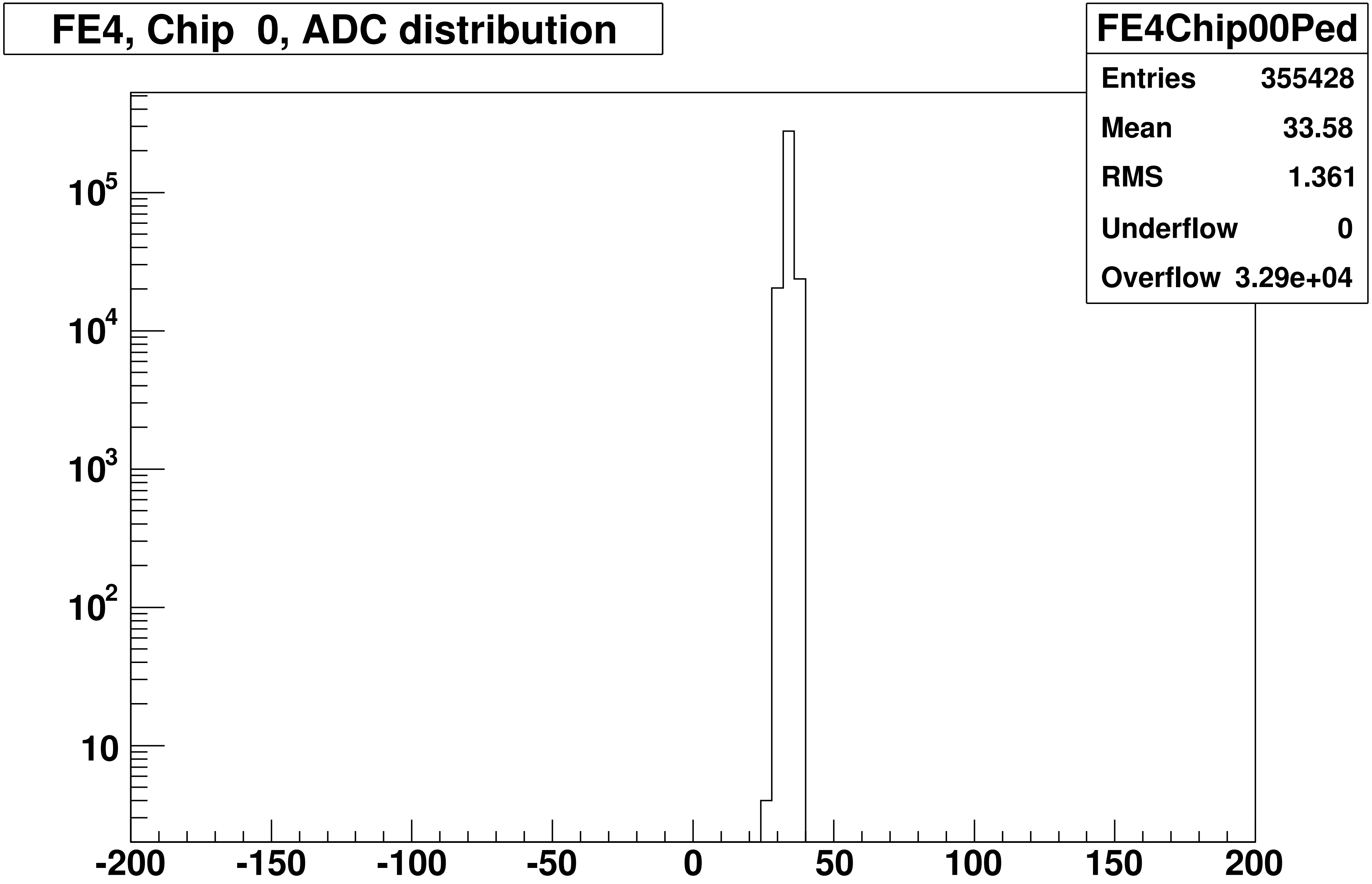
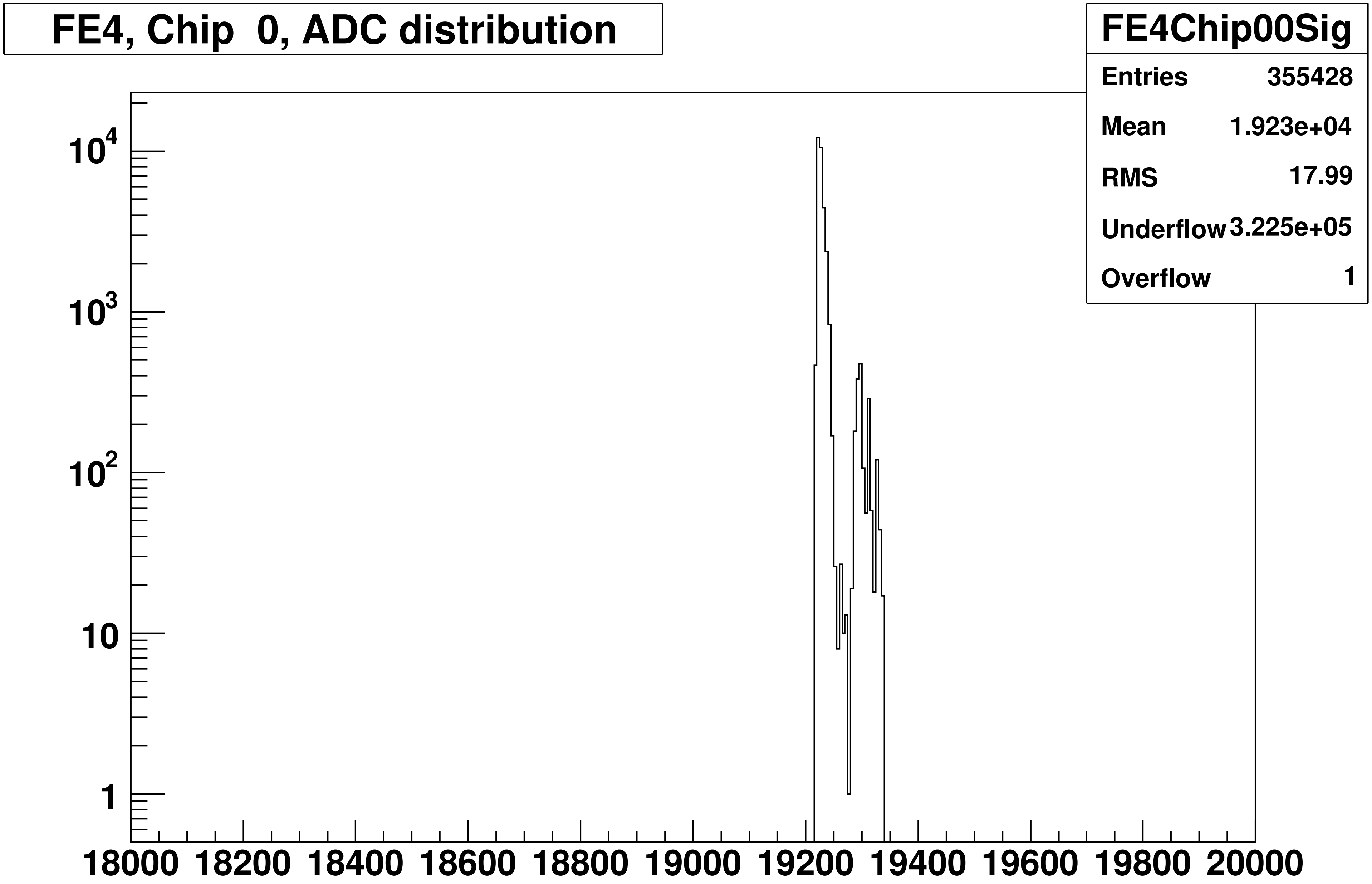
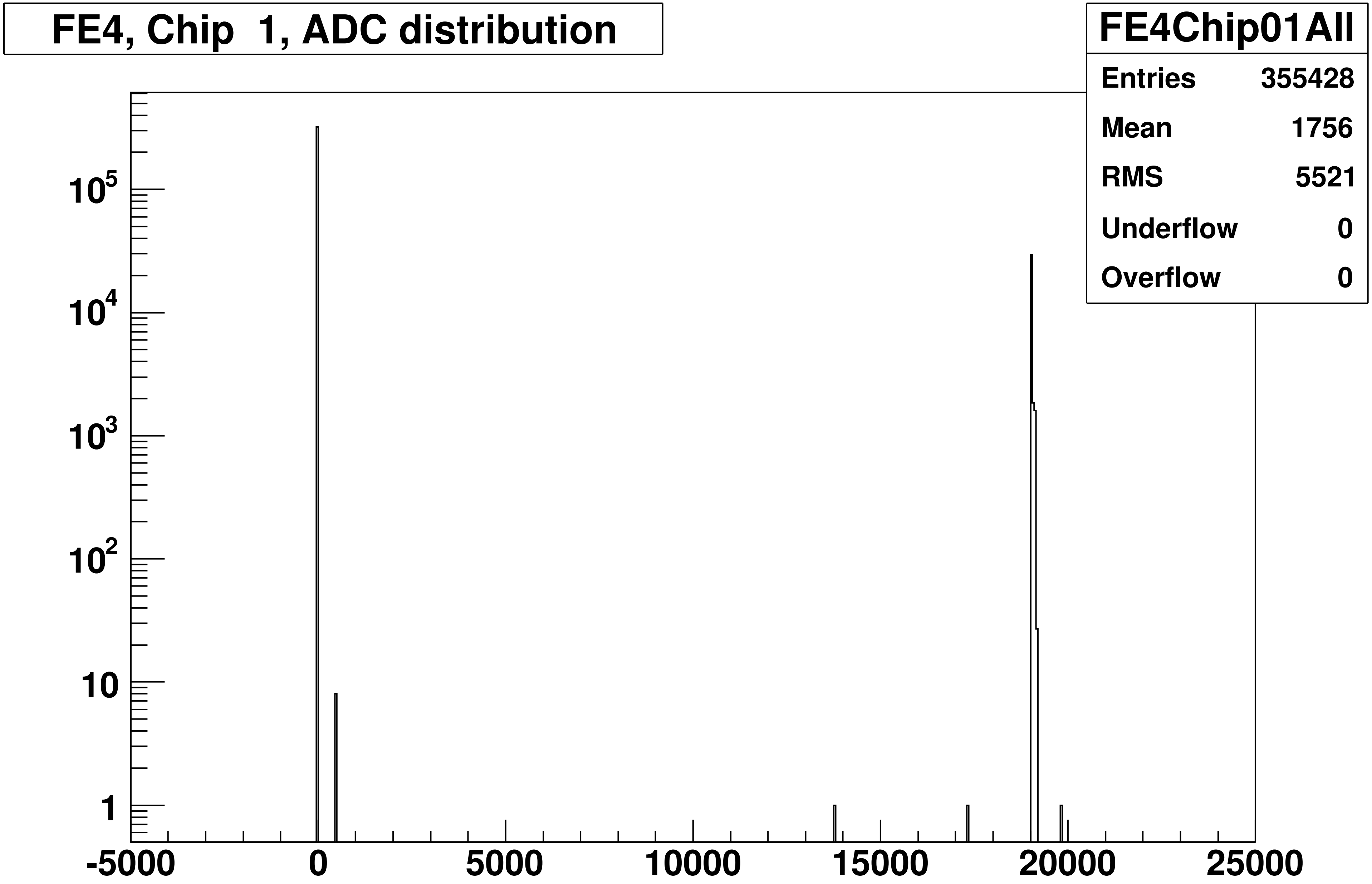
100
150
200


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 355428
Mean 1.914e+04
RMS 14.03
Underflow 3.224e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 355428
Mean 1810
RMS 5563
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 355428
Mean 33.58
RMS 1.361
Underflow 0
Overflow 3.29e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 355428
Mean 1.923e+04
RMS 17.99
Underflow 3.225e+05
Overflow 1
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 355428
Mean 1756
RMS 5521
Underflow 0
Overflow 0
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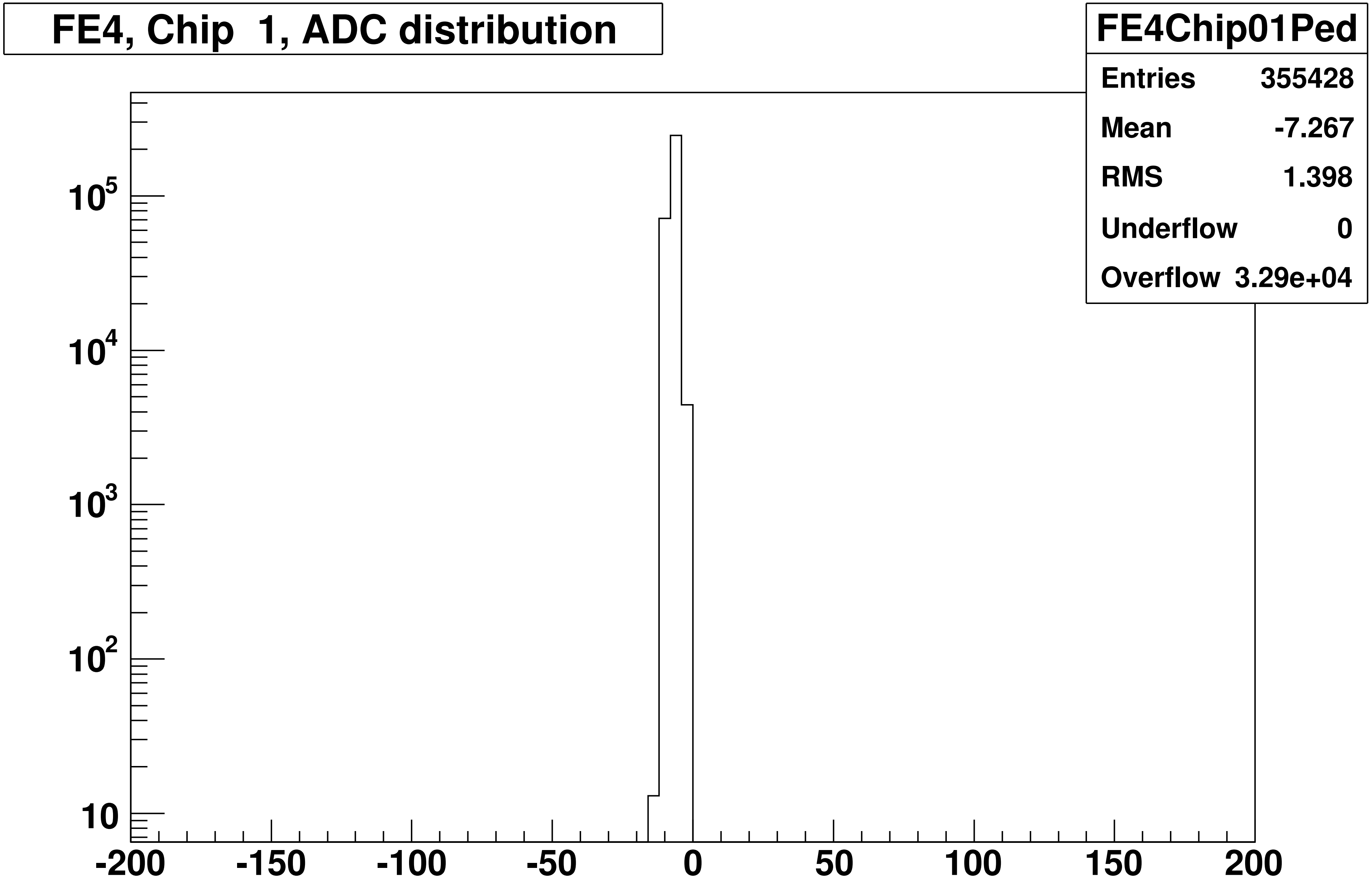
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 355428
Mean -7.267
RMS 1.398
Underflow 0
Overflow 3.29e+04
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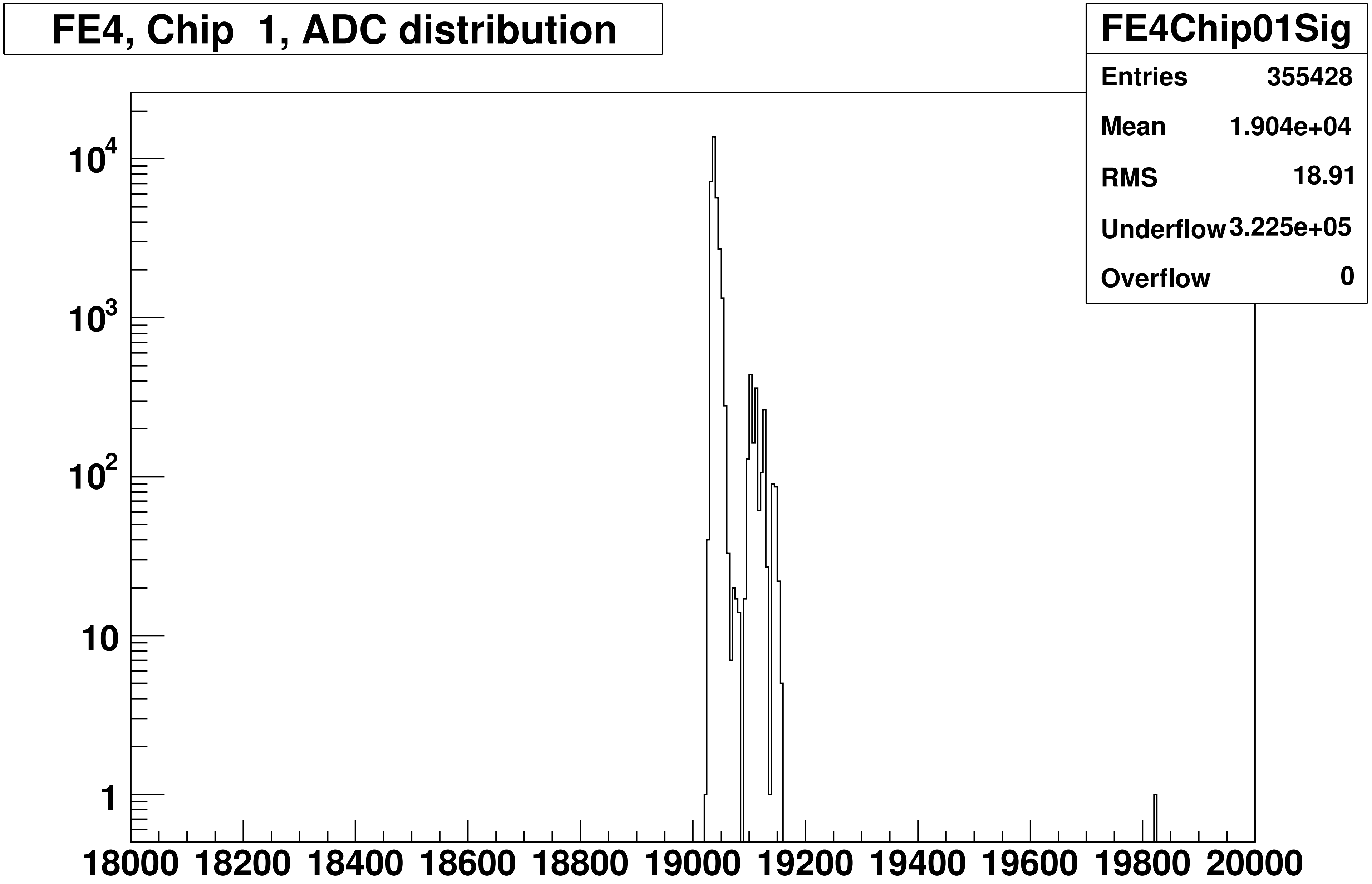
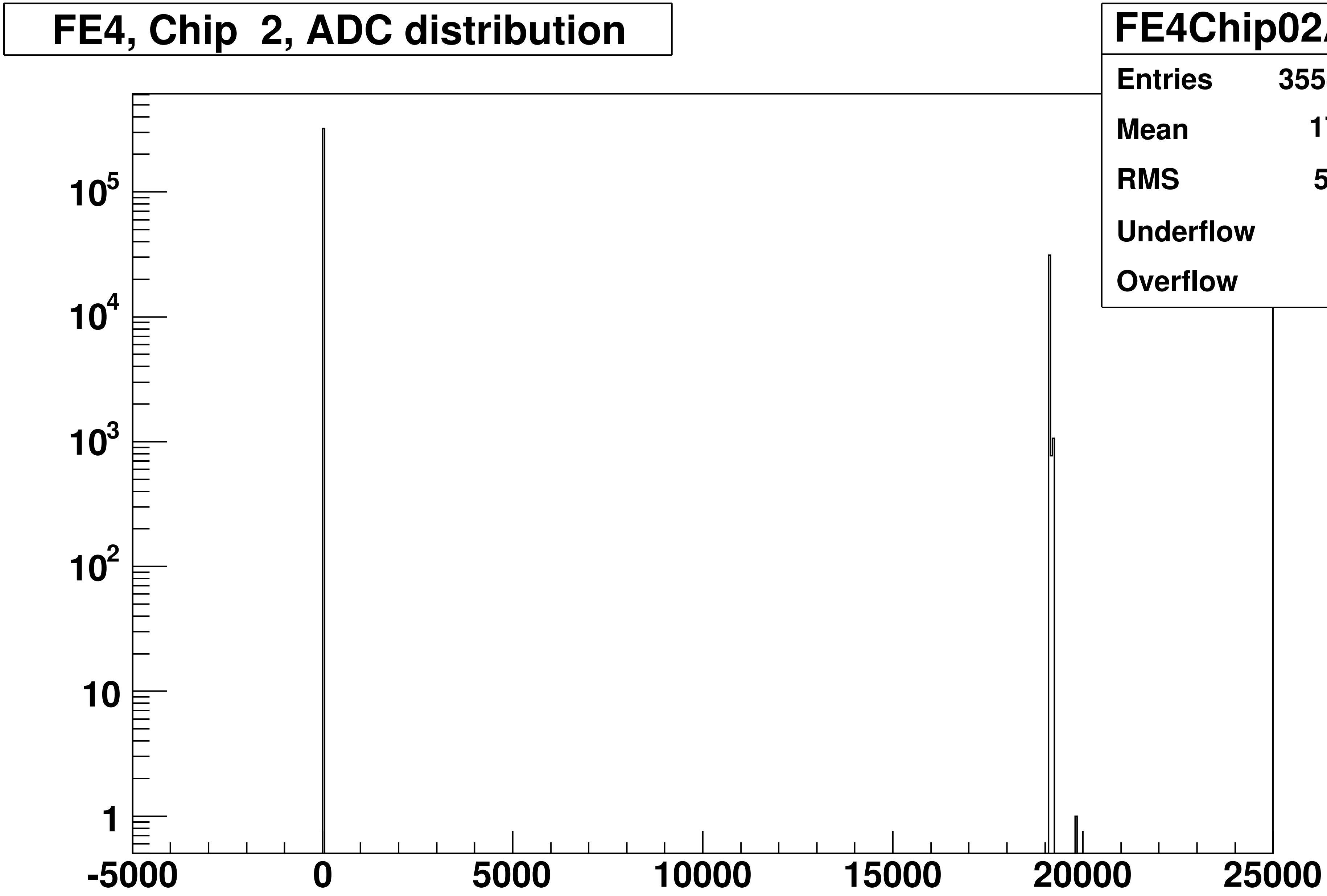
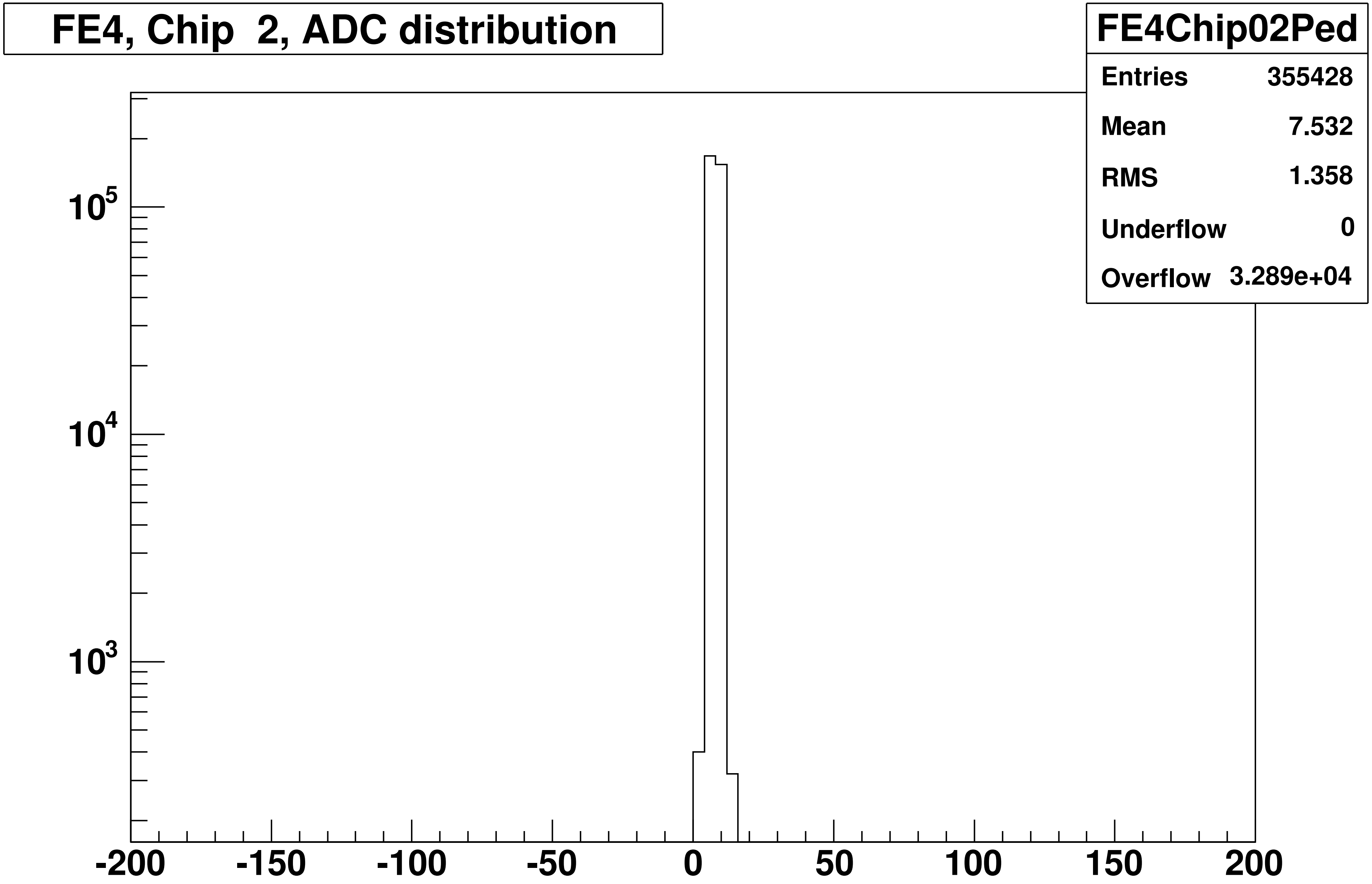
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 355428
Mean 1.904e+04
RMS 18.91
Underflow 3.225e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 355428
Mean 1777
RMS 5541
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 355428
Mean 7.532
RMS 1.358
Underflow 0
Overflow 3.289e+04
10^5
10^4
10^3
-200
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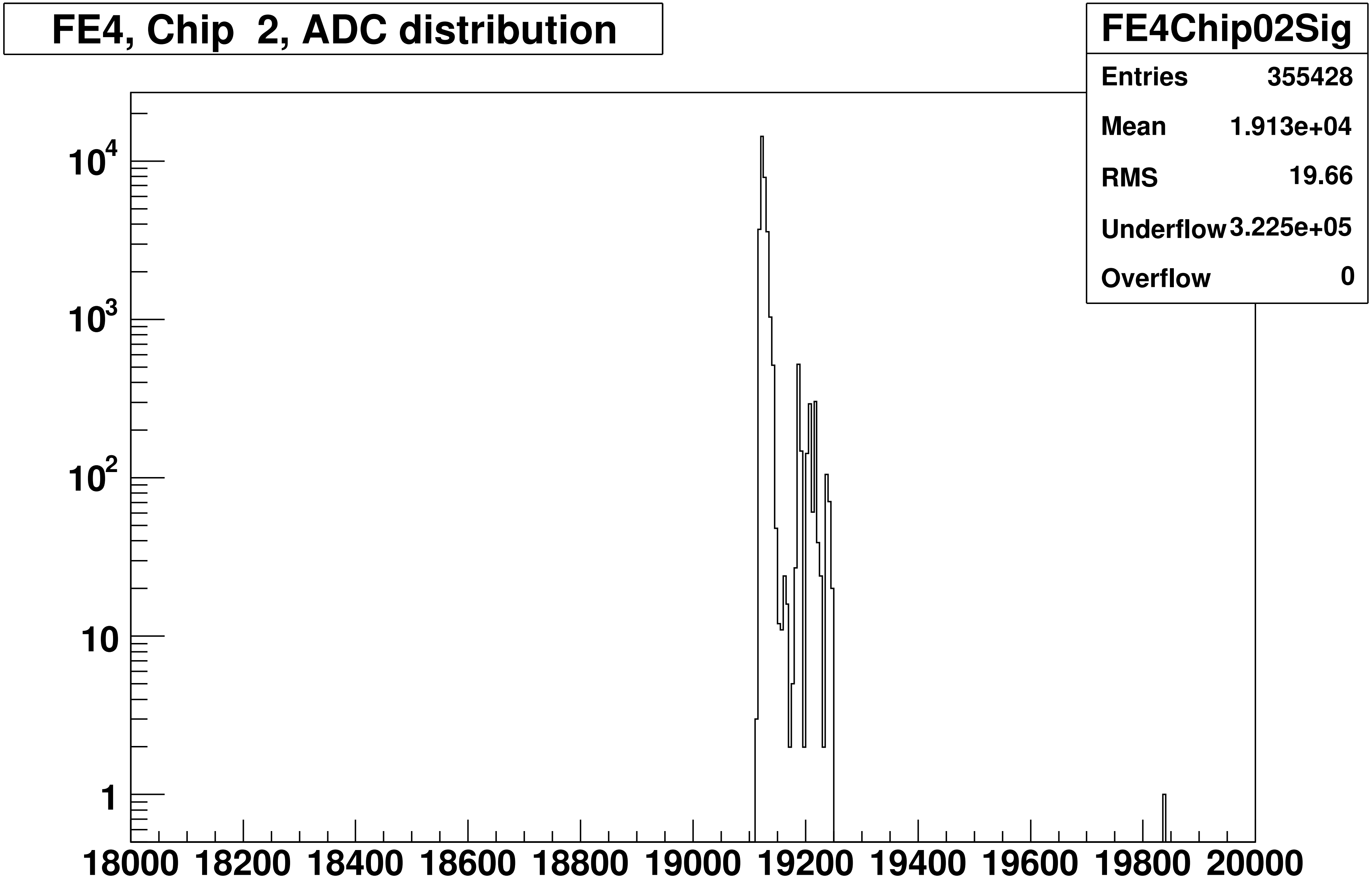
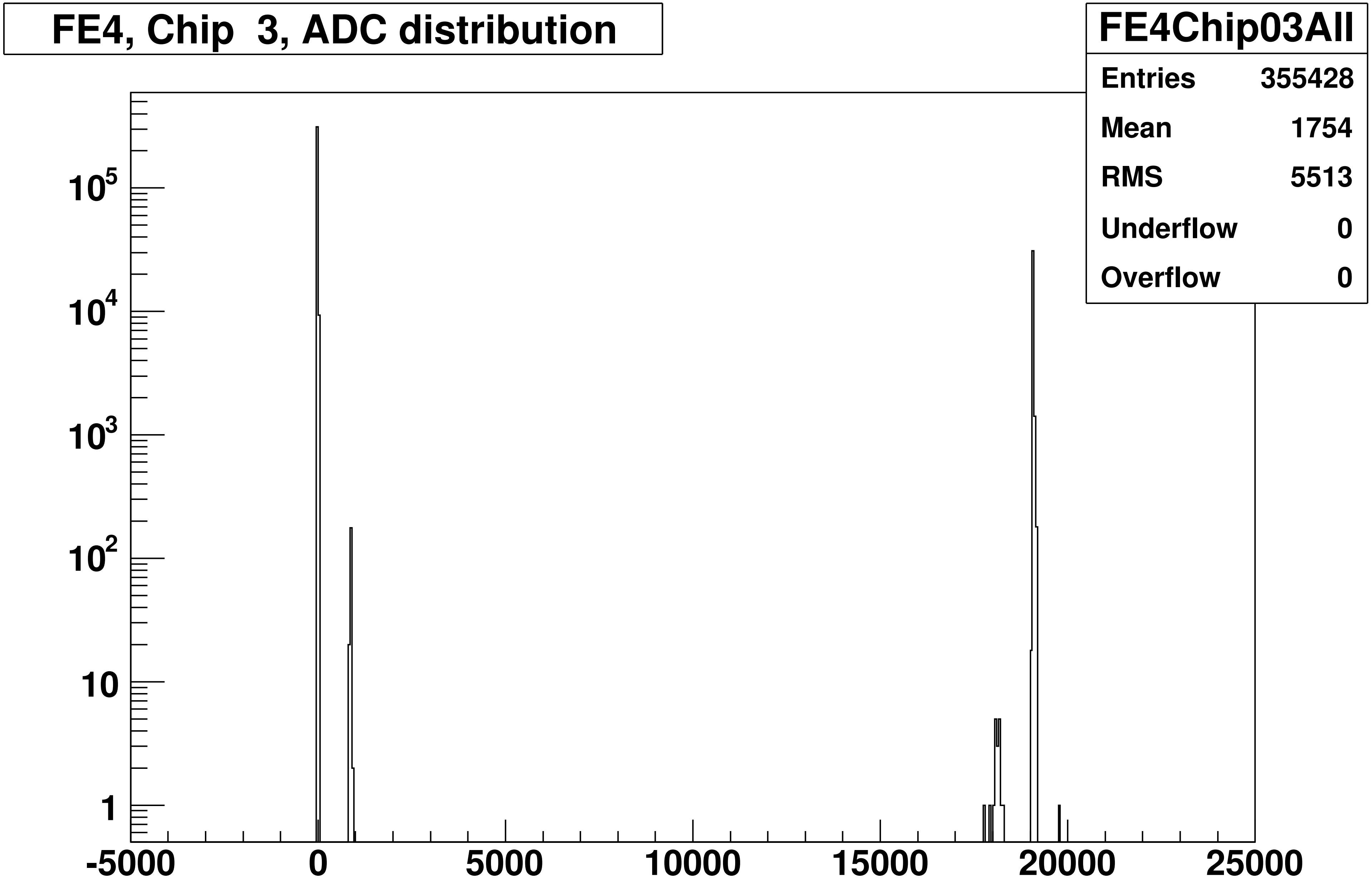
-100
-50
0
50
100
150
200


FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 355428
Mean 1.913e+04
RMS 19.66
Underflow 3.225e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 355428
Mean 1754
RMS 5513
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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10000
15000
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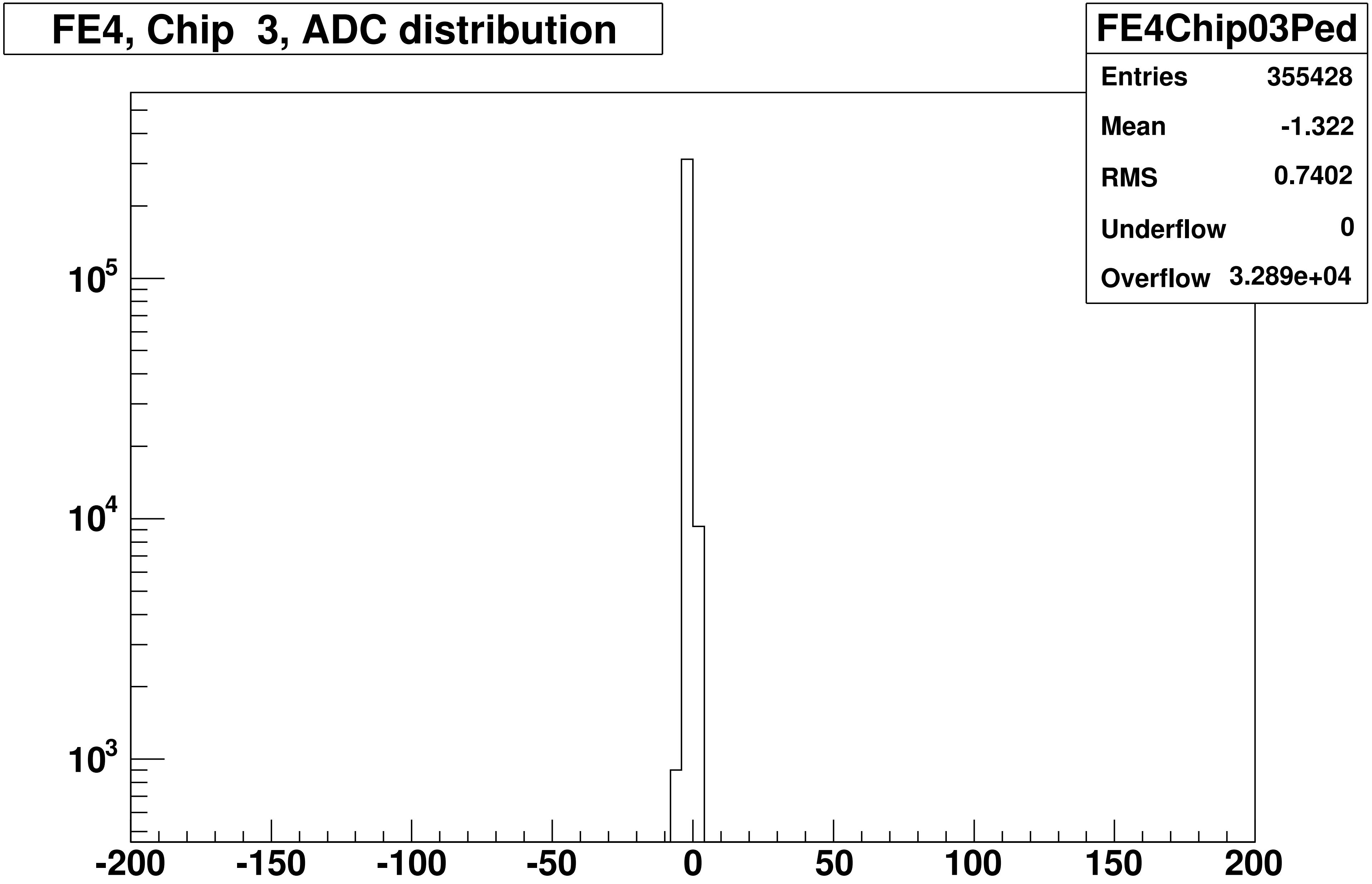
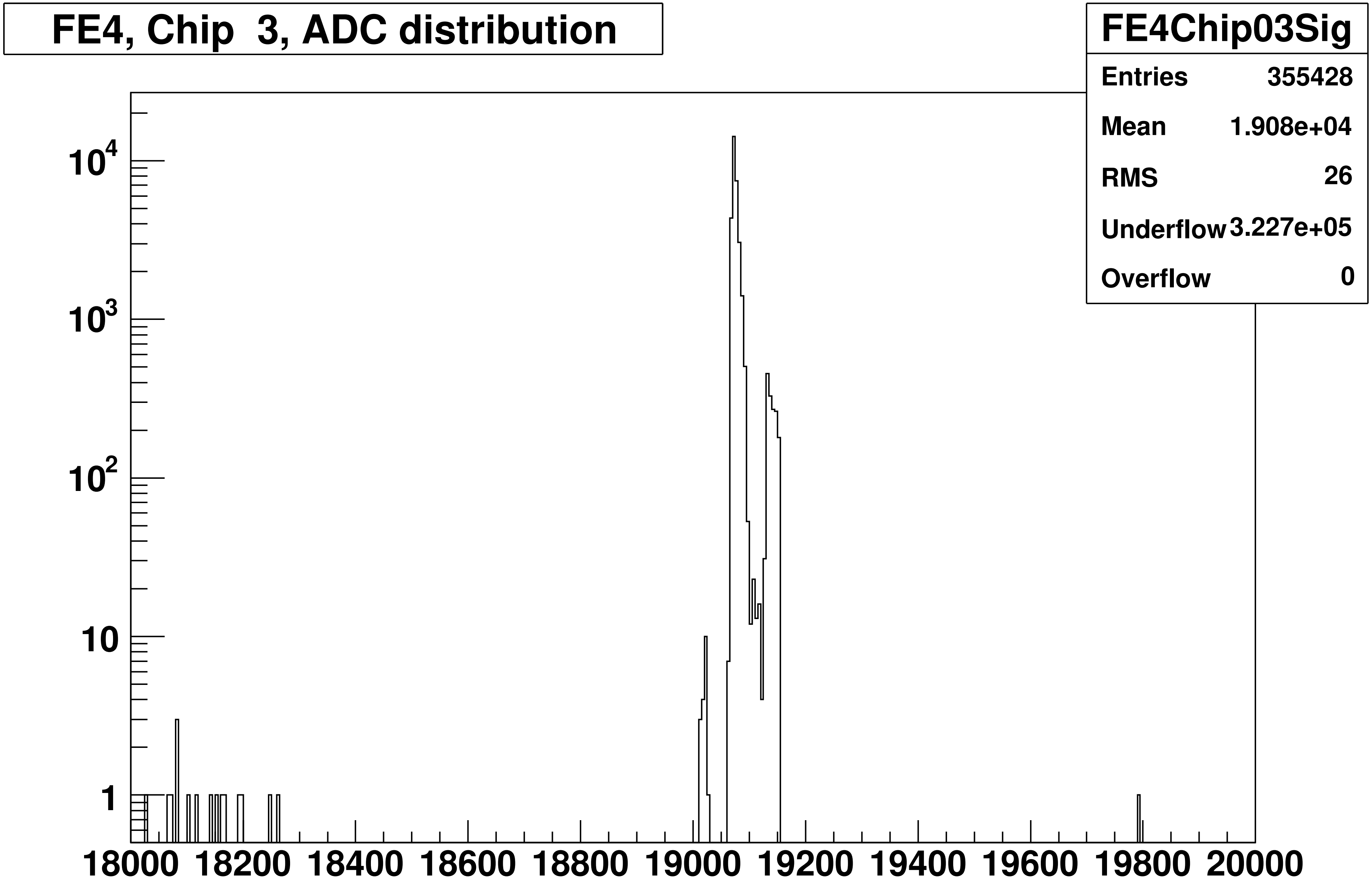
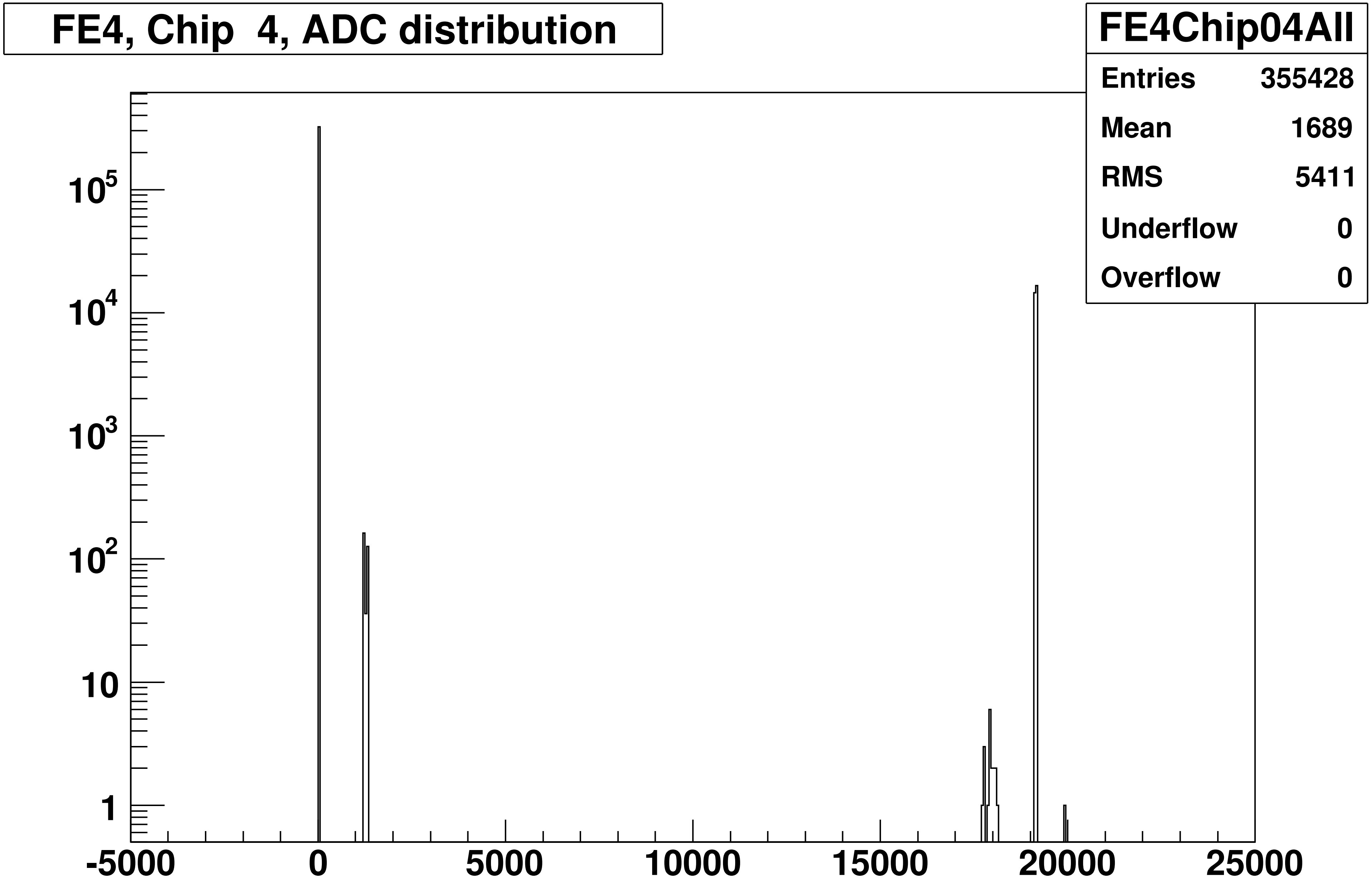
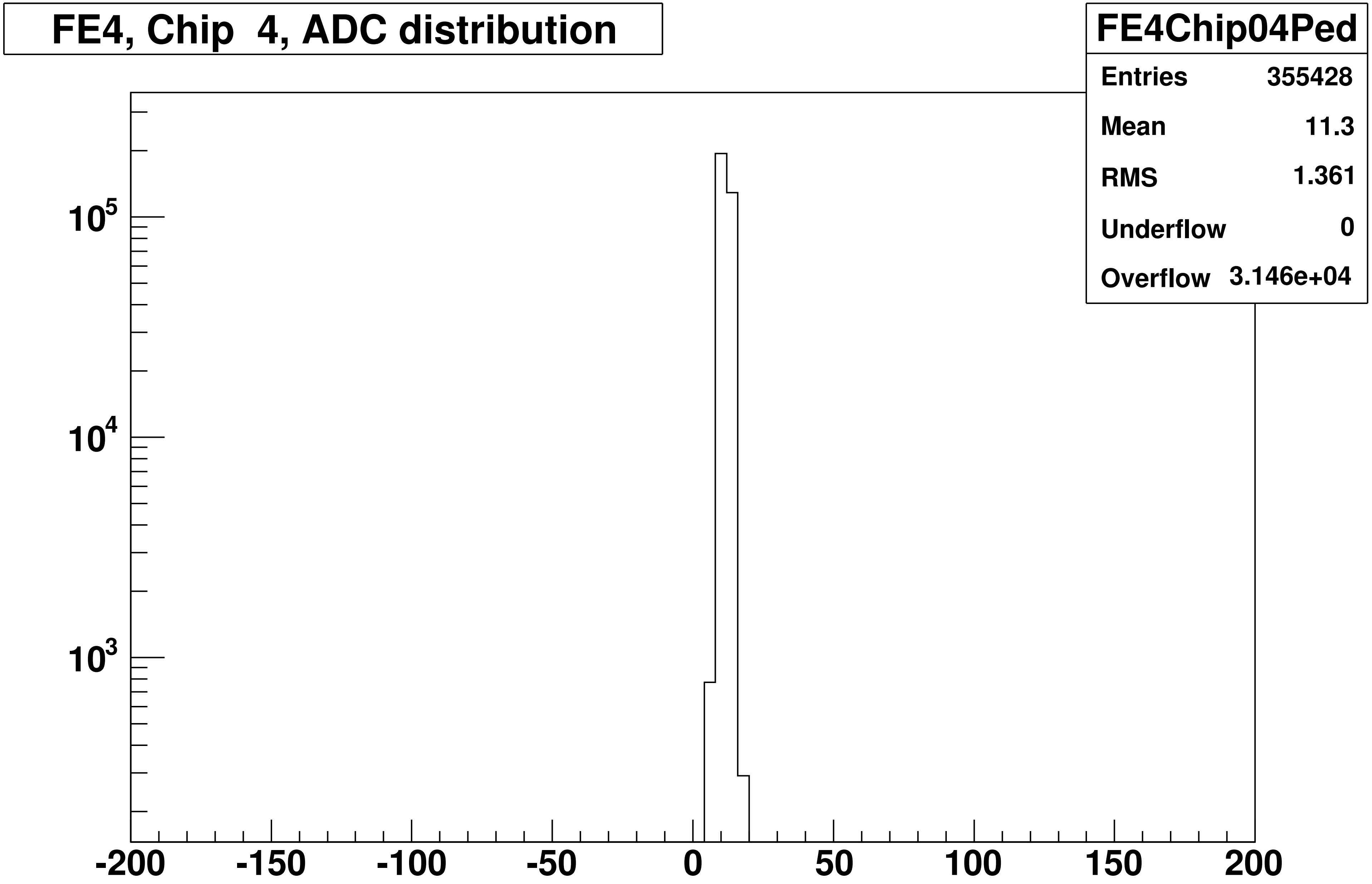
25000


FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 355428
Mean -1.322
RMS 0.7402
Underflow 0
Overflow 3.289e+04
10^5
10^4
10^3
-200 -150 -100 -50 0 50 100 150 200


FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 355428
Mean 1.908e+04
RMS 26
Underflow 3.227e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 355428
Mean 1689
RMS 5411
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 355428
Mean 11.3
RMS 1.361
Underflow 0
Overflow 3.146e+04
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10^4
10^3
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-150
-100
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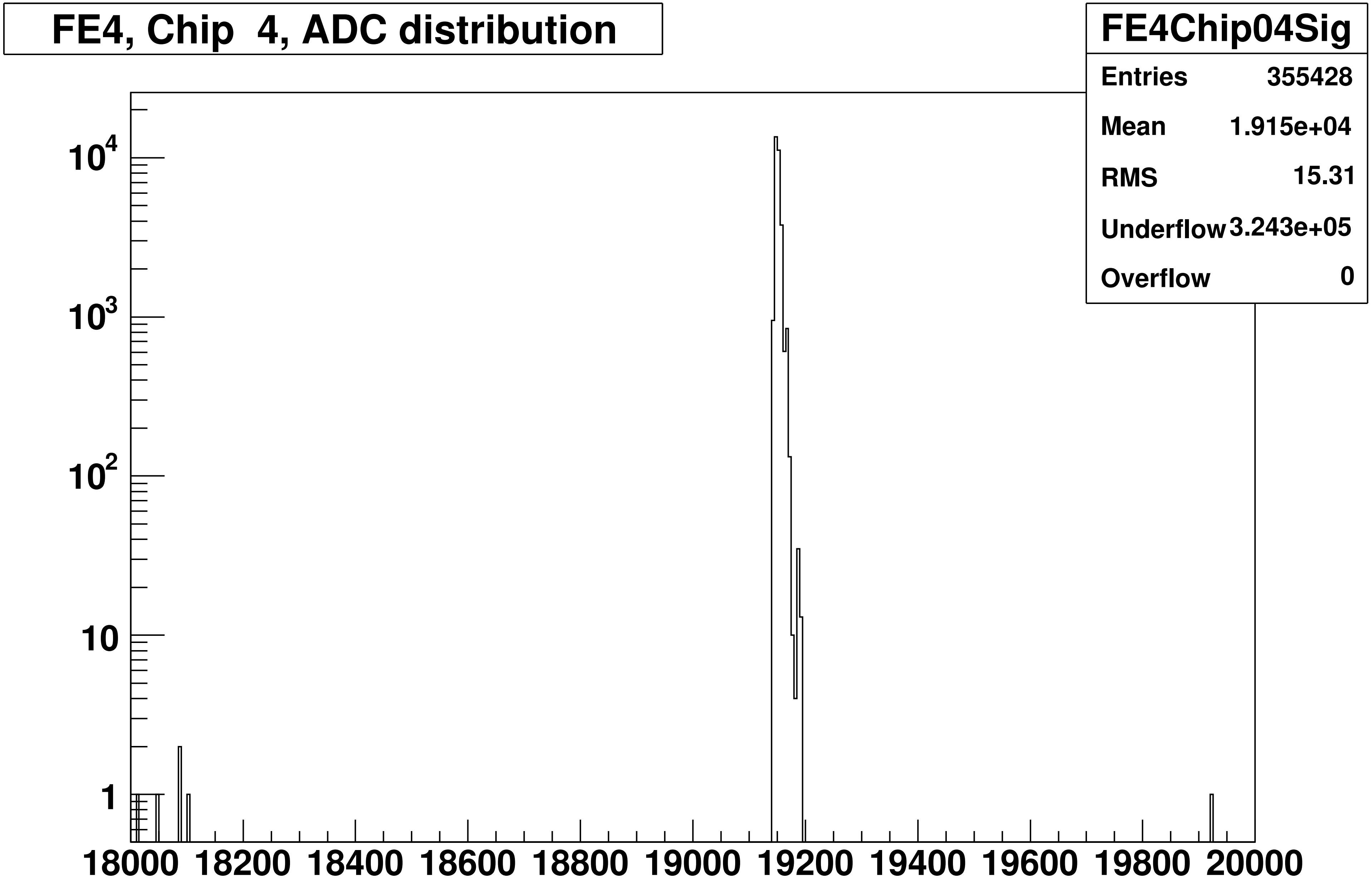
0
50
100
150
200



FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 355428
Mean 1.915e+04
RMS 15.31
Underflow 3.243e+05
Overflow 0
10^4
10^3
10^2
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1
18000
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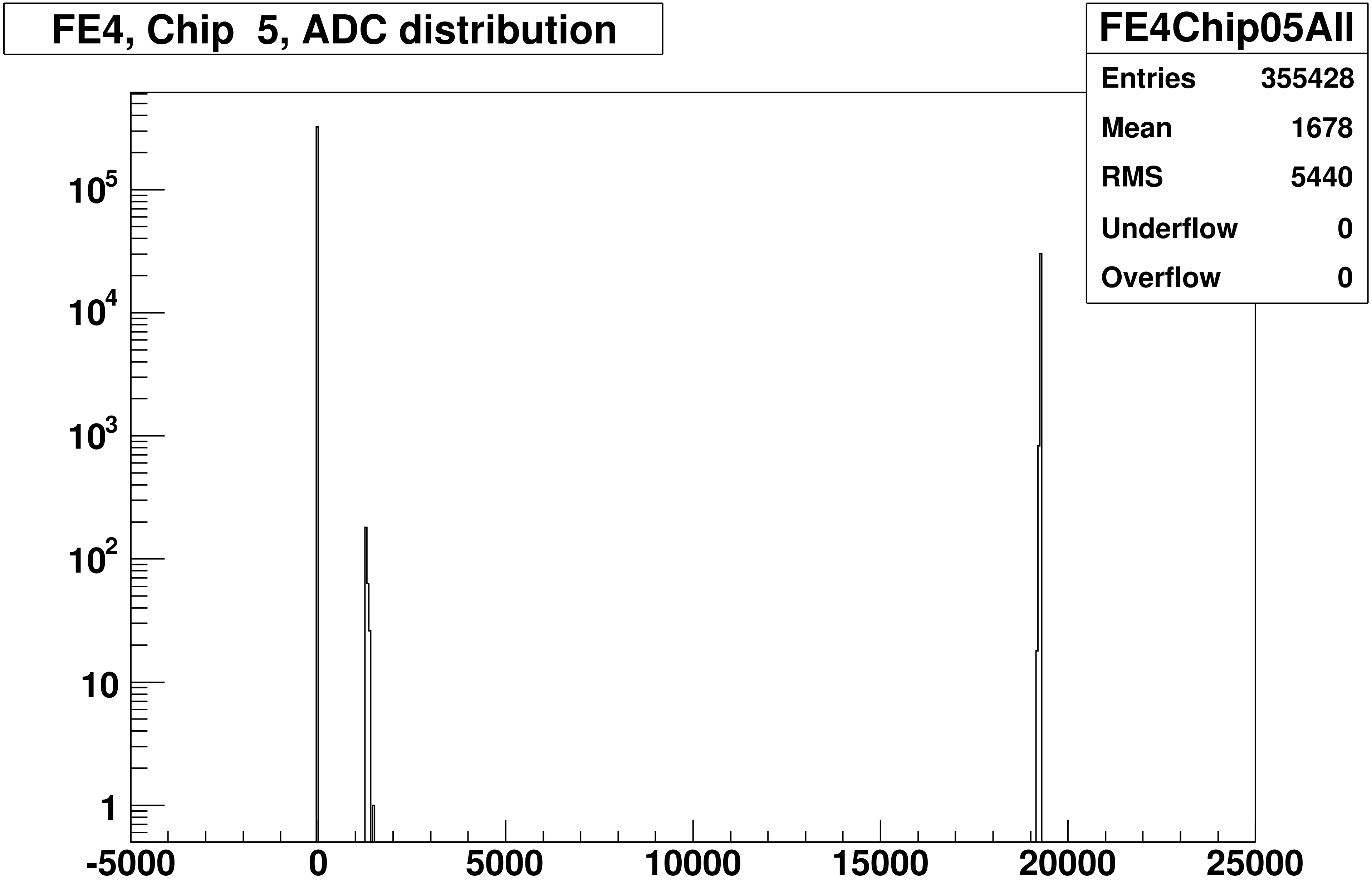
19800
20000


FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 355428
Mean 1678
RMS 5440
Underflow 0
Overflow 0
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10
10^2
10^3
10^4
10^5
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0
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10000
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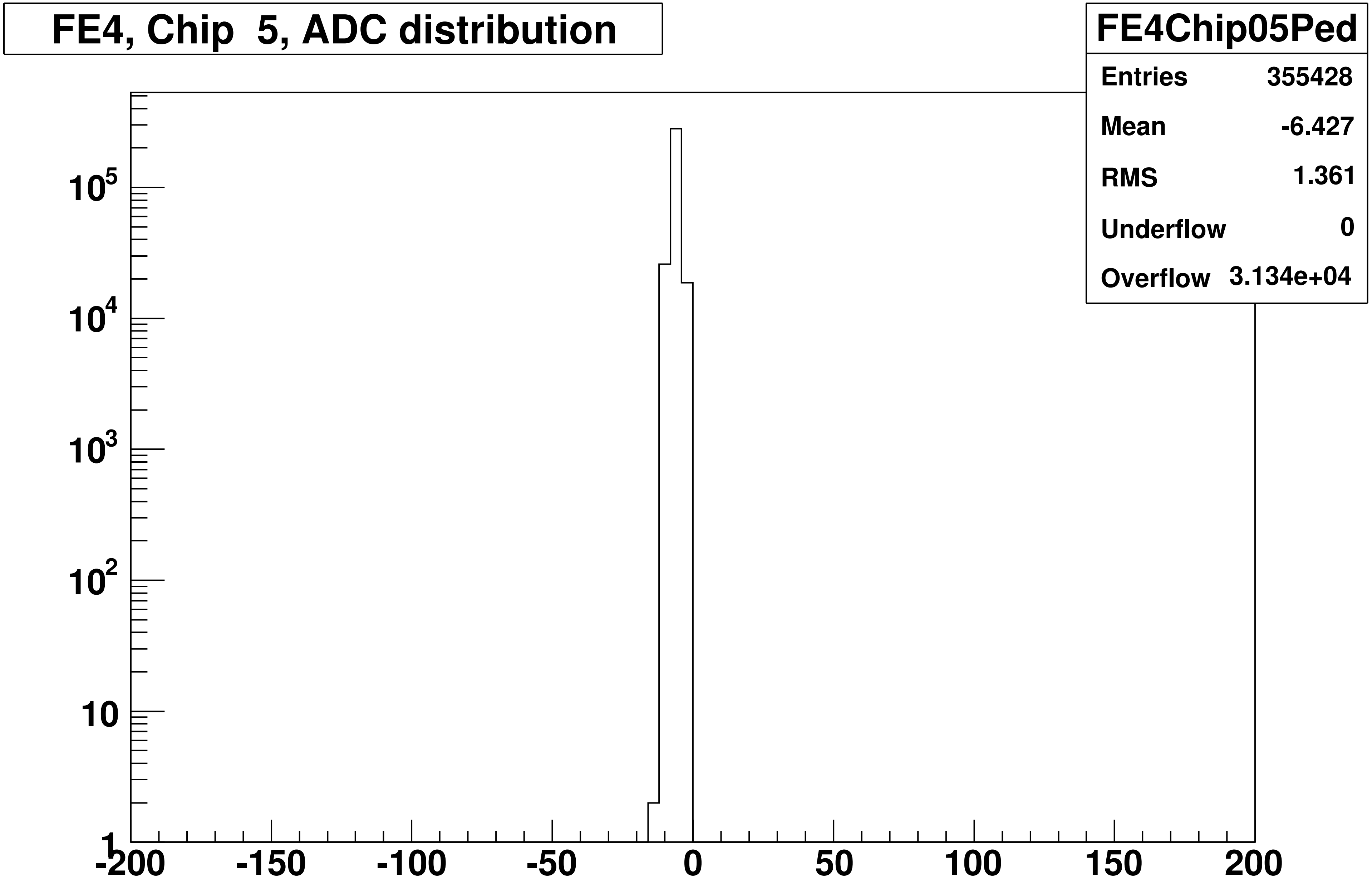
25000



FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 355428
Mean -6.427
RMS 1.361
Underflow 0
Overflow 3.134e+04
10^5
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10^3
10^2
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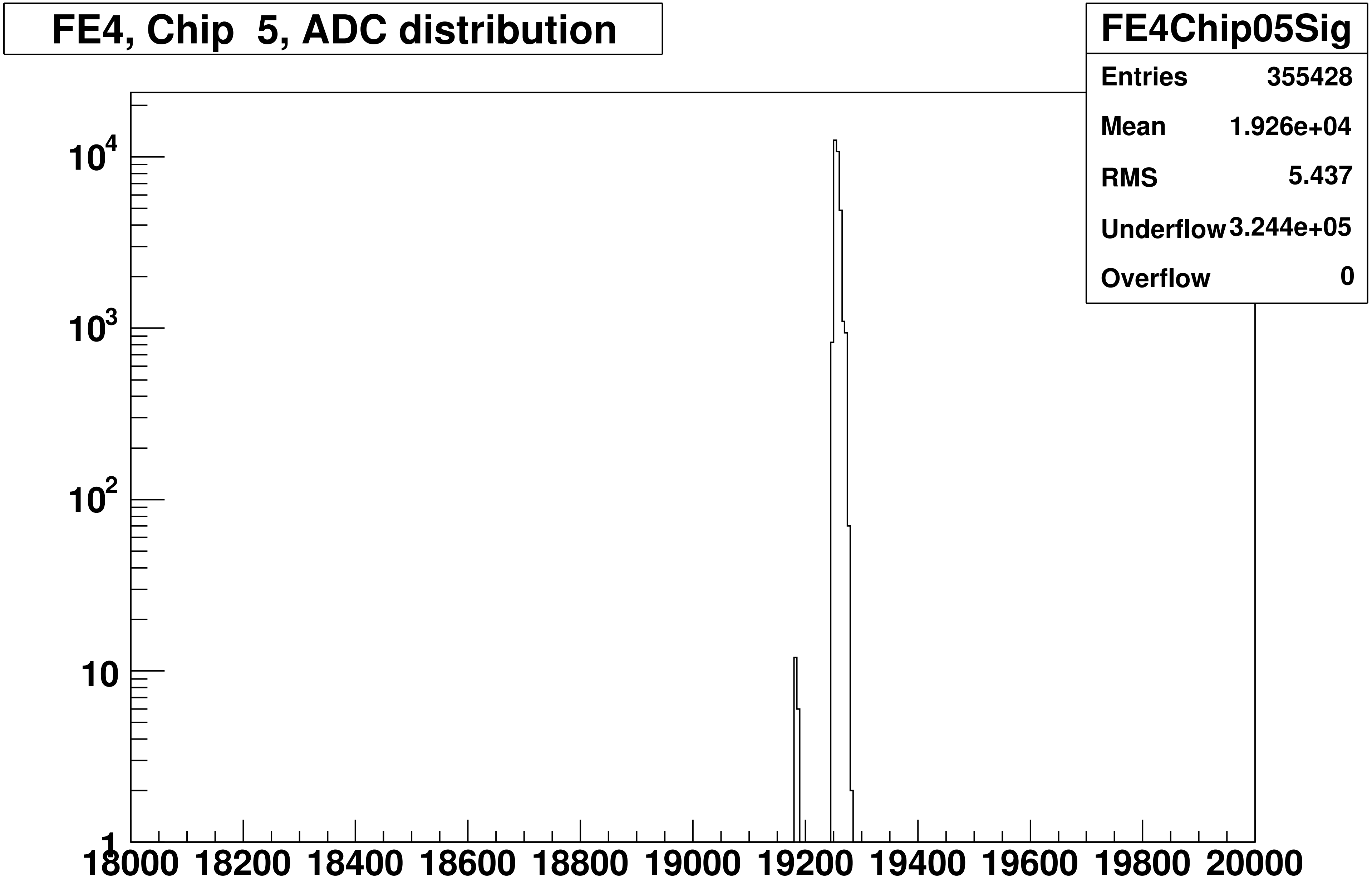
-150
-100
-50
0
50
100
150
200


FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 355428
Mean 1.926e+04
RMS 5.437
Underflow 3.244e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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19600
19800
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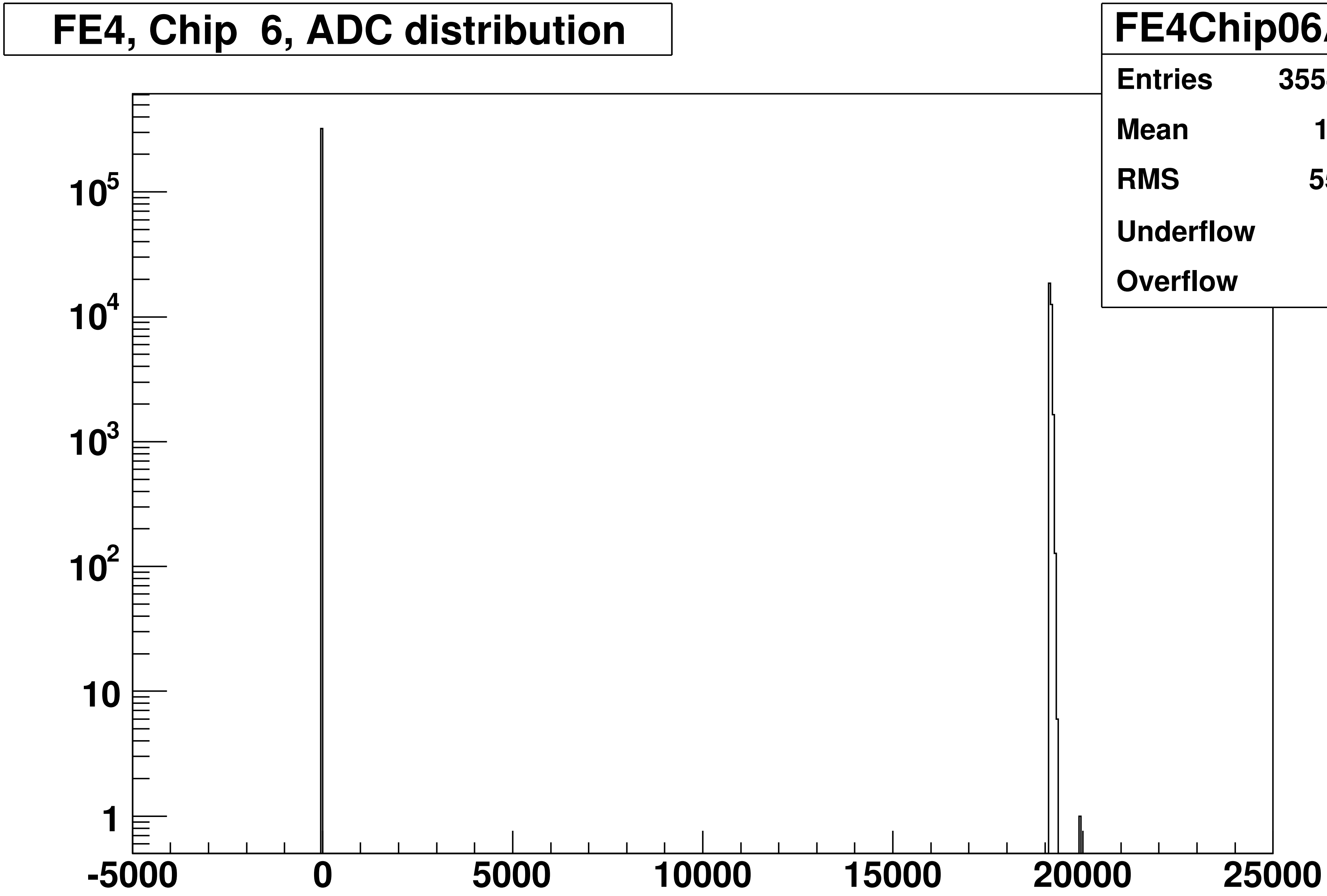
FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 355428
Mean 1761
RMS 5555
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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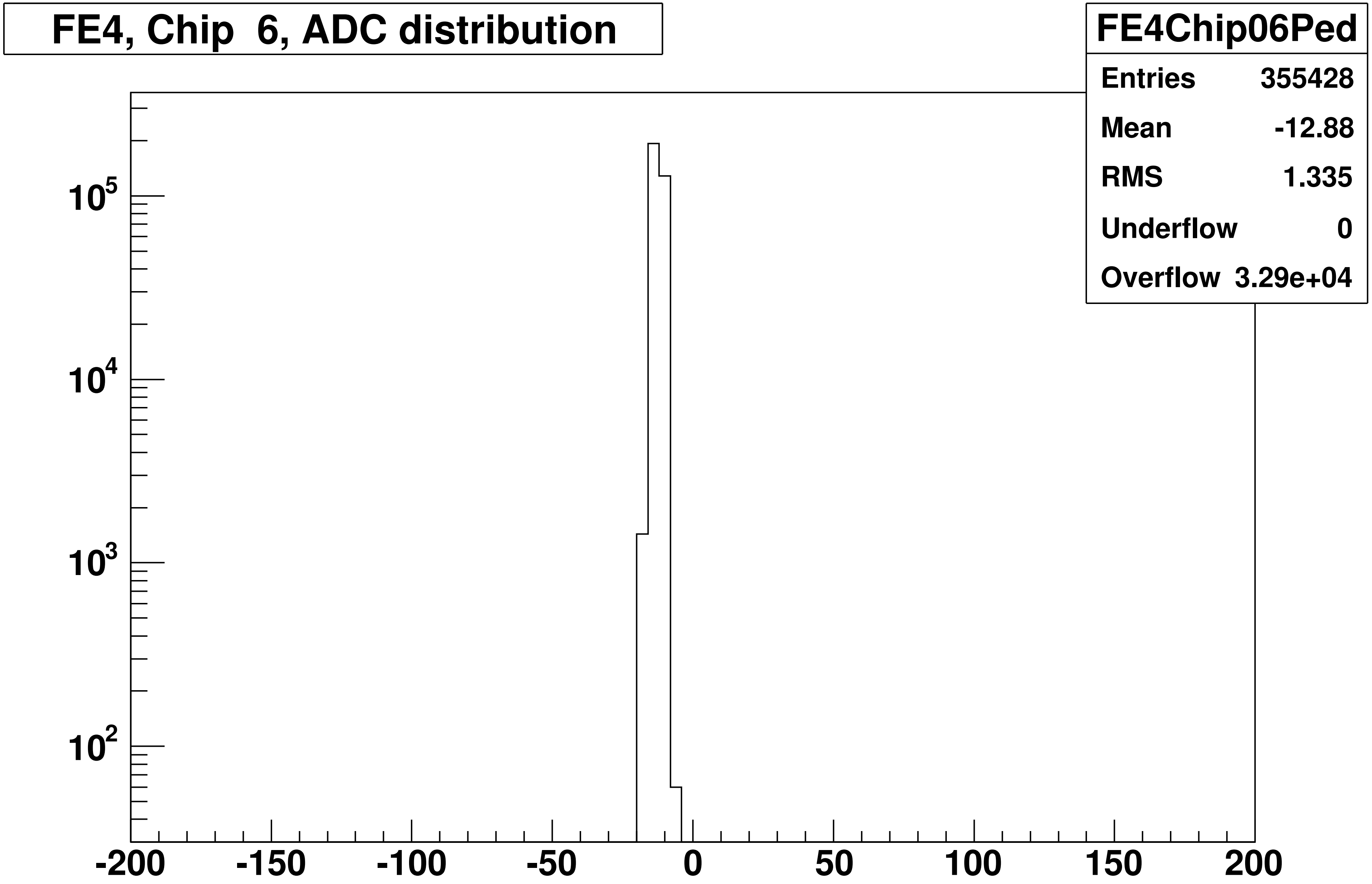
FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 355428
Mean -12.88
RMS 1.335
Underflow 0
Overflow 3.29e+04
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10^4
10^3
10^2
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100
150
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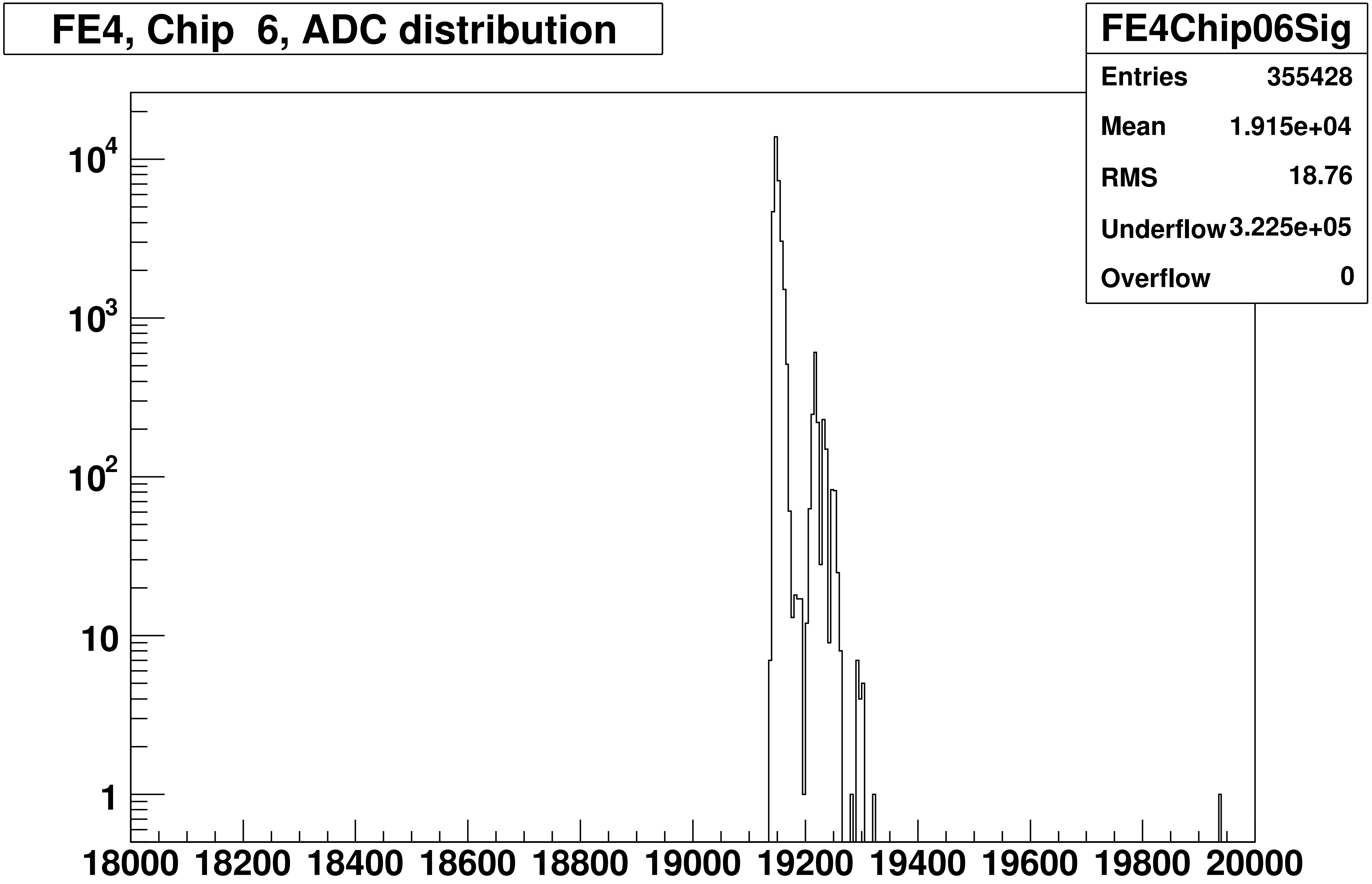
FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 355428
Mean 1.915e+04
RMS 18.76
Underflow 3.225e+05
Overflow 0
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10^3
10^2
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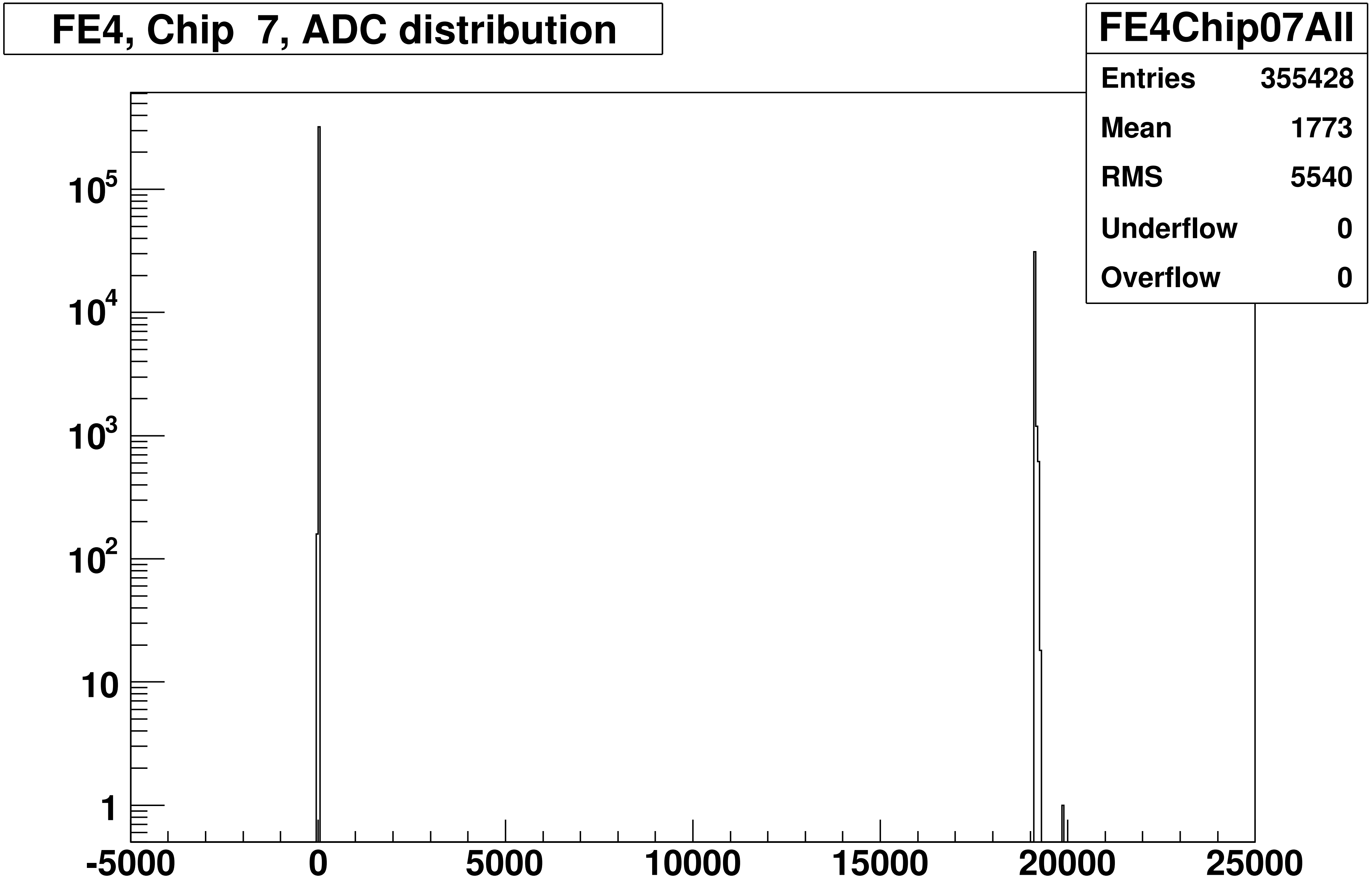
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 355428
Mean 1773
RMS 5540
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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-5000
0
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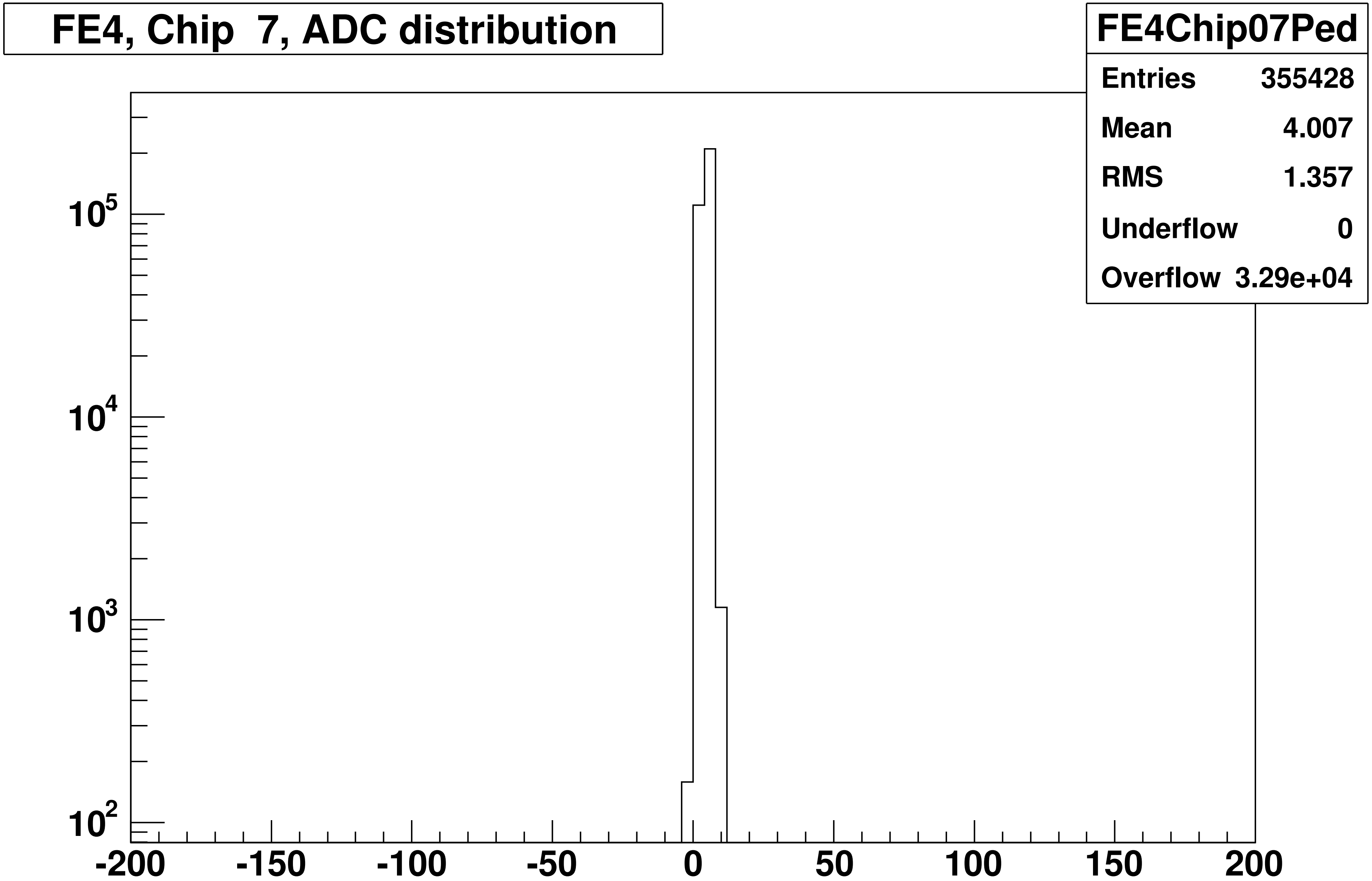
10000
15000
20000
25000


FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 355428
Mean 4.007
RMS 1.357
Underflow 0
Overflow 3.29e+04
10^5
10^4
10^3
10^2
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-100
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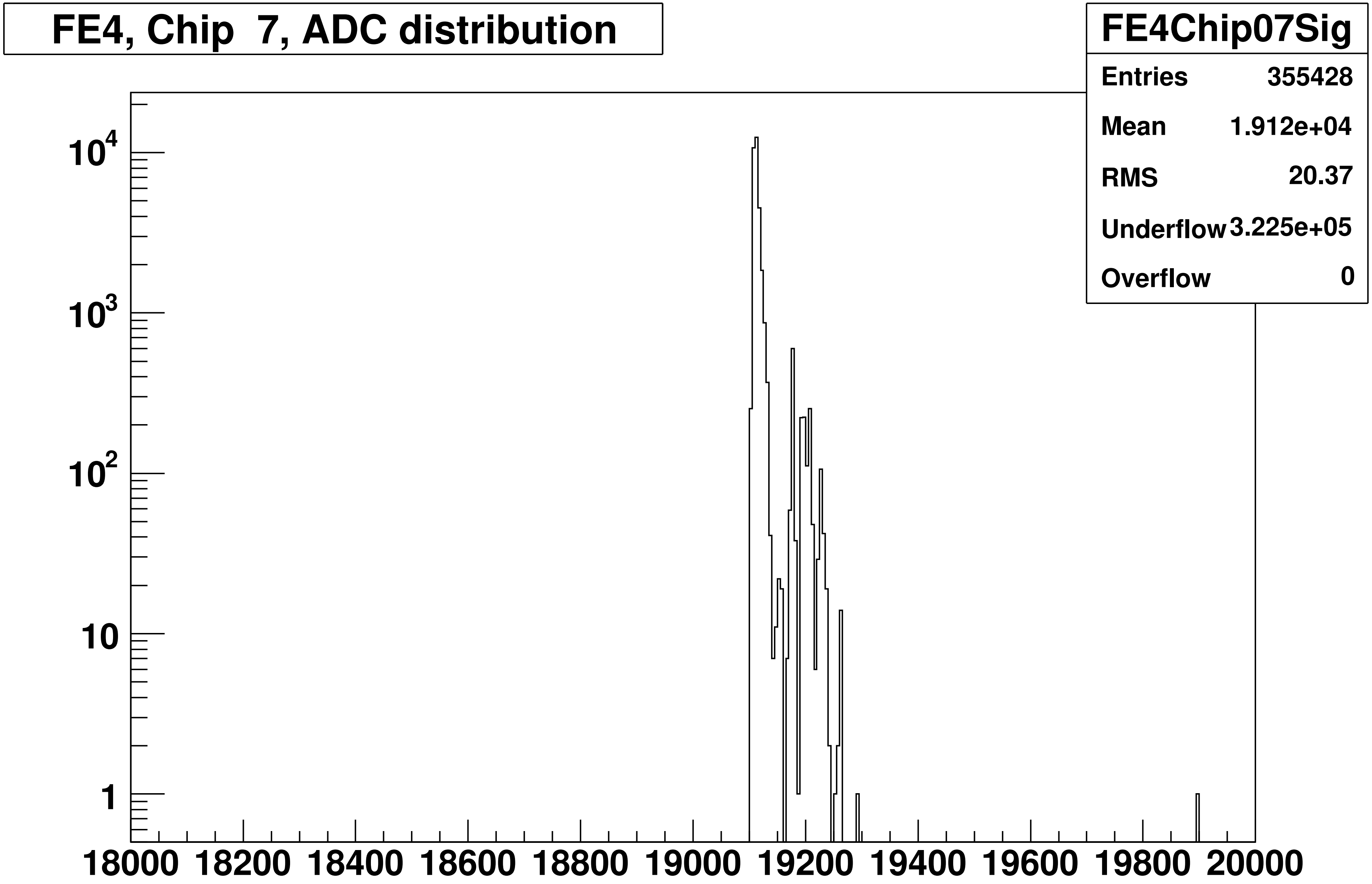
150
200


FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 355428
Mean 1.912e+04
RMS 20.37
Underflow 3.225e+05
Overflow 0
10^4
10^3
10^2
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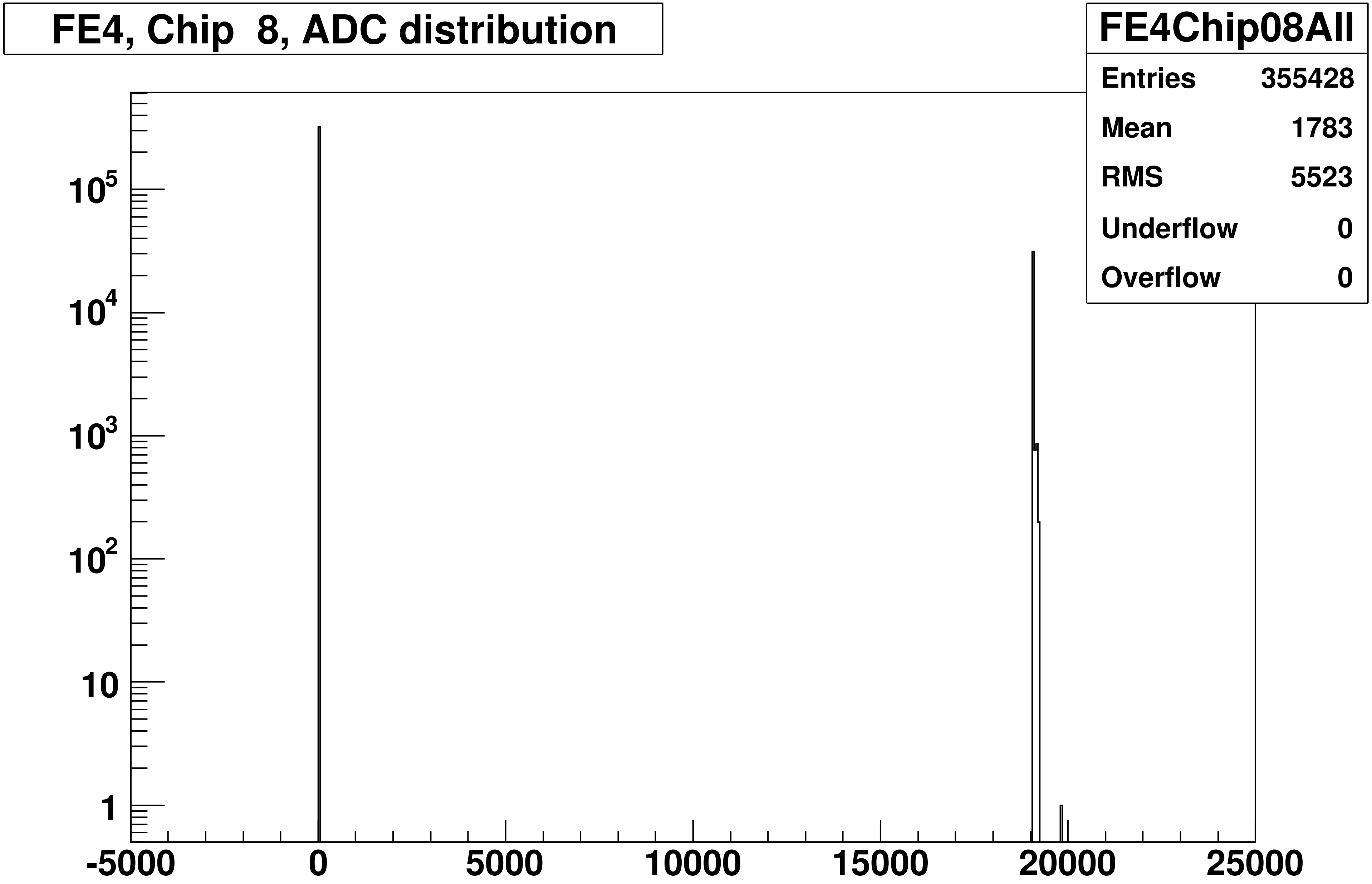
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 355428
Mean 1783
RMS 5523
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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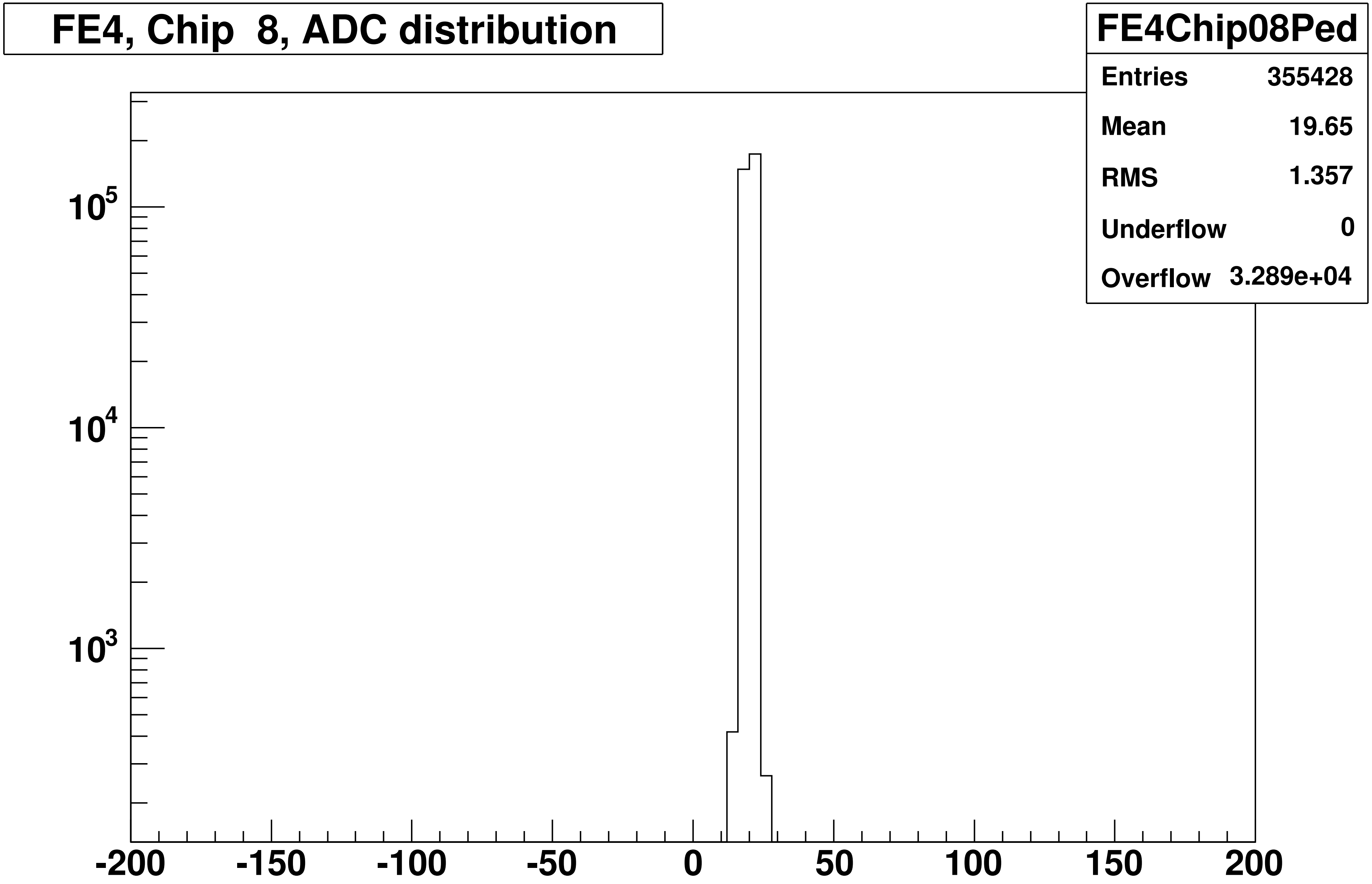
15000
20000
25000


FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 355428
Mean 19.65
RMS 1.357
Underflow 0
Overflow 3.289e+04
10^5
10^4
10^3
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-100
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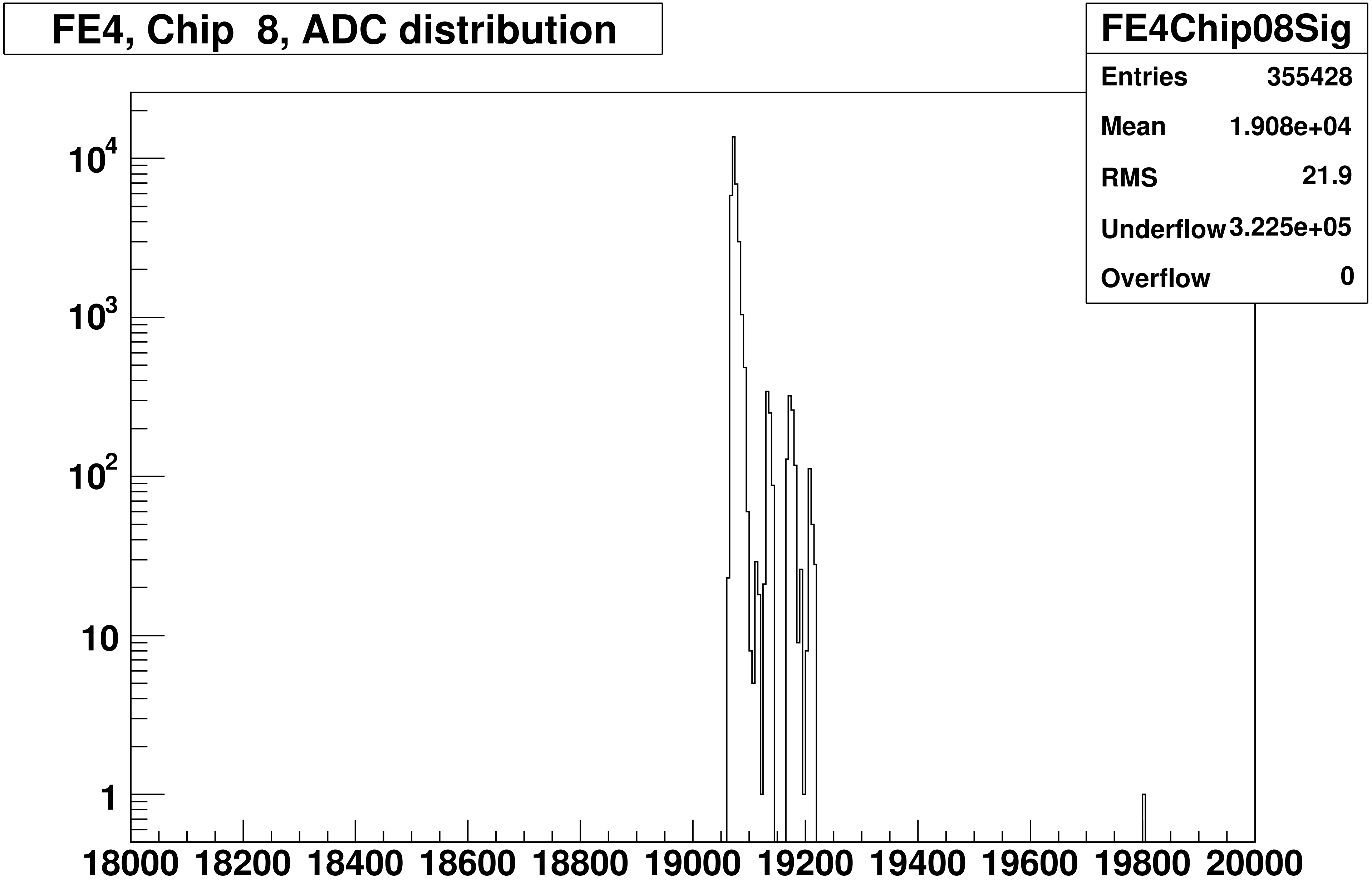
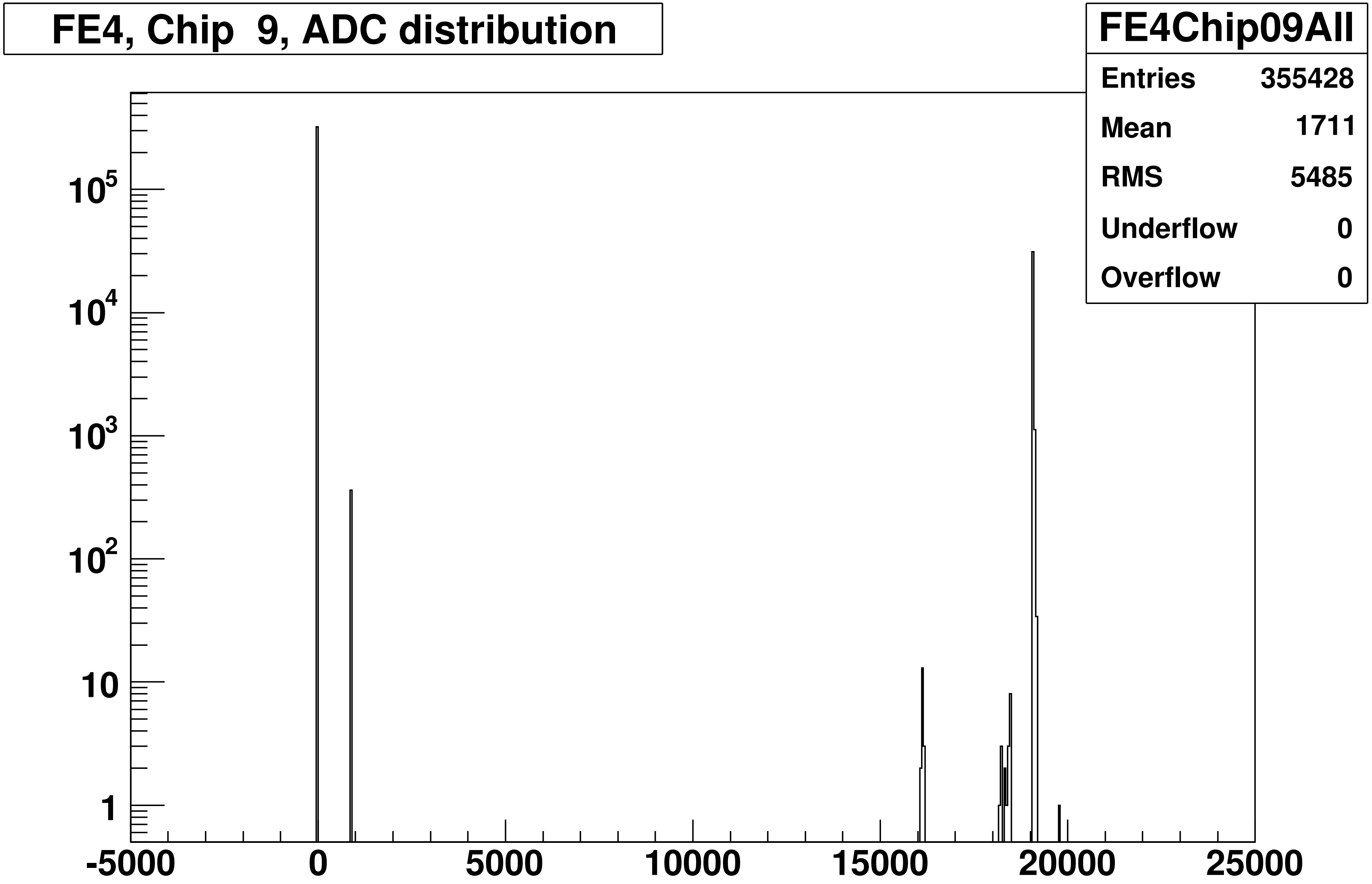
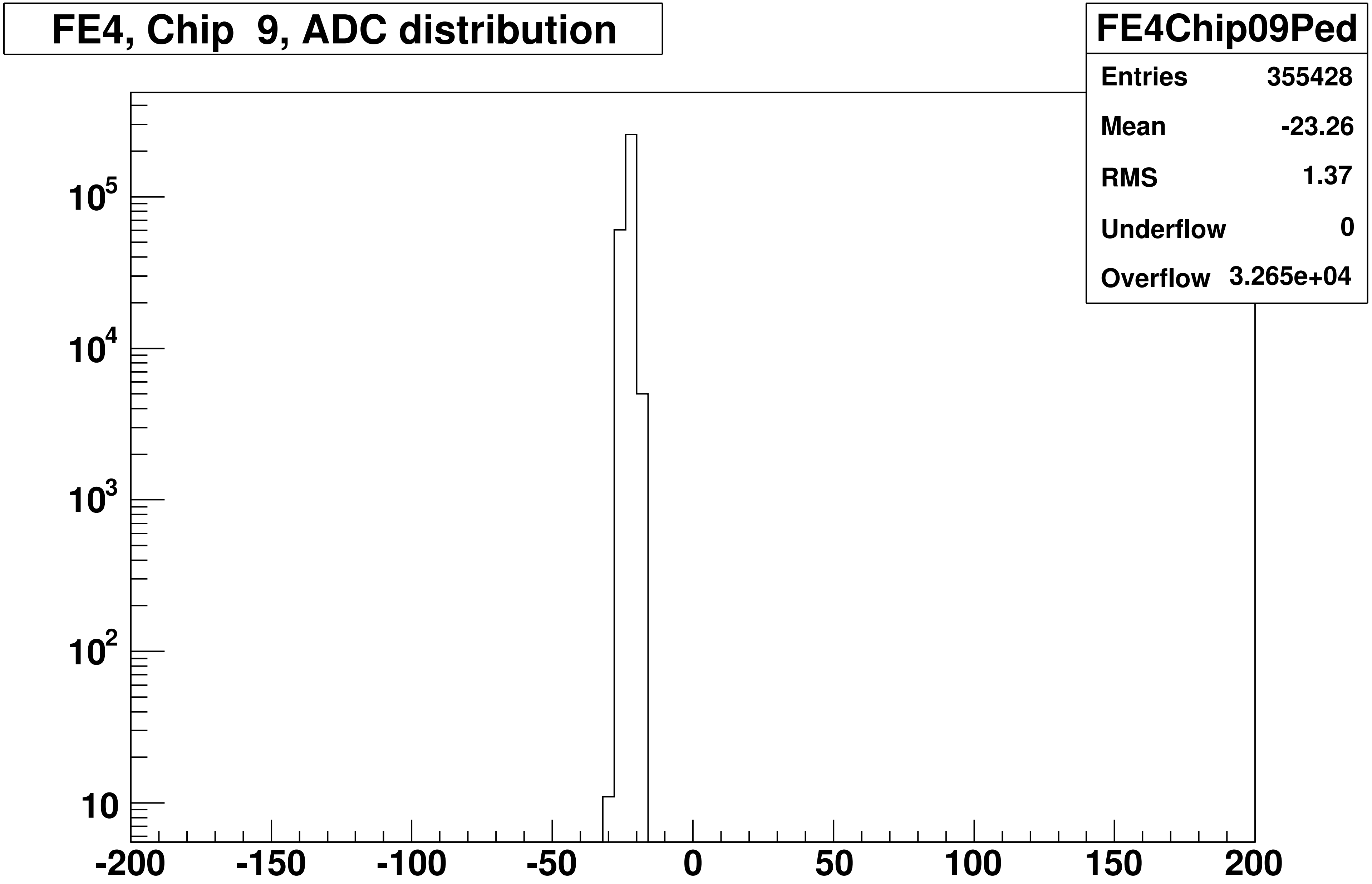
100
150
200



FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 355428
Mean 1.908e+04
RMS 21.9
Underflow 3.225e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 355428
Mean 1711
RMS 5485
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 355428
Mean -23.26
RMS 1.37
Underflow 0
Overflow 3.265e+04
10^5
10^4
10^3
10^2
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-100
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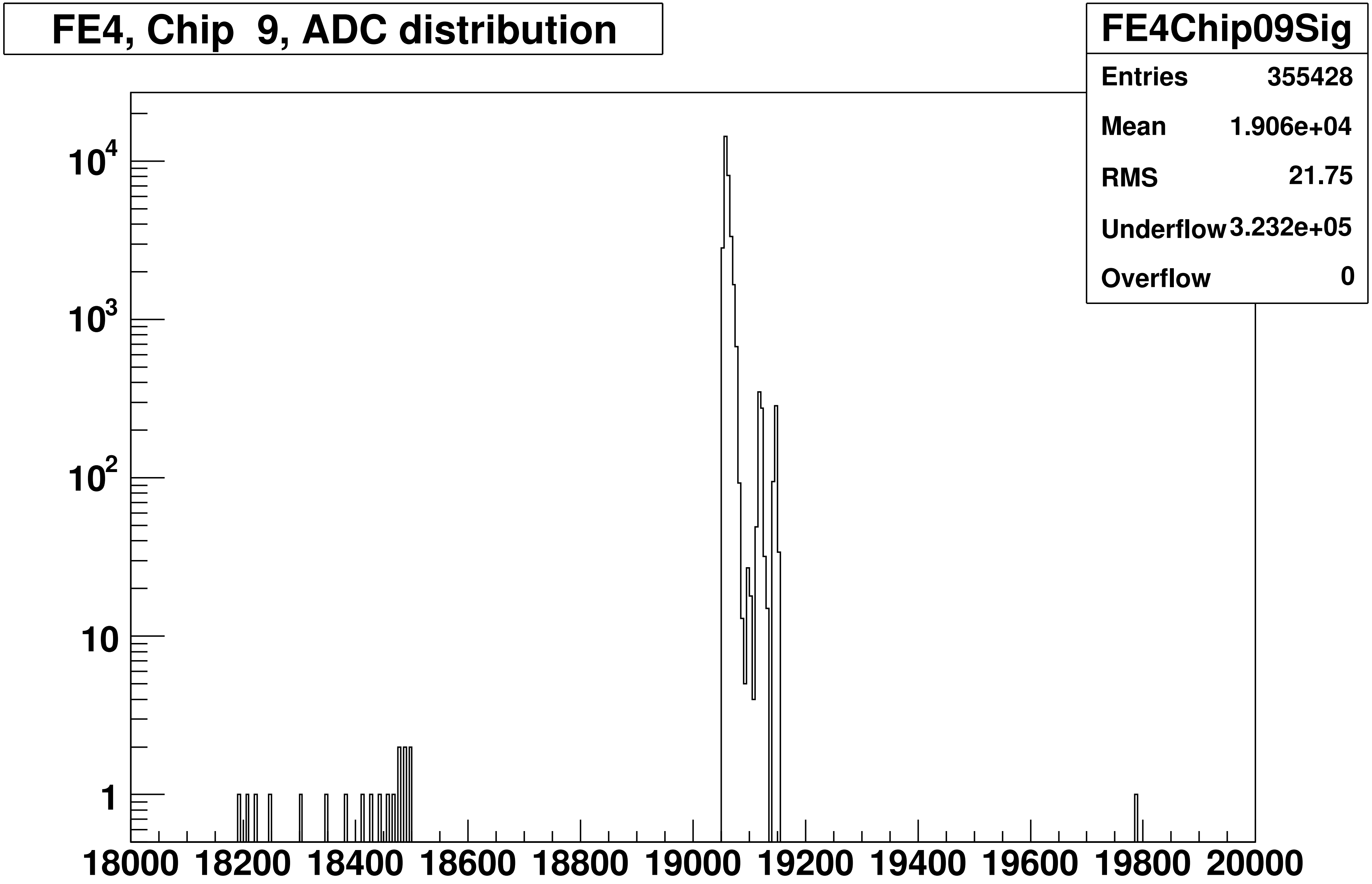
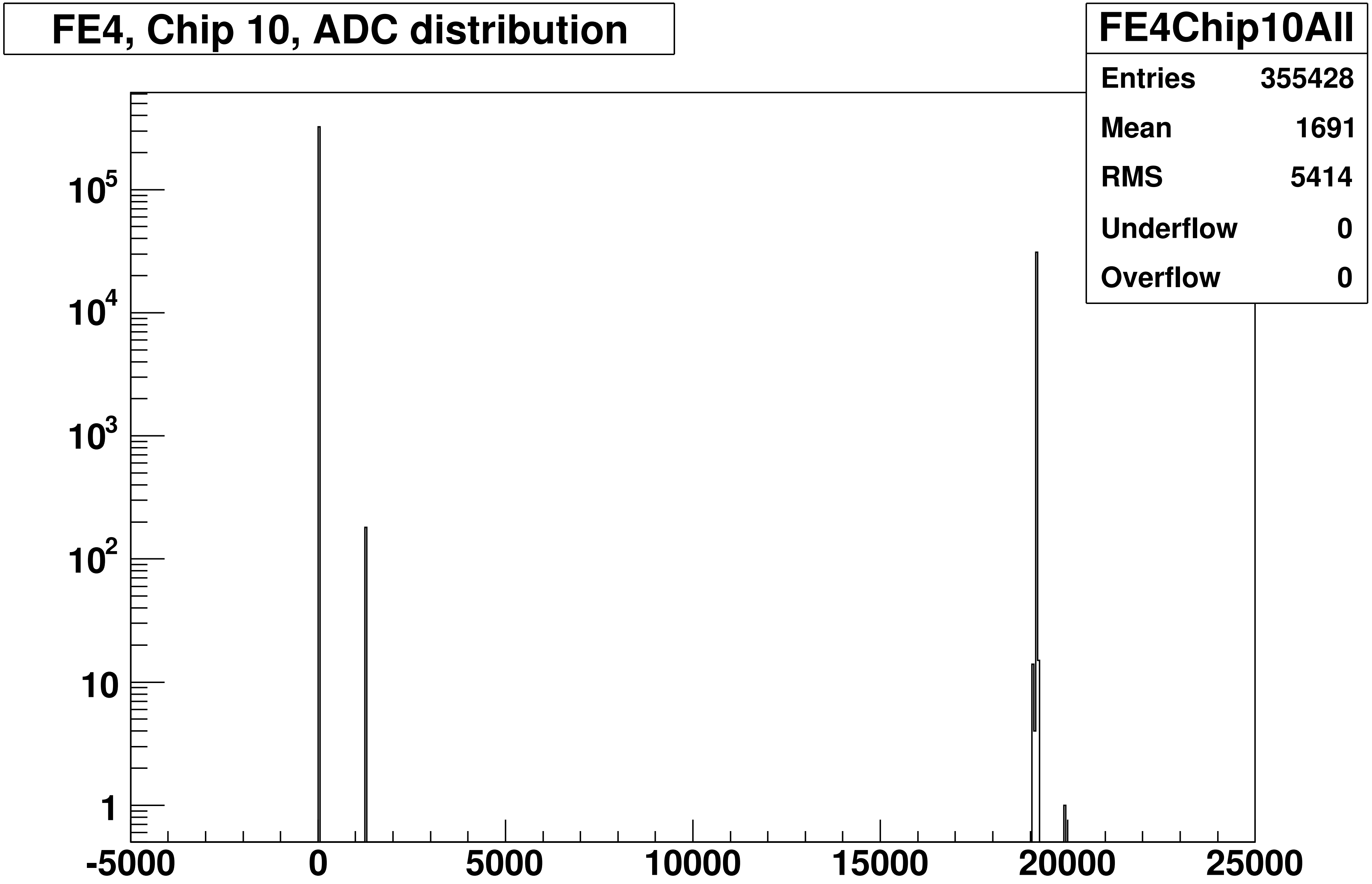
100
150
200



FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 355428
Mean 1.906e+04
RMS 21.75
Underflow 3.232e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 355428
Mean 1691
RMS 5414
Underflow 0
Overflow 0
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10^4
10^3
10^2
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1
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0
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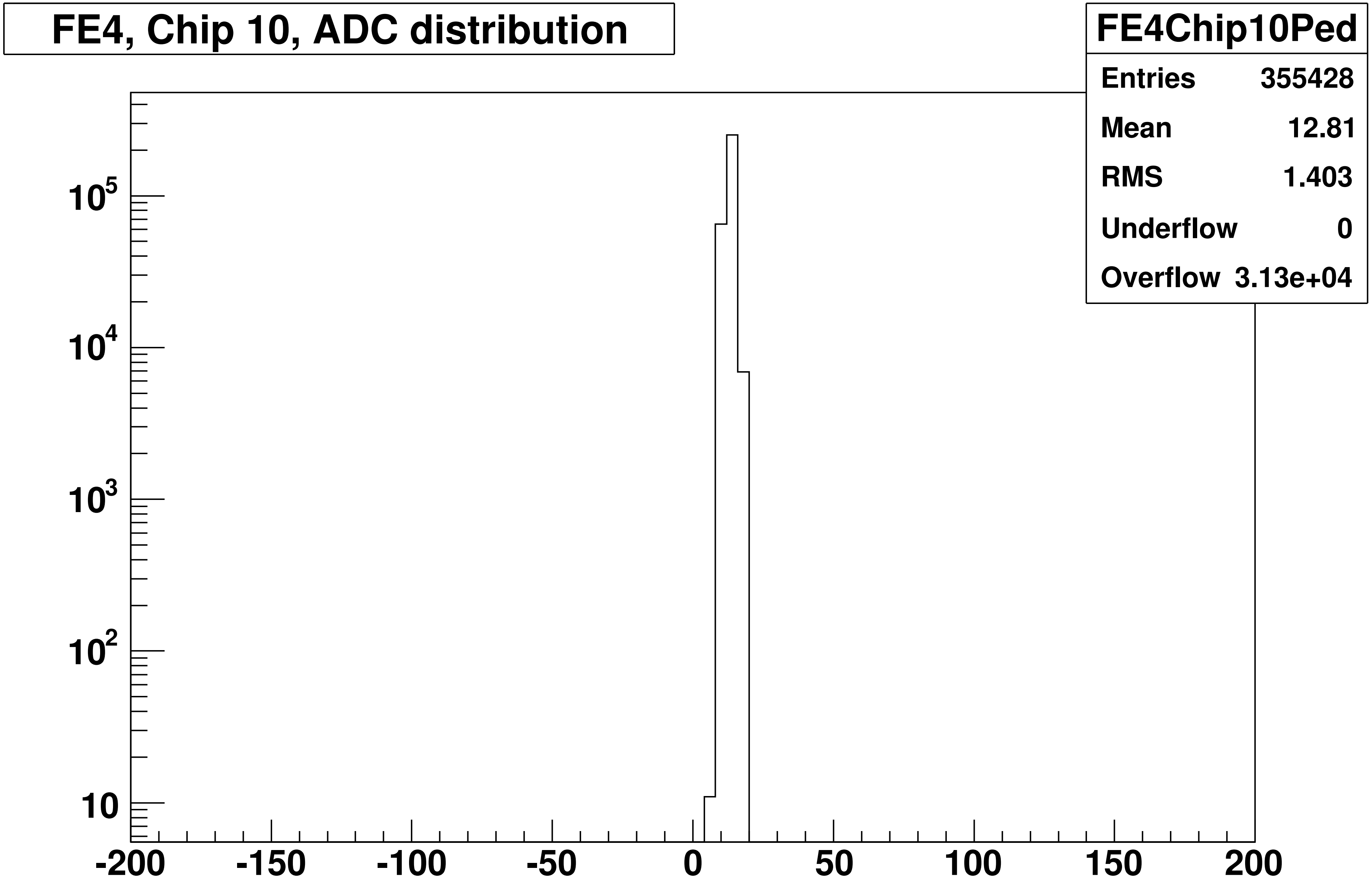
10000
15000
20000
25000


FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 355428
Mean 12.81
RMS 1.403
Underflow 0
Overflow 3.13e+04
10^5
10^4
10^3
10^2
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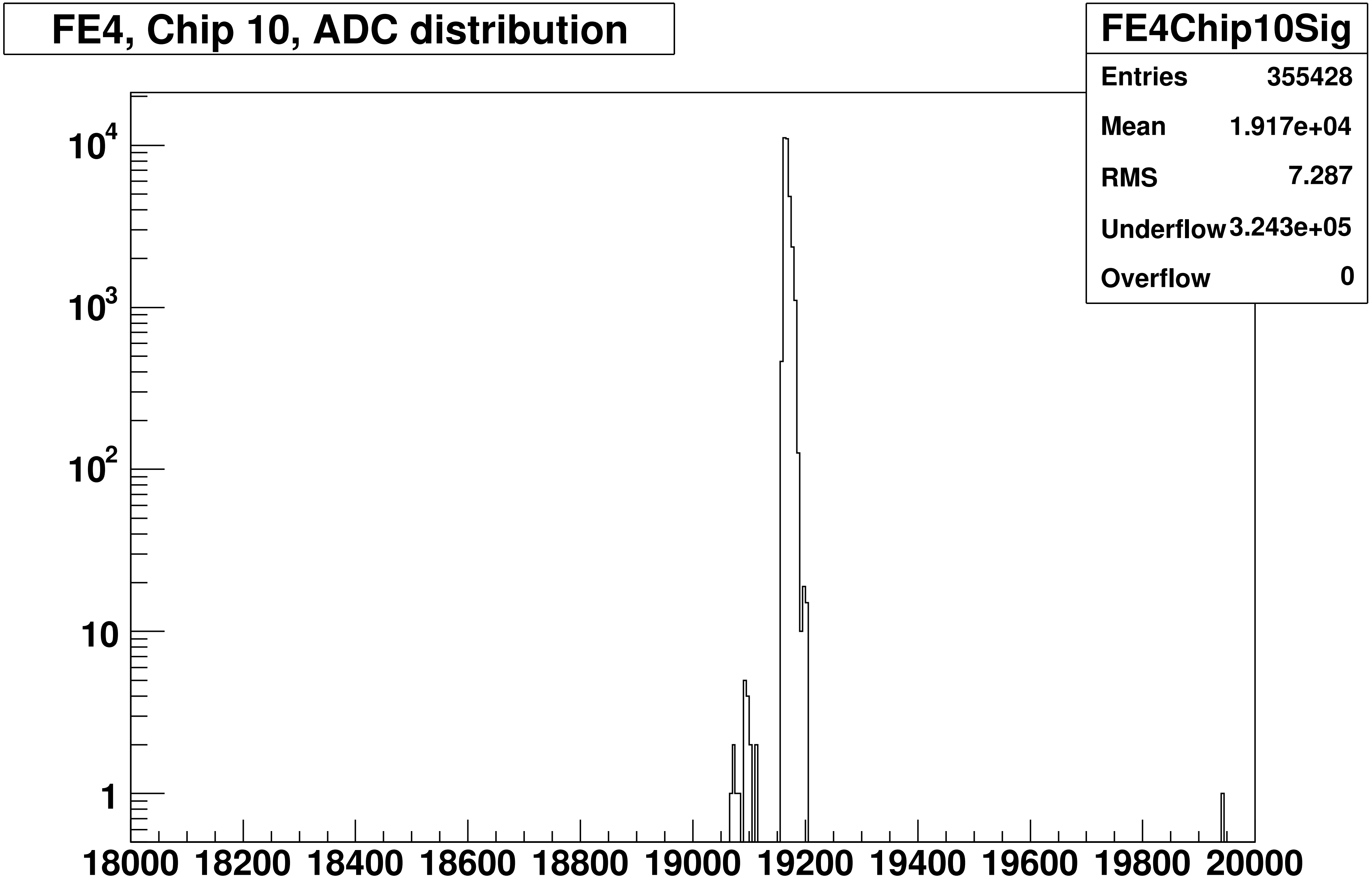
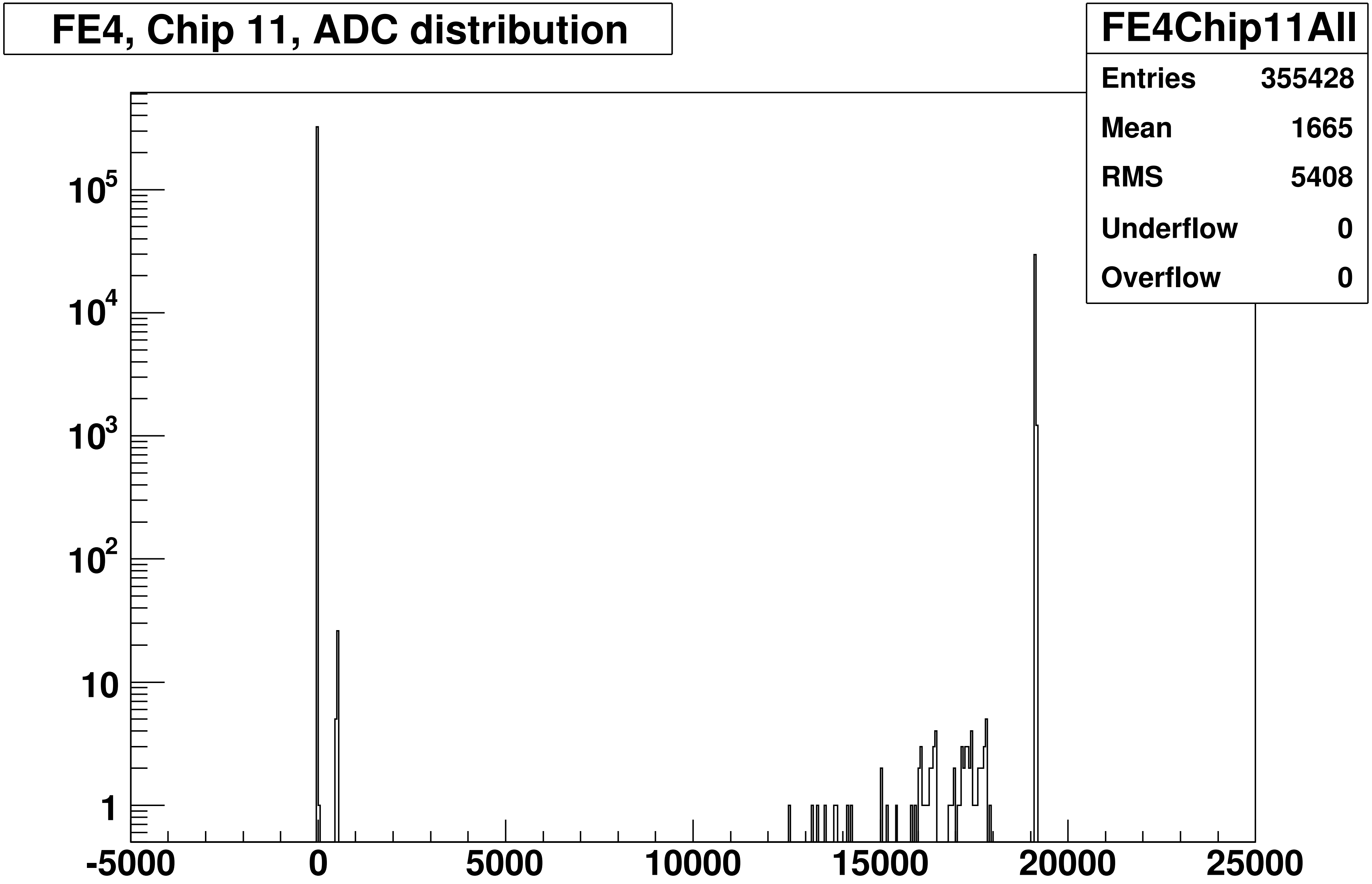
100
150
200



FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 355428
Mean 1.917e+04
RMS 7.287
Underflow 3.243e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 355428
Mean 1665
RMS 5408
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
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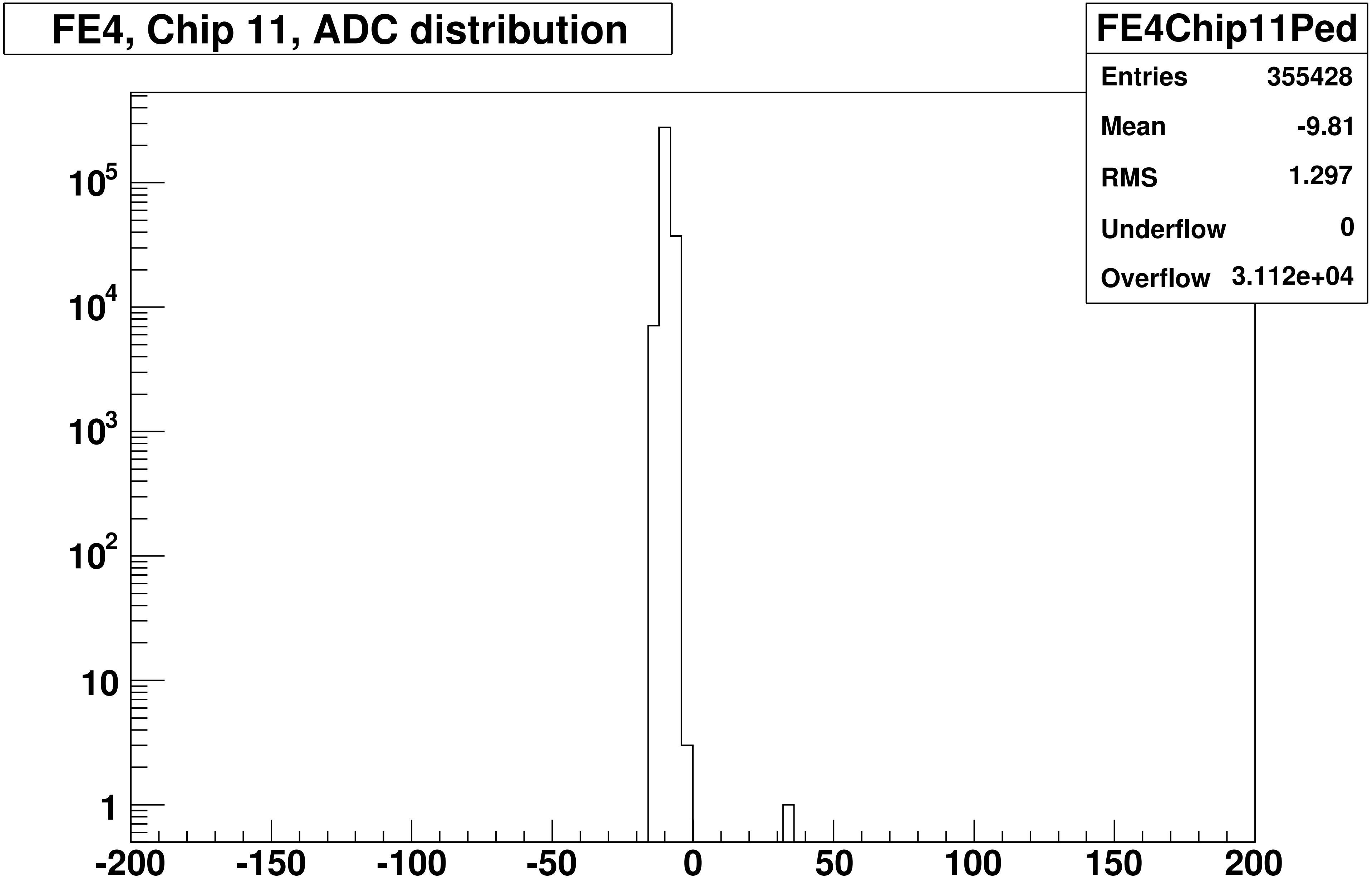
10000
15000
20000
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 355428
Mean -9.81
RMS 1.297
Underflow 0
Overflow 3.112e+04
10^5
10^4
10^3
10^2
10
1
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-100
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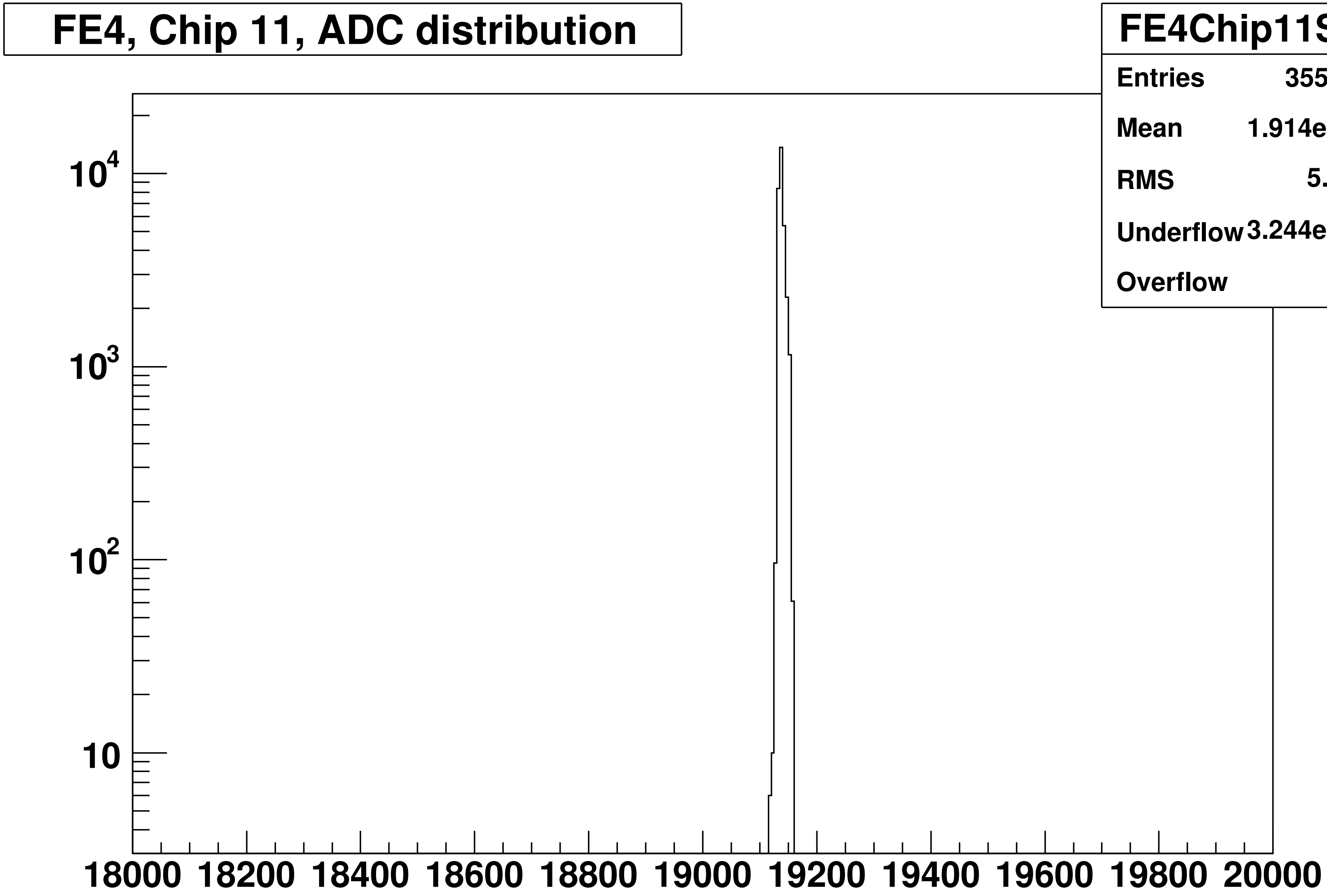
100
150
200



FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 355428
Mean 1.914e+04
RMS 5.056
Underflow 3.244e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
18800
19000
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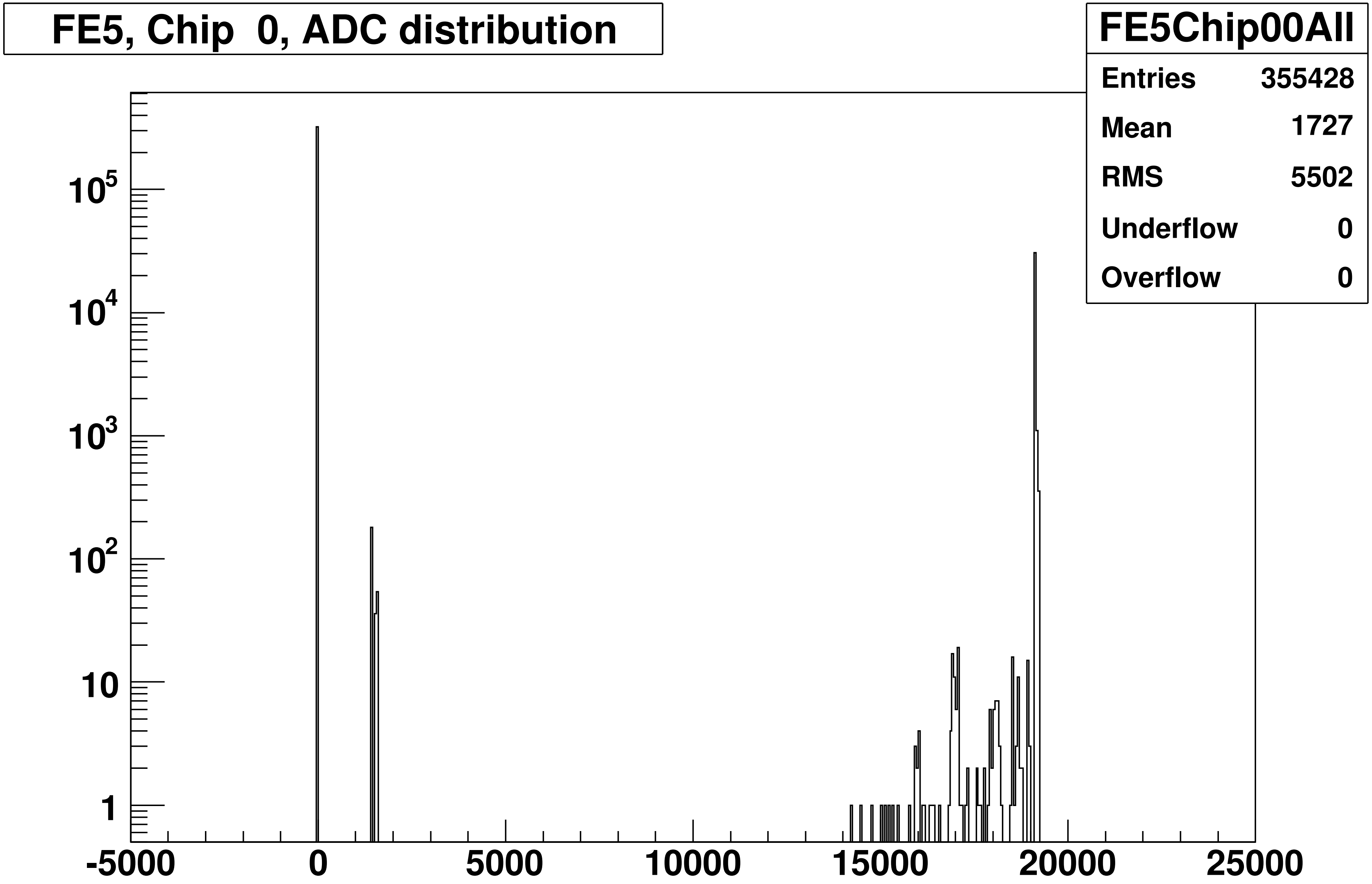
19400
19600
19800
20000


FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 355428
Mean 1727
RMS 5502
Underflow 0
Overflow 0
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10
10^2
10^3
10^4
10^5
-5000
0
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10000
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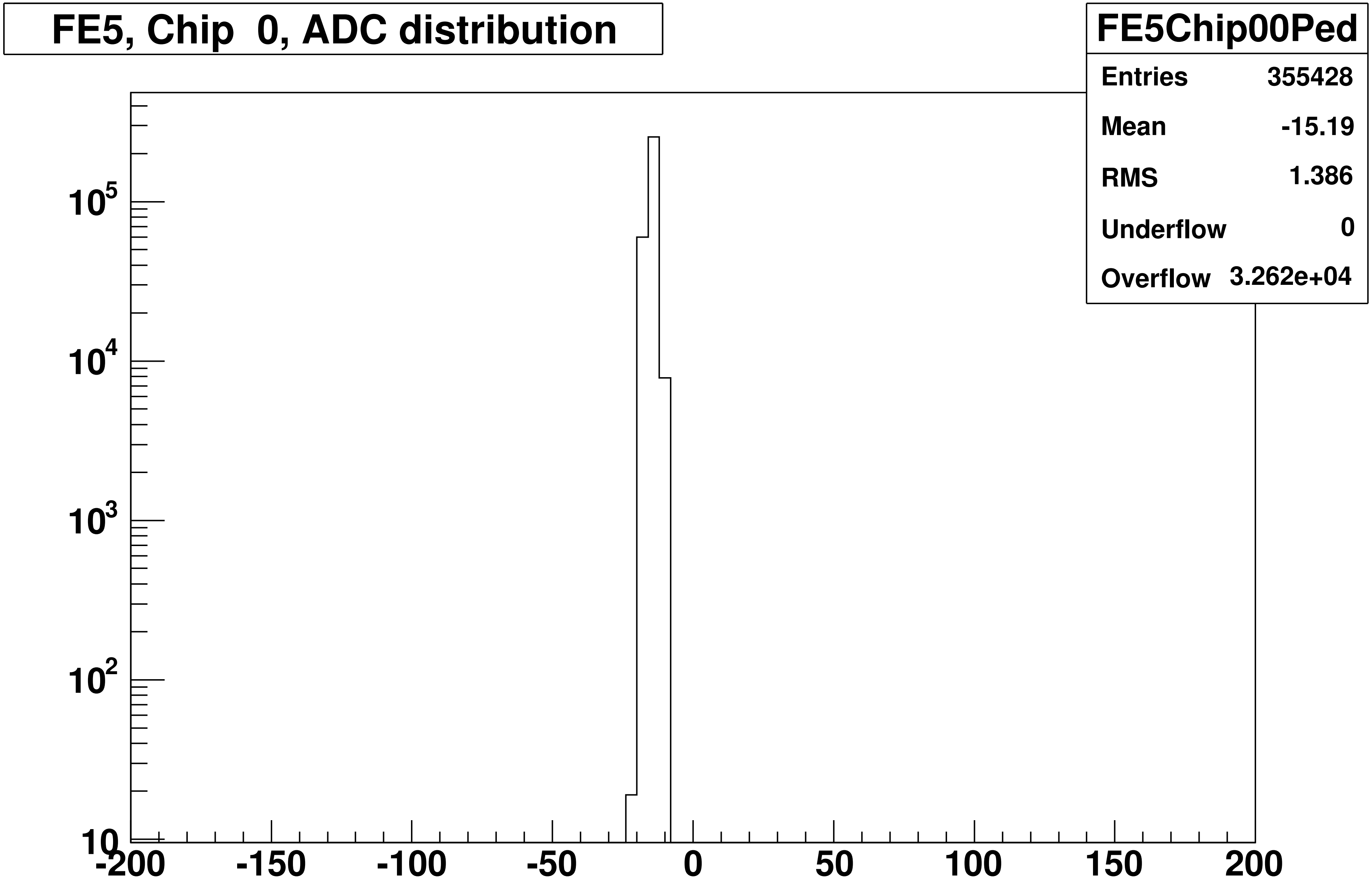
20000
25000



FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 355428
Mean -15.19
RMS 1.386
Underflow 0
Overflow 3.262e+04
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10^4
10^3
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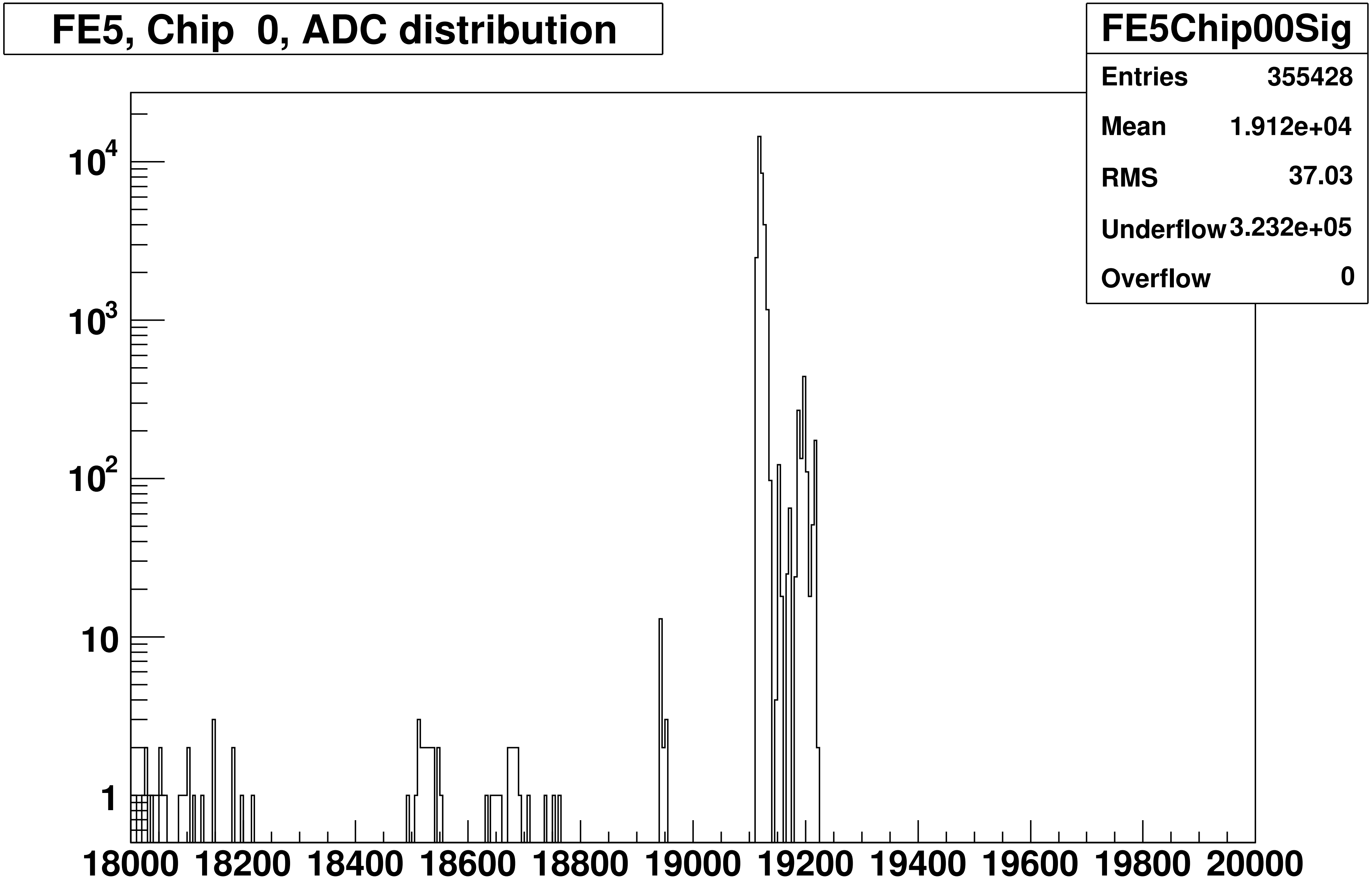
200


FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 355428
Mean 1.912e+04
RMS 37.03
Underflow 3.232e+05
Overflow 0
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10^3
10^2
10
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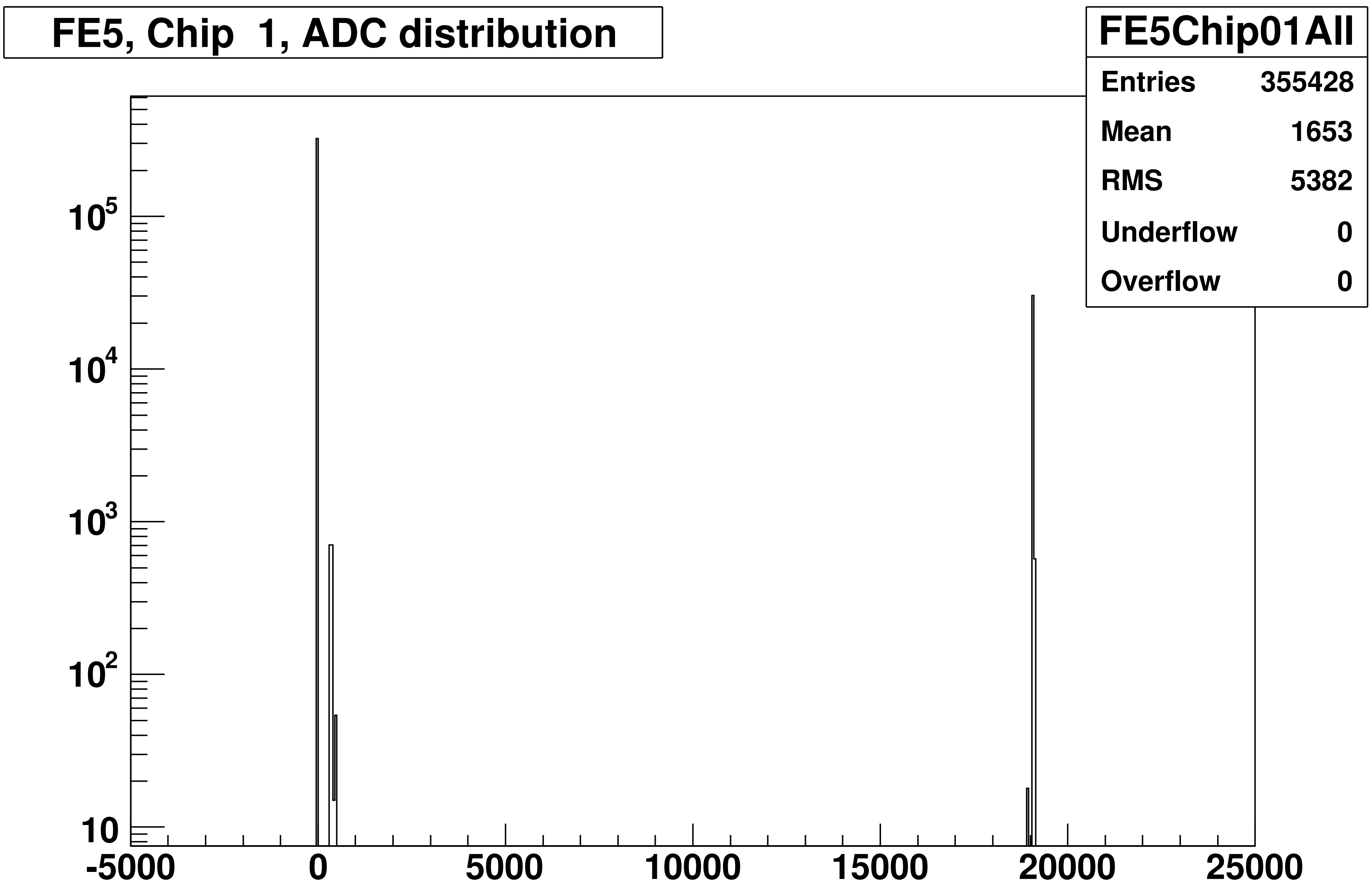
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 355428
Mean 1653
RMS 5382
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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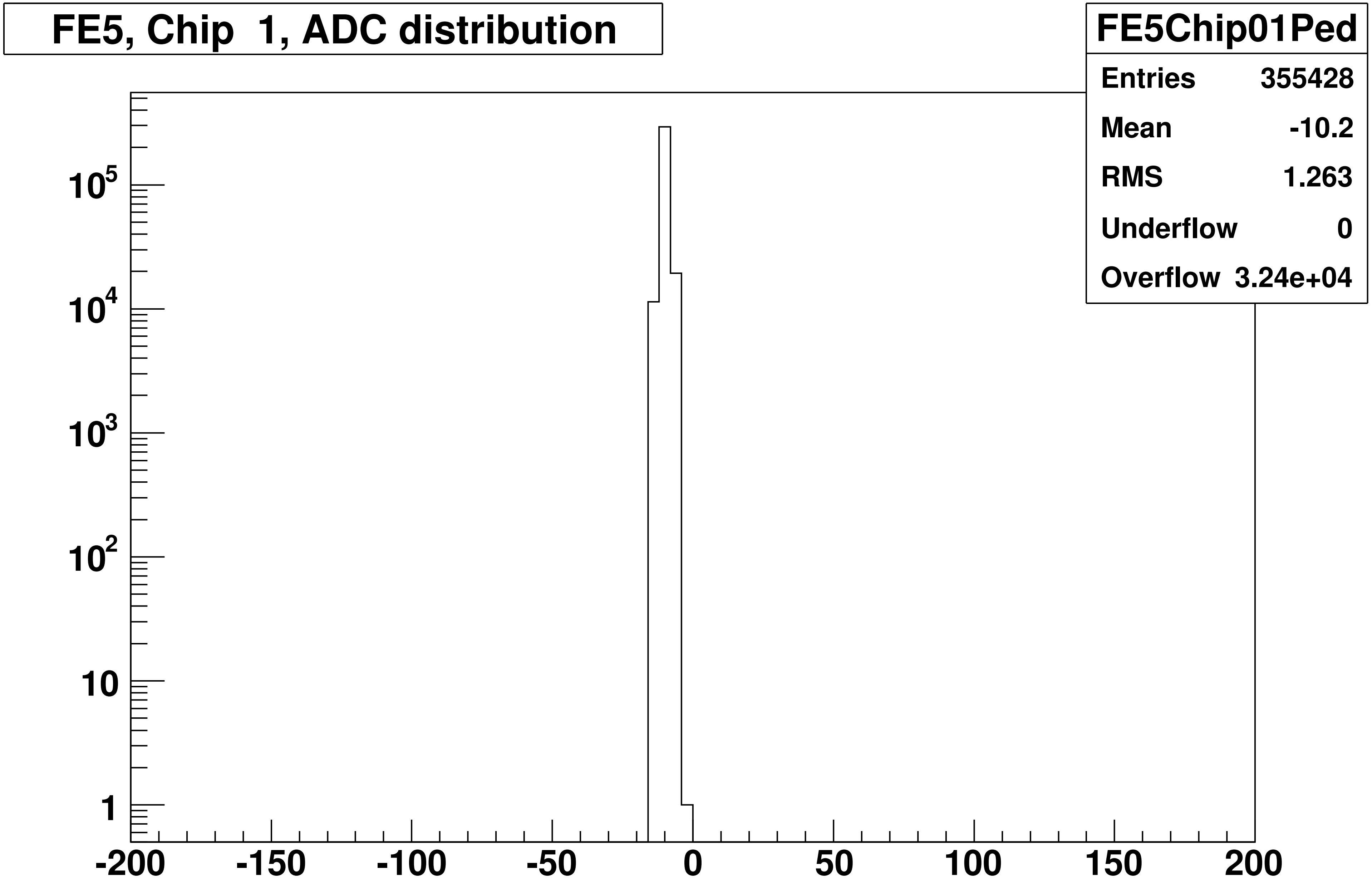

FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 355428
Mean -10.2
RMS 1.263
Underflow 0
Overflow 3.24e+04
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10^3
10^2
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150
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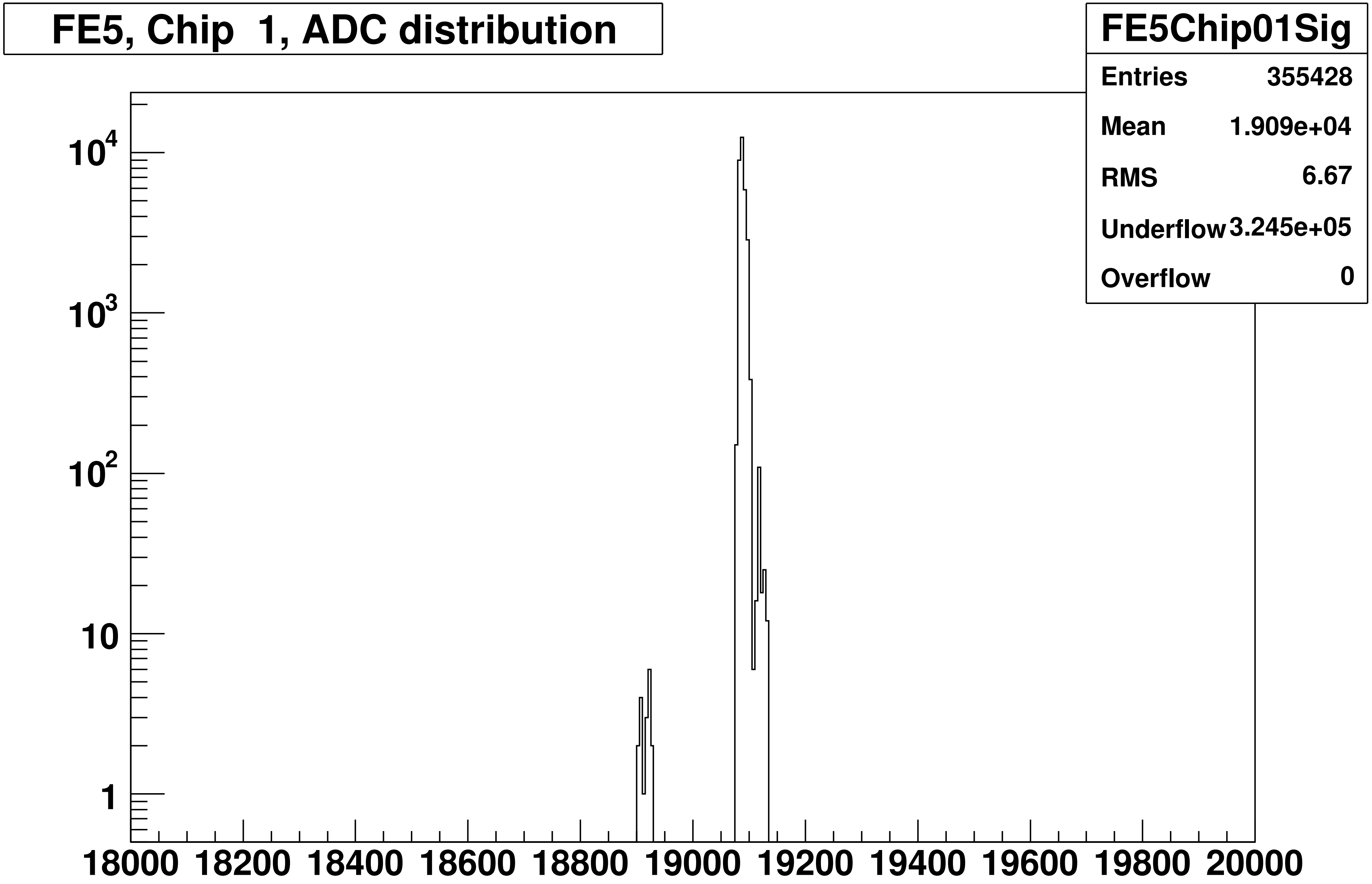

FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 355428
Mean 1.909e+04
RMS 6.67
Underflow 3.245e+05
Overflow 0
10^4
10^3
10^2
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18000
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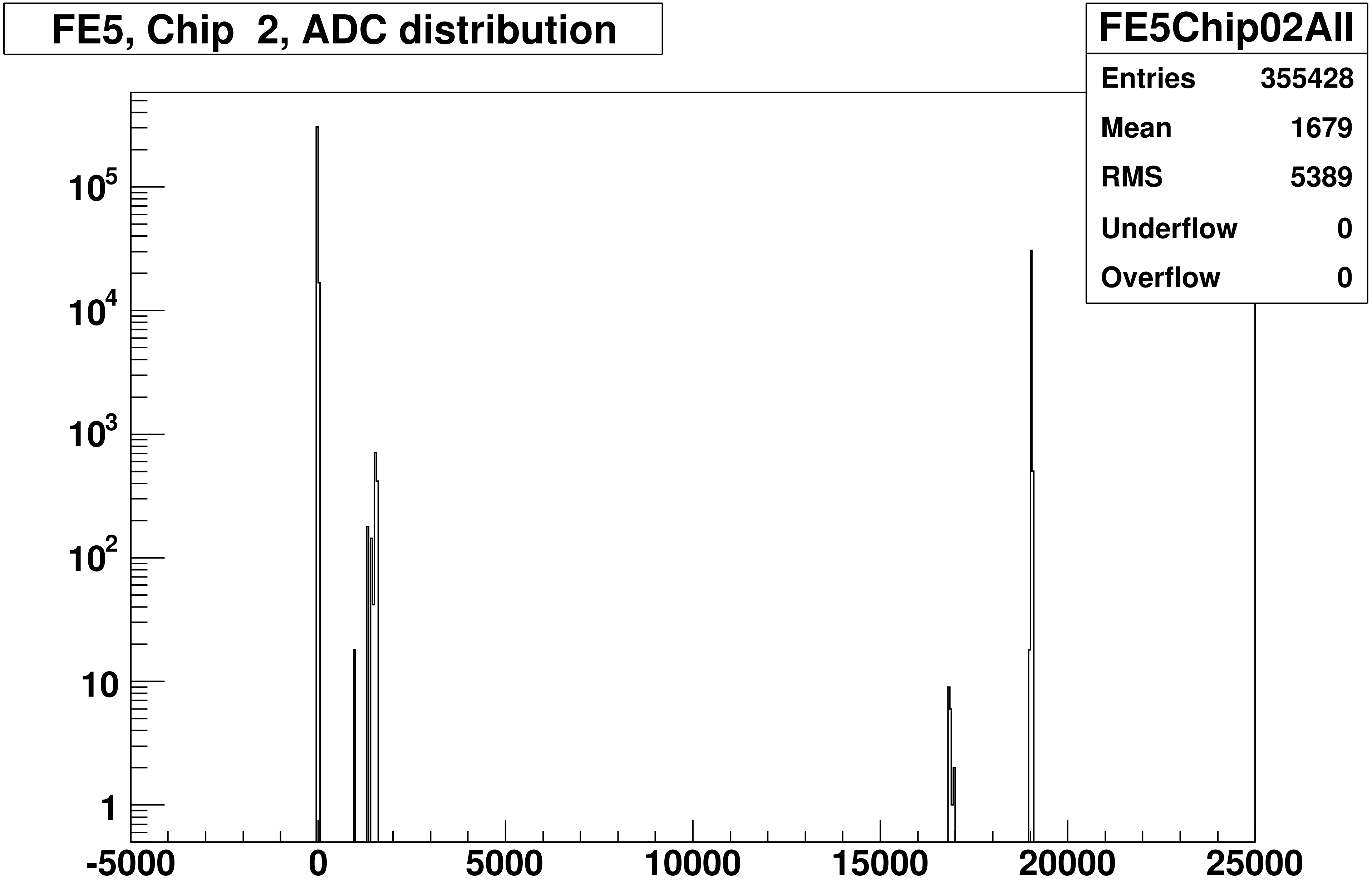

FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 355428
Mean 1679
RMS 5389
Underflow 0
Overflow 0
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10^2
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20000
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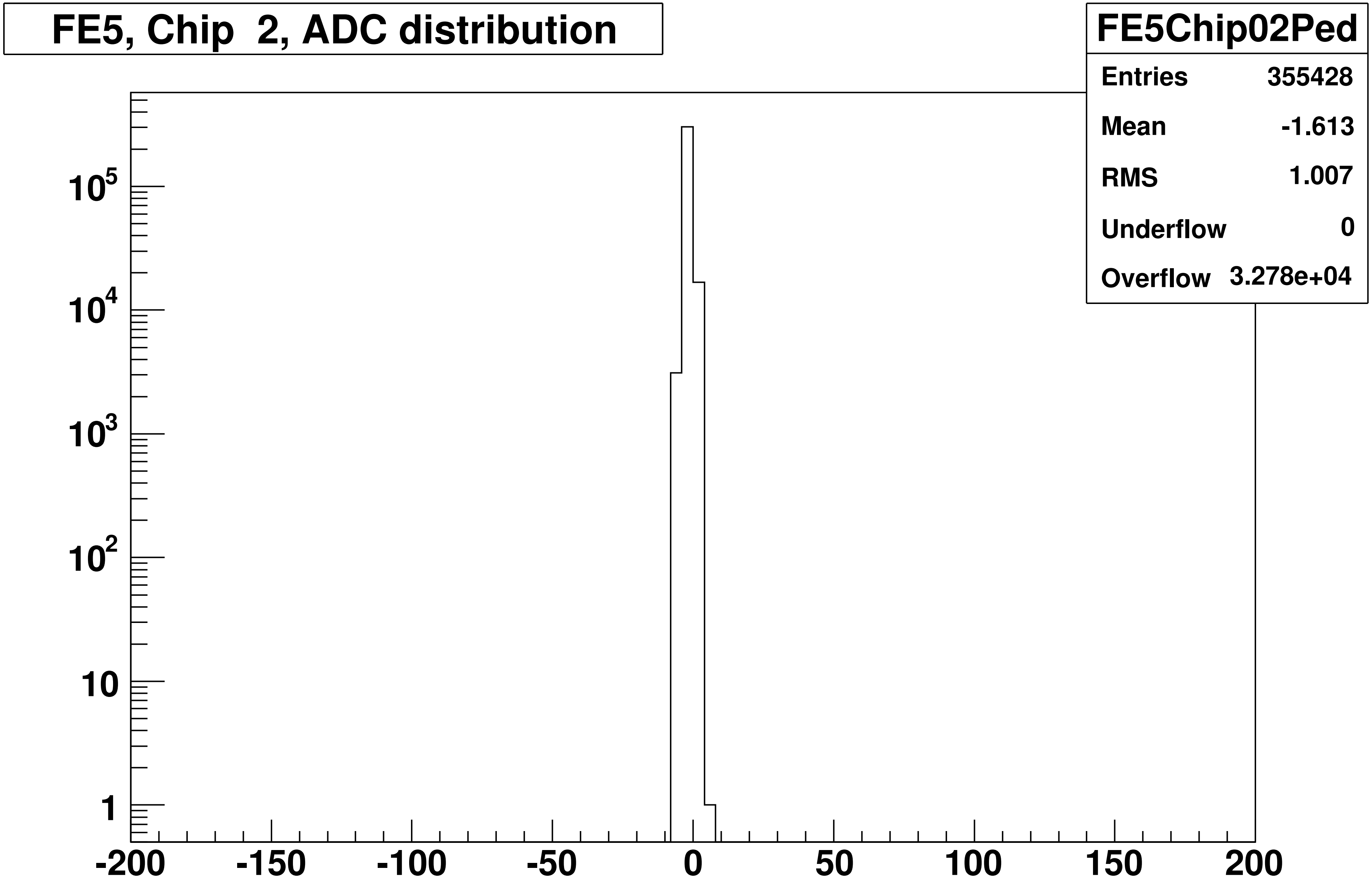
FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 355428
Mean -1.613
RMS 1.007
Underflow 0
Overflow 3.278e+04
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10^3
10^2
10
1
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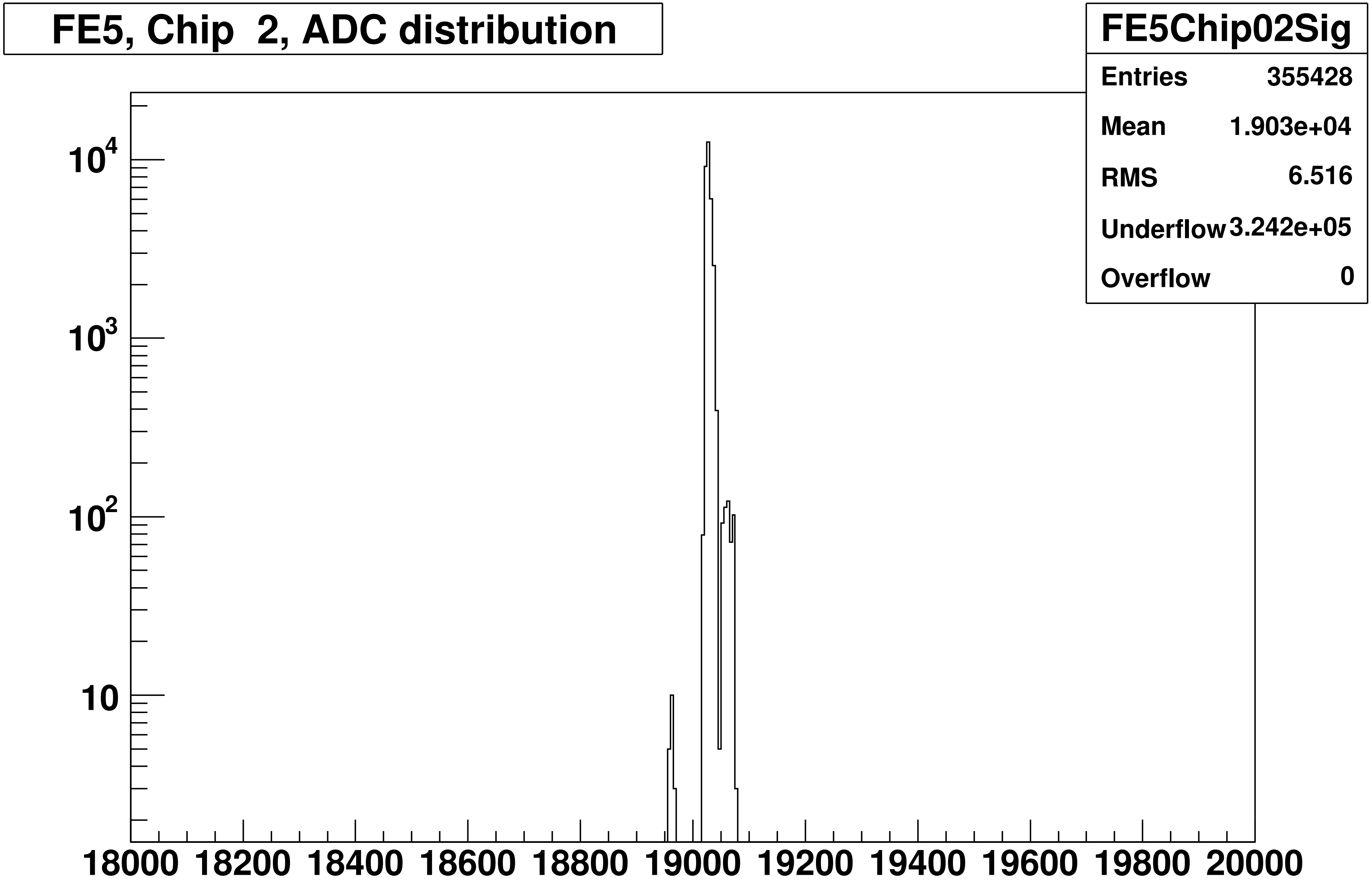
50
100
150
200


FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 355428
Mean 1.903e+04
RMS 6.516
Underflow 3.242e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
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19000
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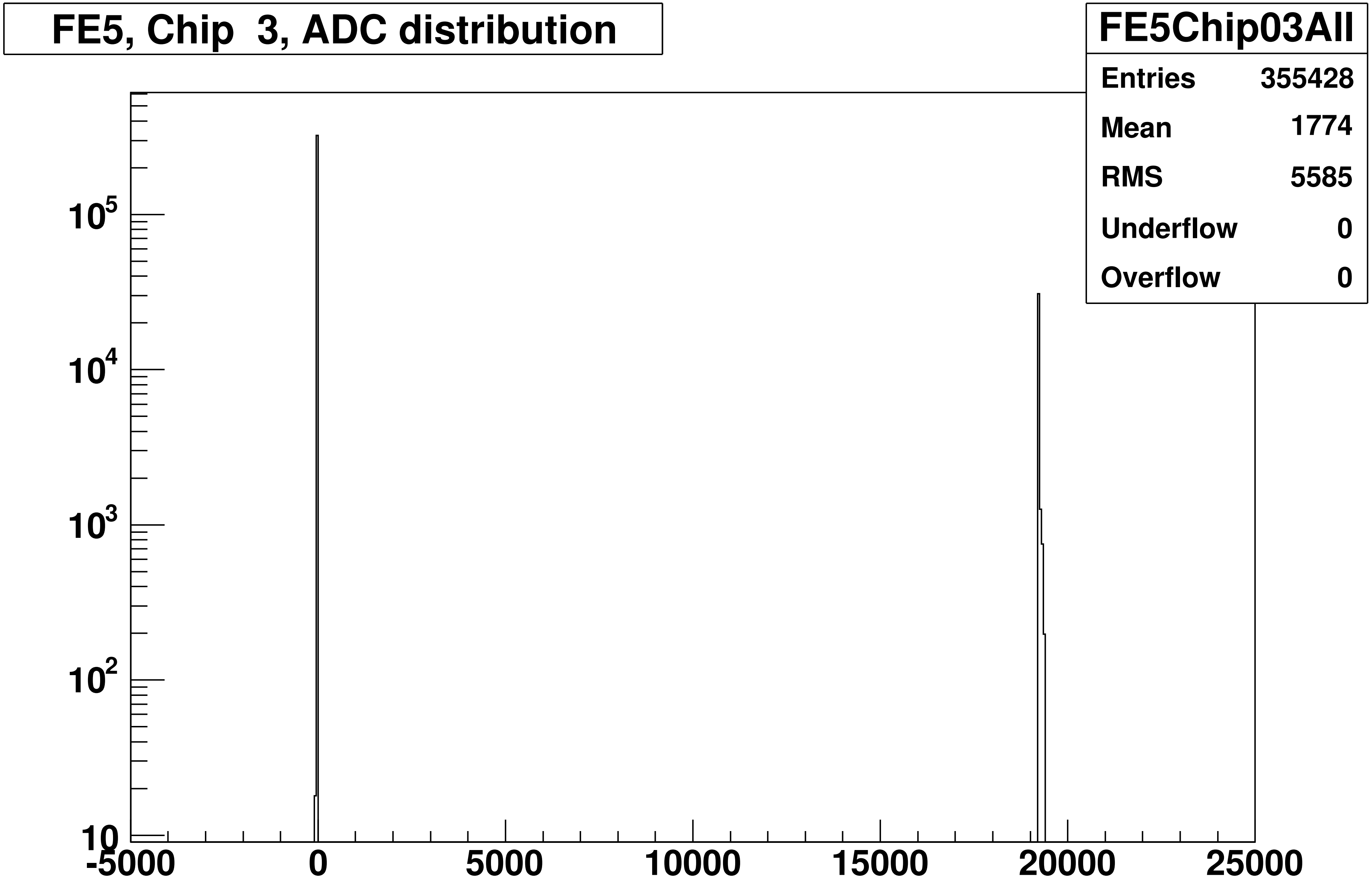
20000


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 355428
Mean 1774
RMS 5585
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
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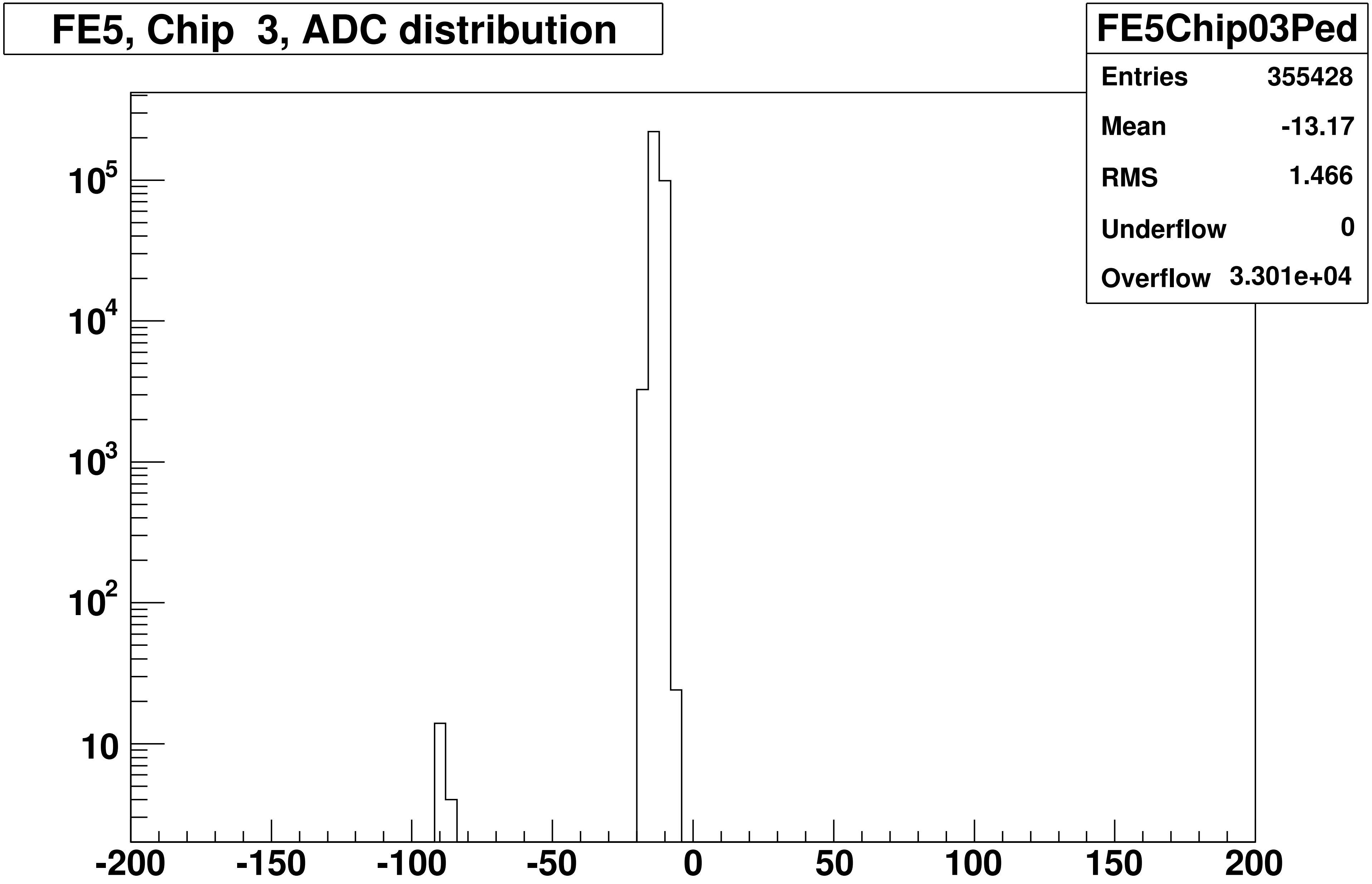
20000
25000



FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 355428
Mean -13.17
RMS 1.466
Underflow 0
Overflow 3.301e+04
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10^4
10^3
10^2
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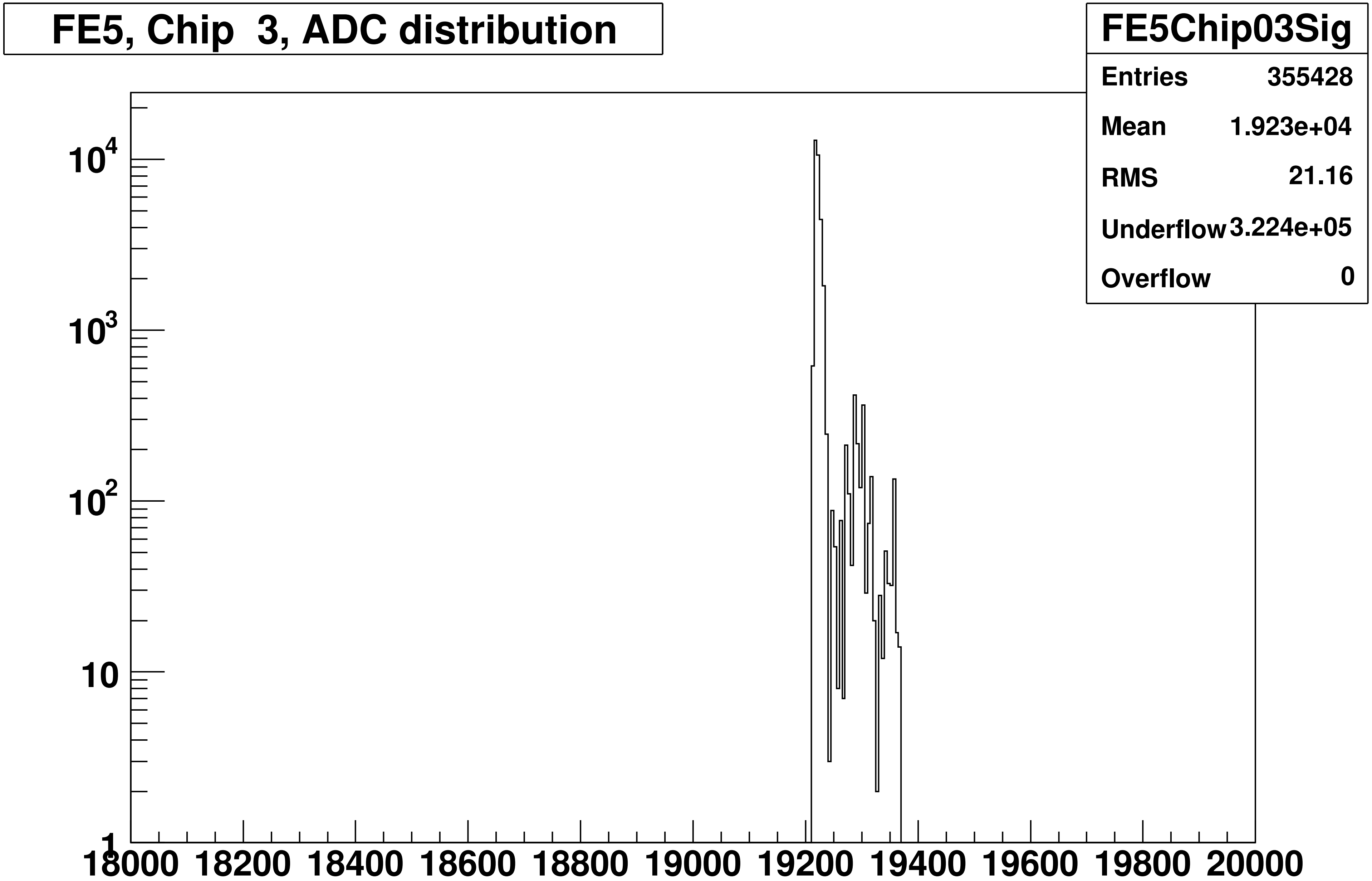
150
200


FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 355428
Mean 1.923e+04
RMS 21.16
Underflow 3.224e+05
Overflow 0
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10^3
10^2
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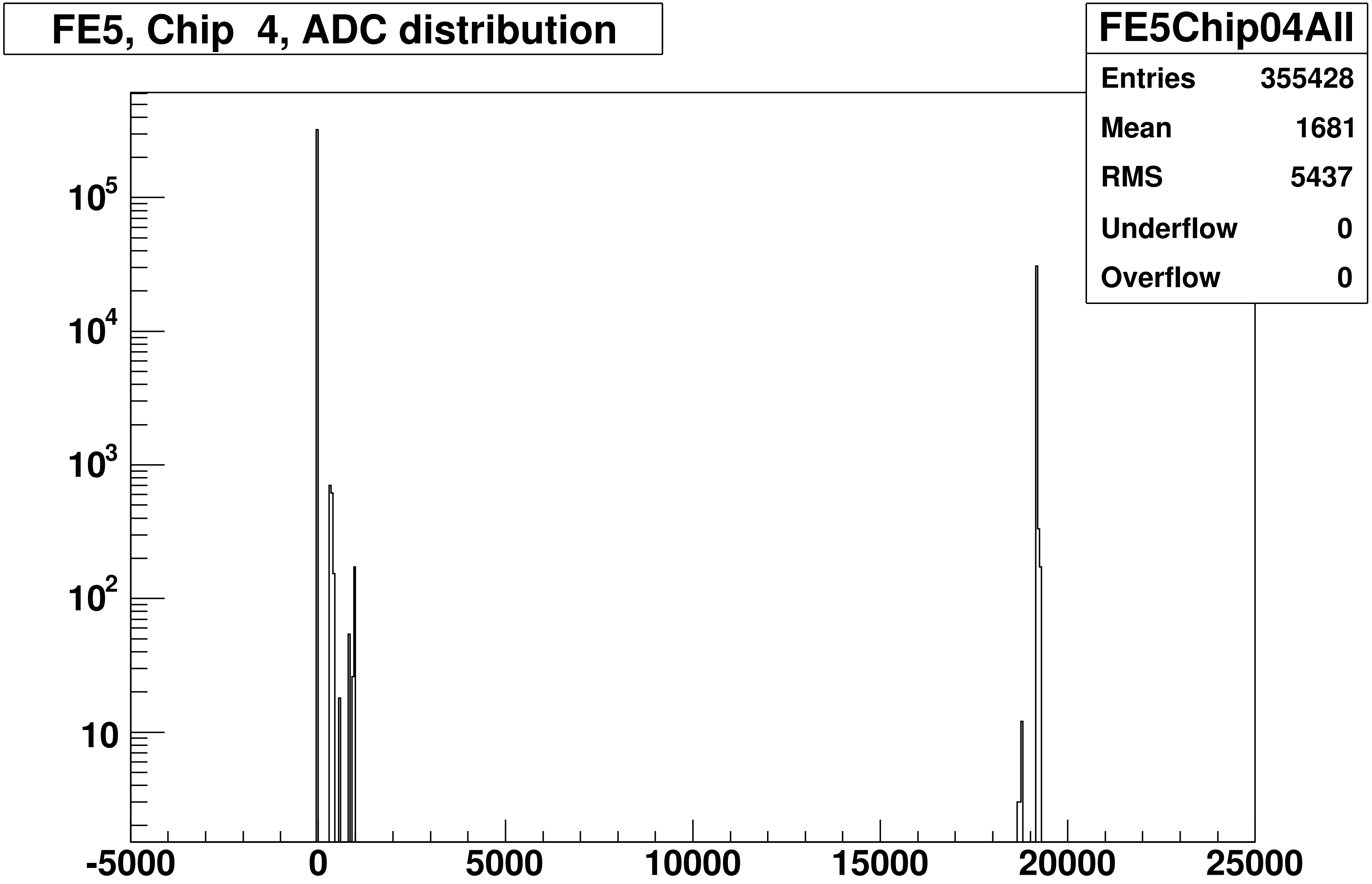
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 355428
Mean 1681
RMS 5437
Underflow 0
Overflow 0
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10^4
10^3
10^2
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-5000
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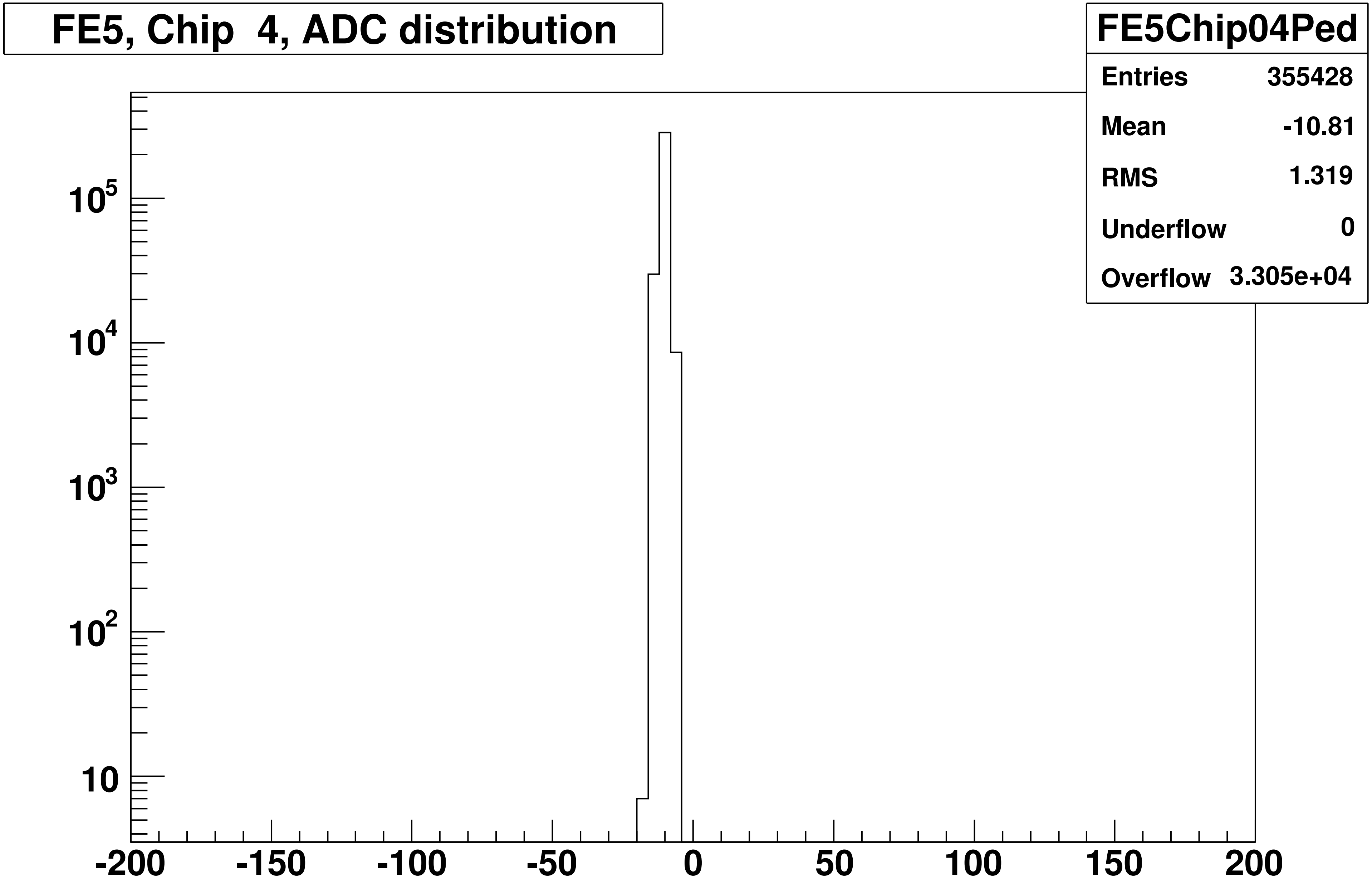
20000
25000



FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 355428
Mean -10.81
RMS 1.319
Underflow 0
Overflow 3.305e+04
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10^4
10^3
10^2
10
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-100
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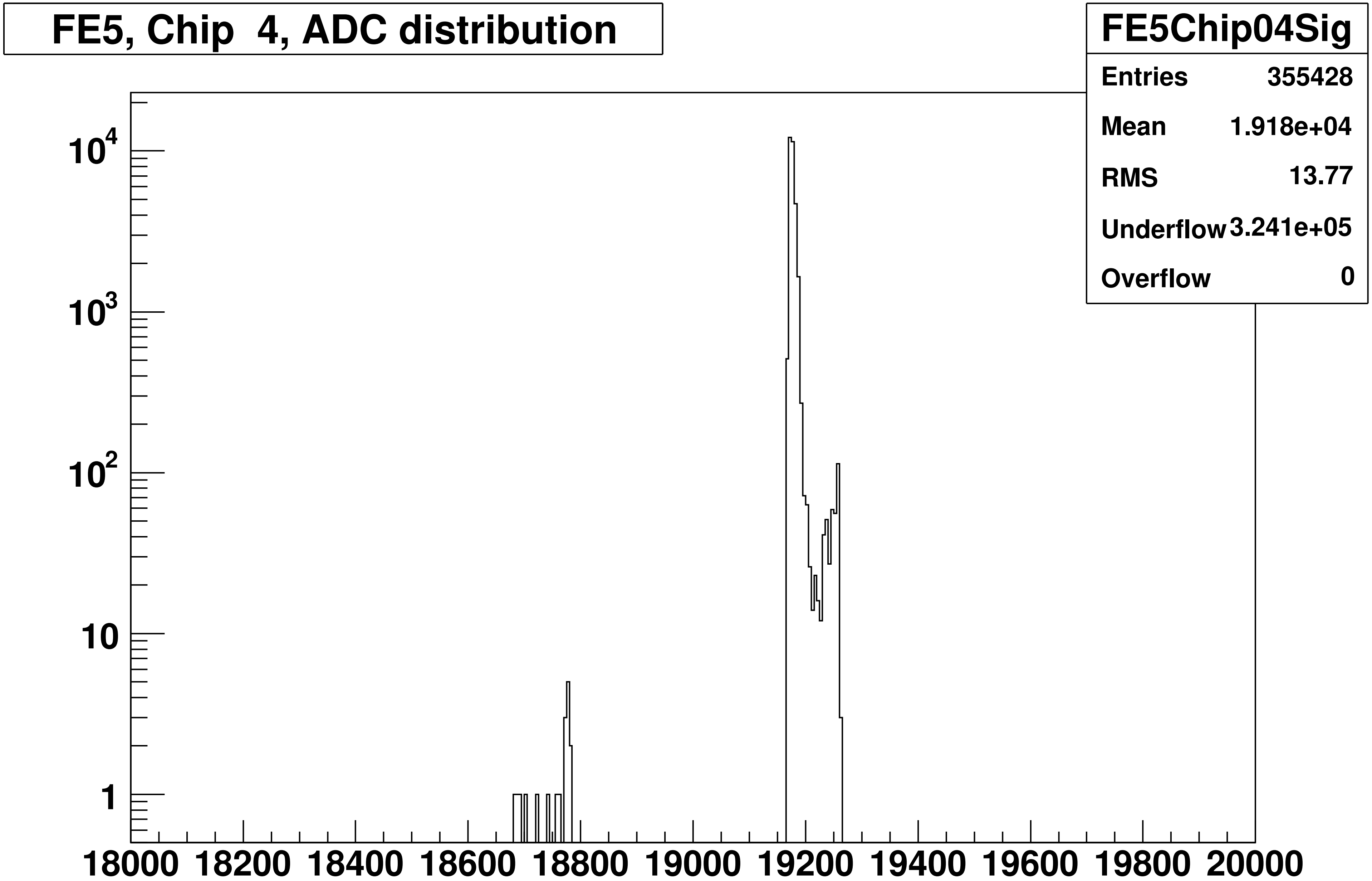
0
50
100
150
200



FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 355428
Mean 1.918e+04
RMS 13.77
Underflow 3.241e+05
Overflow 0
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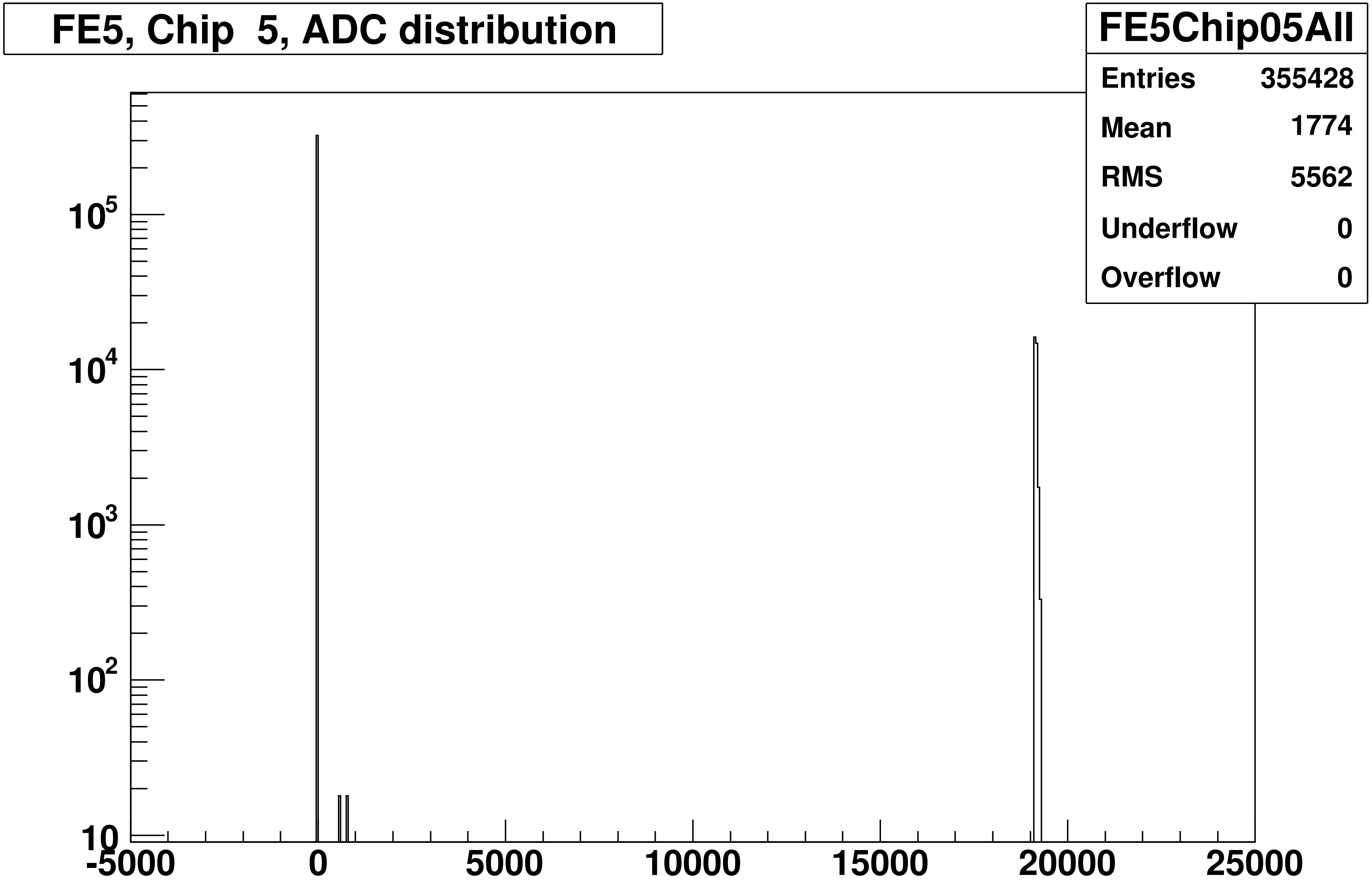
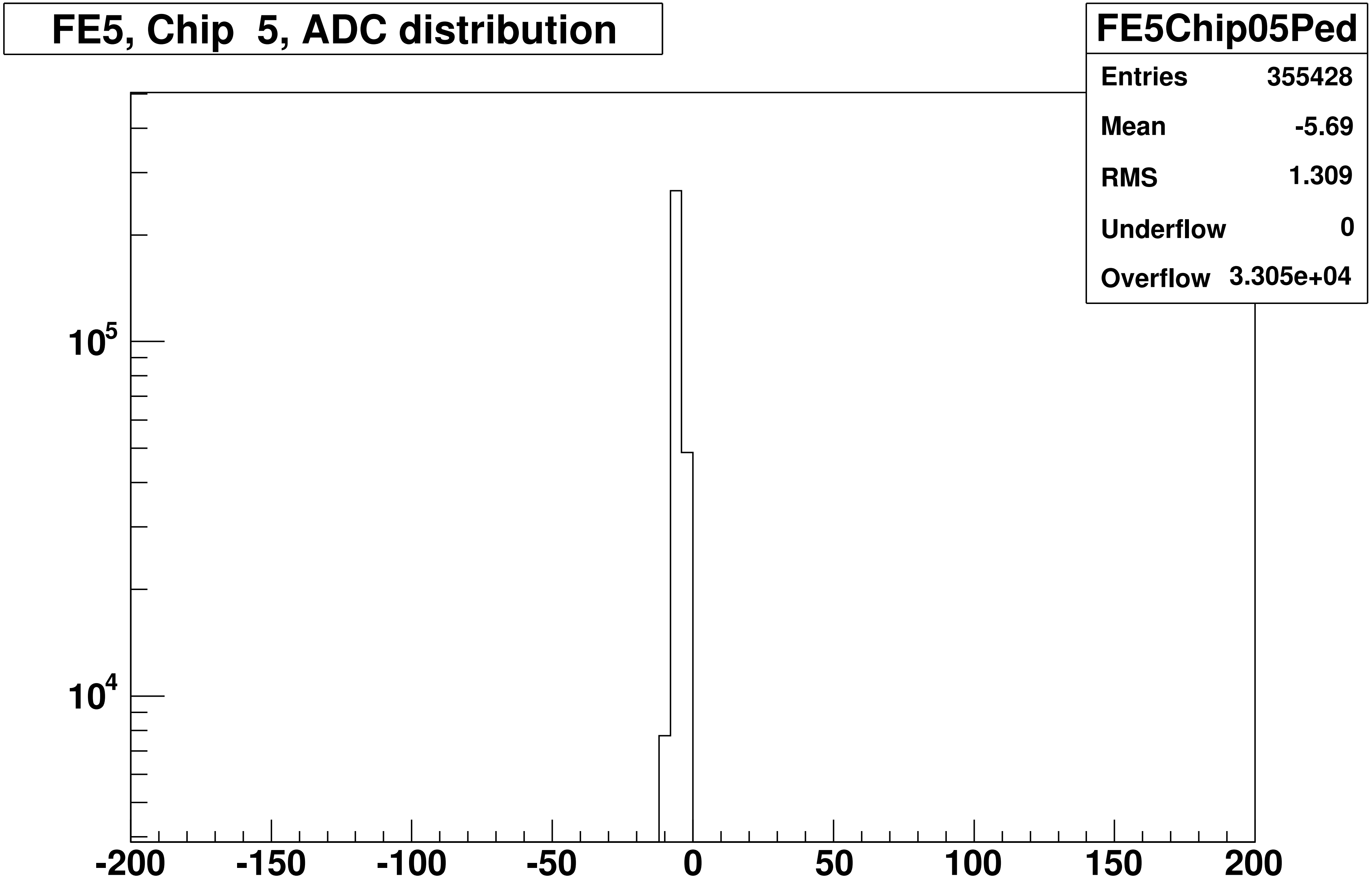
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 355428
Mean 1774
RMS 5562
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 355428
Mean -5.69
RMS 1.309
Underflow 0
Overflow 3.305e+04
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10^4
-200
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-100
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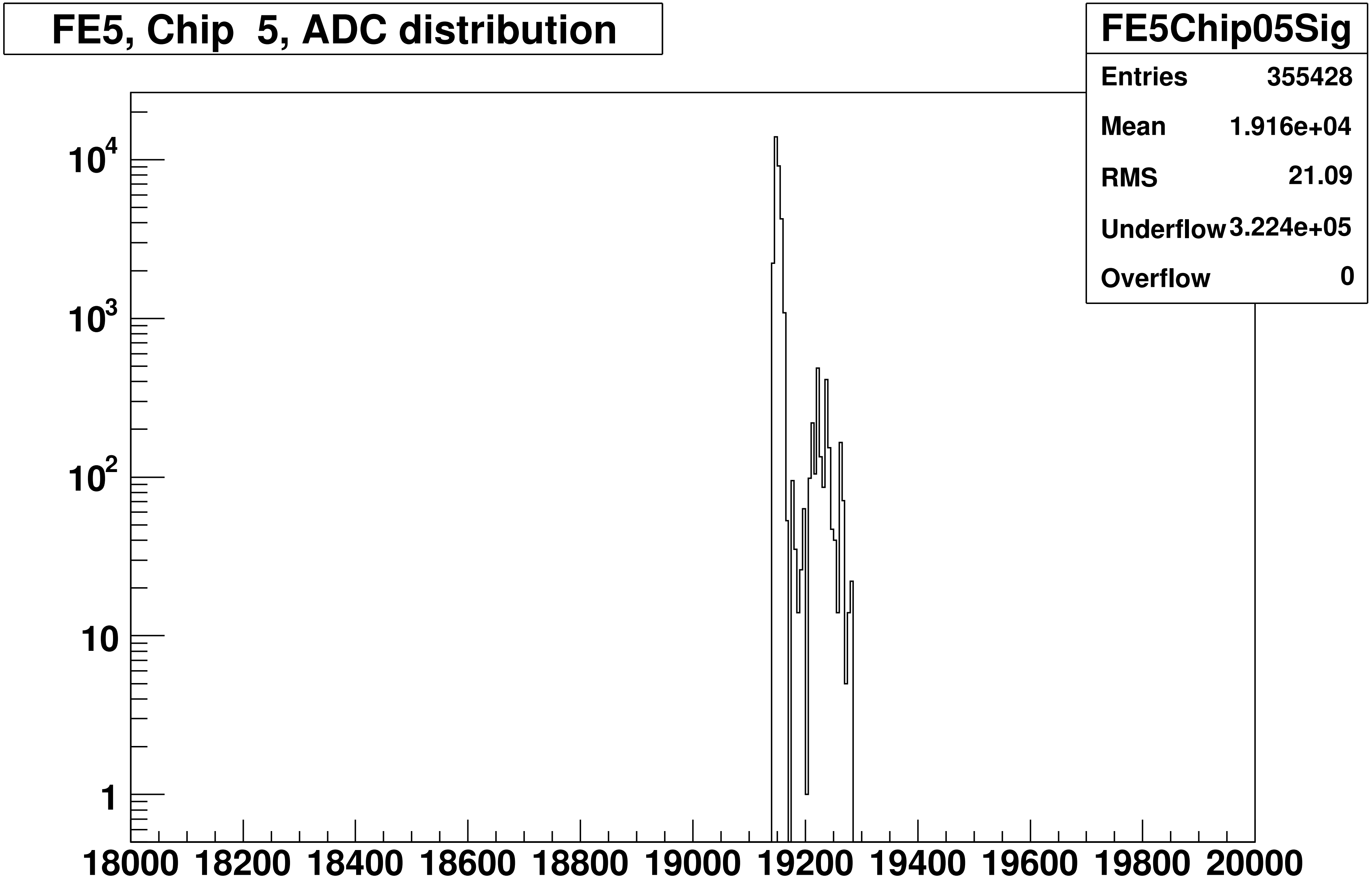
100
150
200


FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 355428
Mean 1.916e+04
RMS 21.09
Underflow 3.224e+05
Overflow 0
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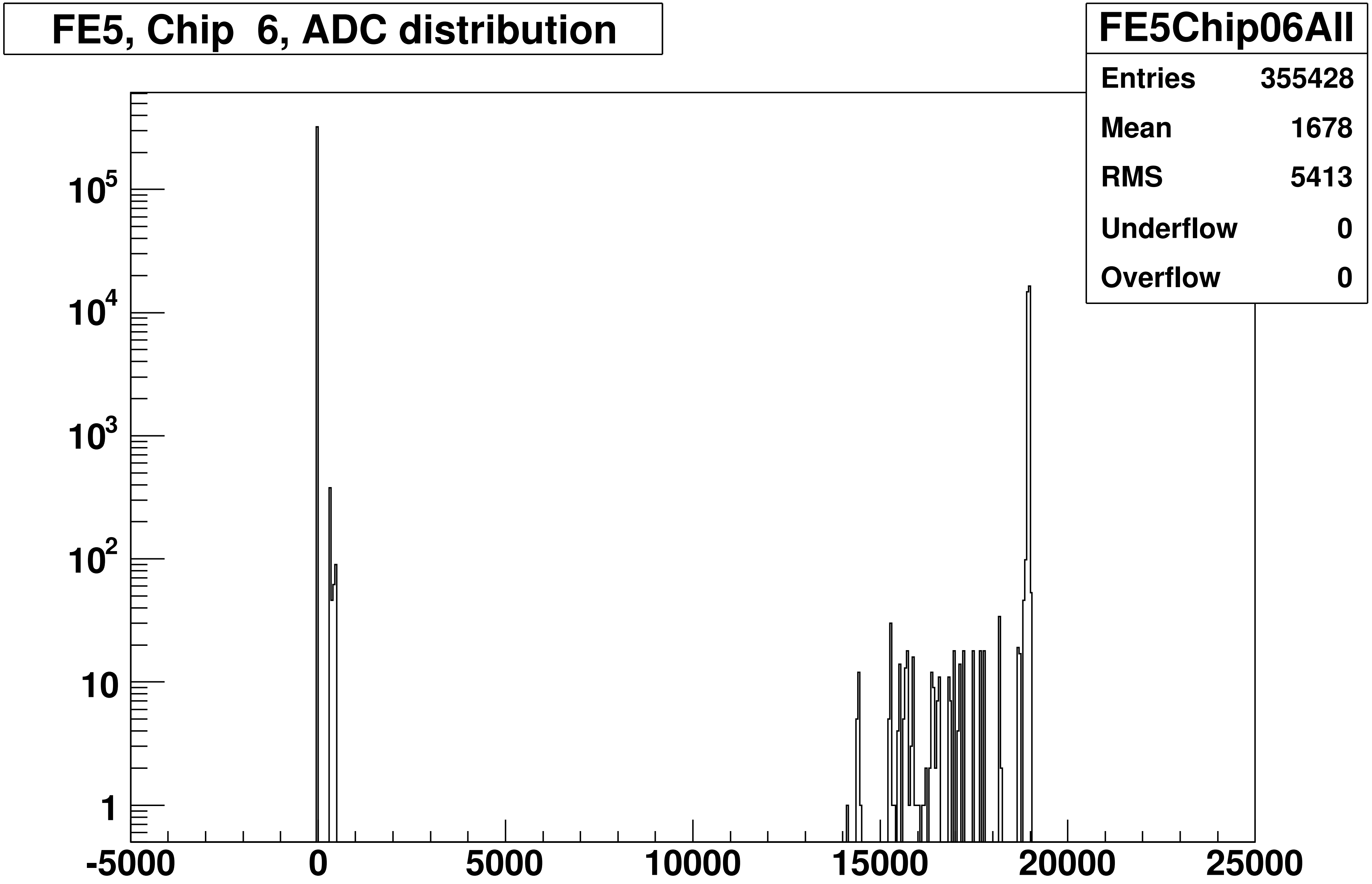
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 355428
Mean 1678
RMS 5413
Underflow 0
Overflow 0
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10^3
10^2
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10000
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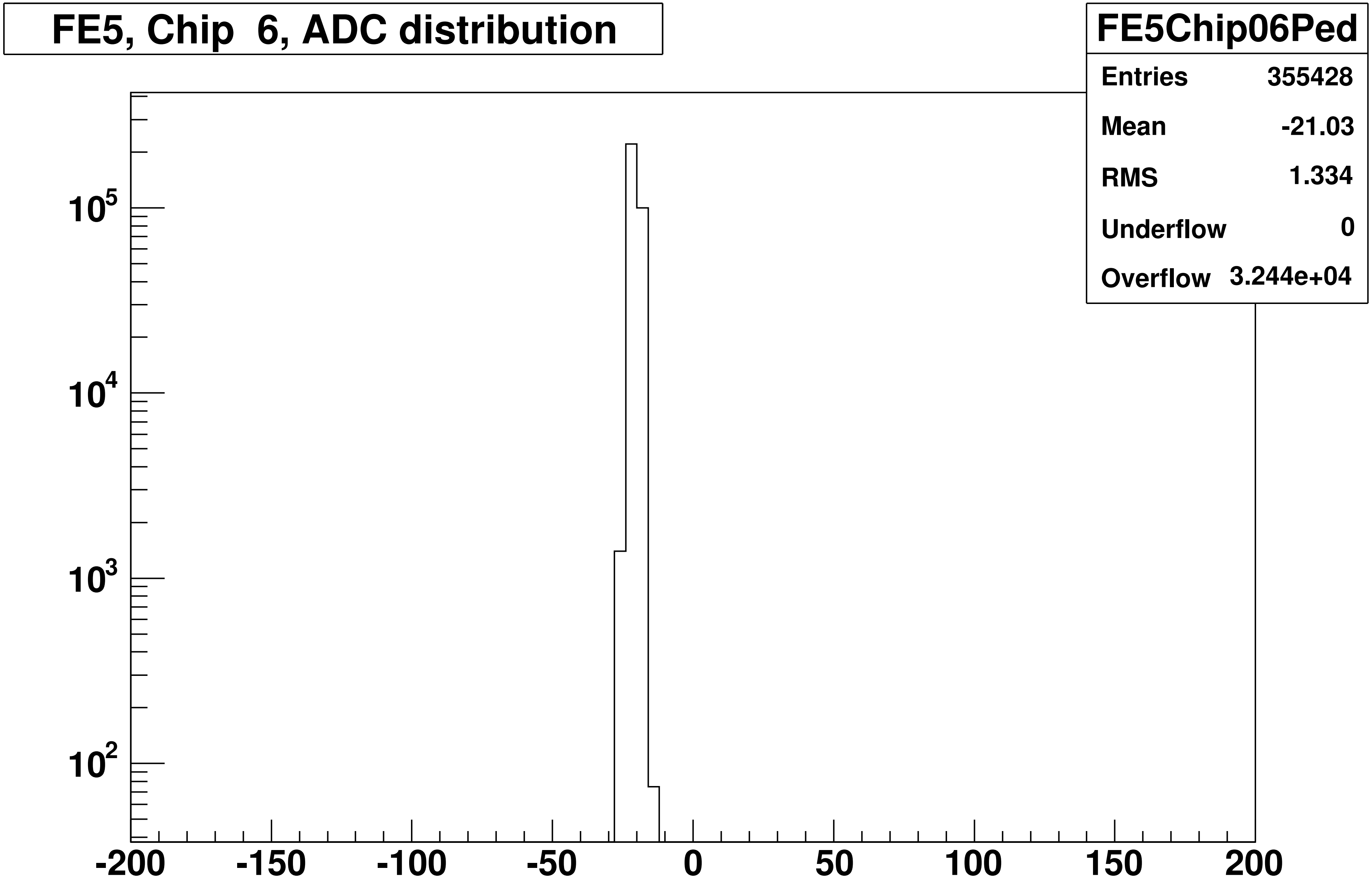
25000



FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 355428
Mean -21.03
RMS 1.334
Underflow 0
Overflow 3.244e+04
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10^4
10^3
10^2
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50
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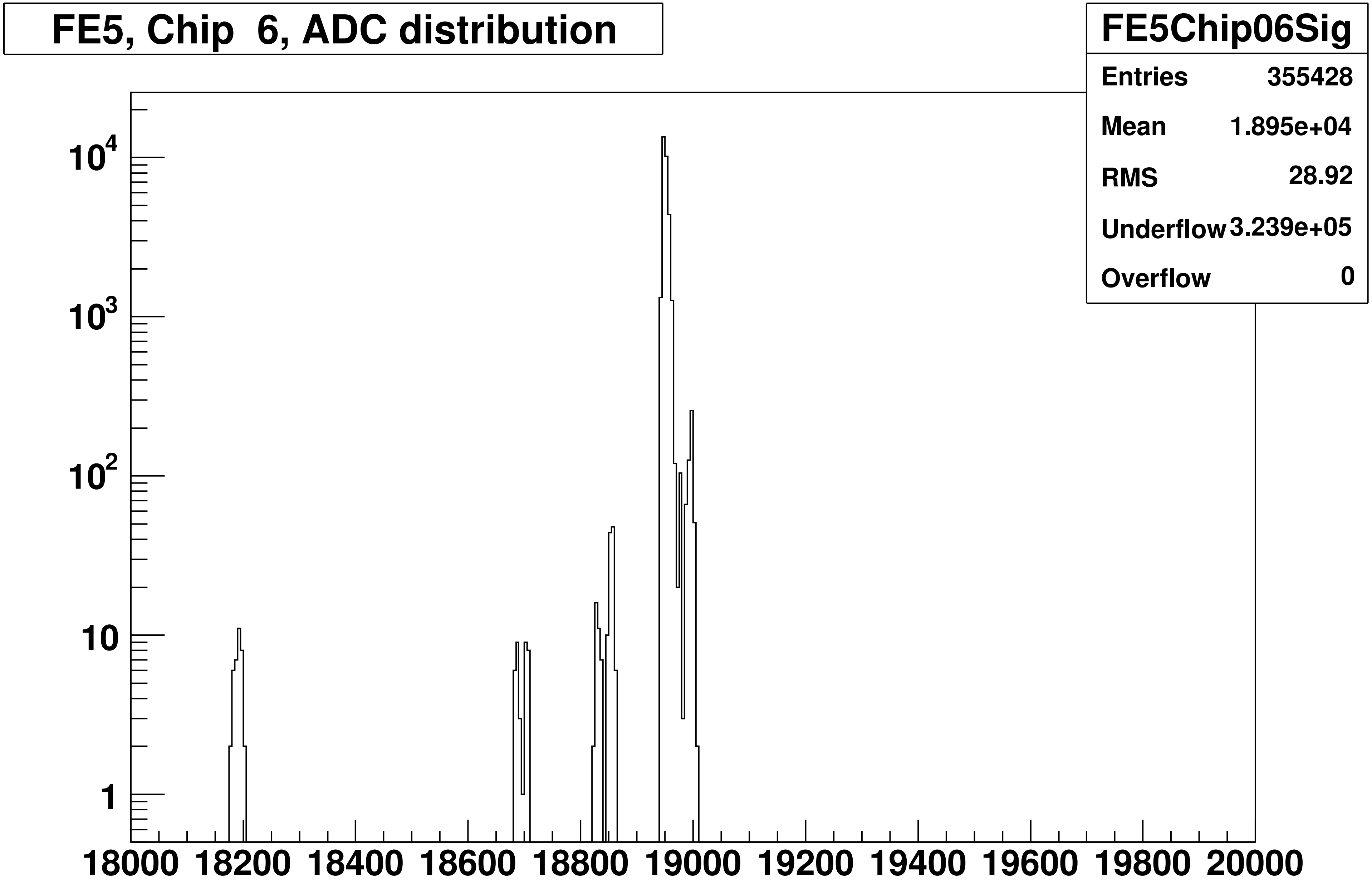
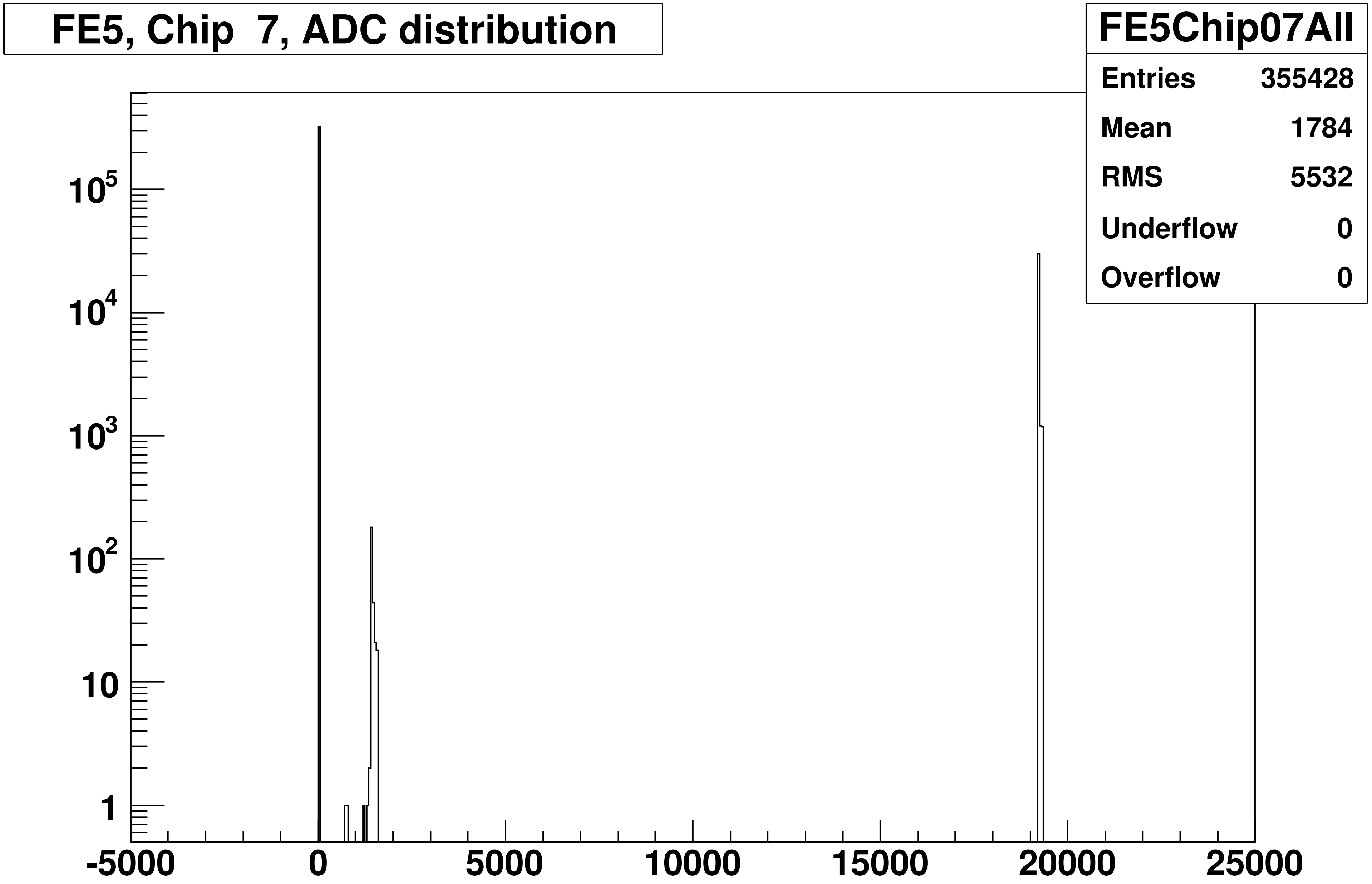
150
200


FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 355428
Mean 1.895e+04
RMS 28.92
Underflow 3.239e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 355428
Mean 1784
RMS 5532
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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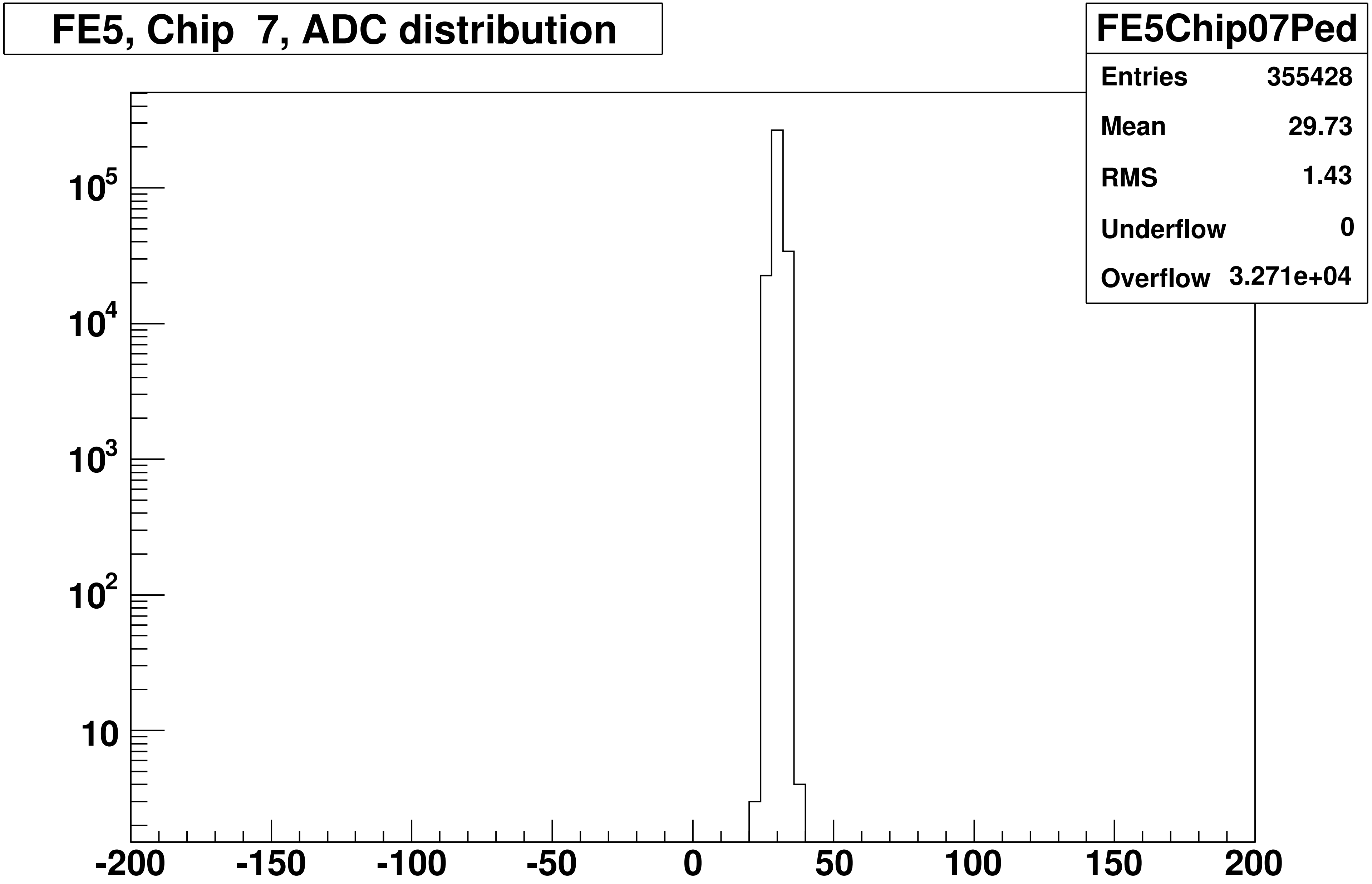
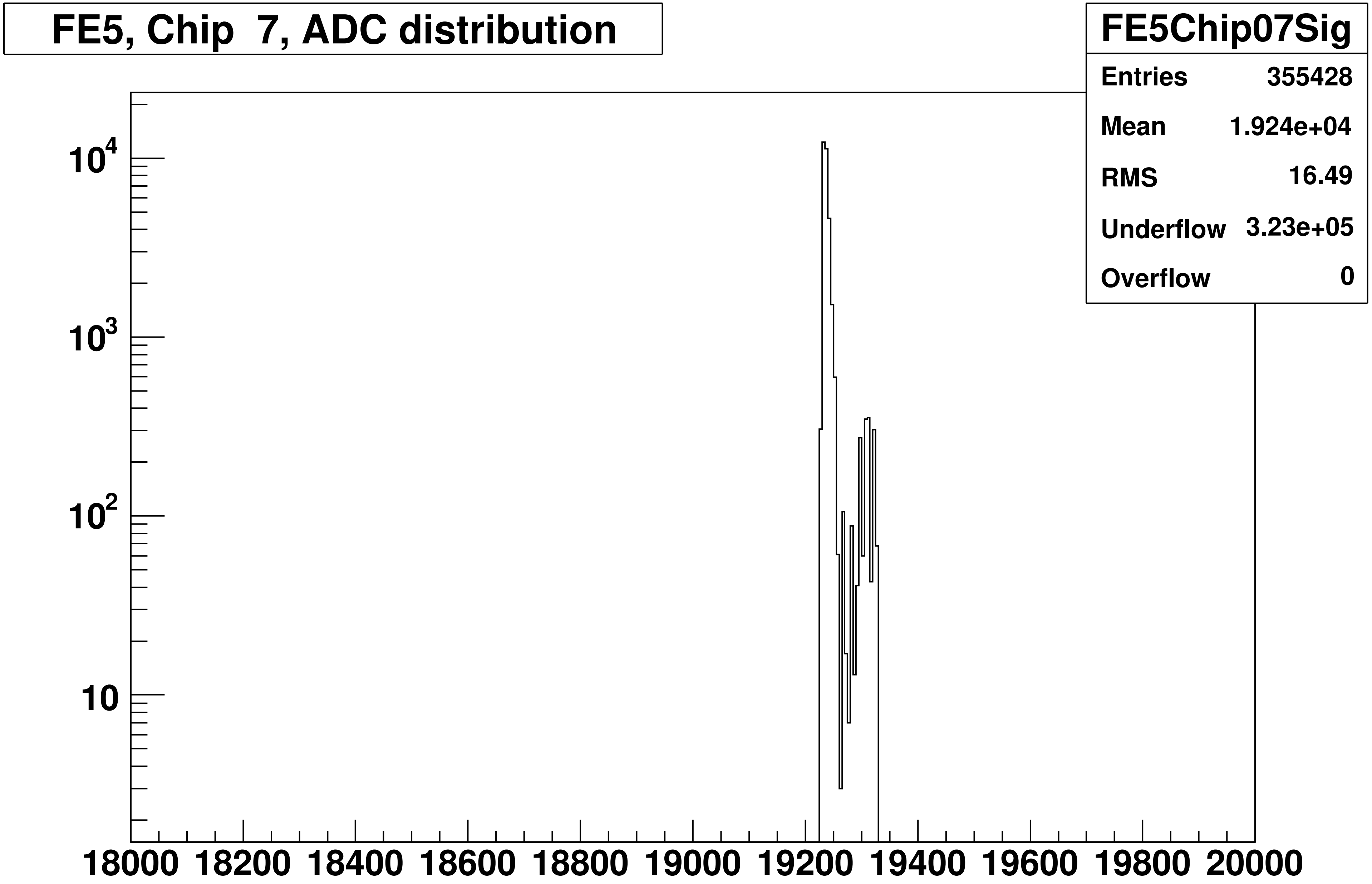
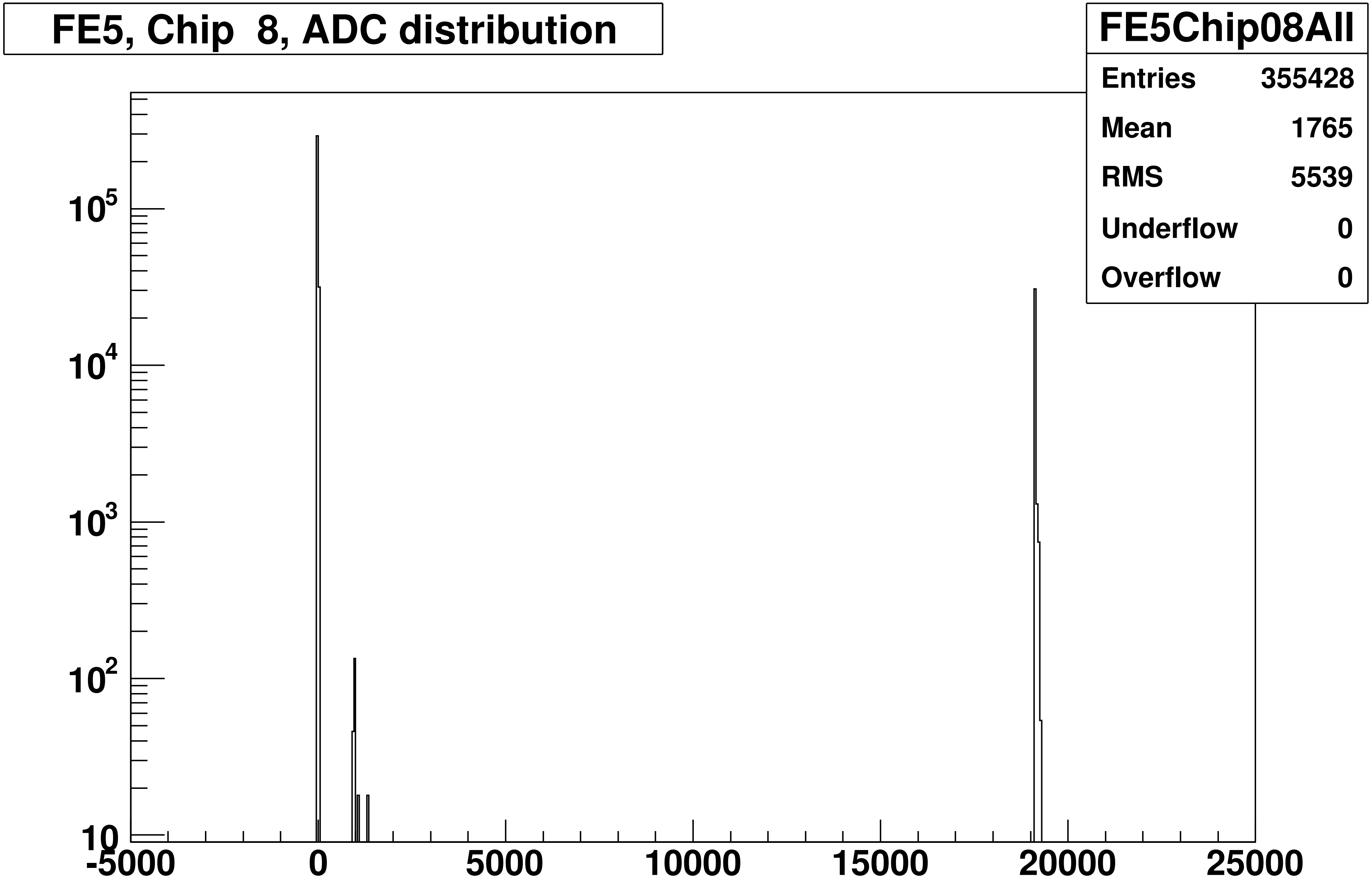
20000
25000


FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 355428
Mean 29.73
RMS 1.43
Underflow 0
Overflow 3.271e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 355428
Mean 1.924e+04
RMS 16.49
Underflow 3.23e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 355428
Mean 1765
RMS 5539
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
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0
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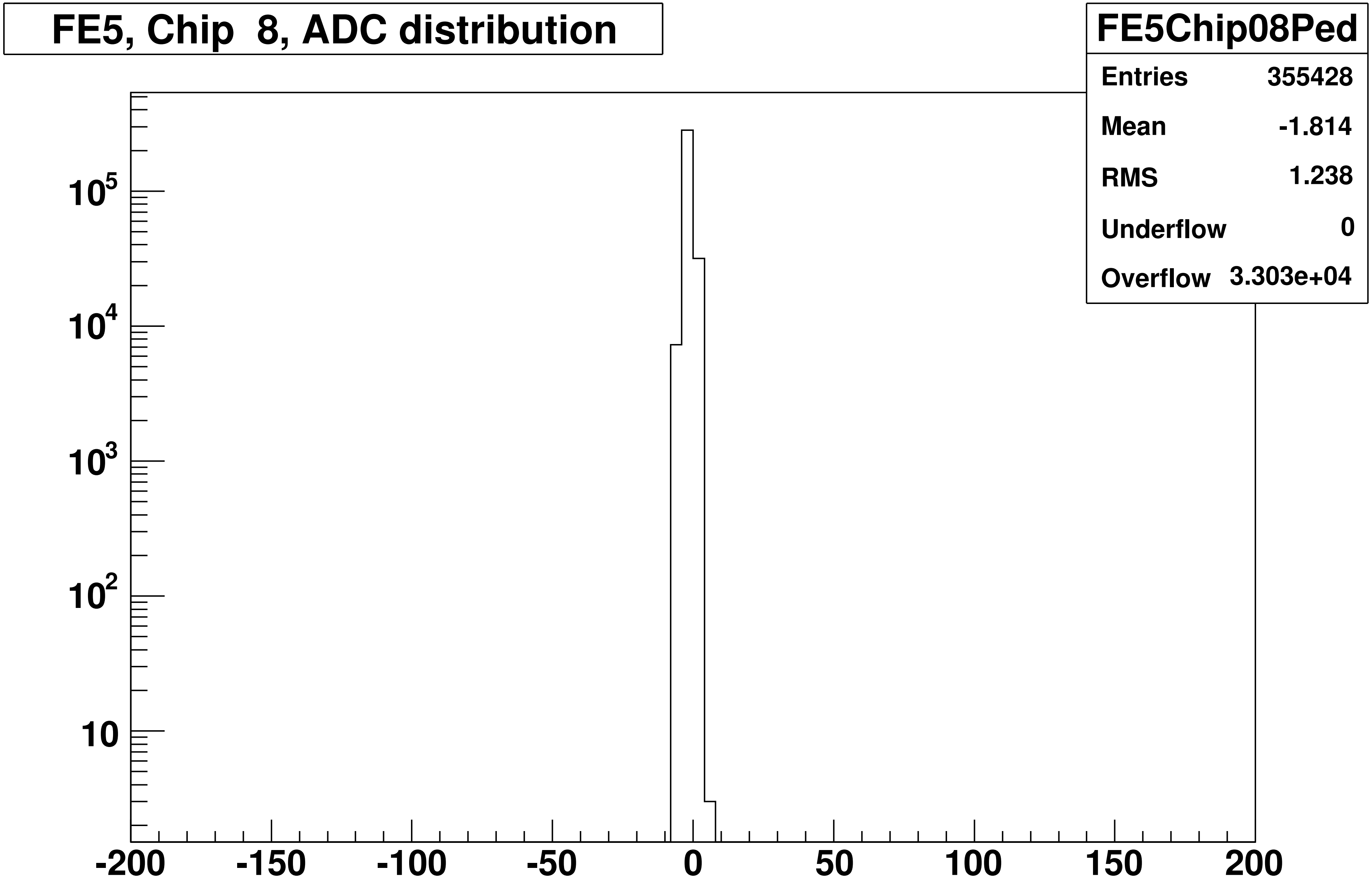
10000
15000
20000
25000



FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 355428
Mean -1.814
RMS 1.238
Underflow 0
Overflow 3.303e+04
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10^4
10^3
10^2
10
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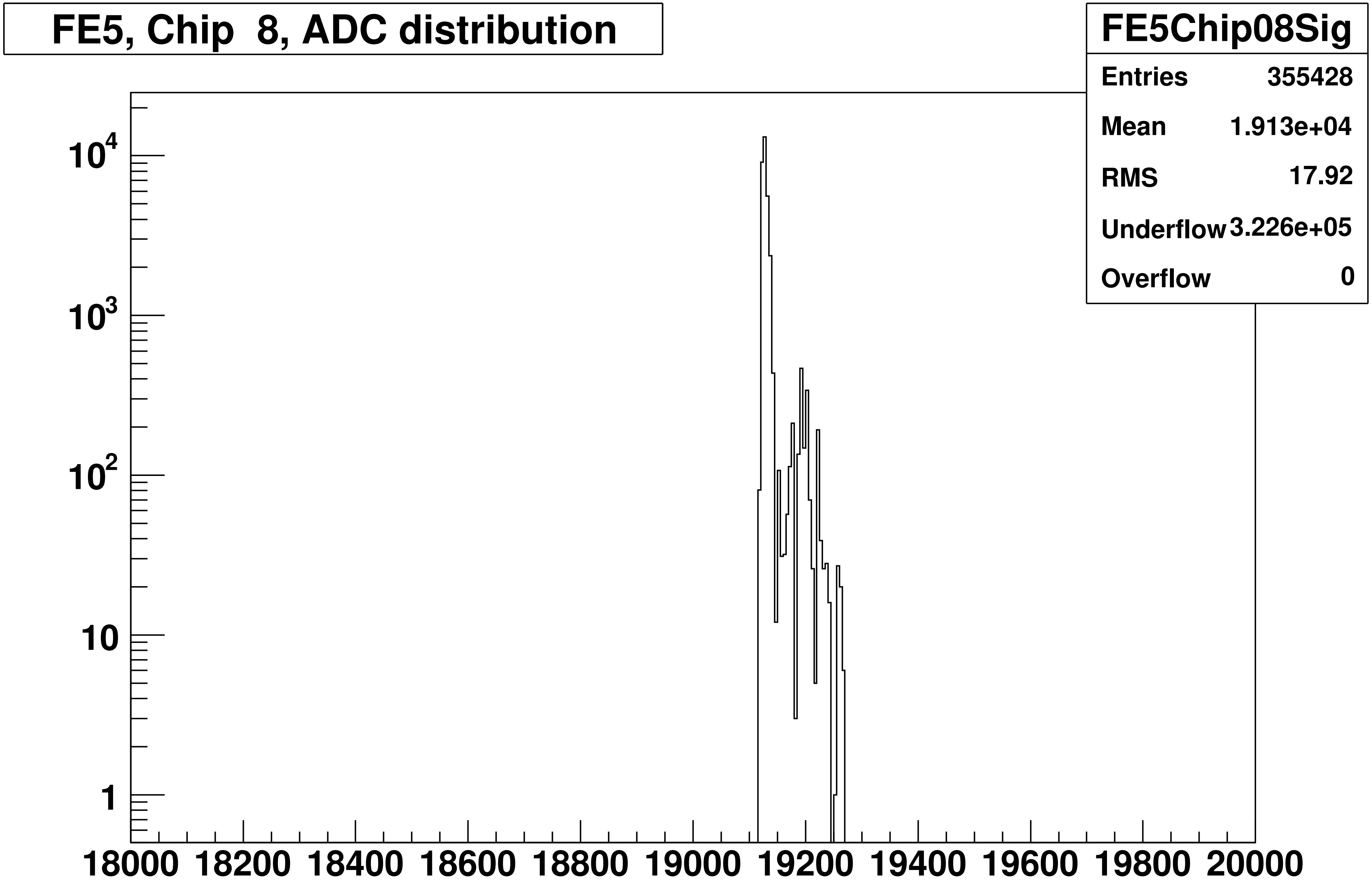
50
100
150
200


FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 355428
Mean 1.913e+04
RMS 17.92
Underflow 3.226e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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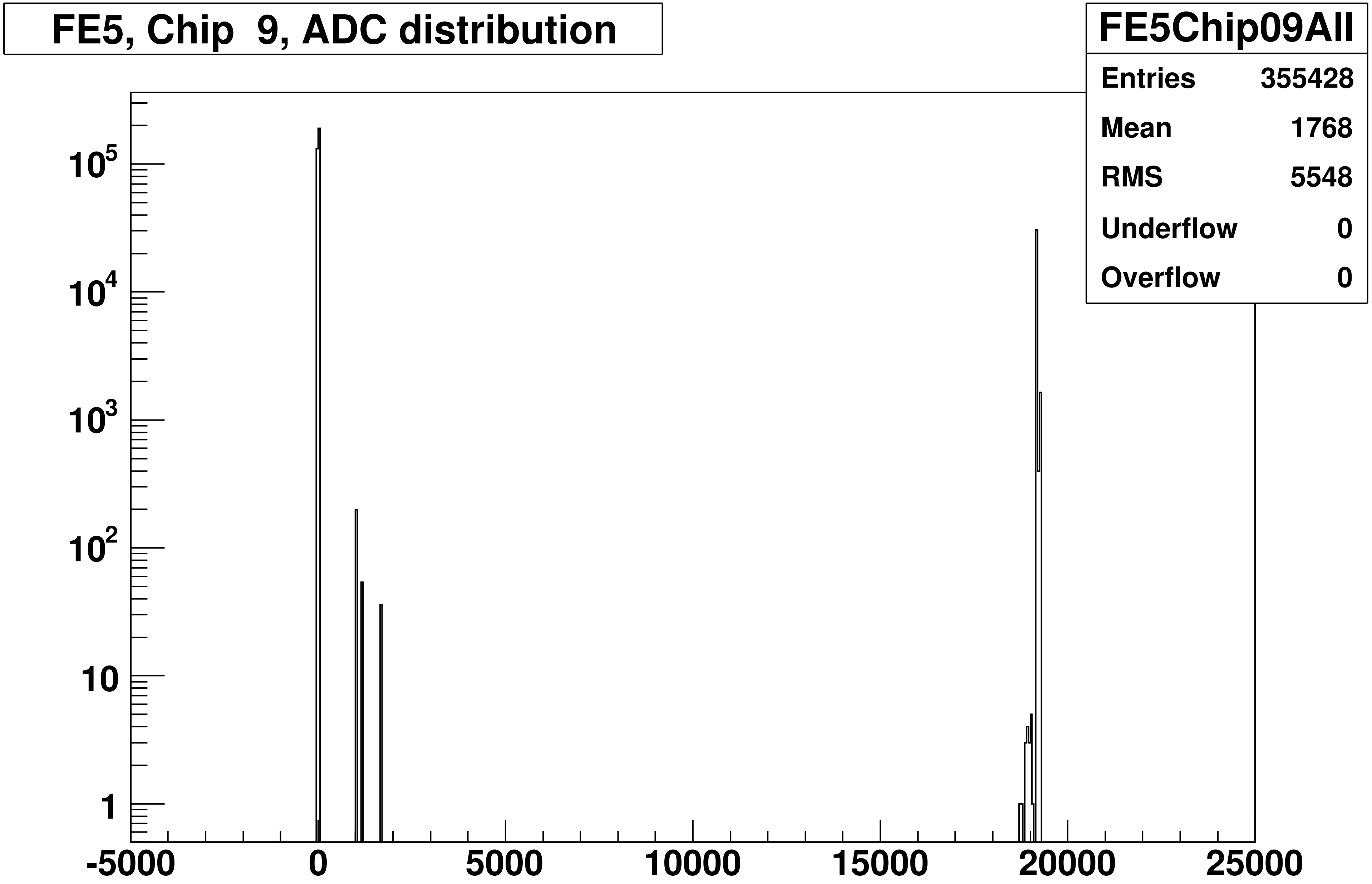
19000
19200
19400
19600
19800
20000


FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 355428
Mean 1768
RMS 5548
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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1
-5000
0
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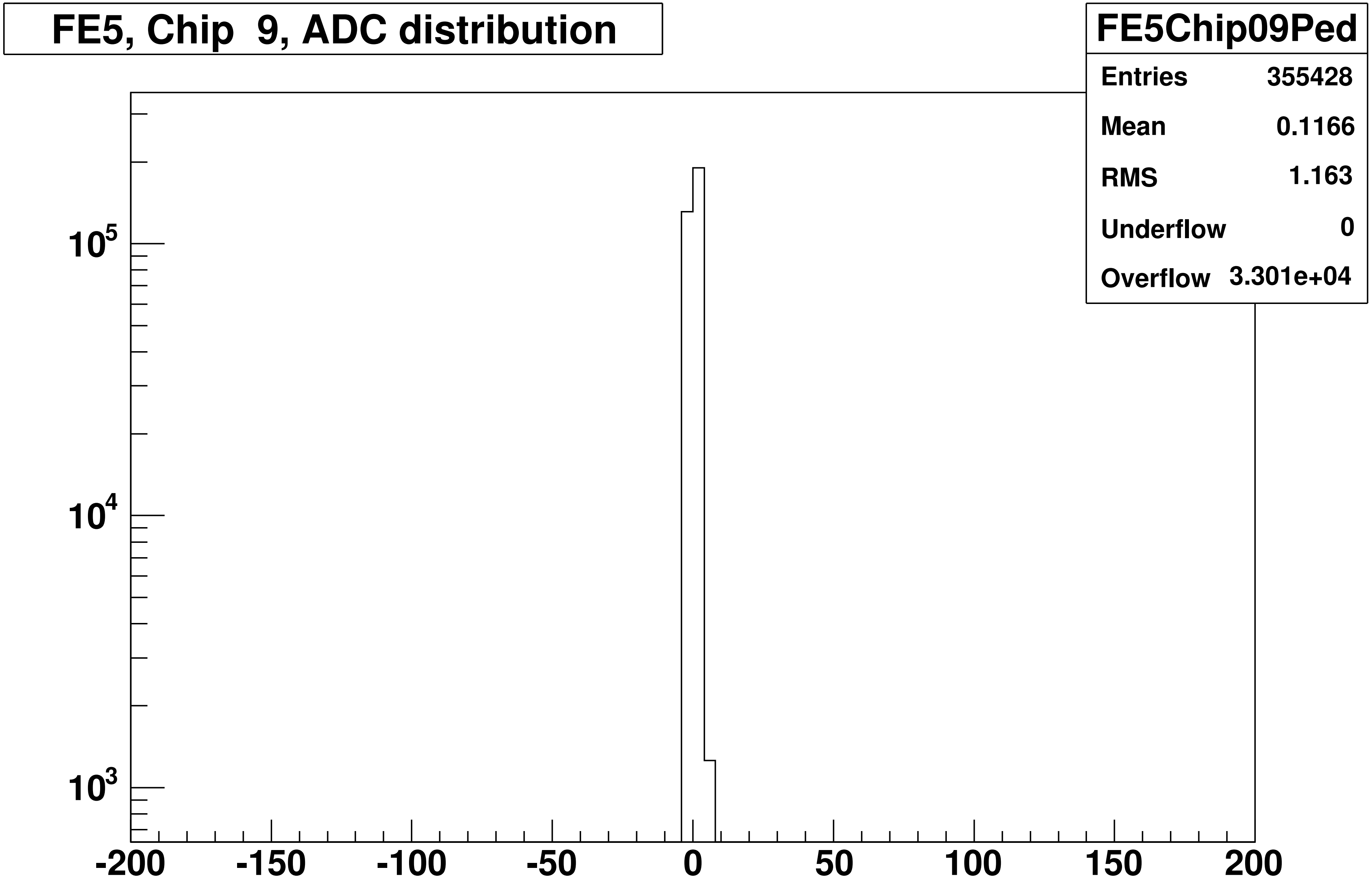
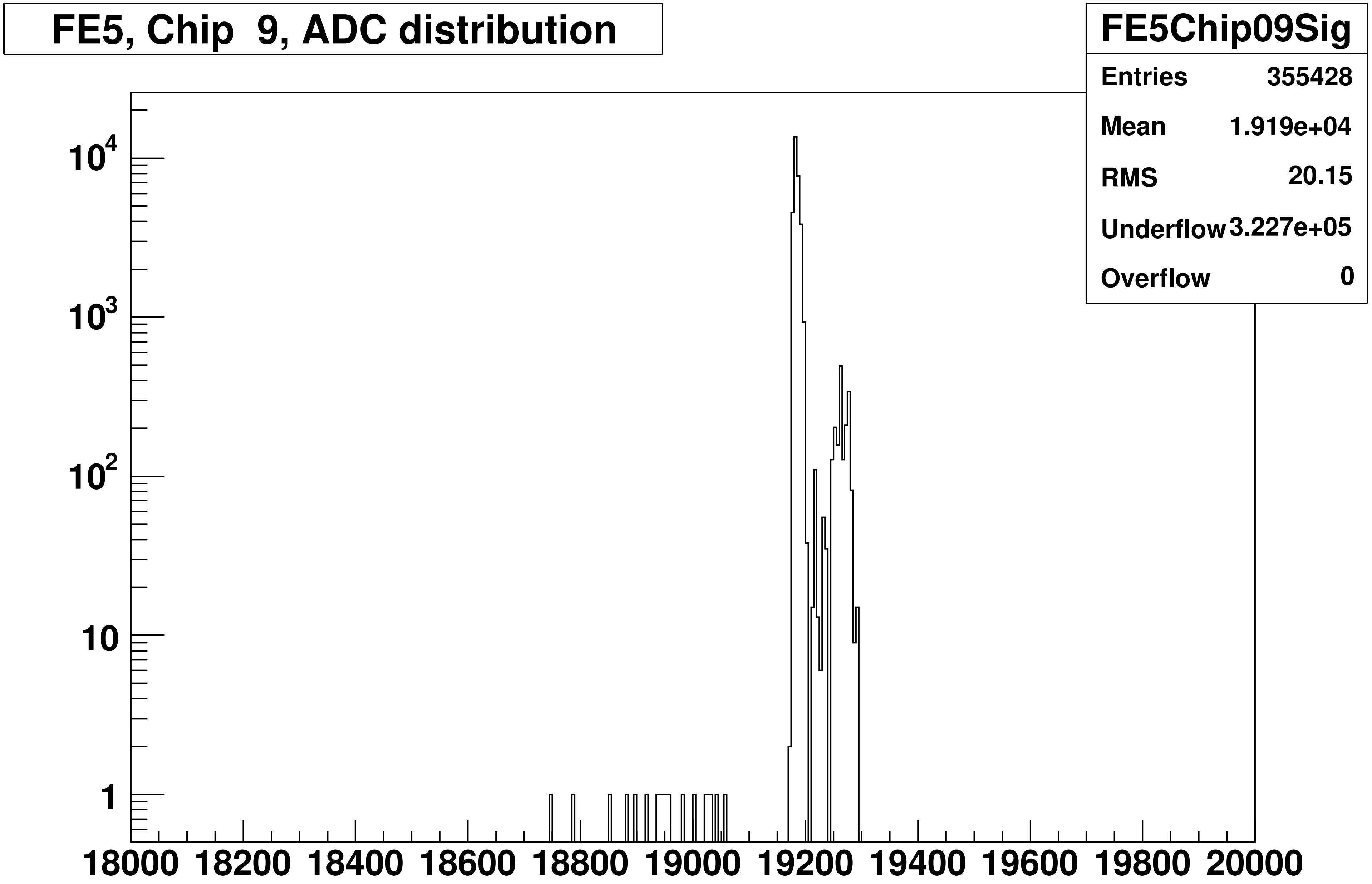
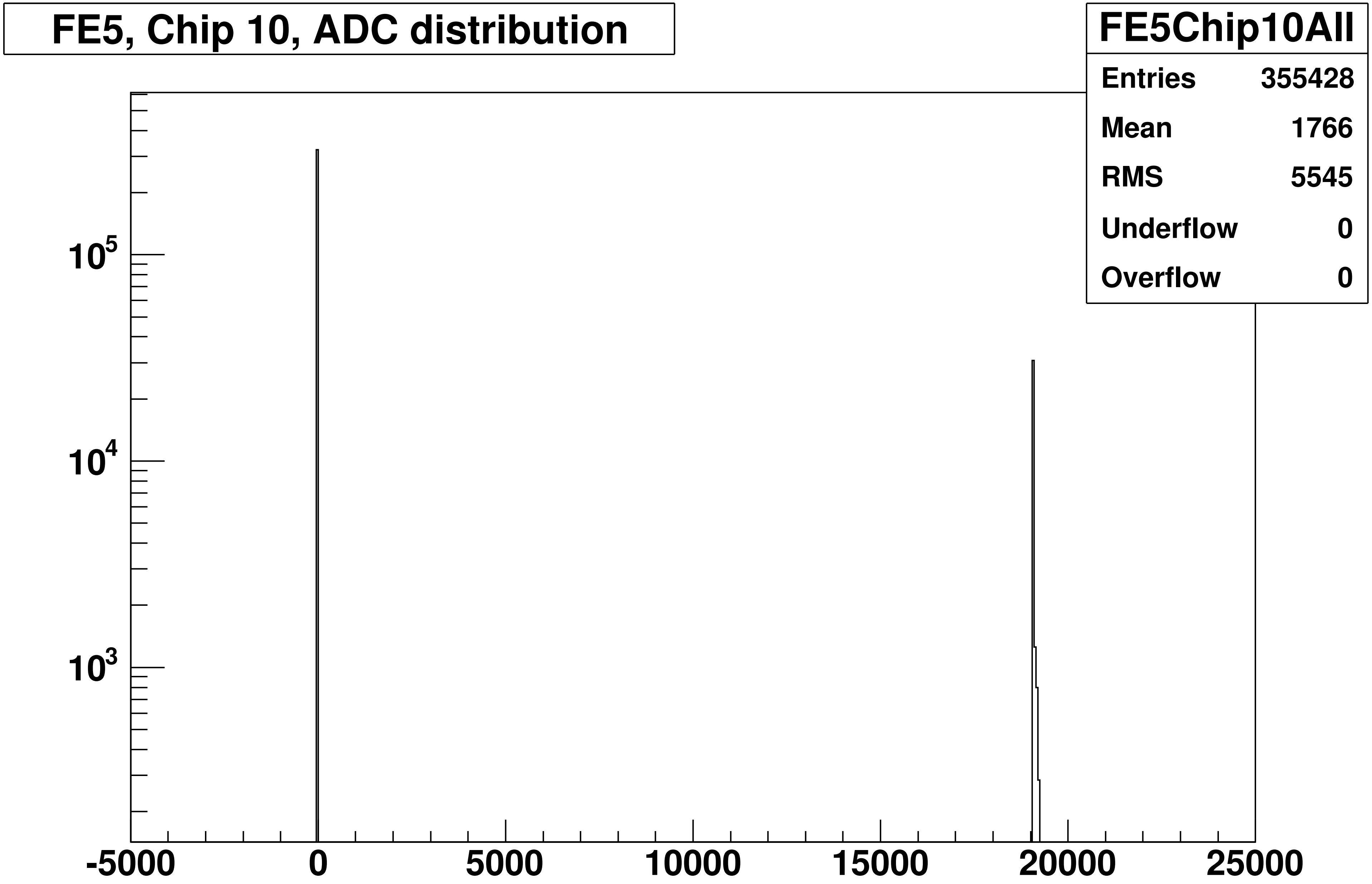
10000
15000
20000
25000


FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 355428
Mean 0.1166
RMS 1.163
Underflow 0
Overflow 3.301e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200



FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 355428
Mean 1.919e+04
RMS 20.15
Underflow 3.227e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 355428
Mean 1766
RMS 5545
Underflow 0
Overflow 0
10^5
10^4
10^3
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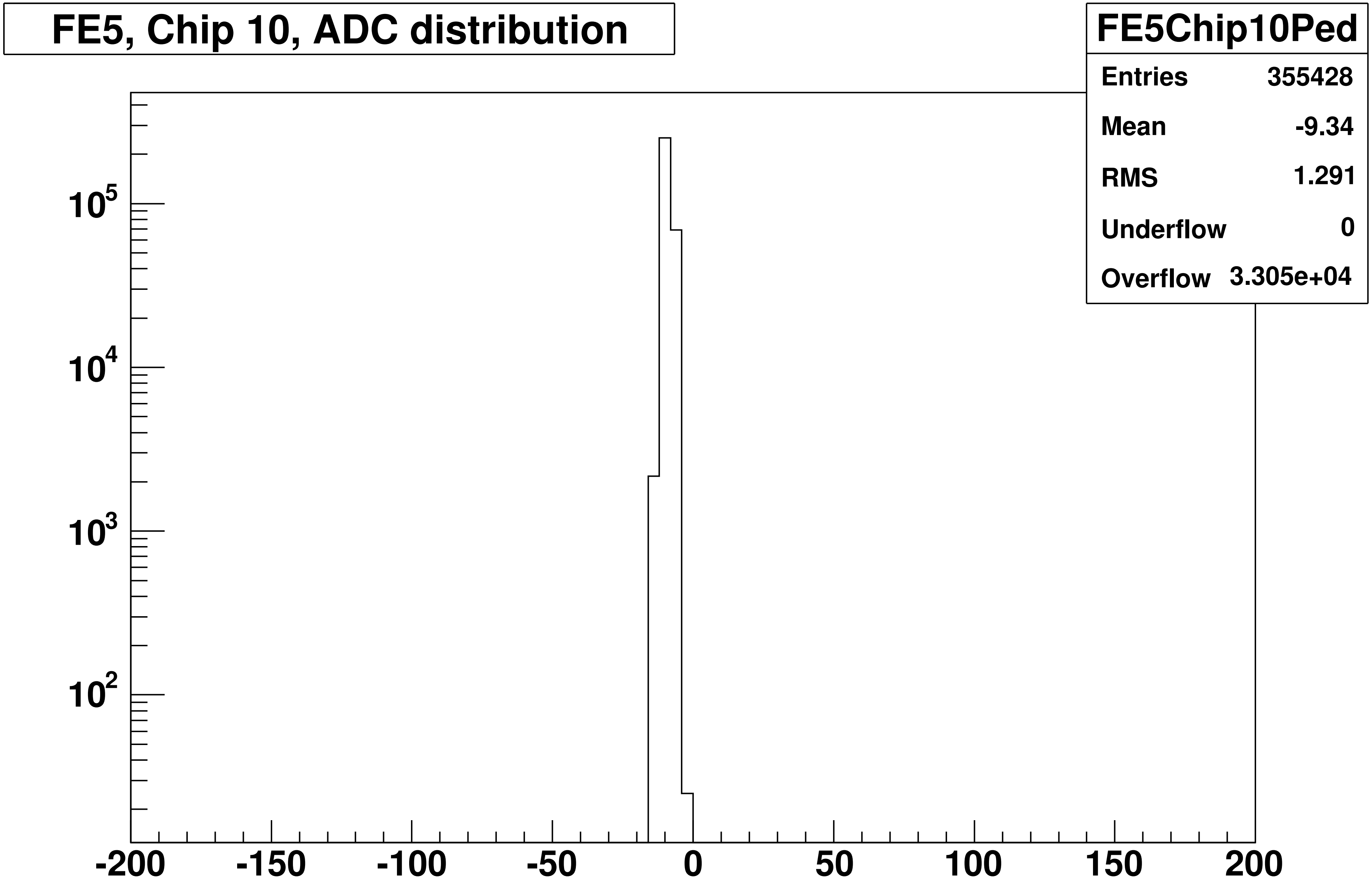
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 355428
Mean -9.34
RMS 1.291
Underflow 0
Overflow 3.305e+04
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10^4
10^3
10^2
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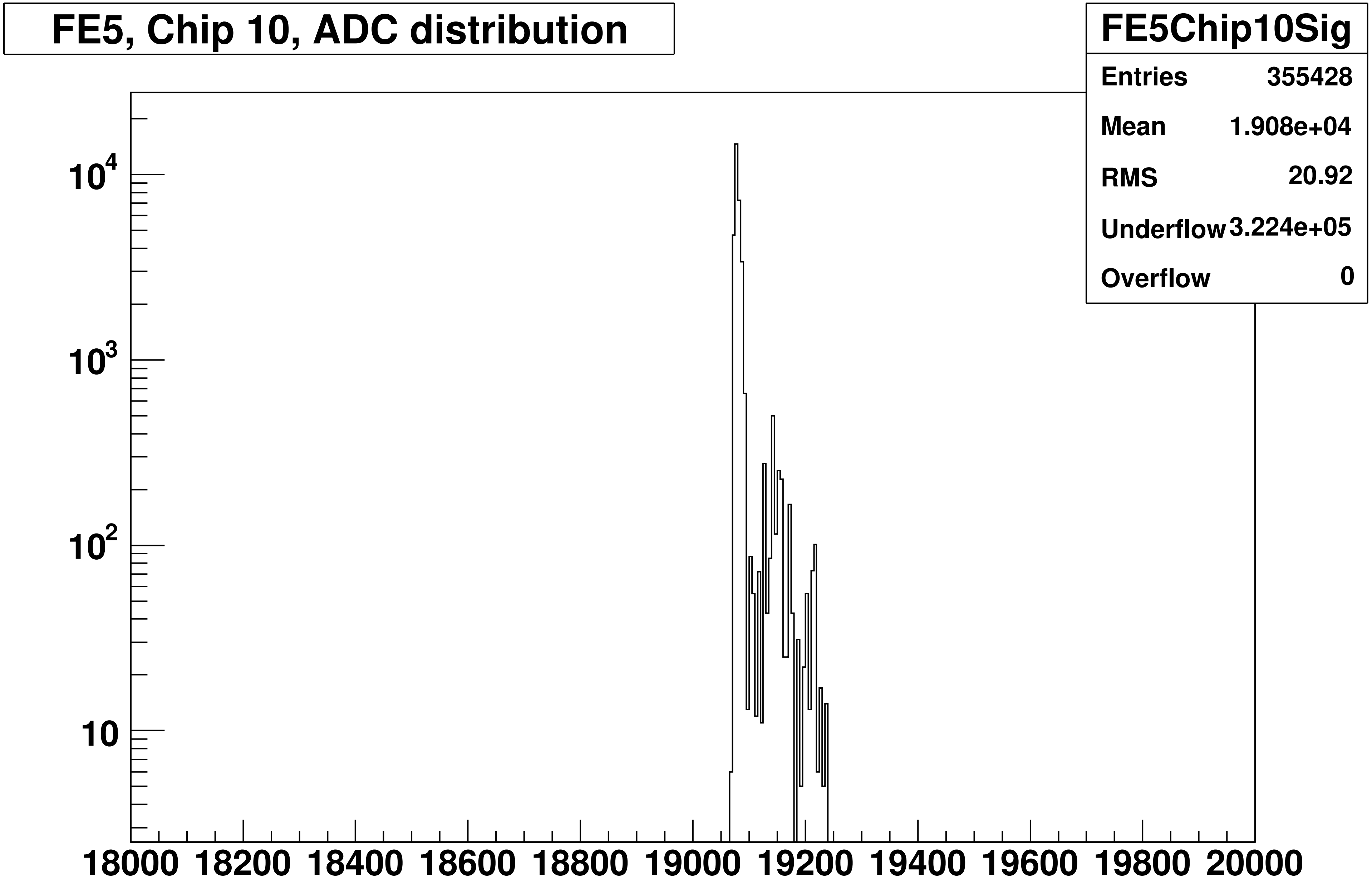
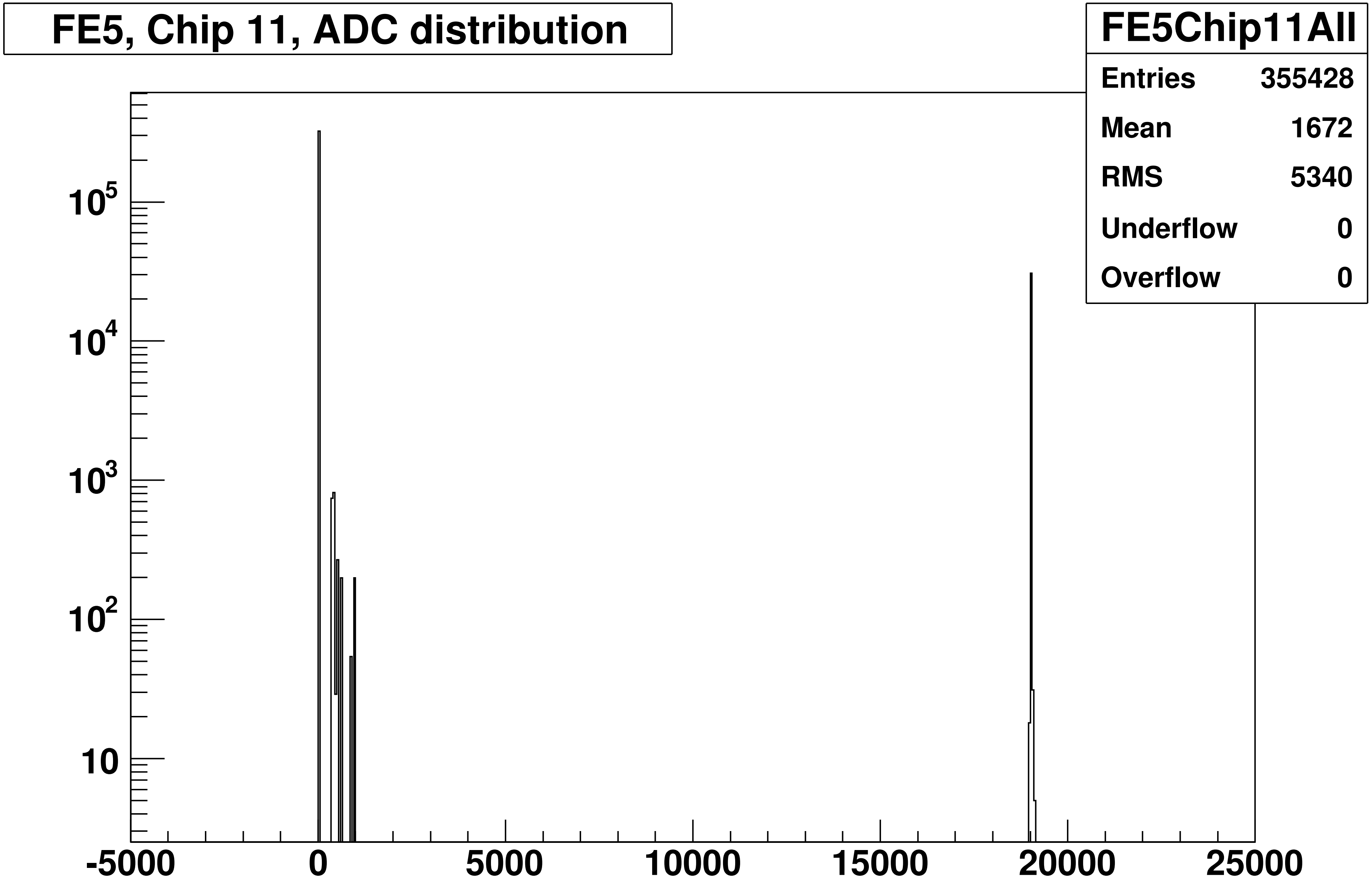
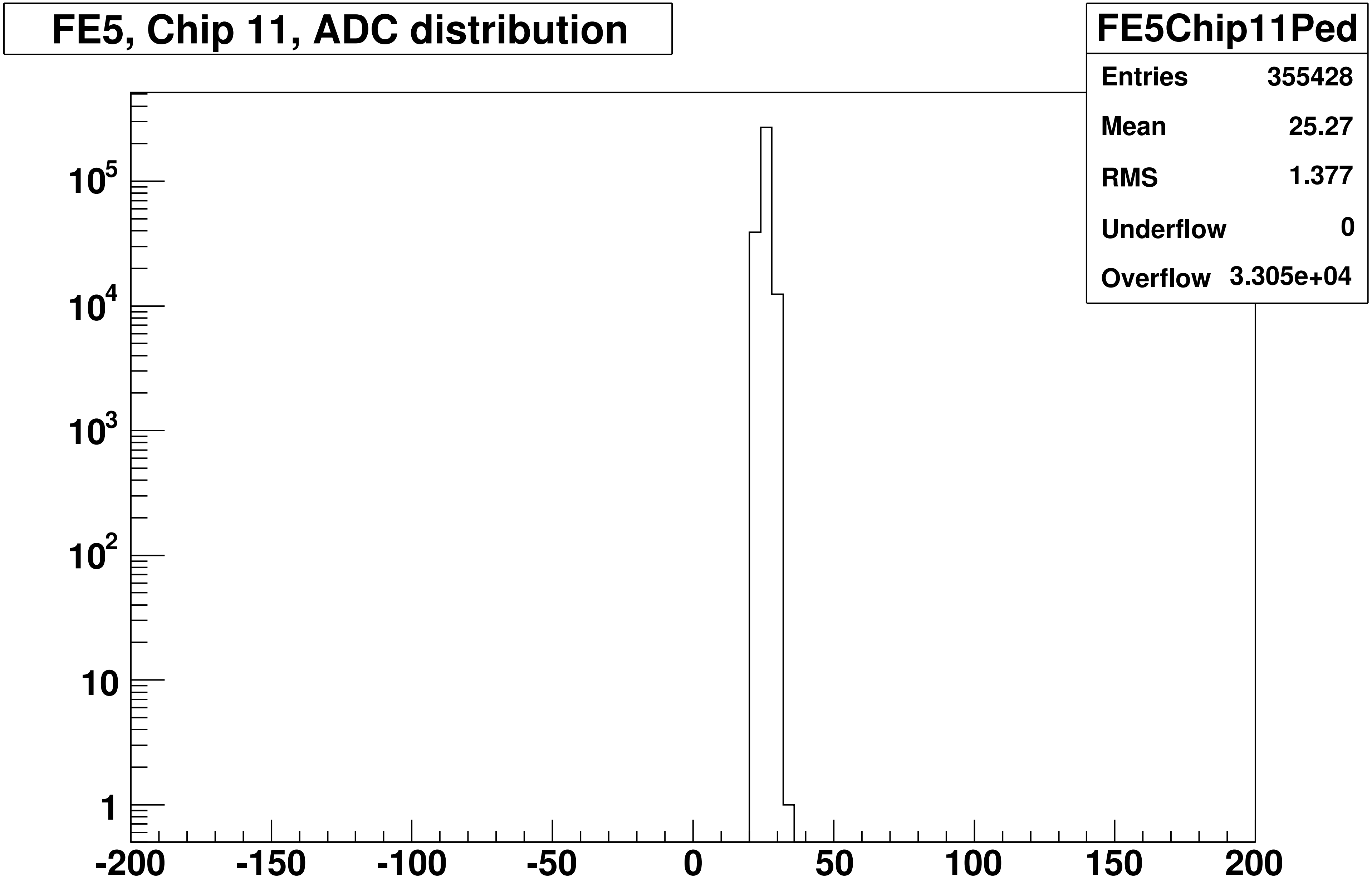
-150
-100
-50
0
50
100
150
200


FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 355428
Mean 1.908e+04
RMS 20.92
Underflow 3.224e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 355428
Mean 1672
RMS 5340
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 355428
Mean 25.27
RMS 1.377
Underflow 0
Overflow 3.305e+04
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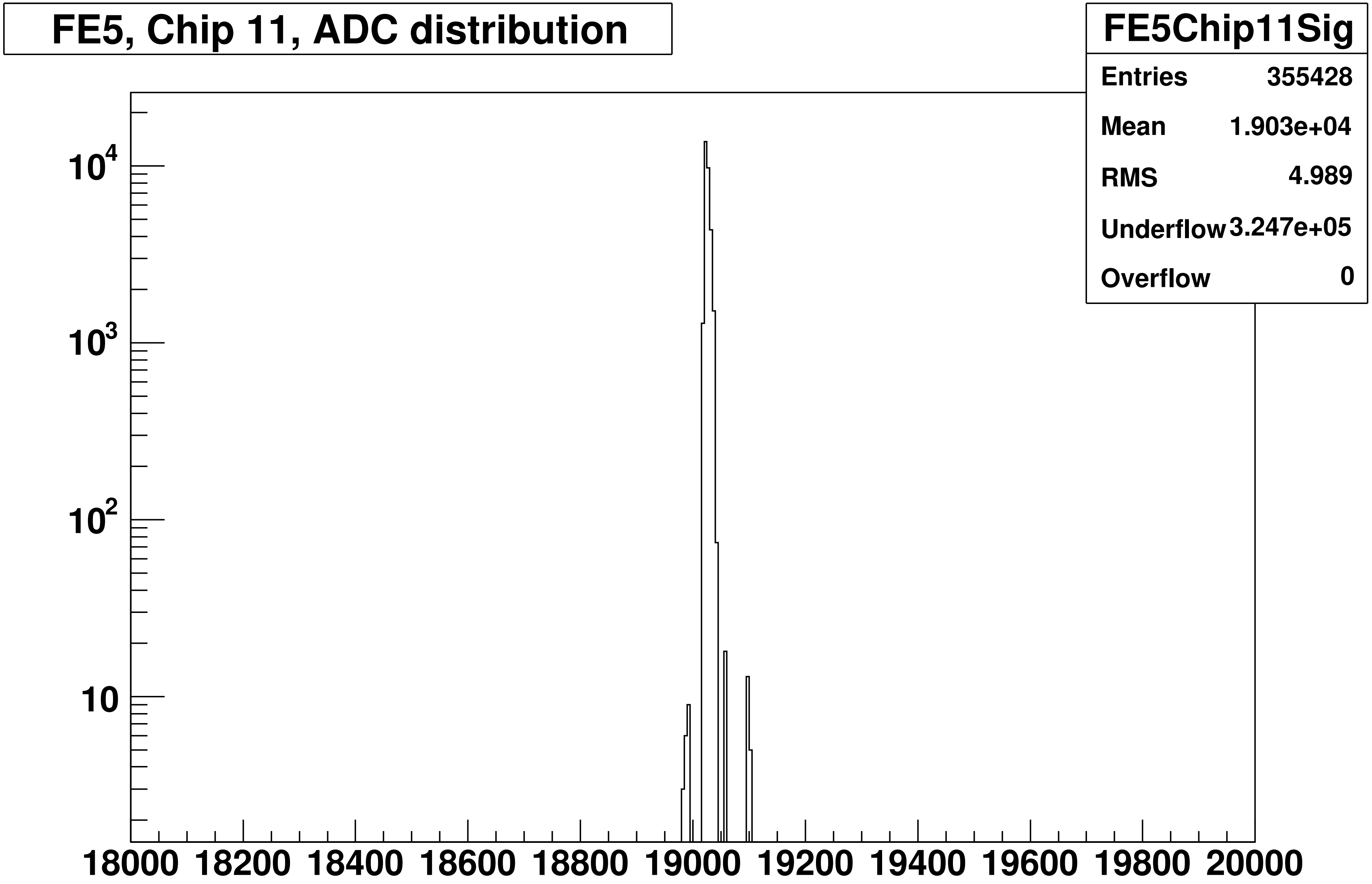
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 355428
Mean 1.903e+04
RMS 4.989
Underflow 3.247e+05
Overflow 0
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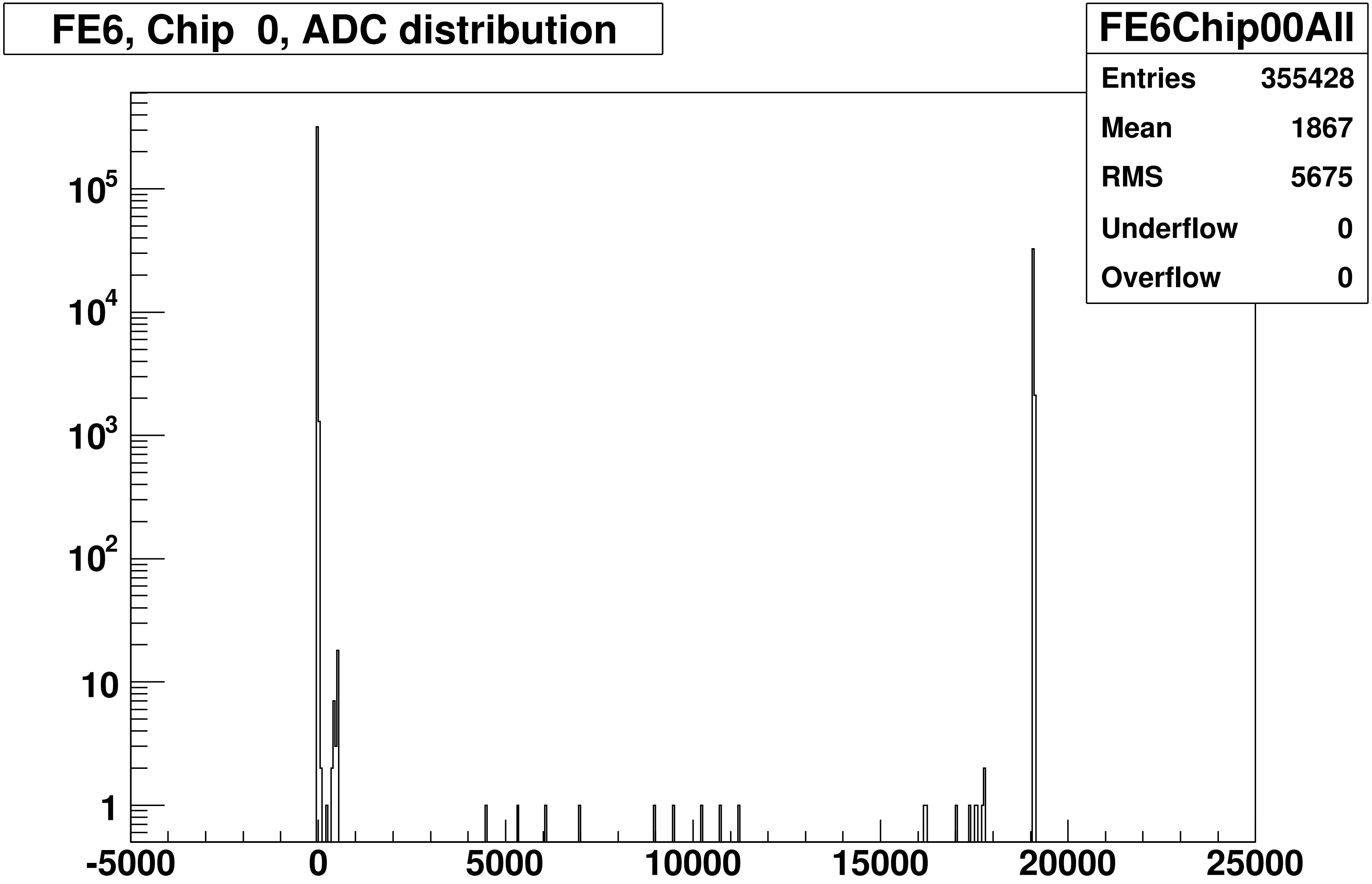
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 355428
Mean 1867
RMS 5675
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
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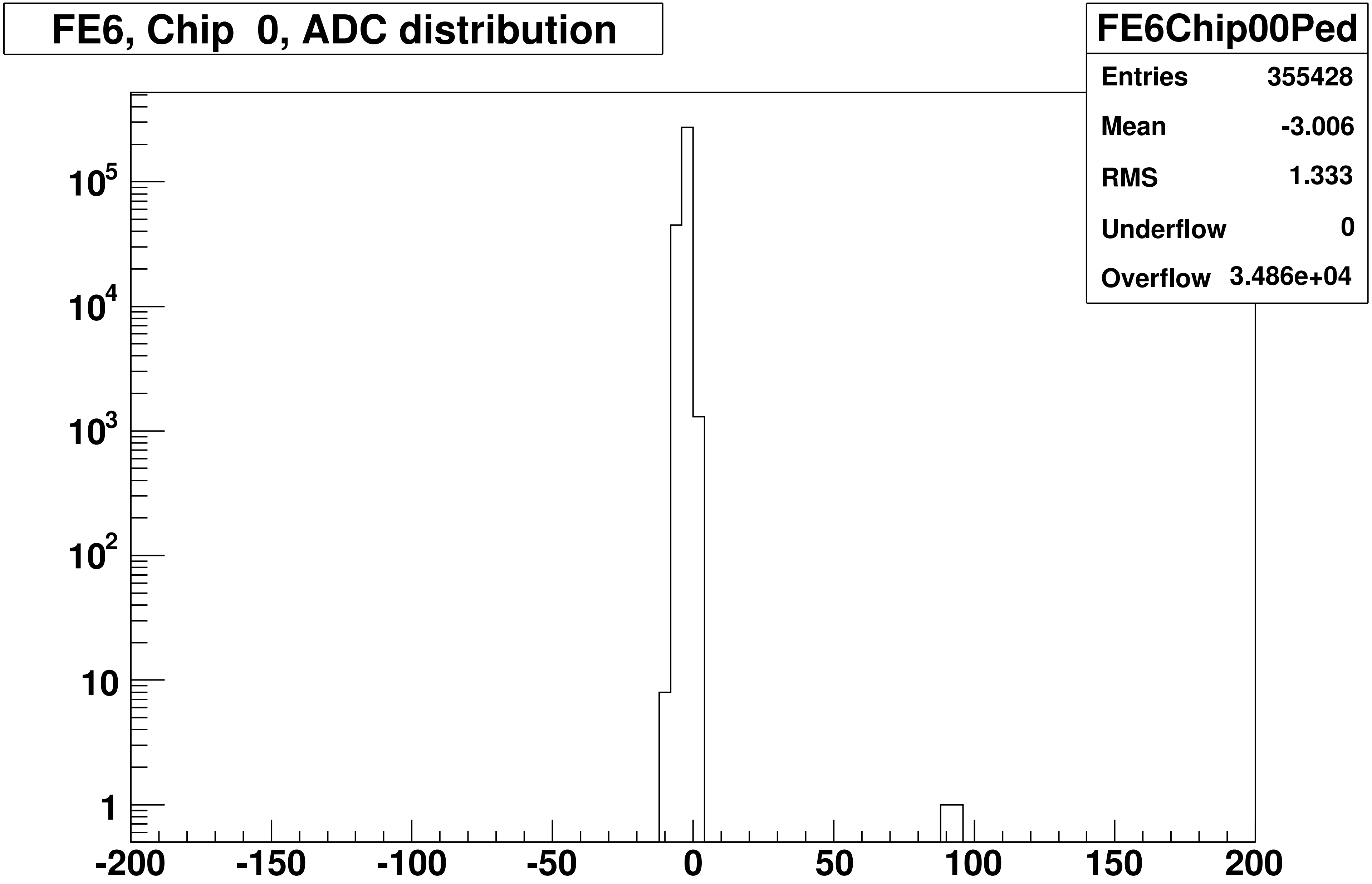
10000
15000
20000
25000


FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 355428
Mean -3.006
RMS 1.333
Underflow 0
Overflow 3.486e+04
1
10
10^2
10^3
10^4
10^5
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-100
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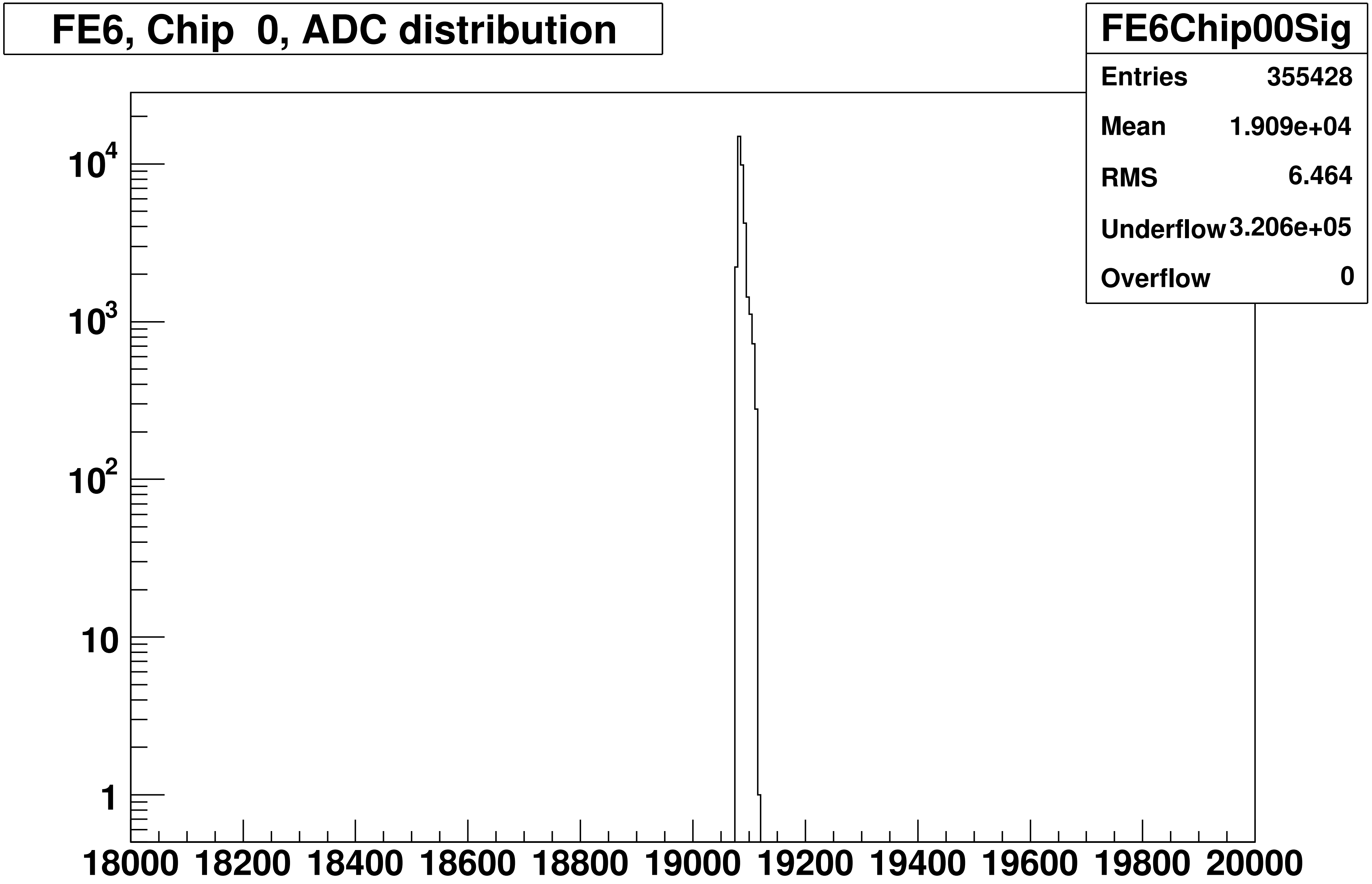
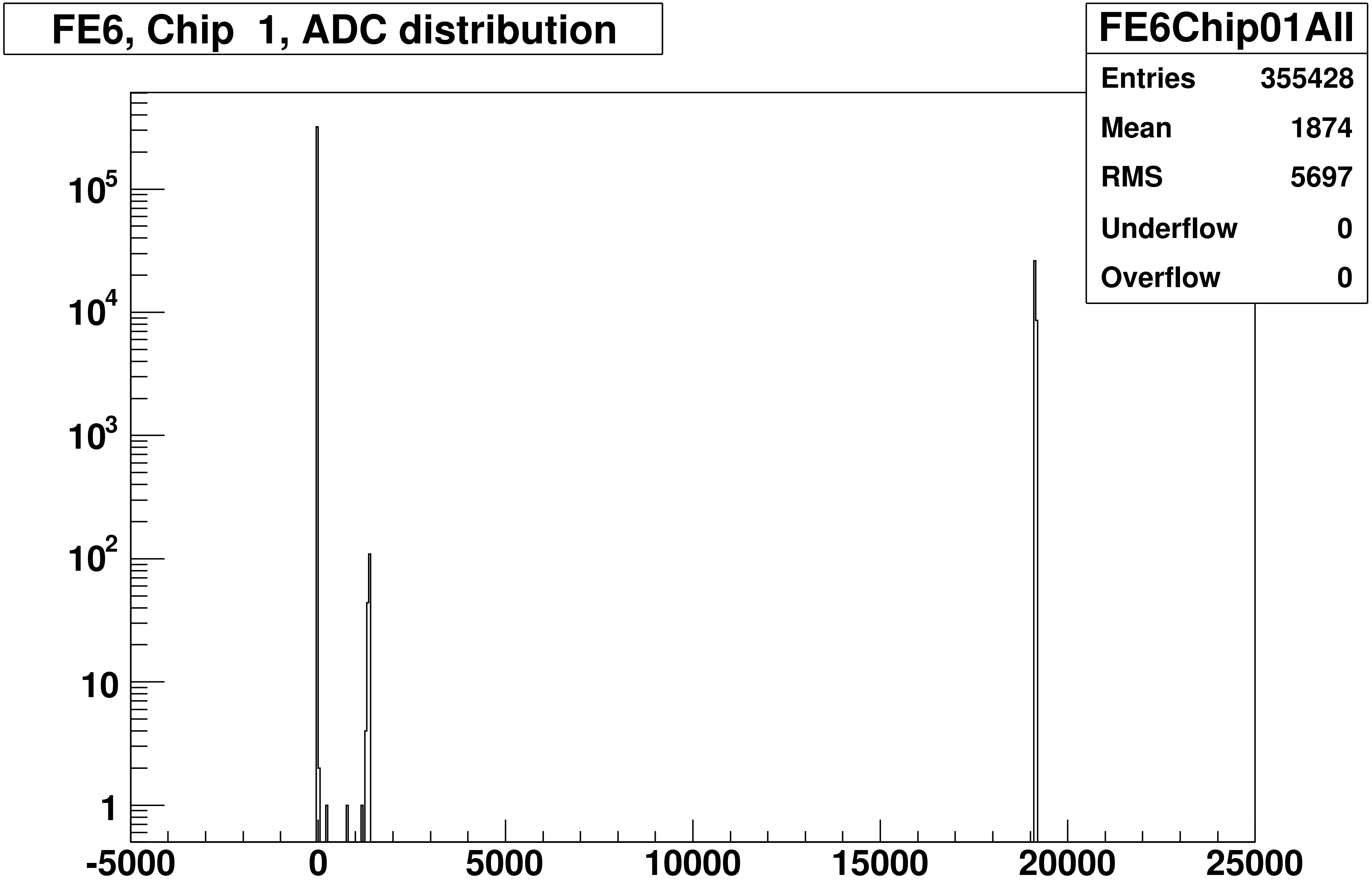
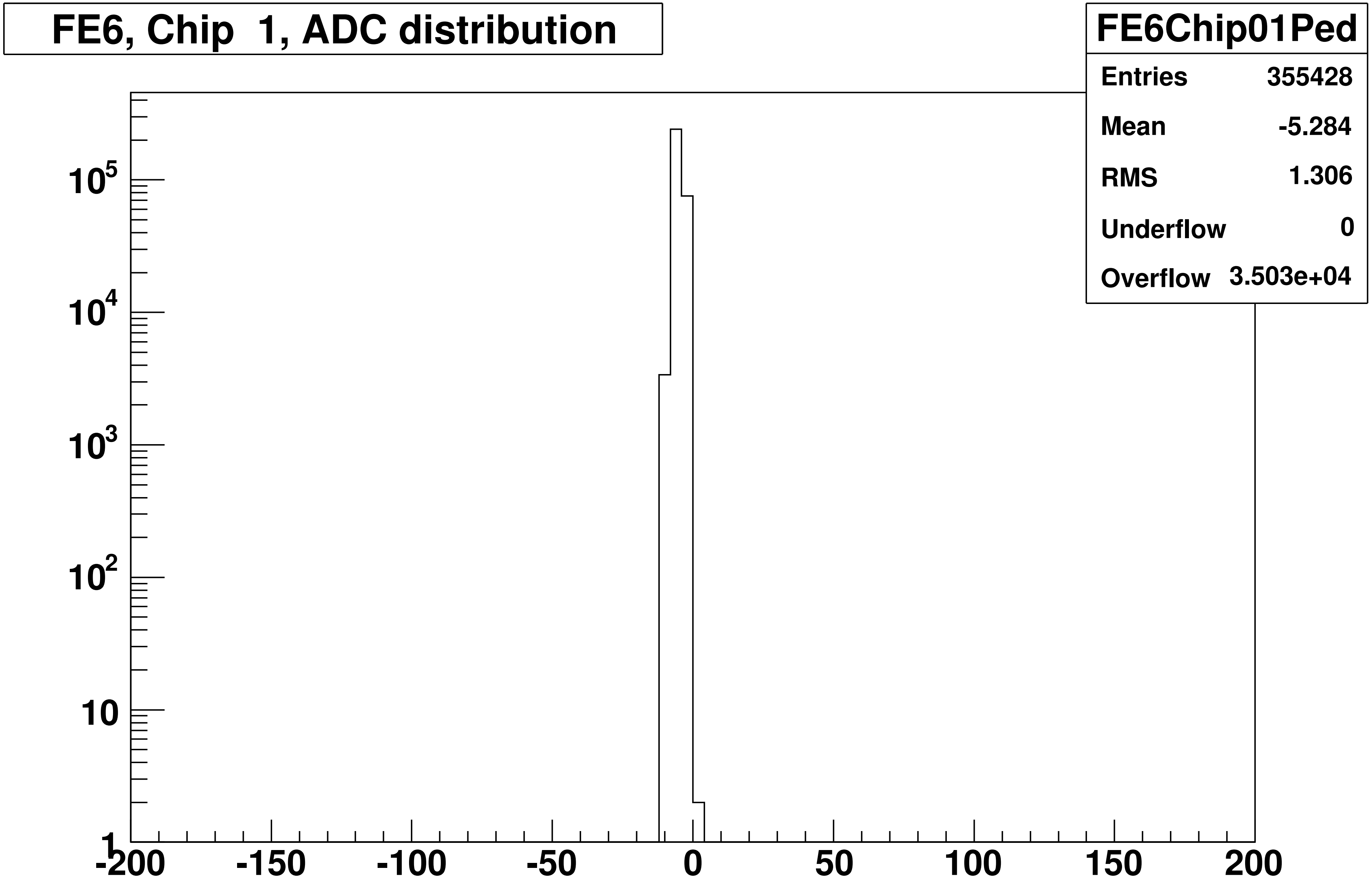
0
50
100
150
200

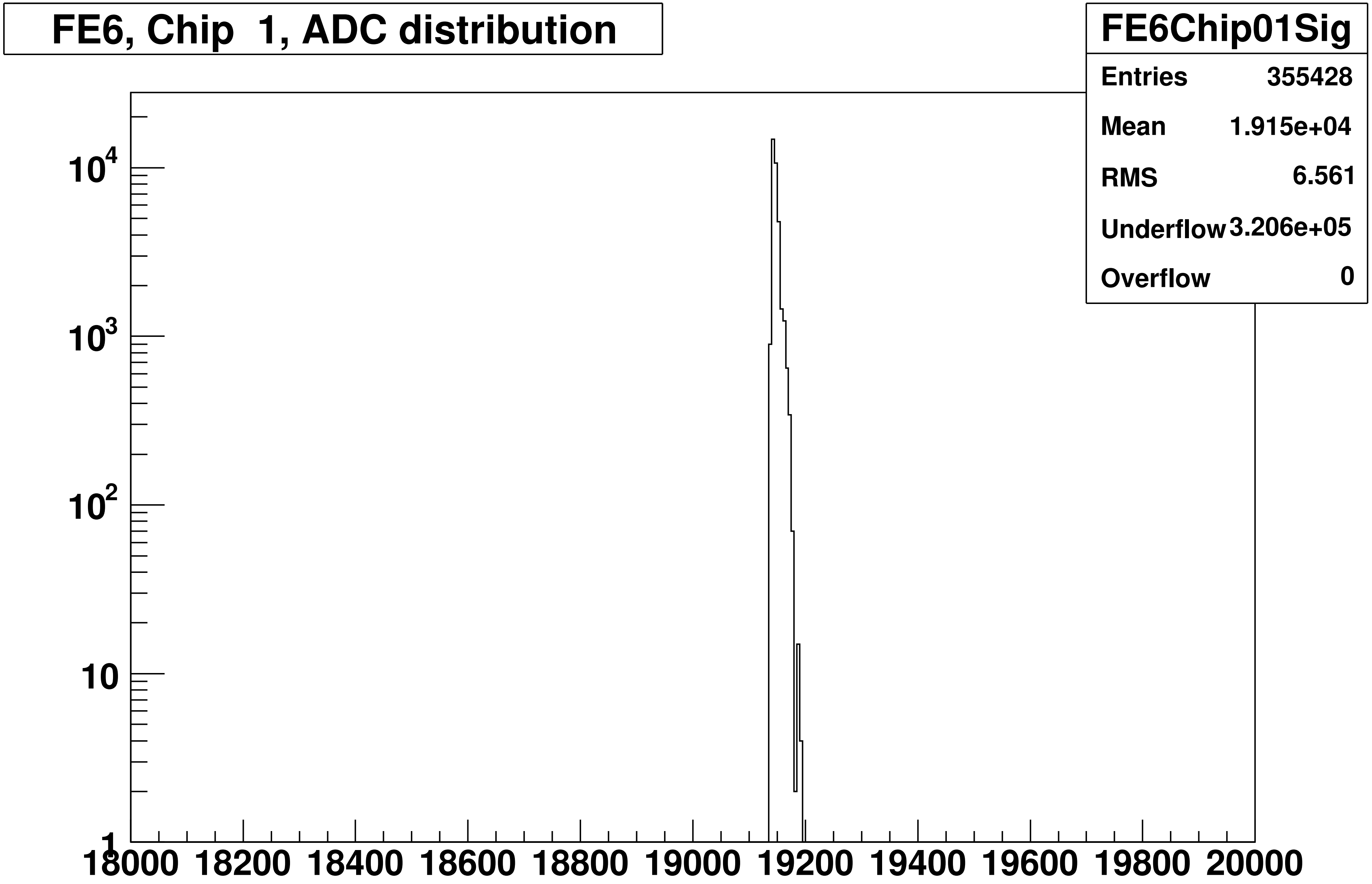


FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 355428
Mean 1.909e+04
RMS 6.464
Underflow 3.206e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

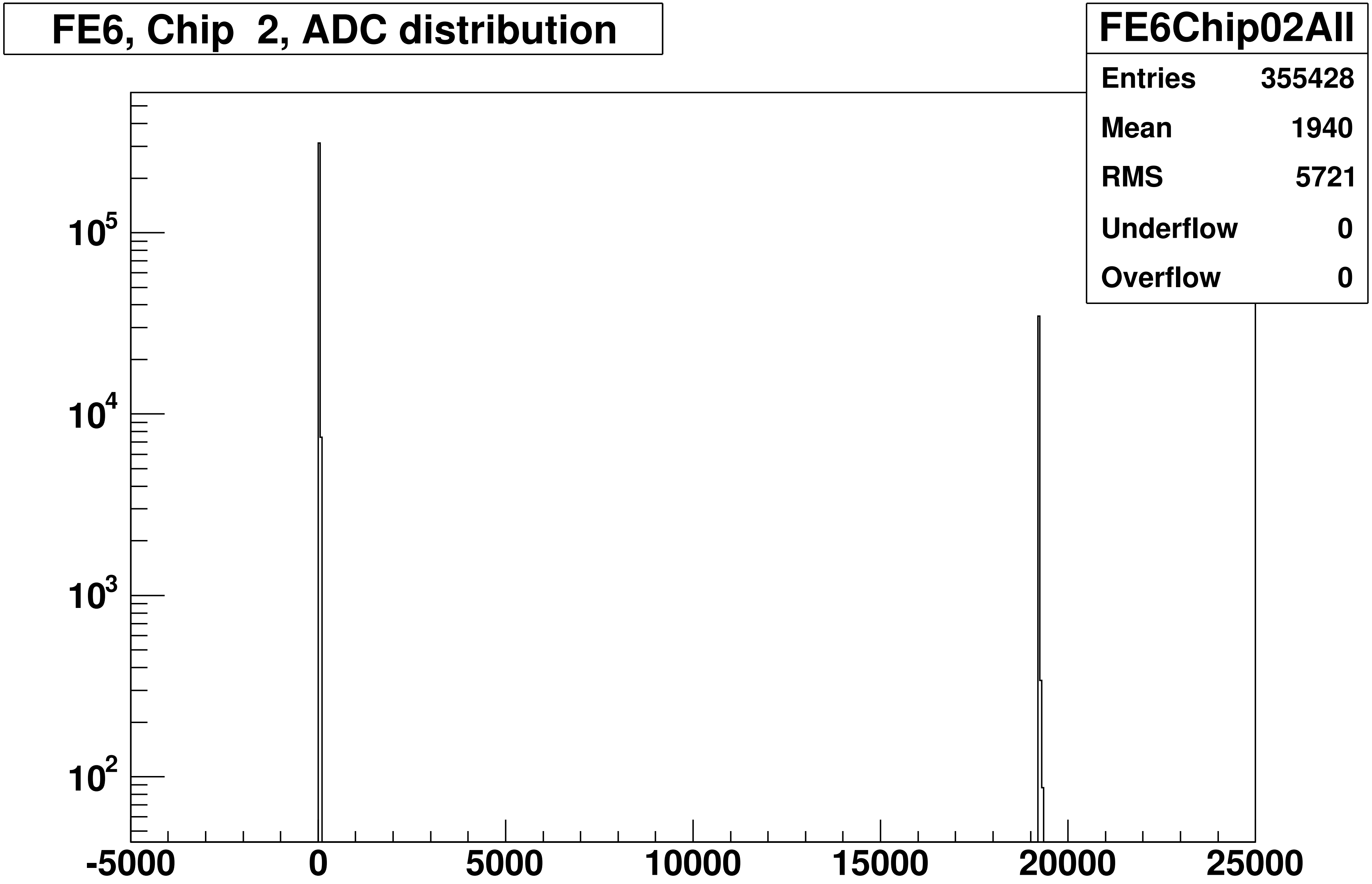

FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 355428
Mean 1874
RMS 5697
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

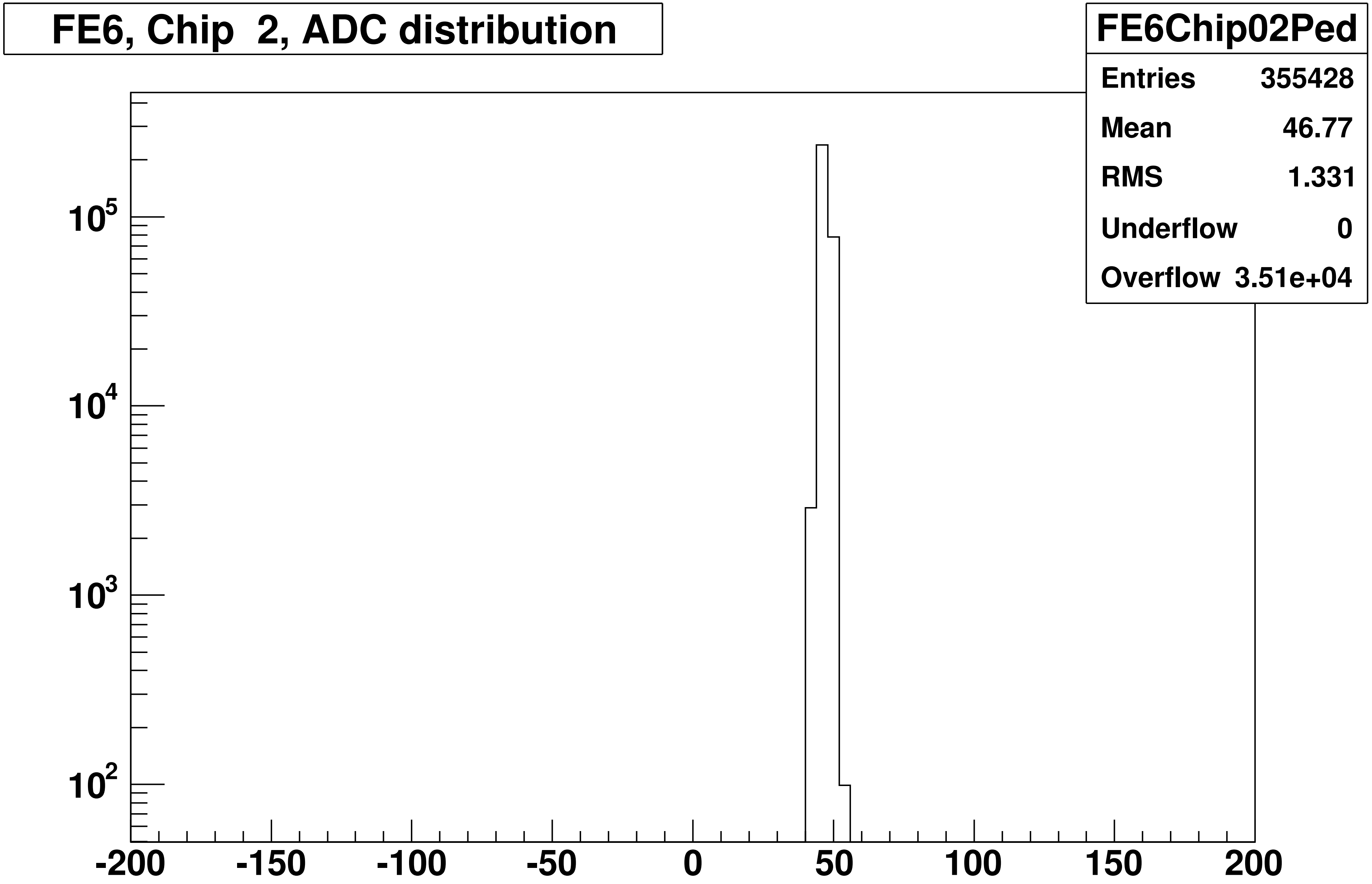


FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 355428
Mean -5.284
RMS 1.306
Underflow 0
Overflow 3.503e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 355428
Mean 1.915e+04
RMS 6.561
Underflow 3.206e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 355428
Mean 1940
RMS 5721
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 355428
Mean 46.77
RMS 1.331
Underflow 0
Overflow 3.51e+04
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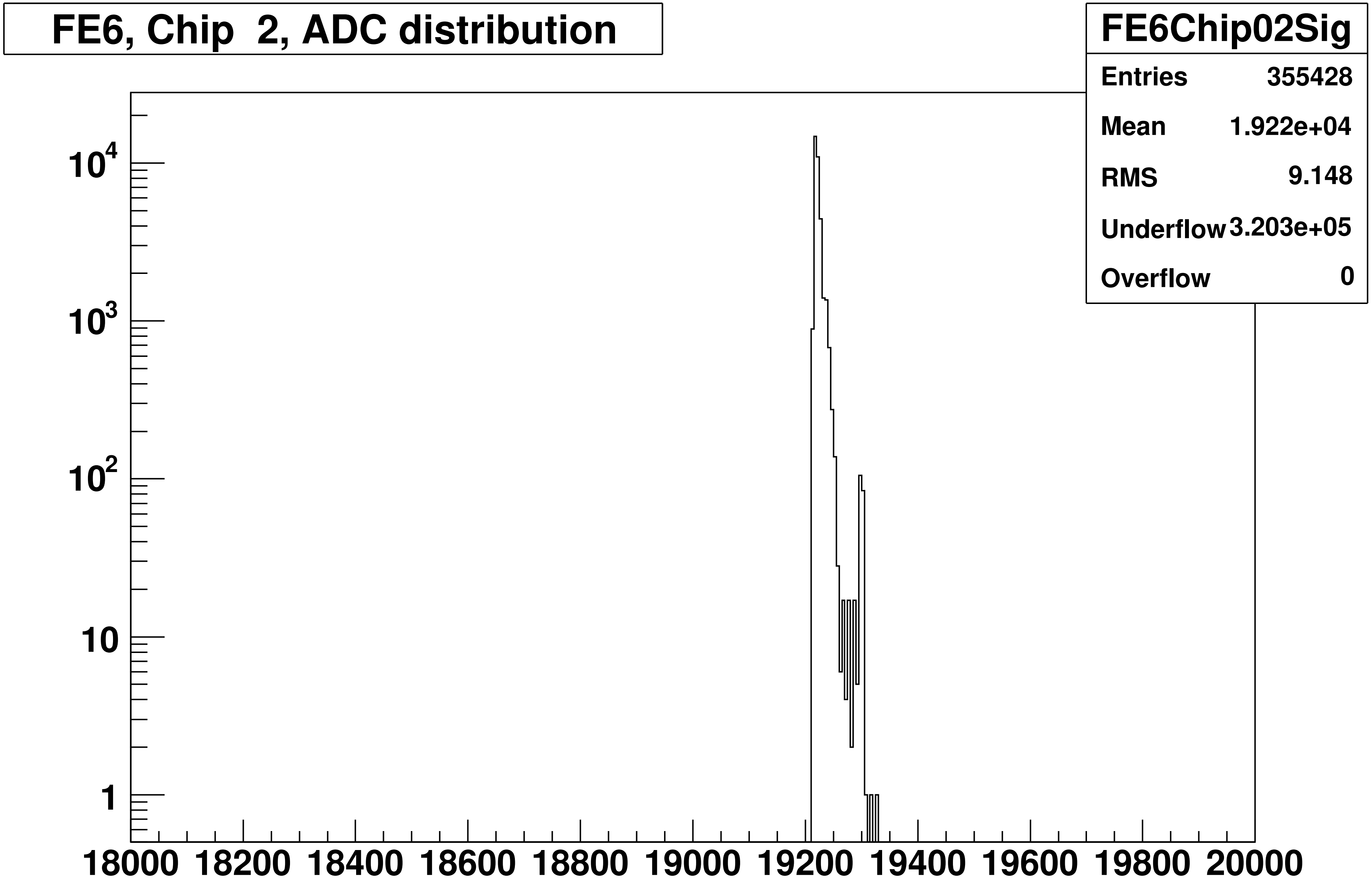
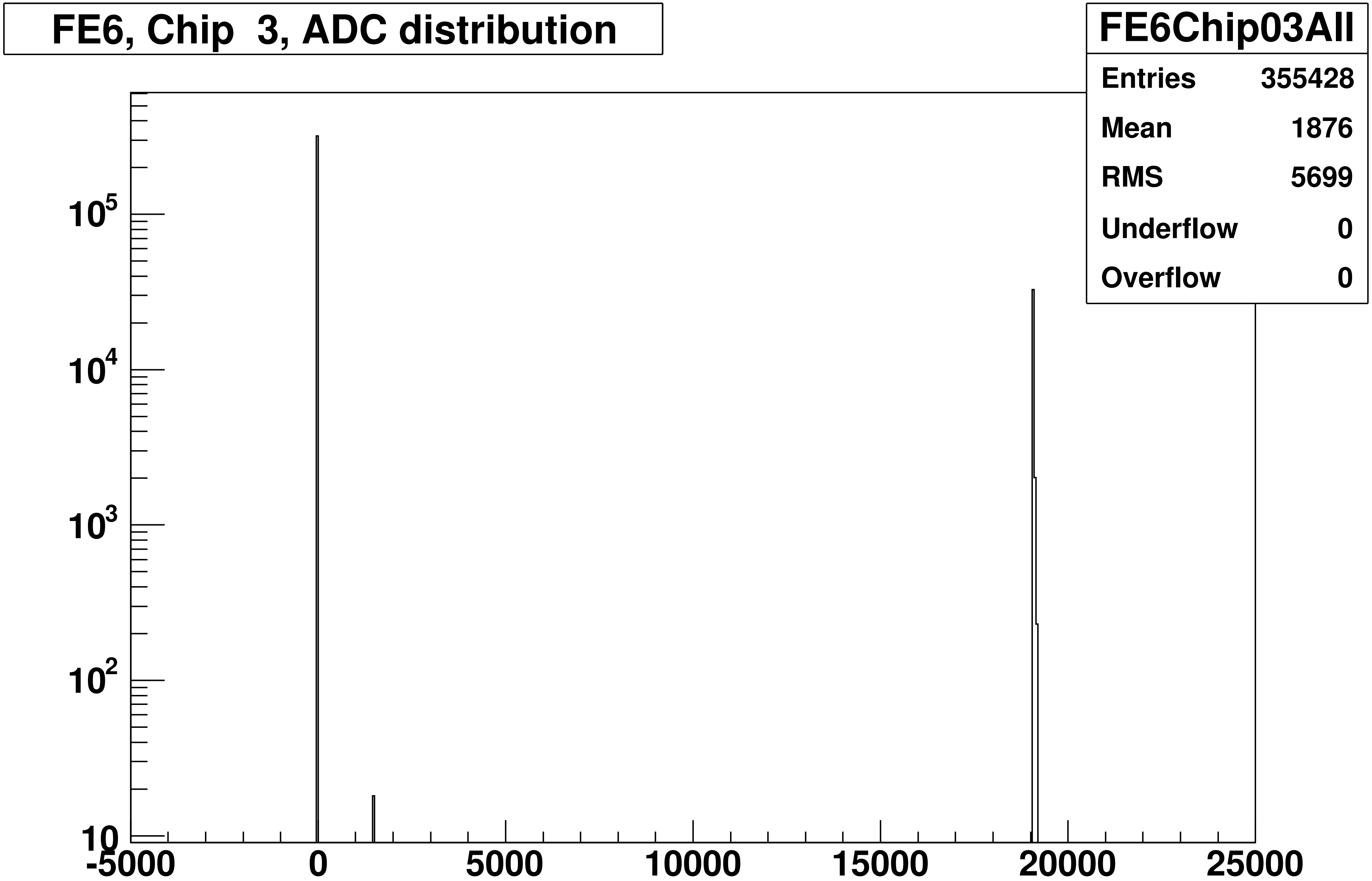
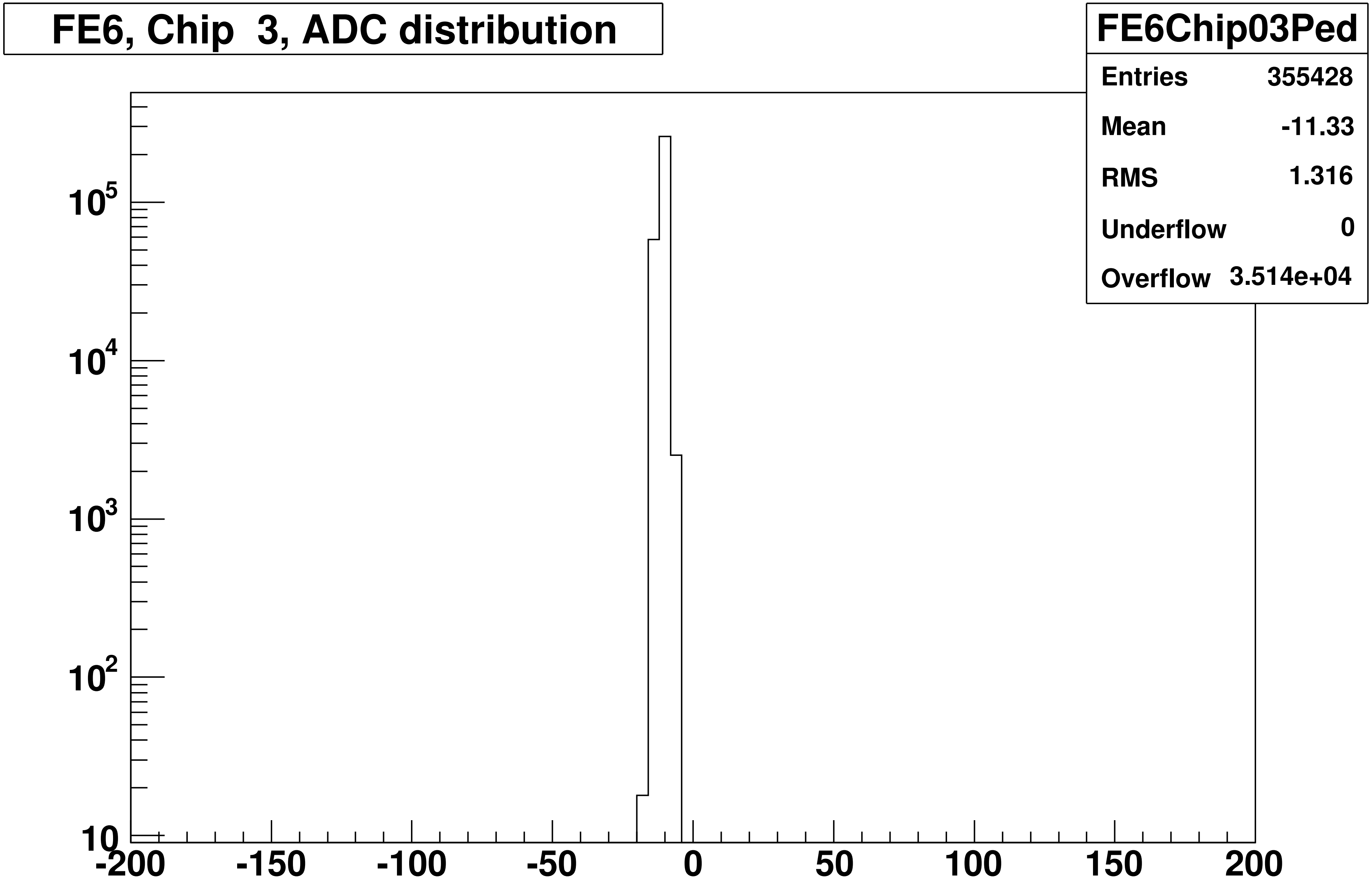
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 355428
Mean 1.922e+04
RMS 9.148
Underflow 3.203e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 355428
Mean 1876
RMS 5699
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 355428
Mean -11.33
RMS 1.316
Underflow 0
Overflow 3.514e+04
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10^4
10^3
10^2
10
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-100
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0
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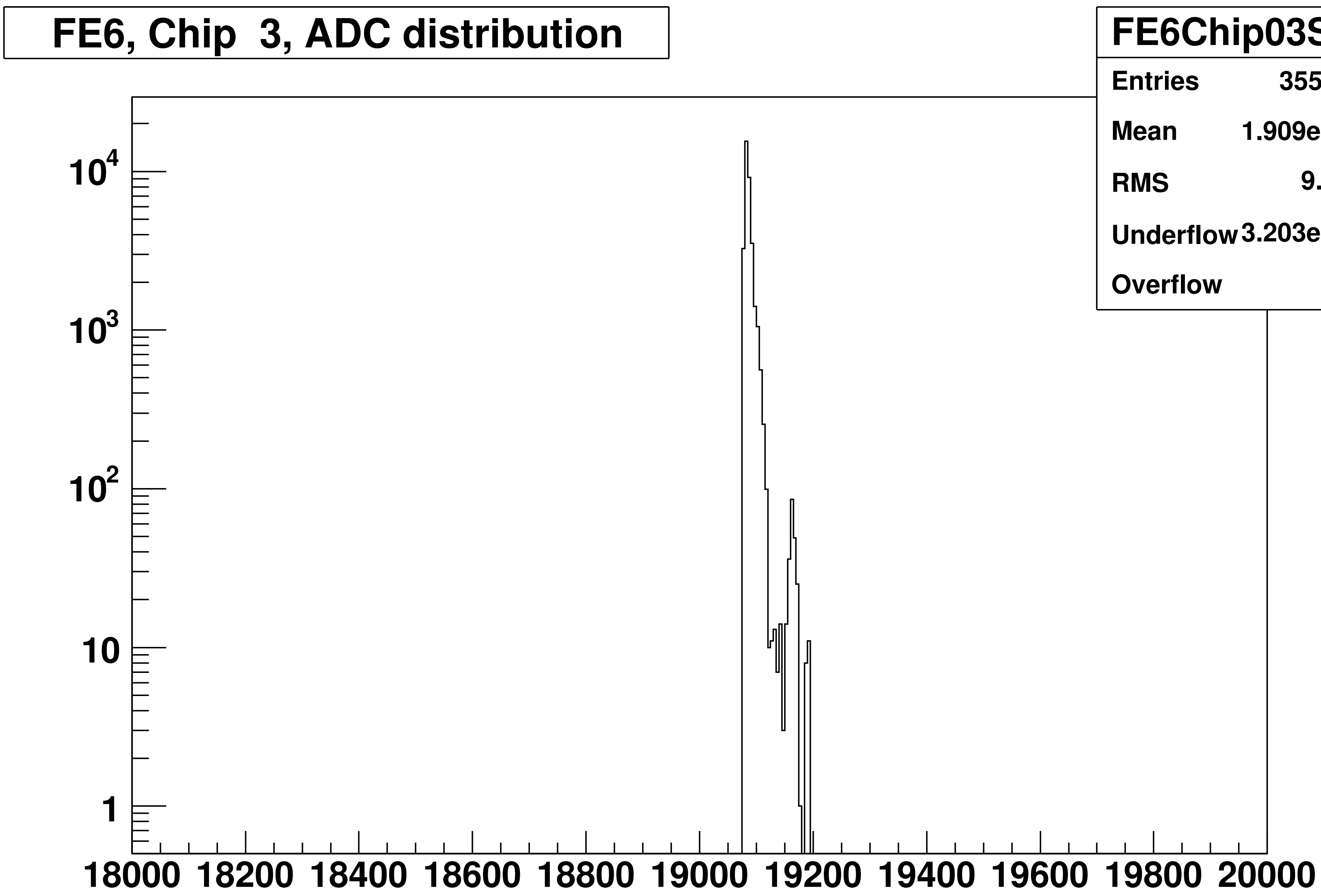
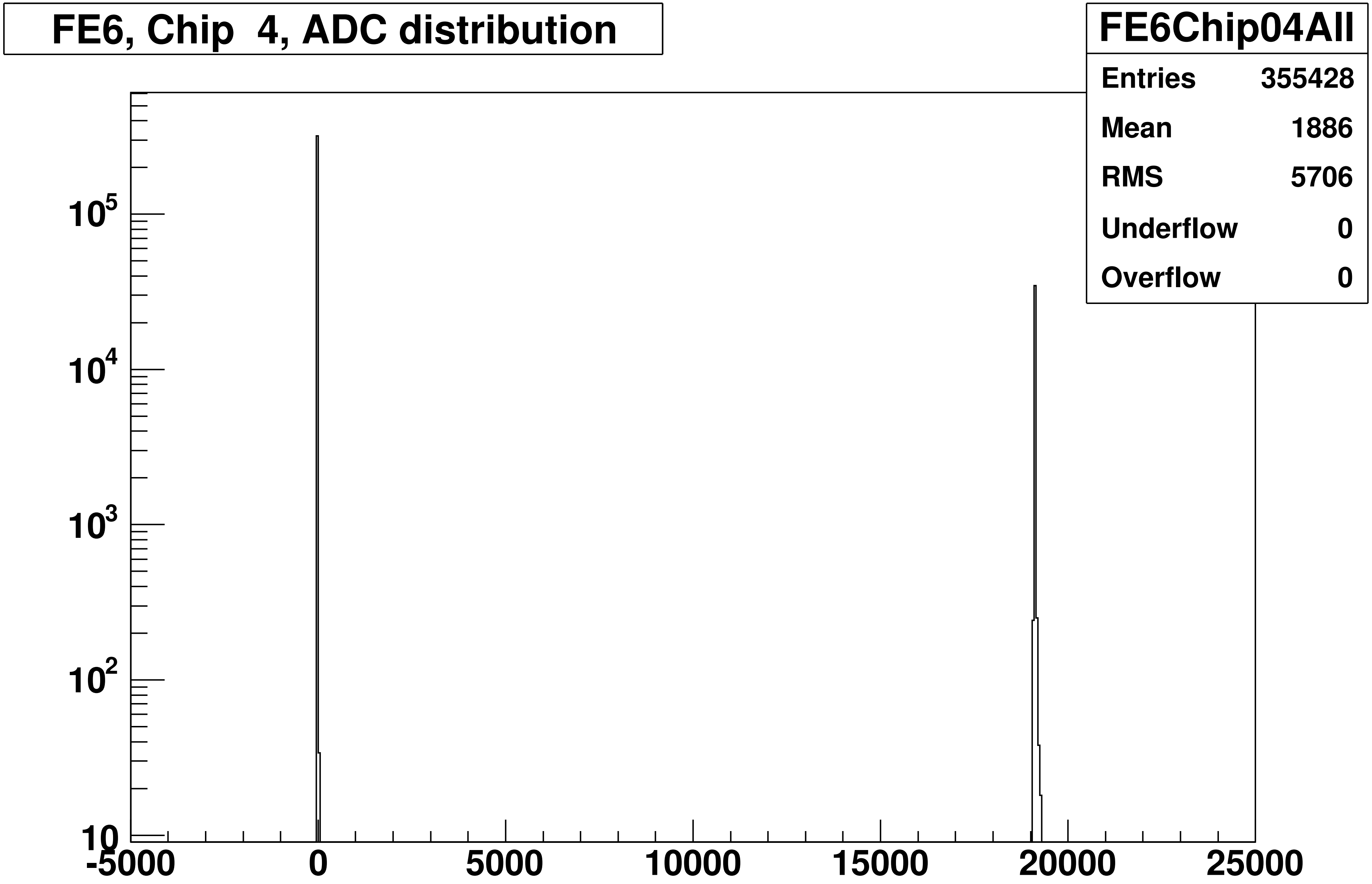
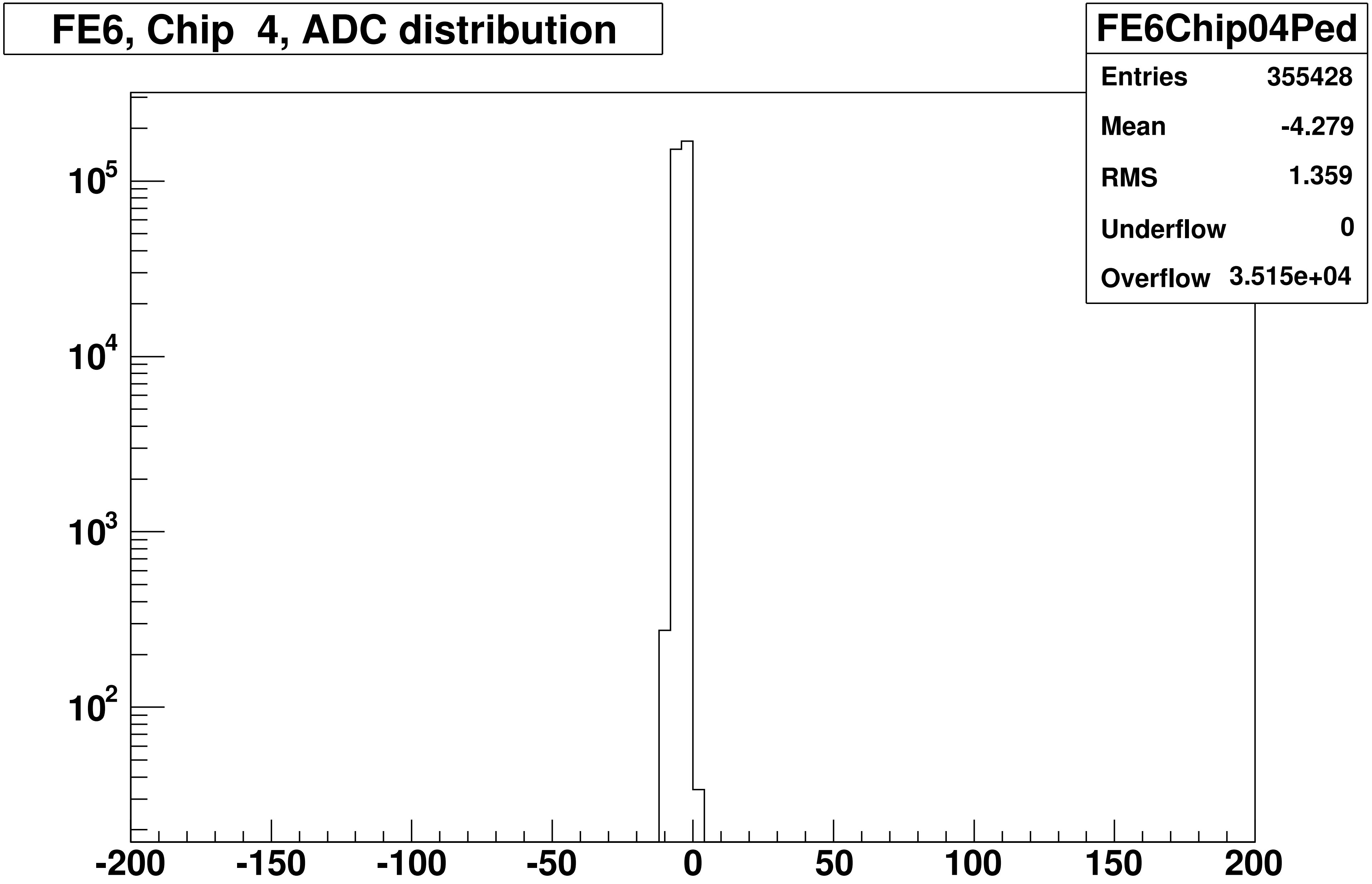
100
150
200



FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 355428
Mean 1.909e+04
RMS 9.418
Underflow 3.203e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 355428
Mean 1886
RMS 5706
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 355428
Mean -4.279
RMS 1.359
Underflow 0
Overflow 3.515e+04
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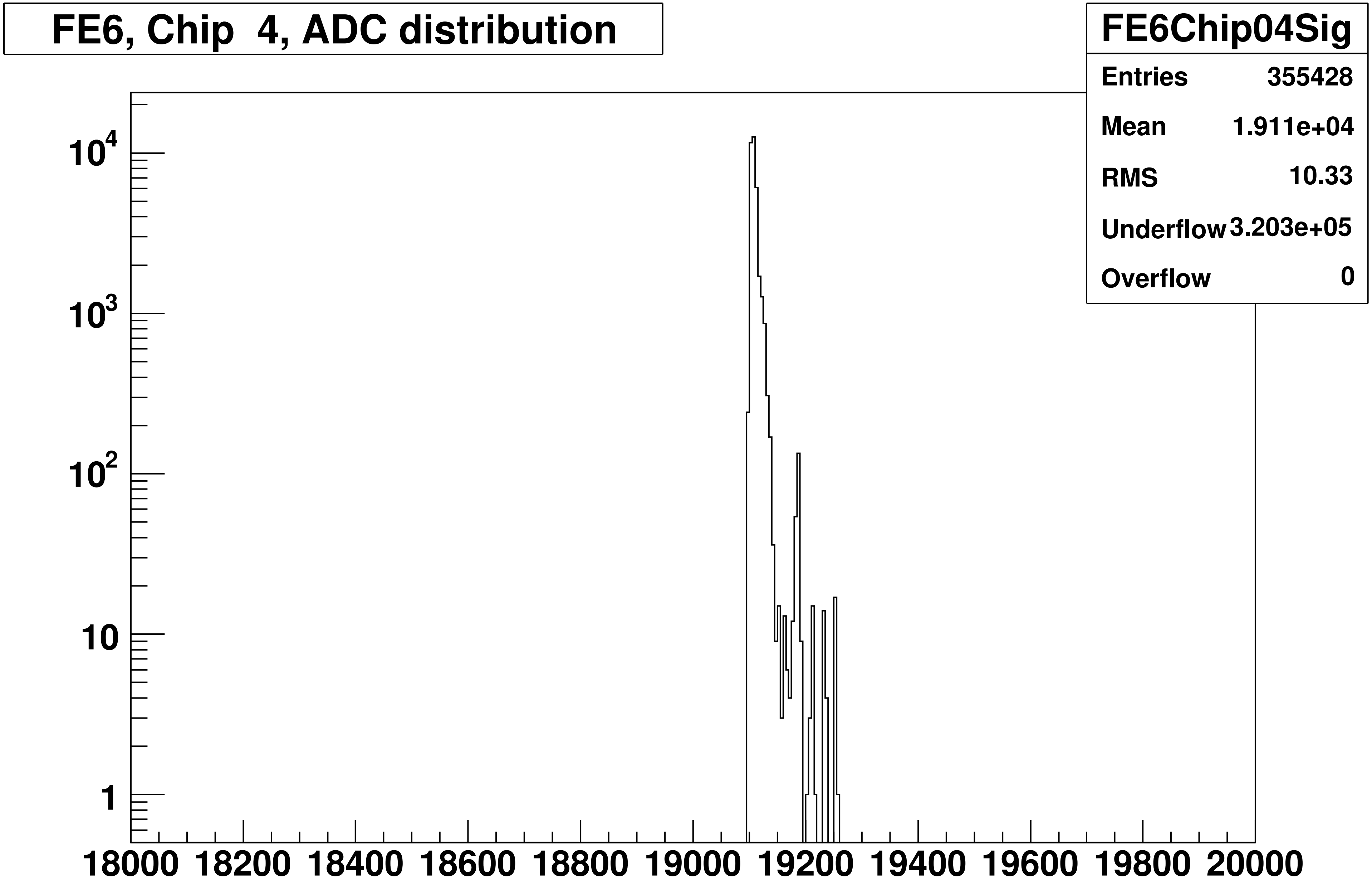
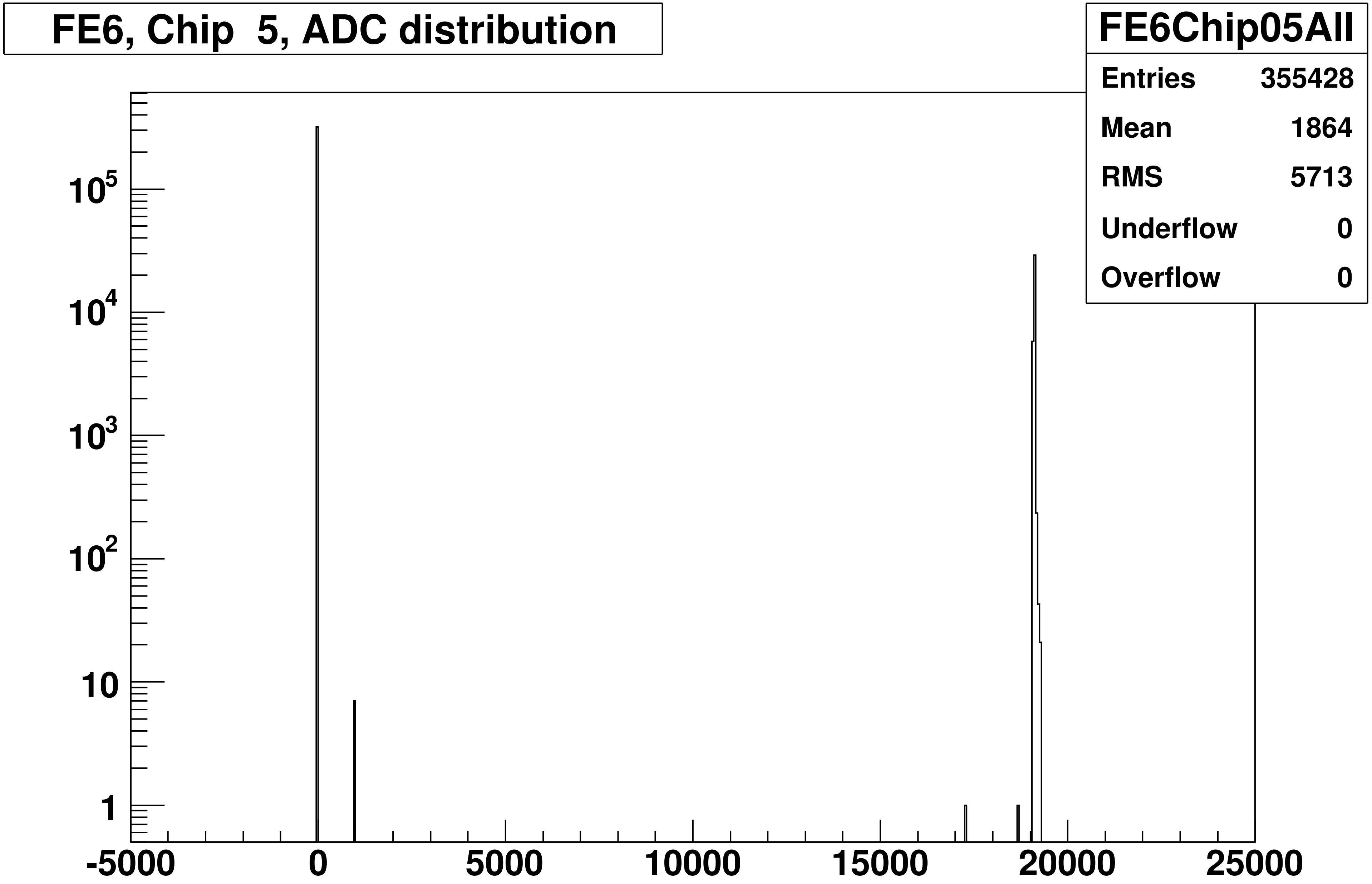
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 355428
Mean 1.911e+04
RMS 10.33
Underflow 3.203e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 355428
Mean 1864
RMS 5713
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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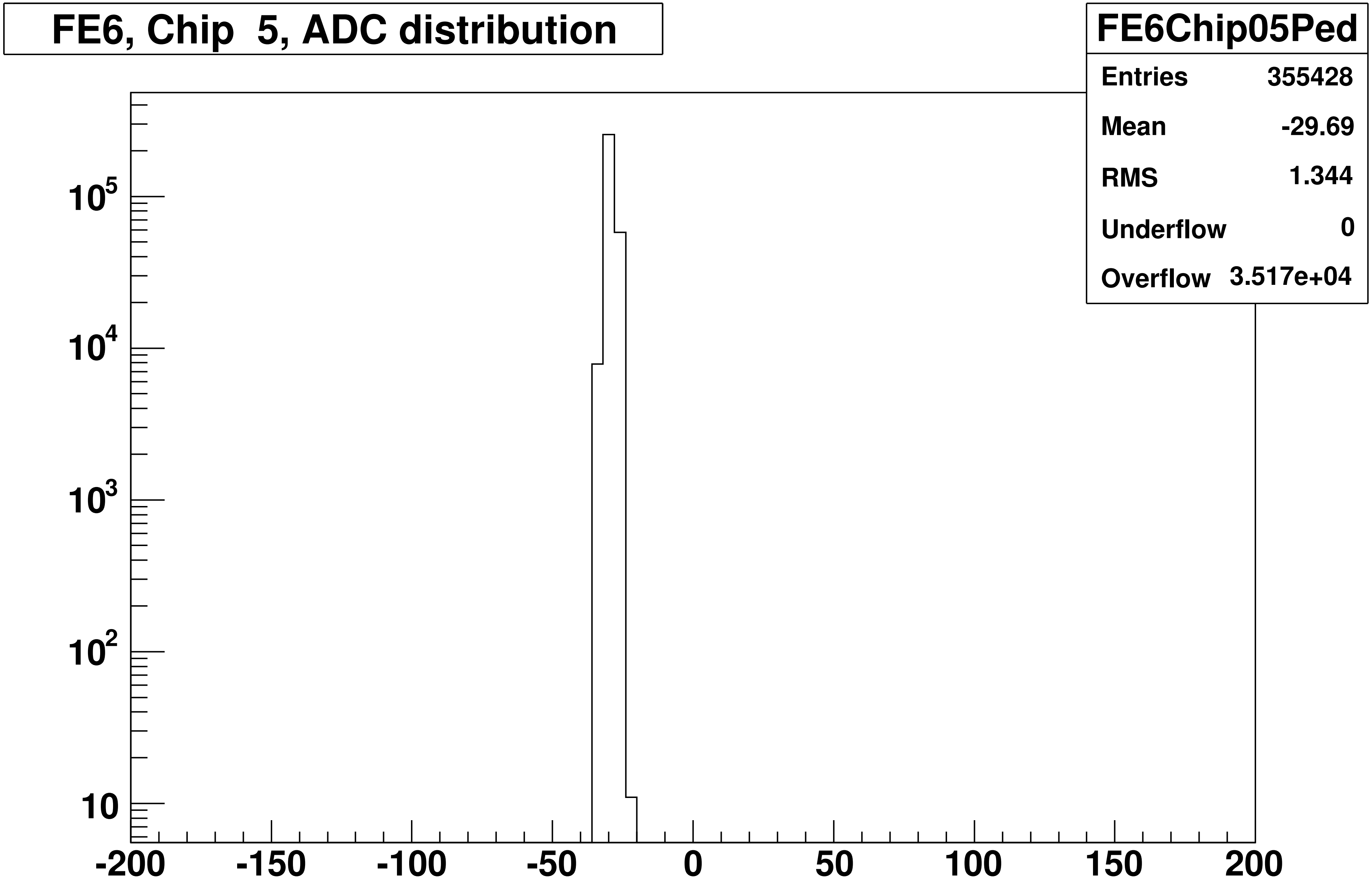
10000
15000
20000
25000


FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 355428
Mean -29.69
RMS 1.344
Underflow 0
Overflow 3.517e+04
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10^3
10^4
10^5
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-100
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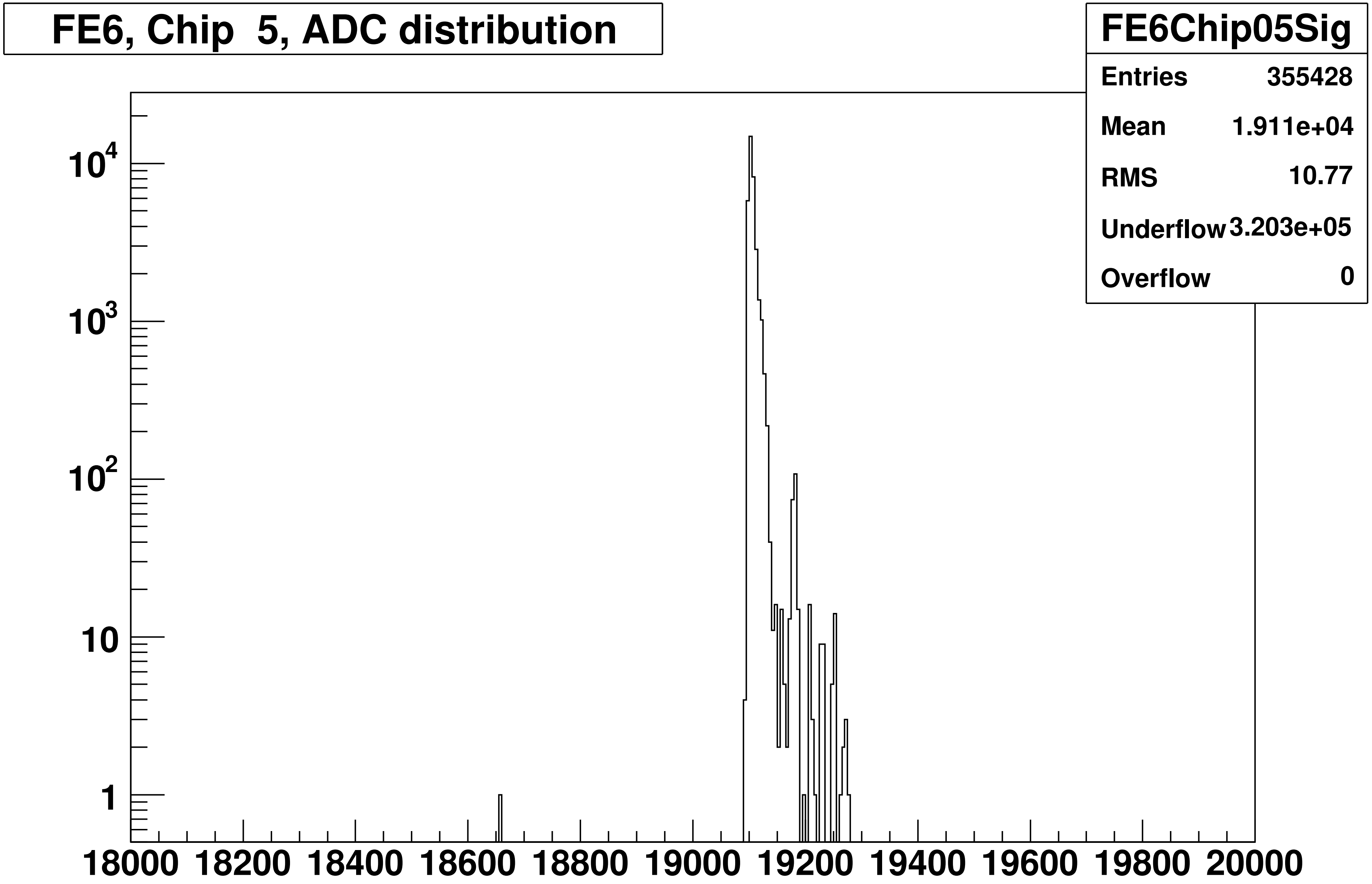
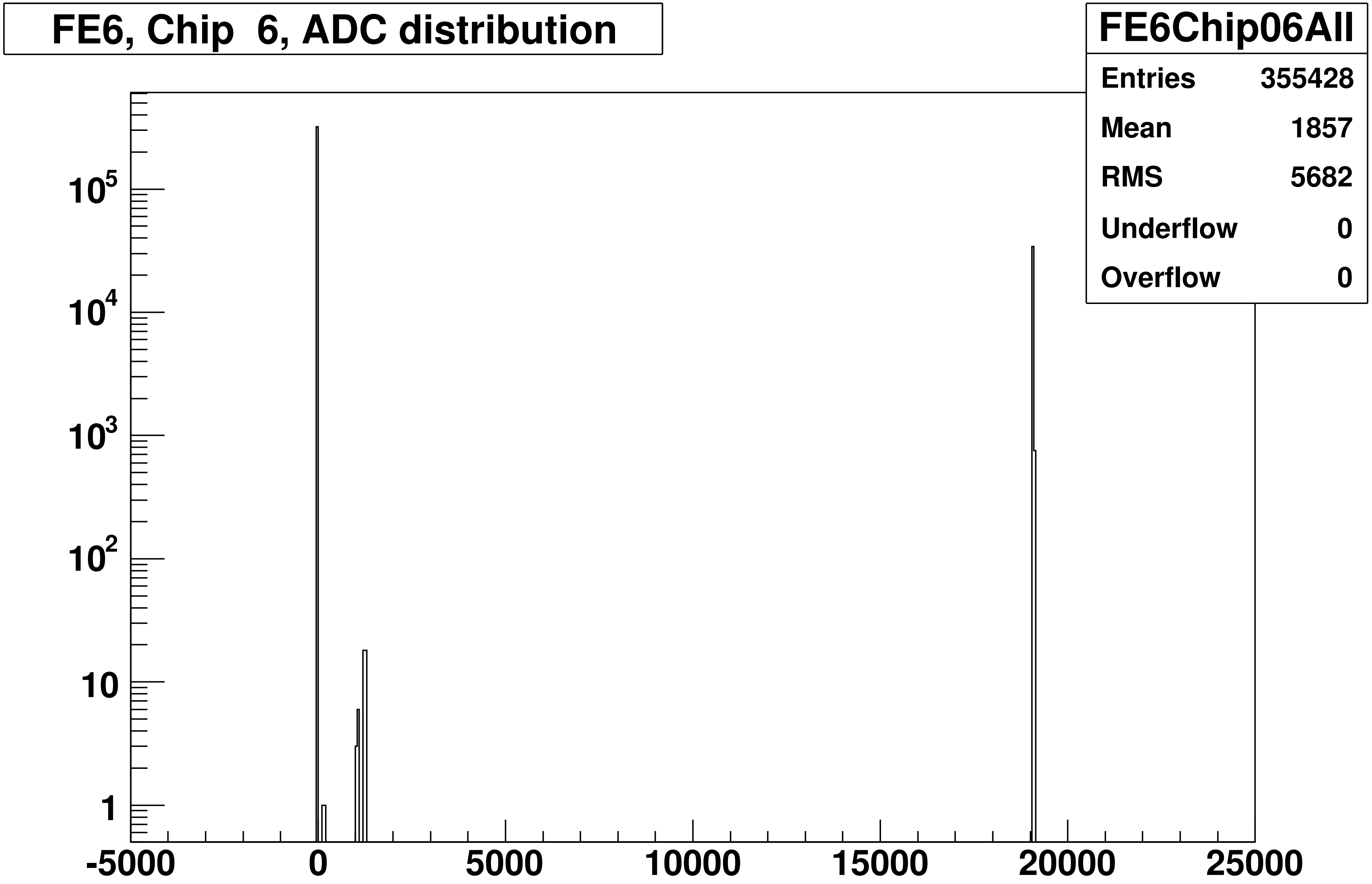
0
50
100
150
200



FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 355428
Mean 1.911e+04
RMS 10.77
Underflow 3.203e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 355428
Mean 1857
RMS 5682
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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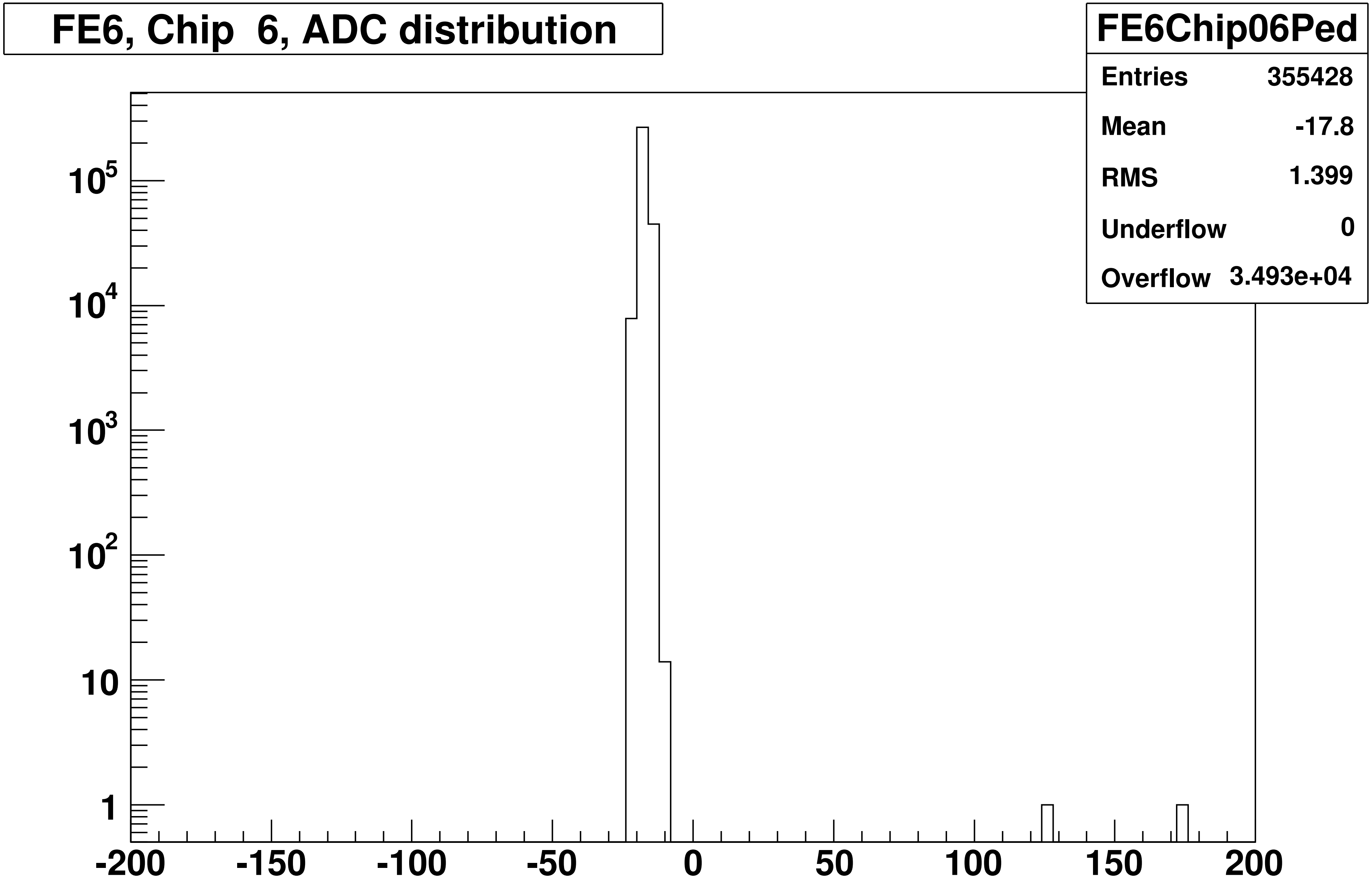
10000
15000
20000
25000


FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 355428
Mean -17.8
RMS 1.399
Underflow 0
Overflow 3.493e+04
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10^4
10^3
10^2
10
1
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-100
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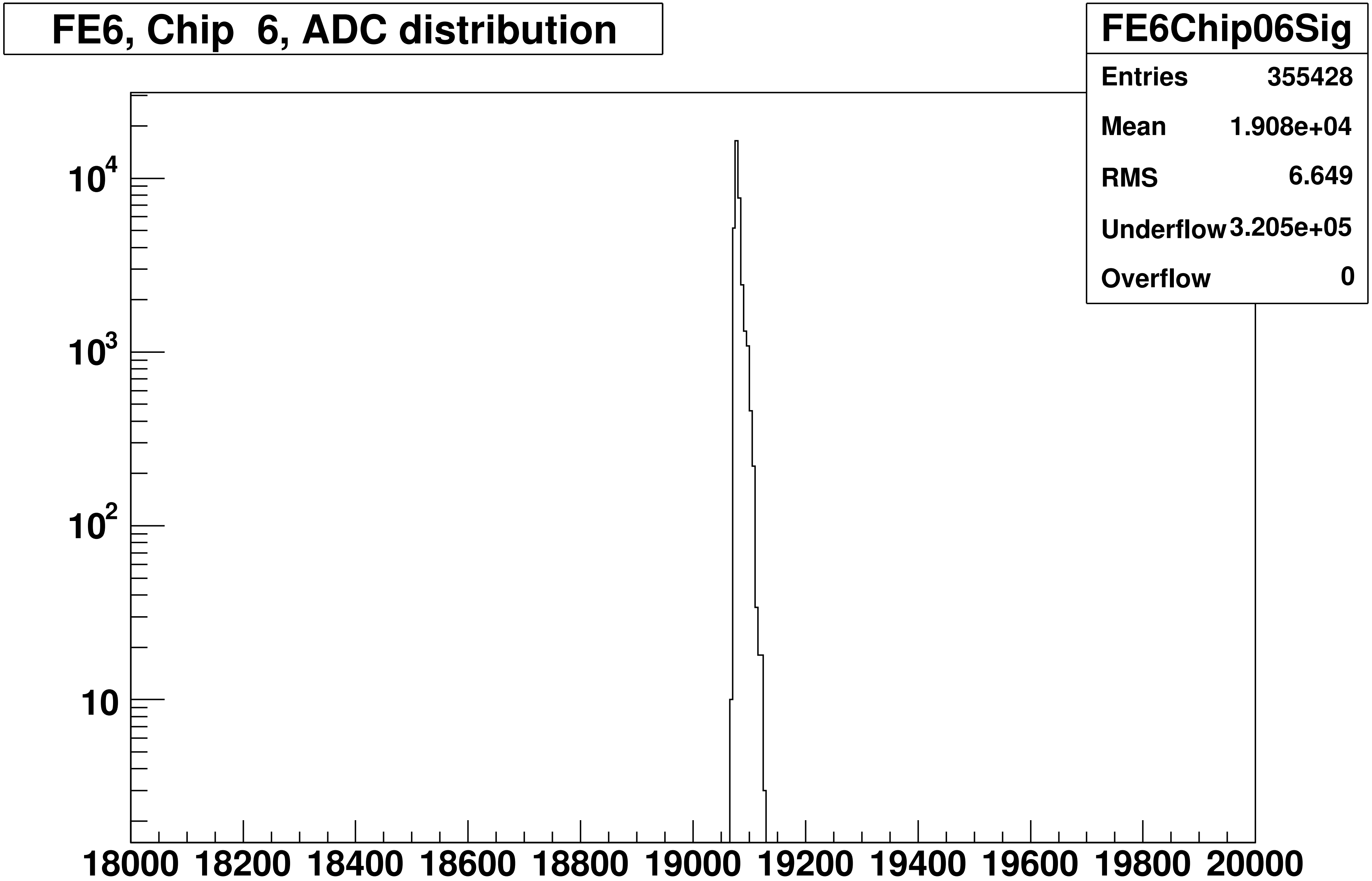
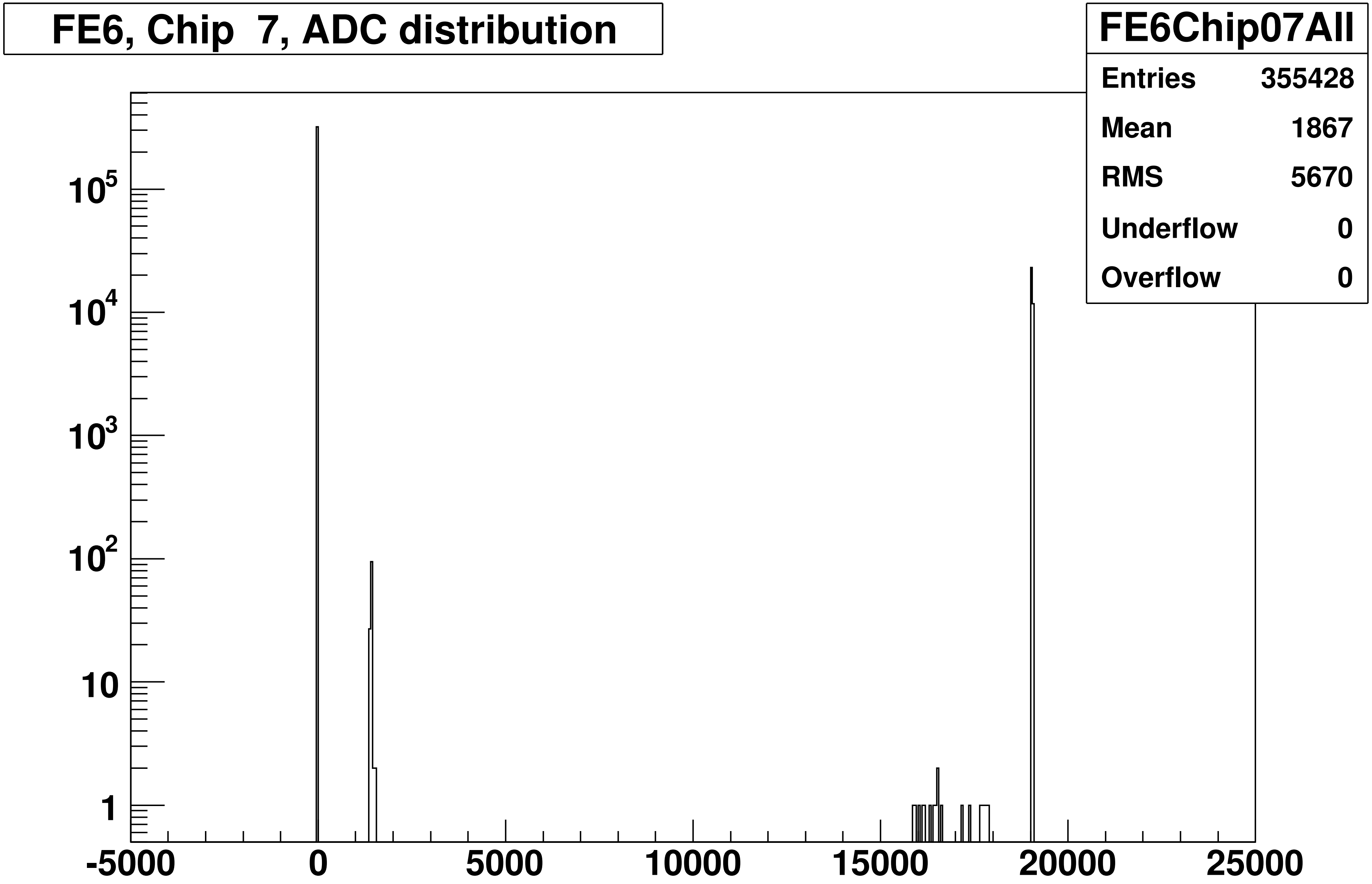
0
50
100
150
200



FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 355428
Mean 1.908e+04
RMS 6.649
Underflow 3.205e+05
Overflow 0
10
10^2
10^3
10^4
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 355428
Mean 1867
RMS 5670
Underflow 0
Overflow 0
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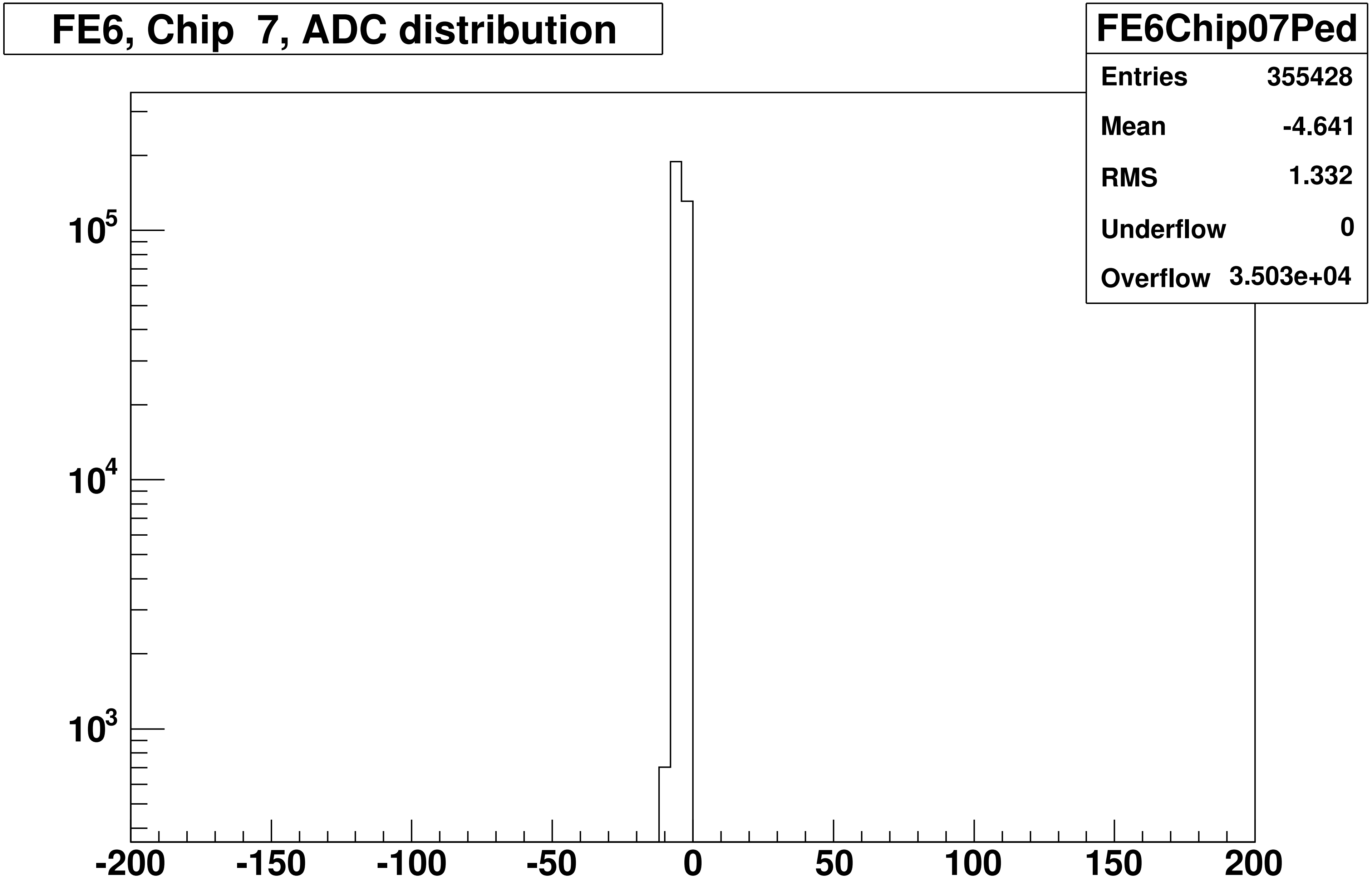
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 355428
Mean -4.641
RMS 1.332
Underflow 0
Overflow 3.503e+04
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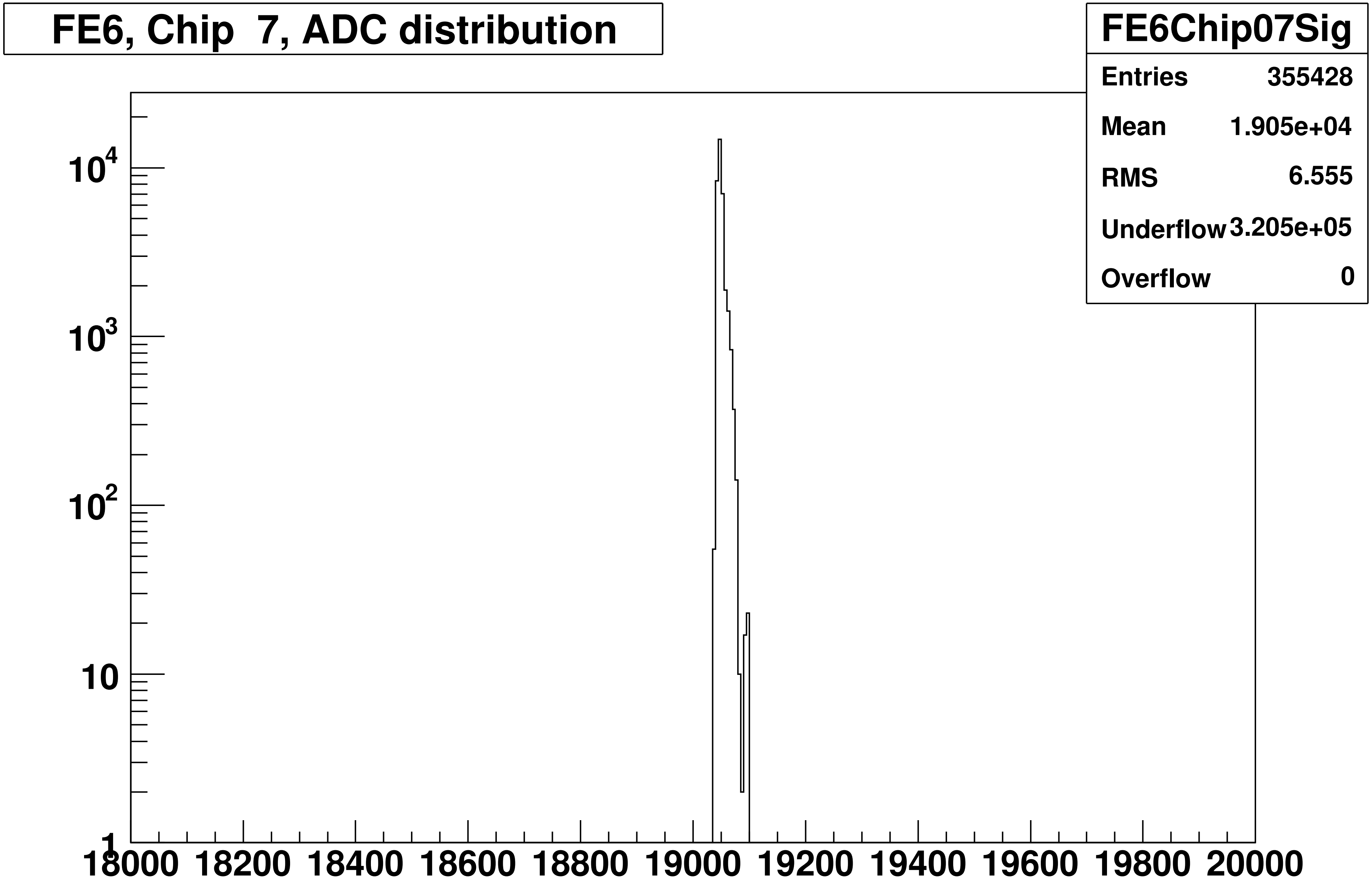
10^4
10^3
-200 -150 -100 -50 0 50 100 150 200


FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 355428
Mean 1.905e+04
RMS 6.555
Underflow 3.205e+05
Overflow 0
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10^3
10^2
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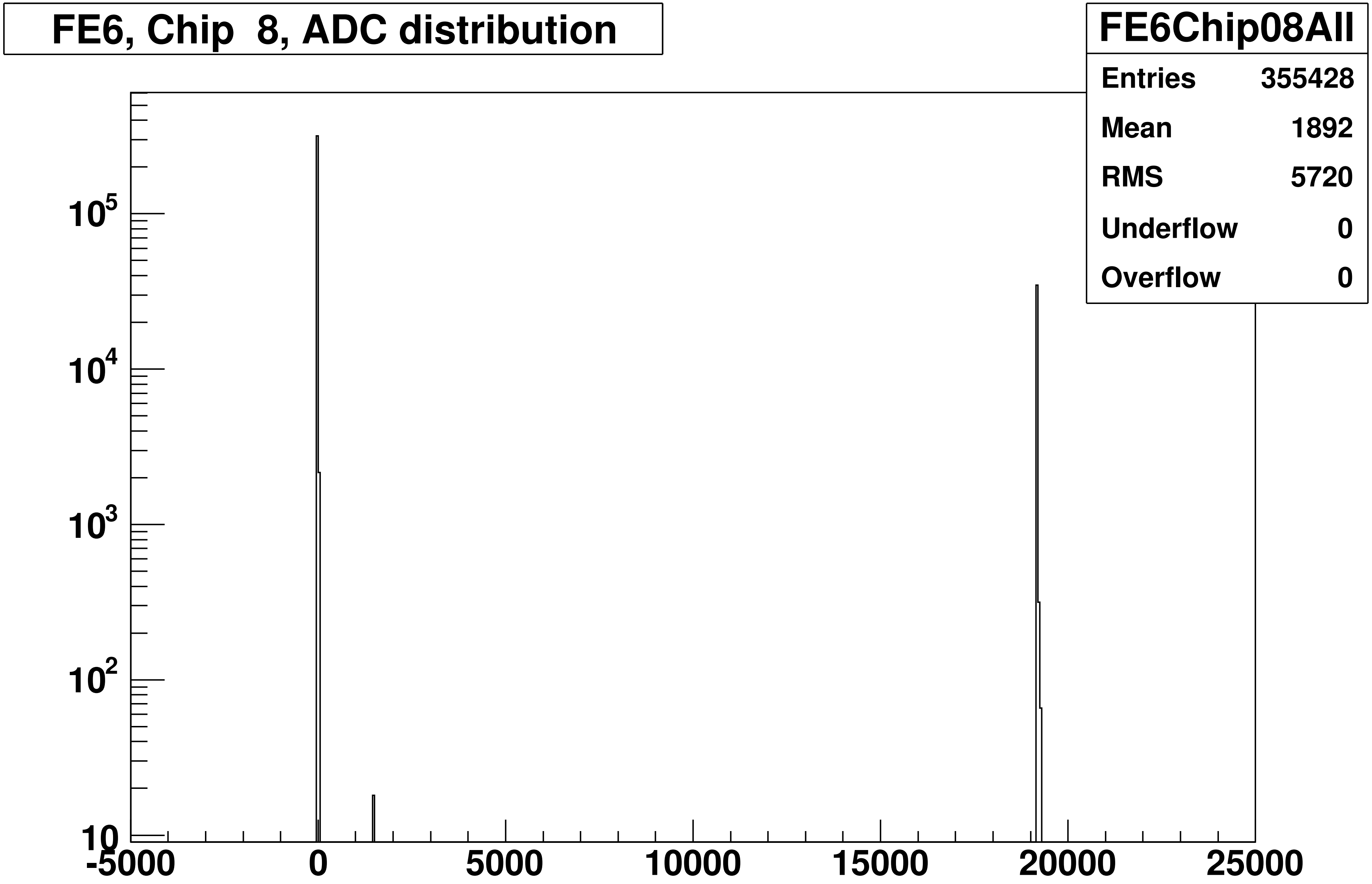
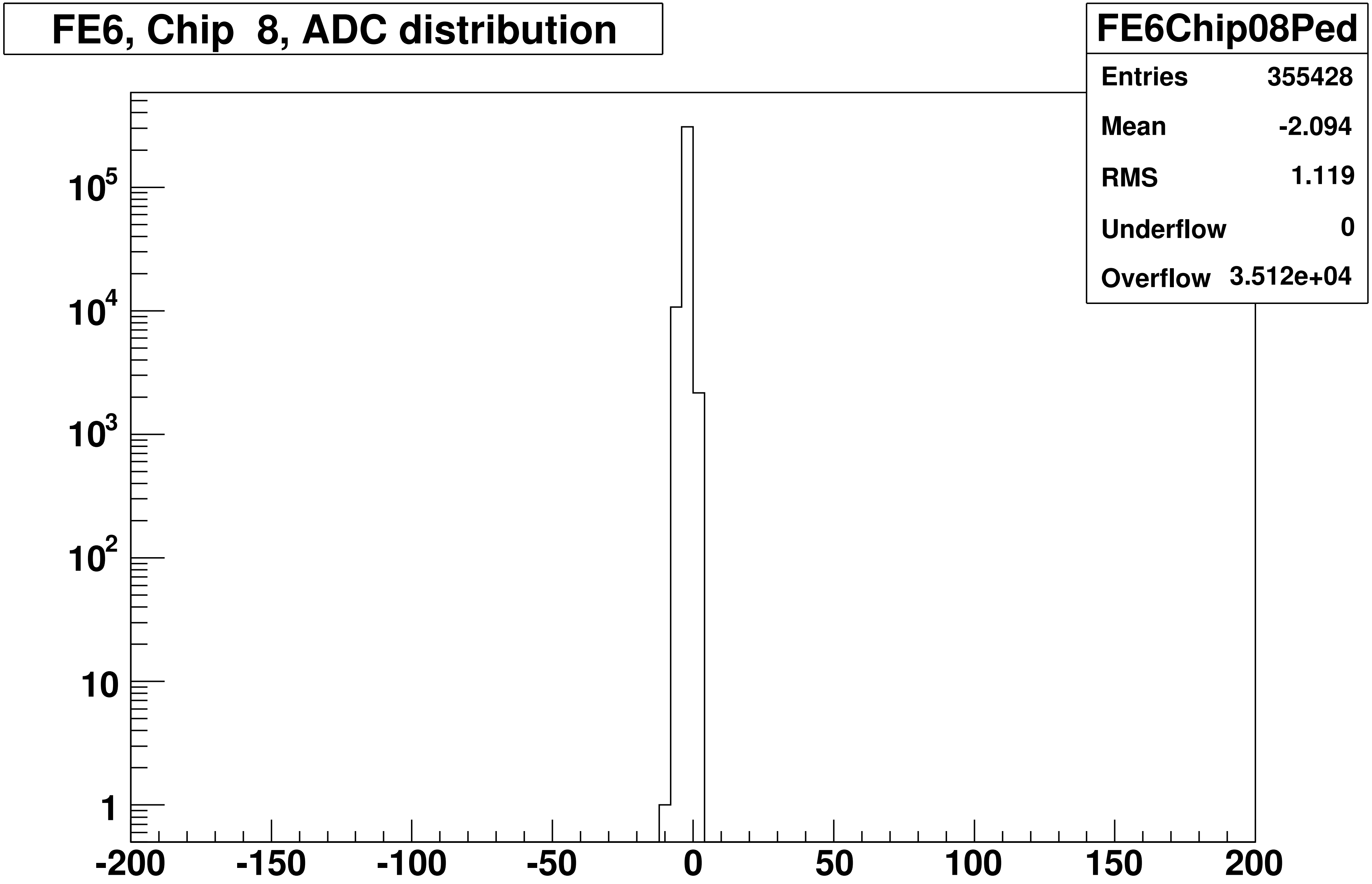
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 355428
Mean 1892
RMS 5720
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 355428
Mean -2.094
RMS 1.119
Underflow 0
Overflow 3.512e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
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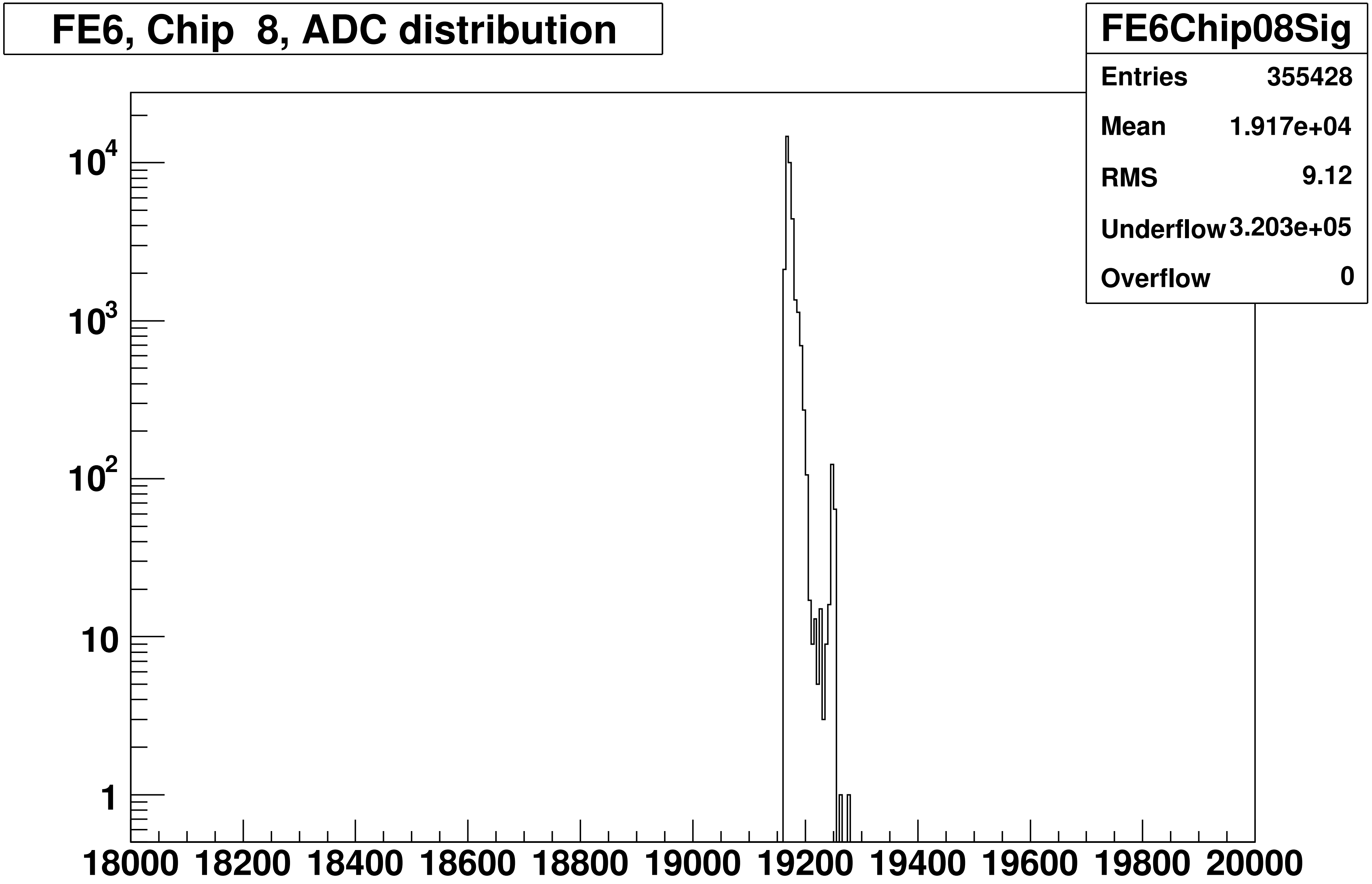
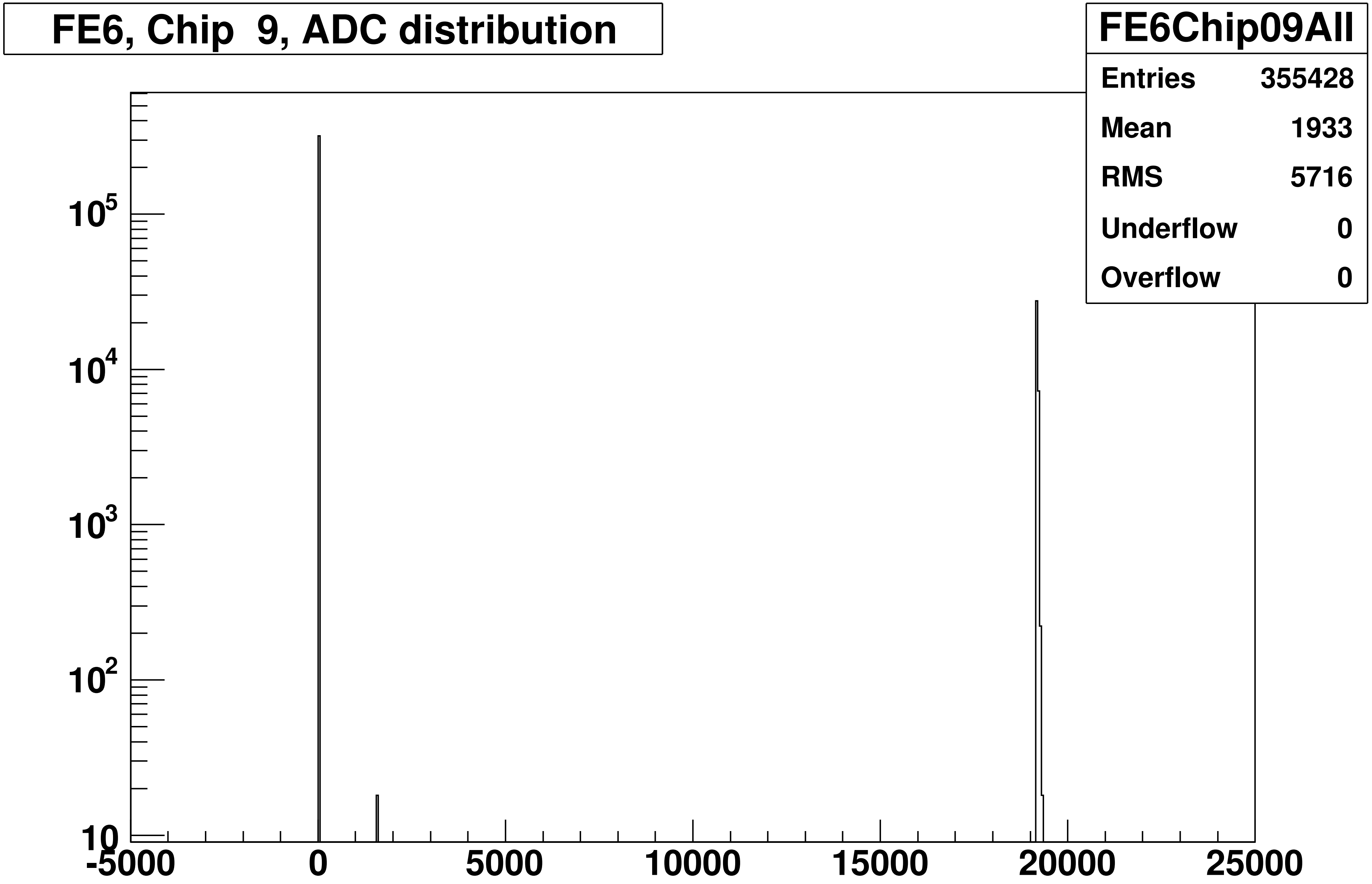
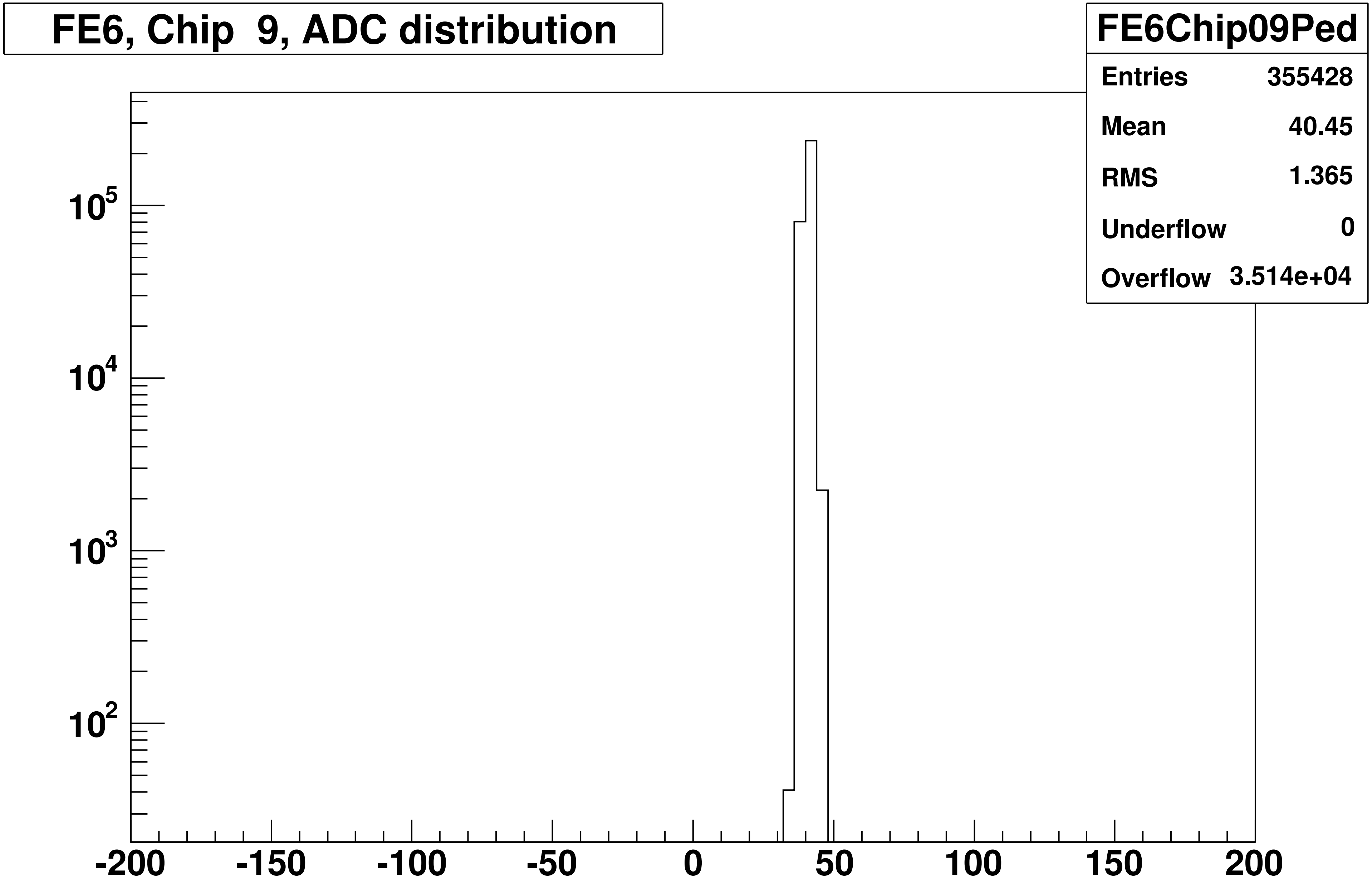
100
150
200

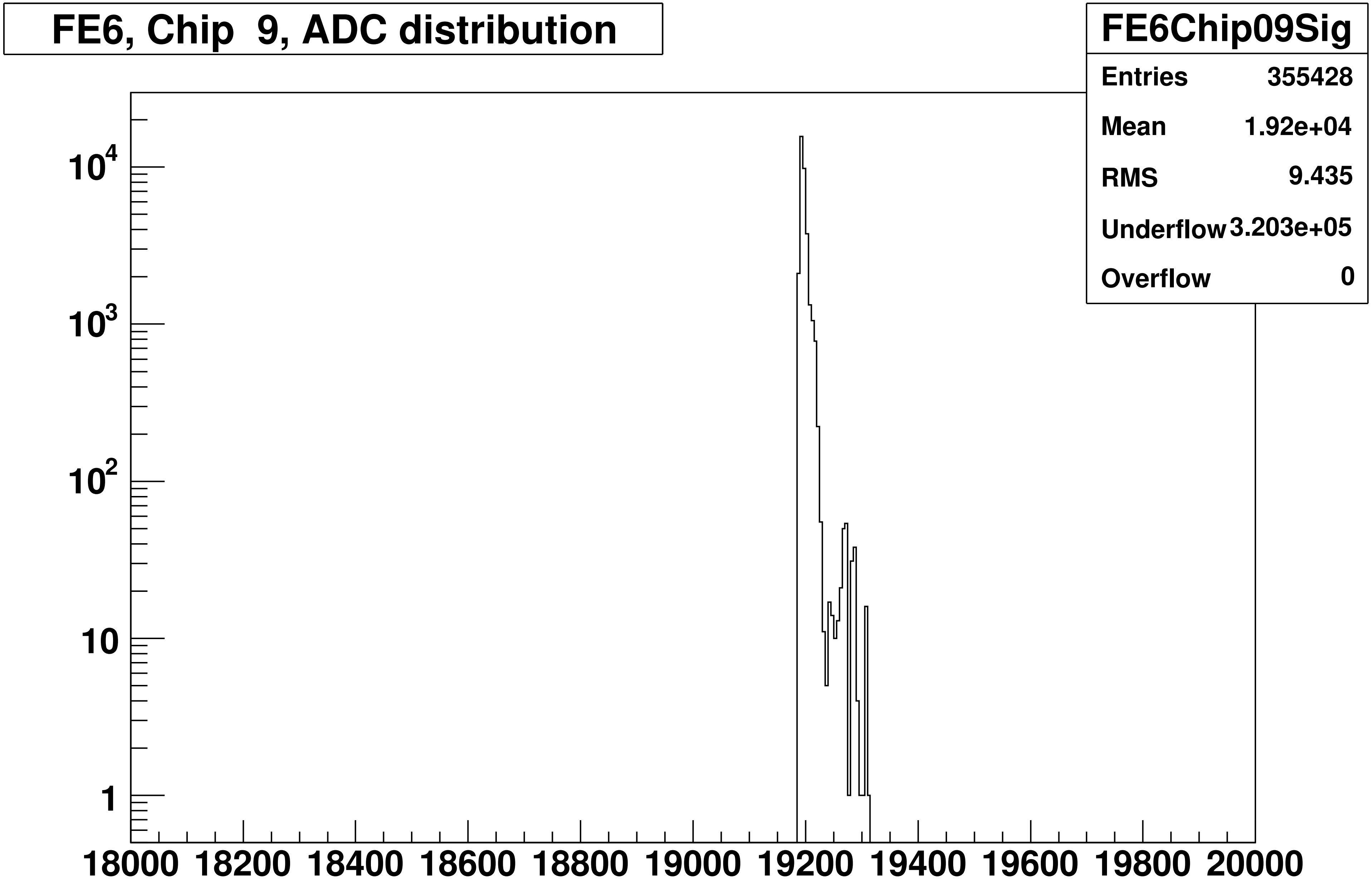


FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 355428
Mean 1.917e+04
RMS 9.12
Underflow 3.203e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

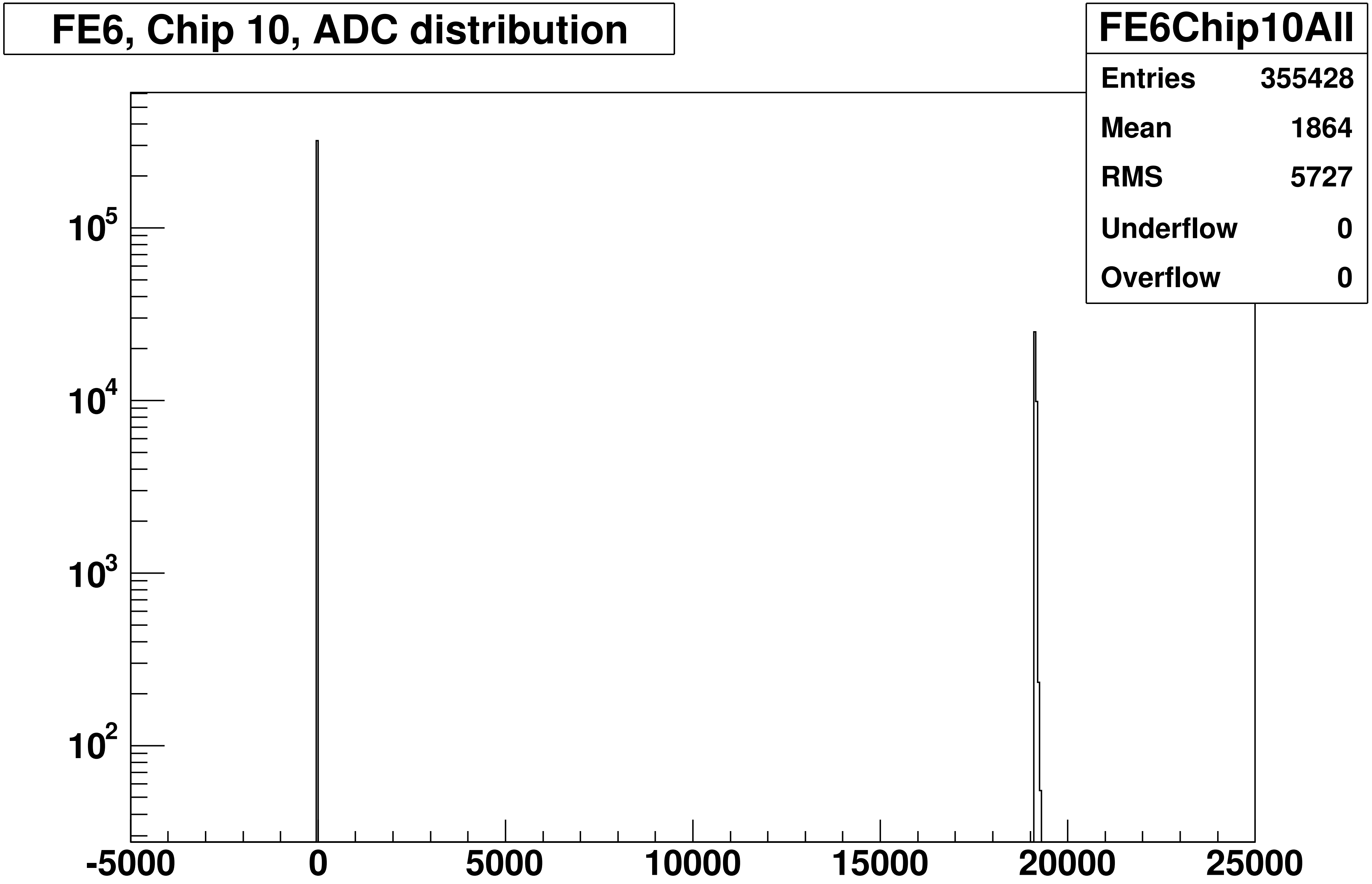


FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 355428
Mean 1933
RMS 5716
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

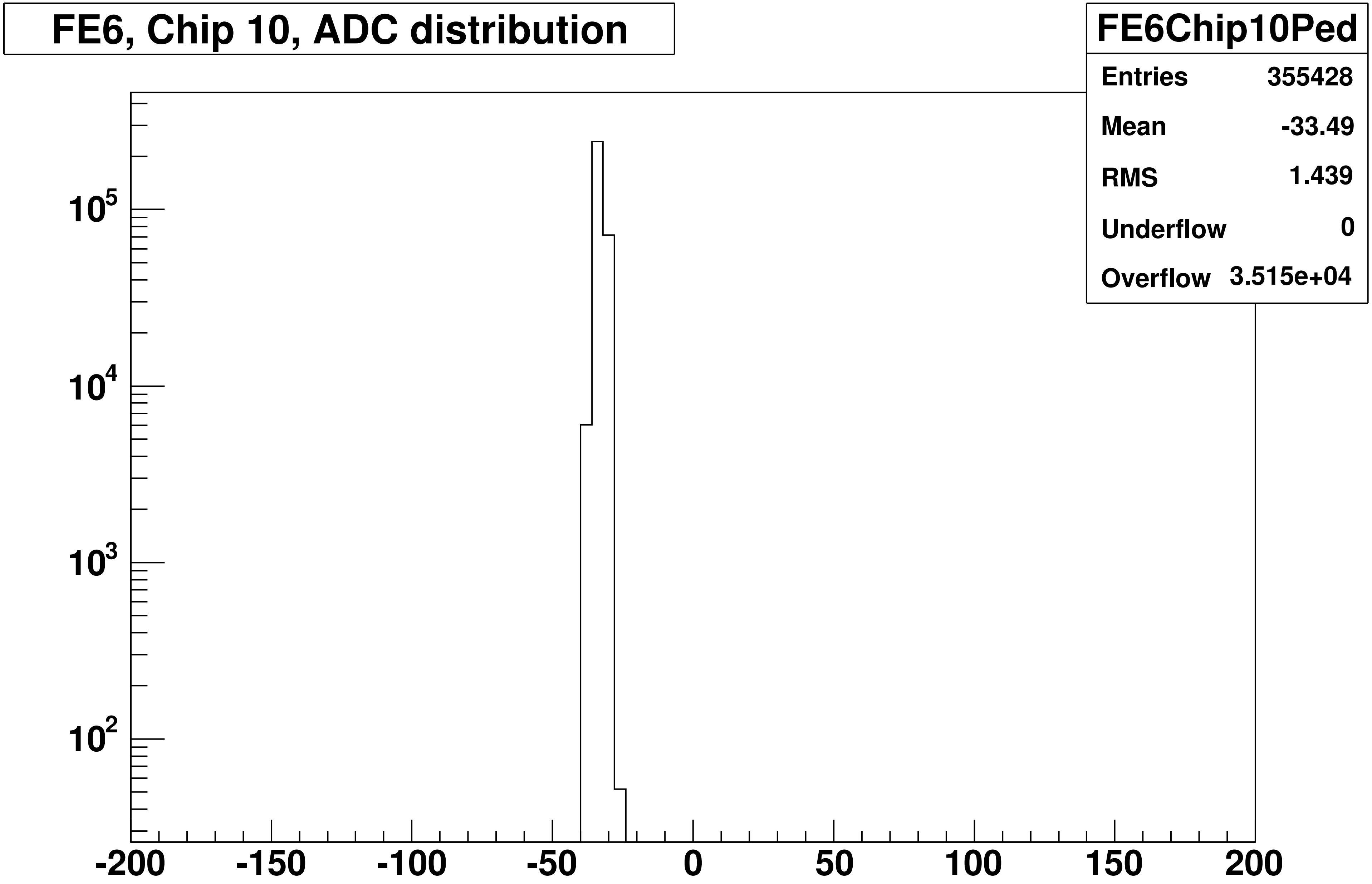

FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 355428
Mean 40.45
RMS 1.365
Underflow 0
Overflow 3.514e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 355428
Mean 1.92e+04
RMS 9.435
Underflow 3.203e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 355428
Mean 1864
RMS 5727
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 355428
Mean -33.49
RMS 1.439
Underflow 0
Overflow 3.515e+04
10^5
10^4
10^3
10^2
-200
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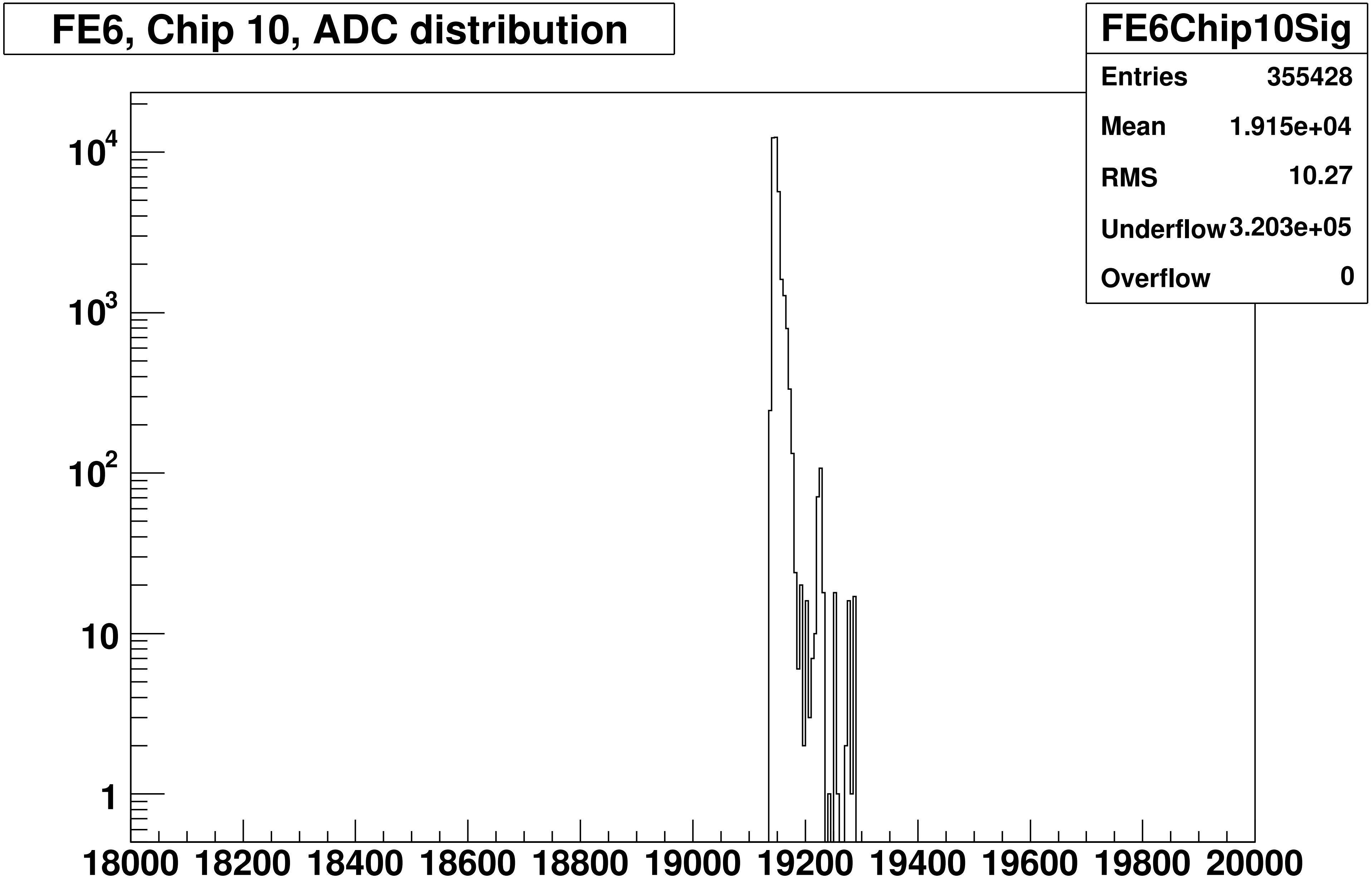
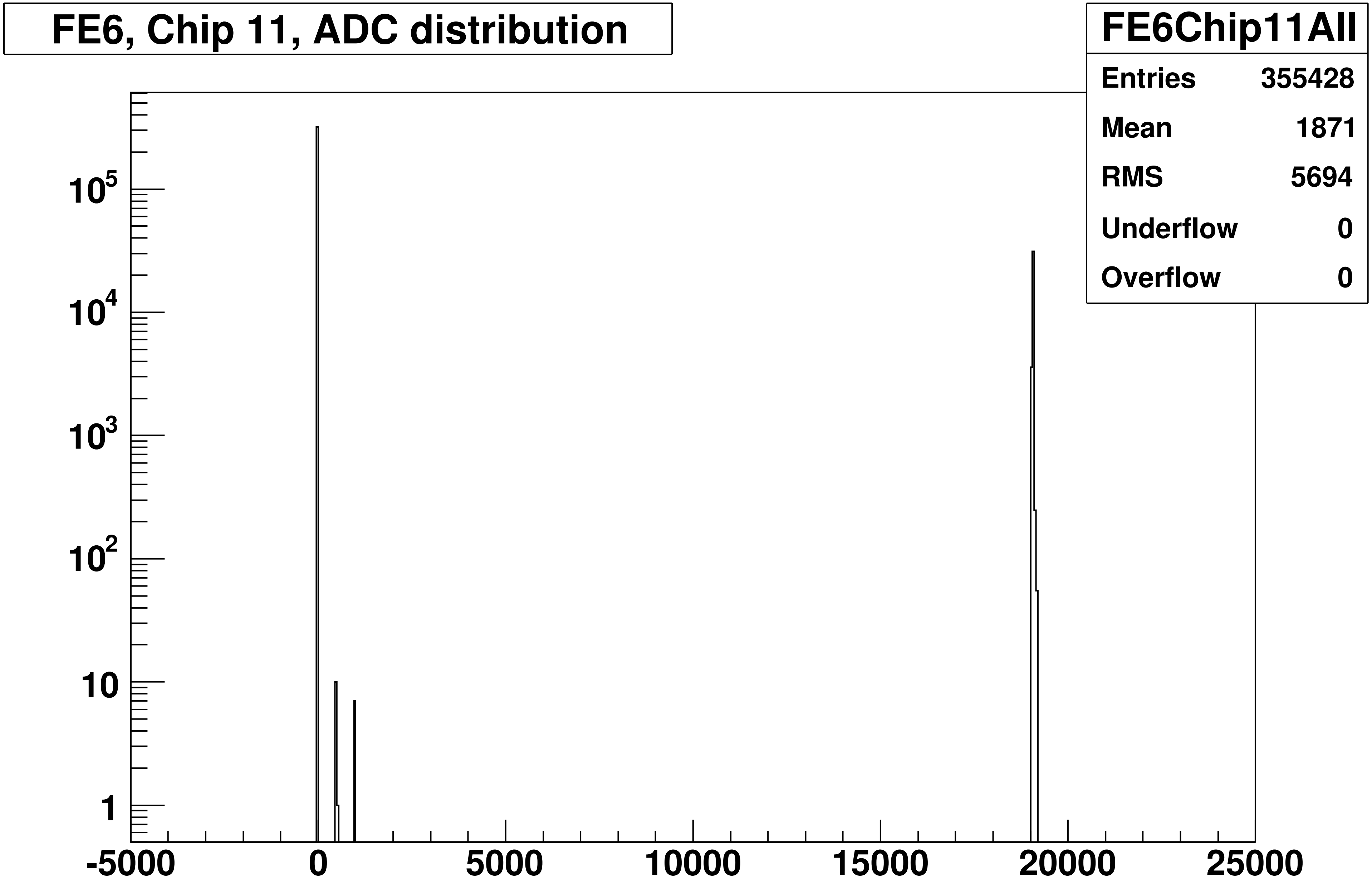
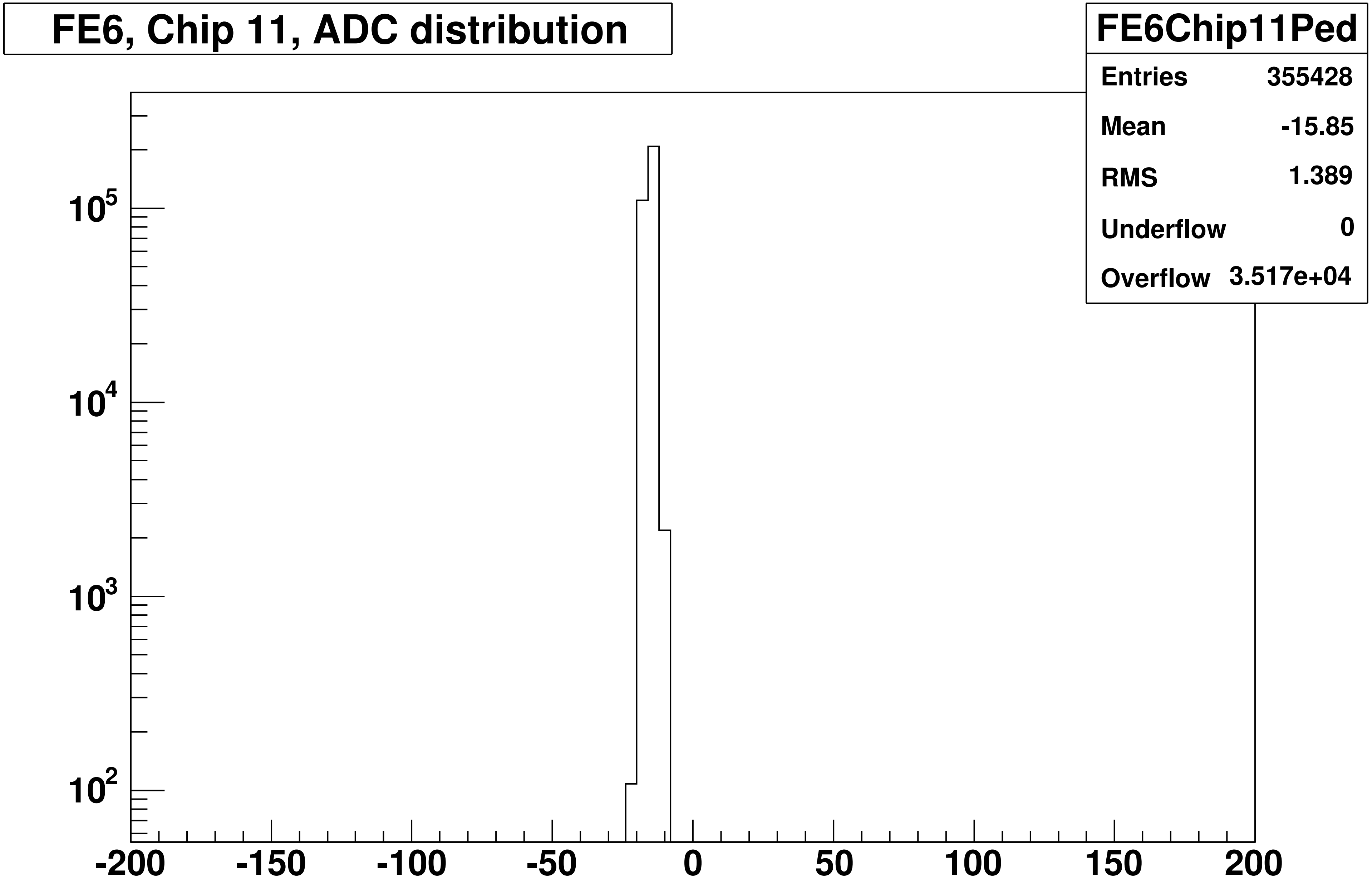
-100
-50
0
50
100
150
200


FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 355428
Mean 1.915e+04
RMS 10.27
Underflow 3.203e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 355428
Mean 1871
RMS 5694
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 355428
Mean -15.85
RMS 1.389
Underflow 0
Overflow 3.517e+04
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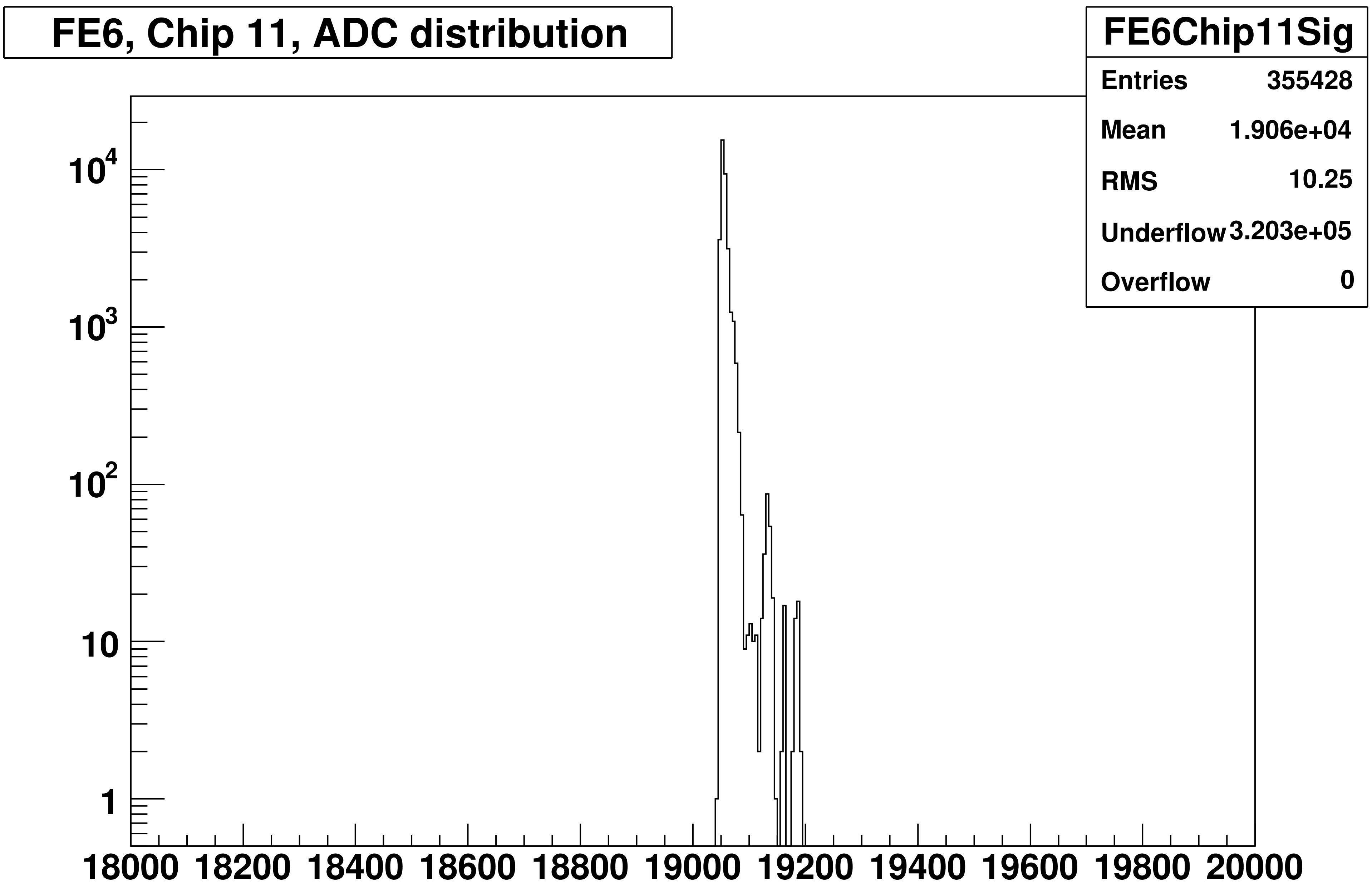
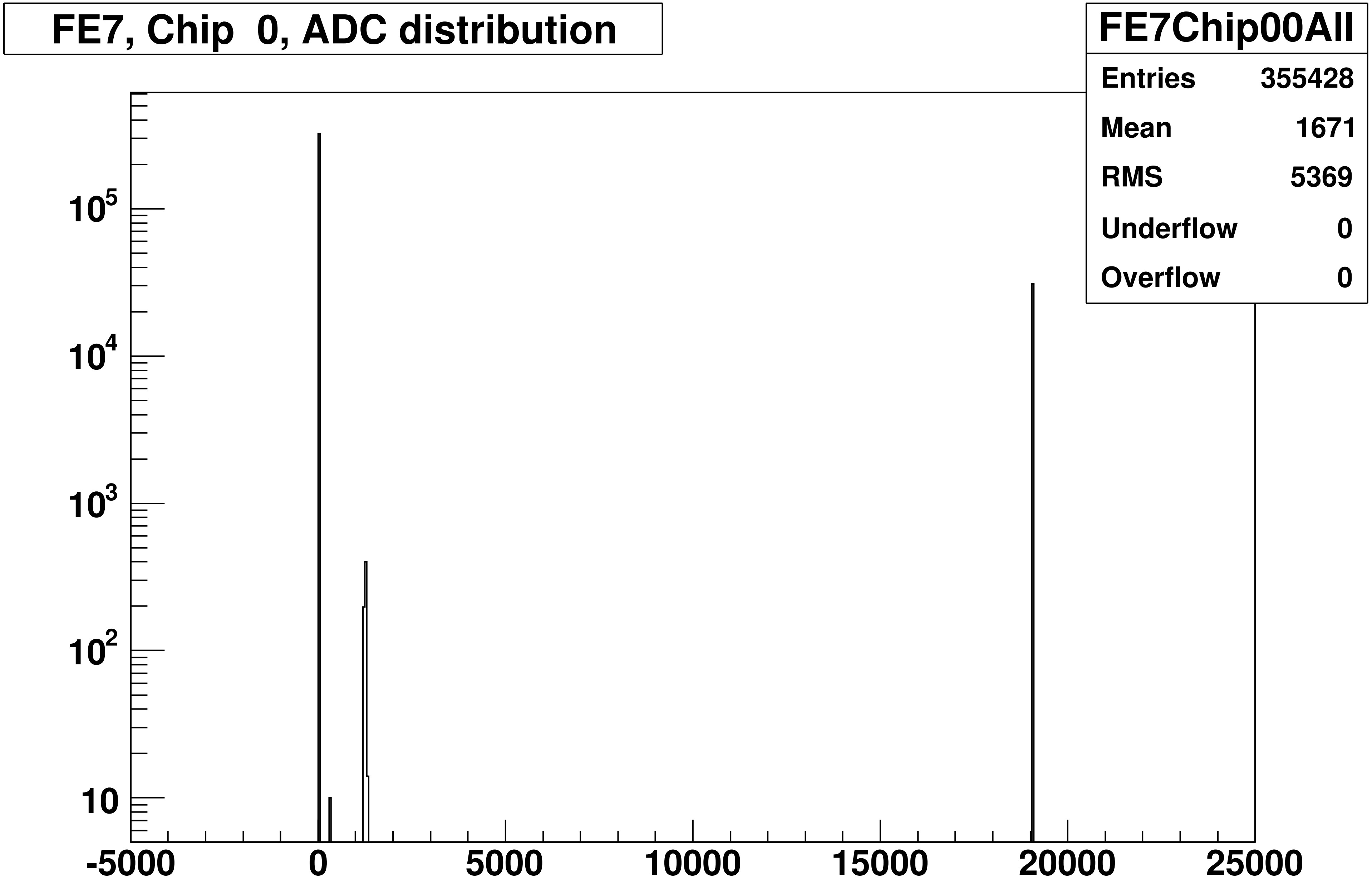
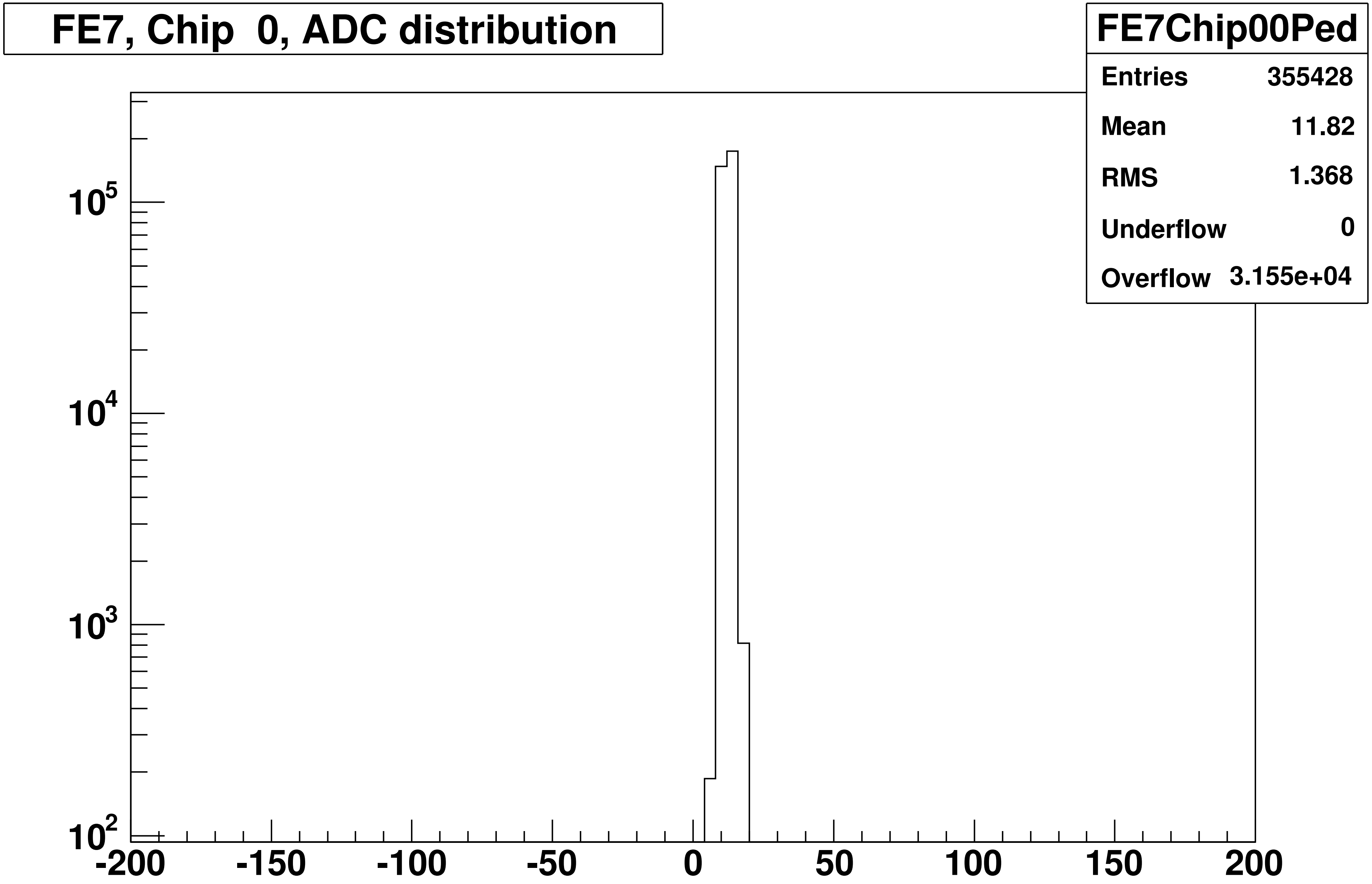
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 355428
Mean 1.906e+04
RMS 10.25
Underflow 3.203e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 355428
Mean 1671
RMS 5369
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 355428
Mean 11.82
RMS 1.368
Underflow 0
Overflow 3.155e+04
10^5
10^4
10^3
10^2
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-100
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0
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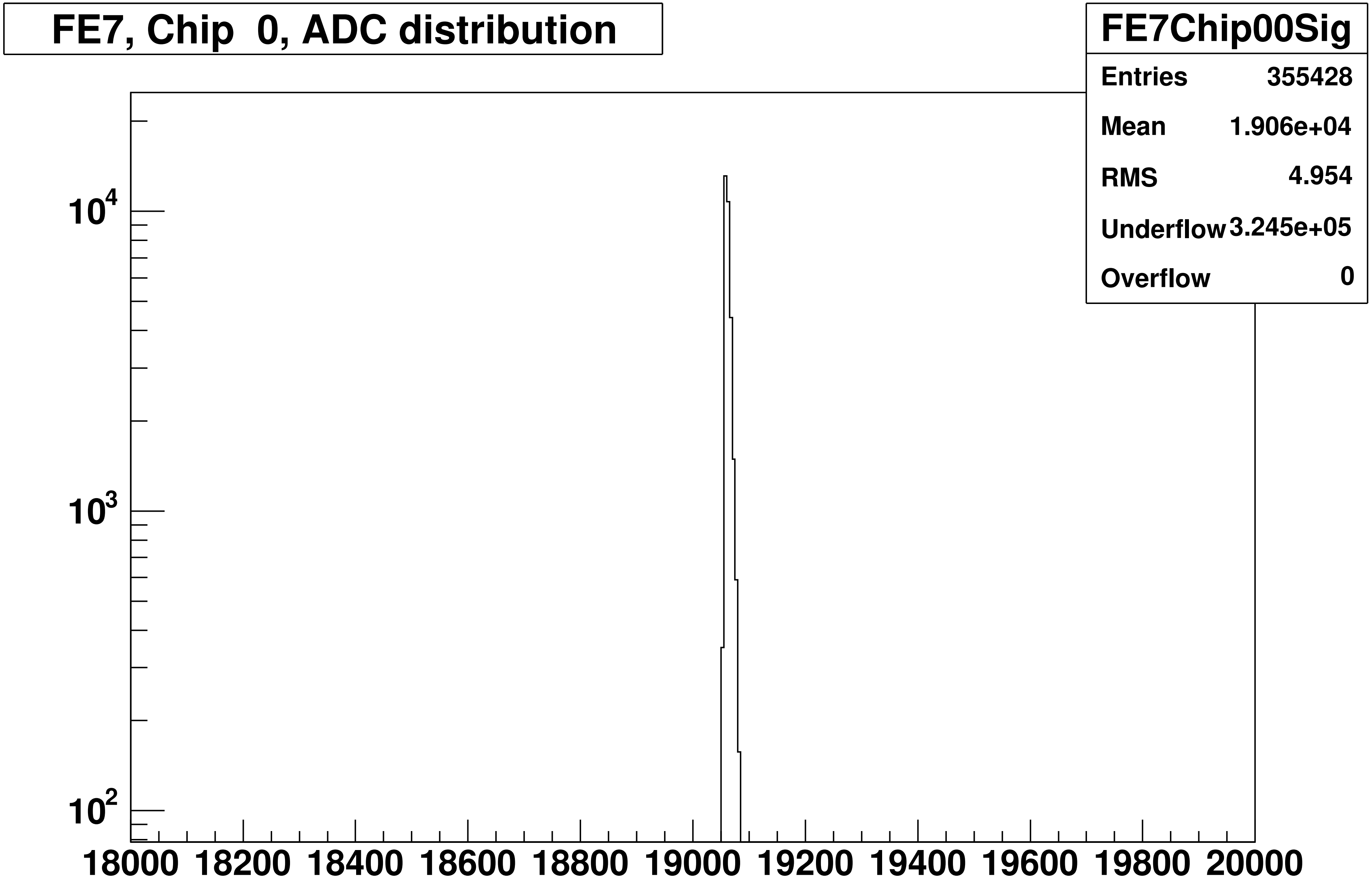
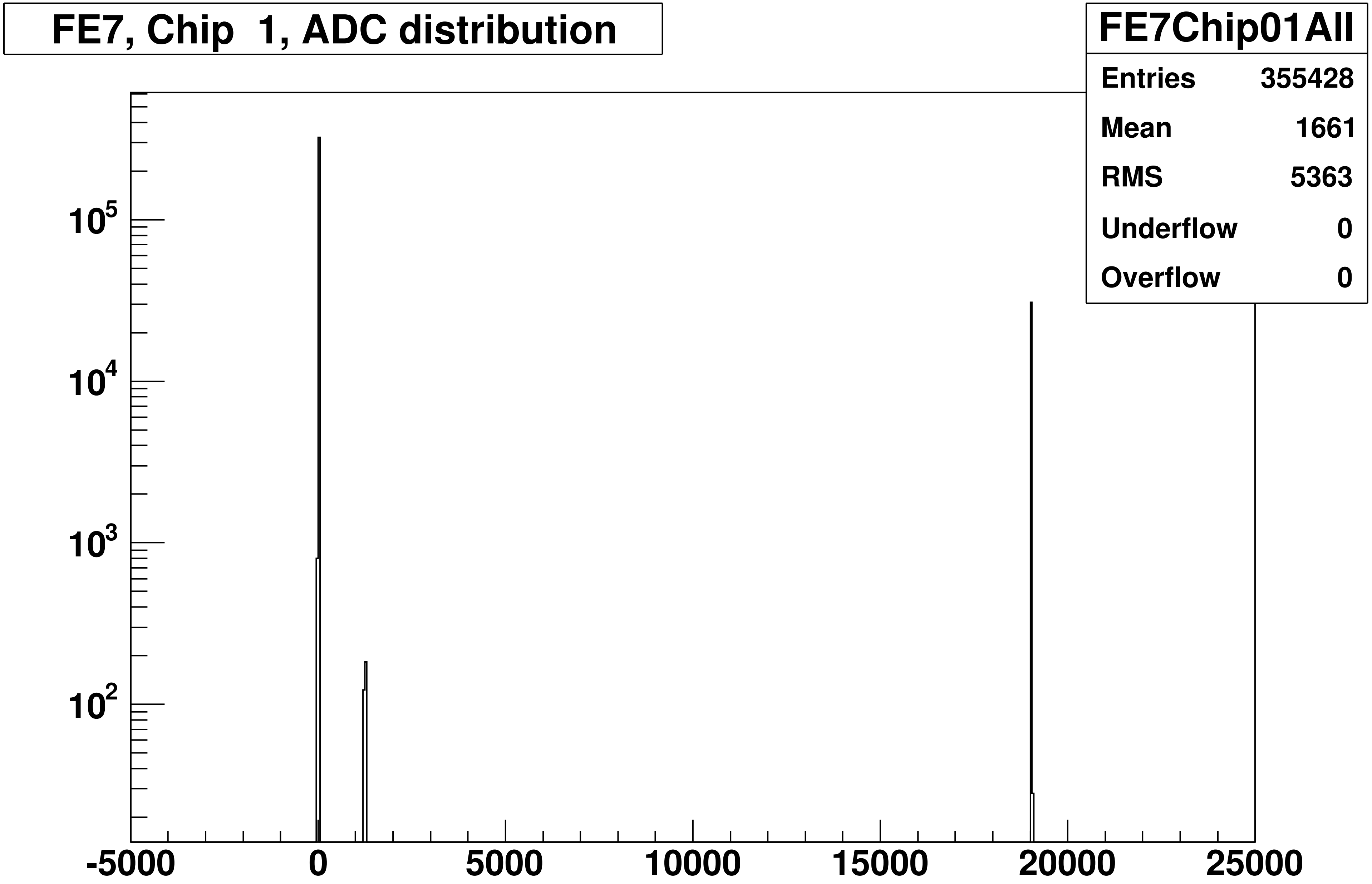
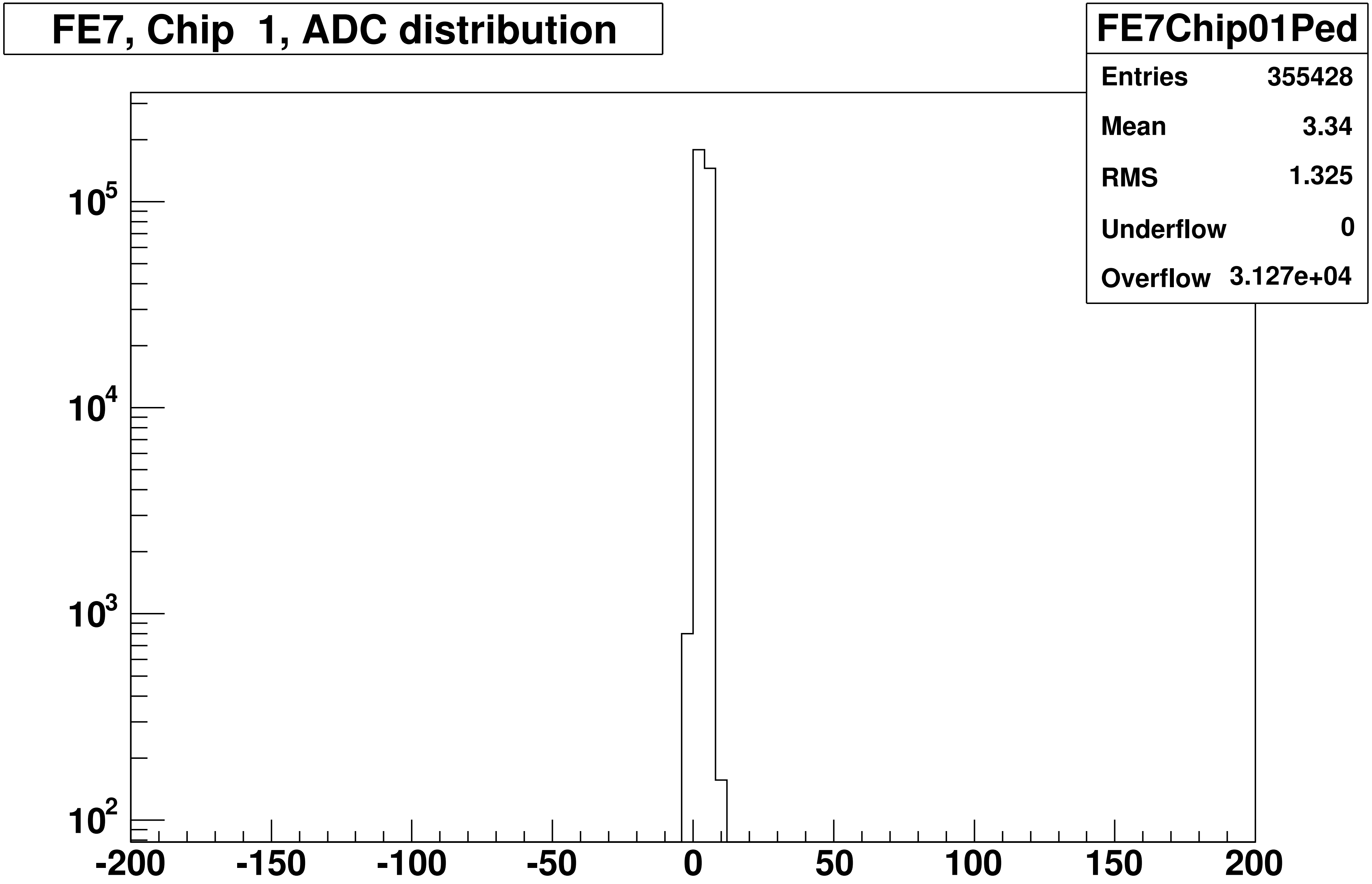
100
150
200



FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 355428
Mean 1.906e+04
RMS 4.954
Underflow 3.245e+05
Overflow 0
10^4
10^3
10^2
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 355428
Mean 1661
RMS 5363
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 355428
Mean 3.34
RMS 1.325
Underflow 0
Overflow 3.127e+04
10^5
10^4
10^3
10^2
-200
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-100
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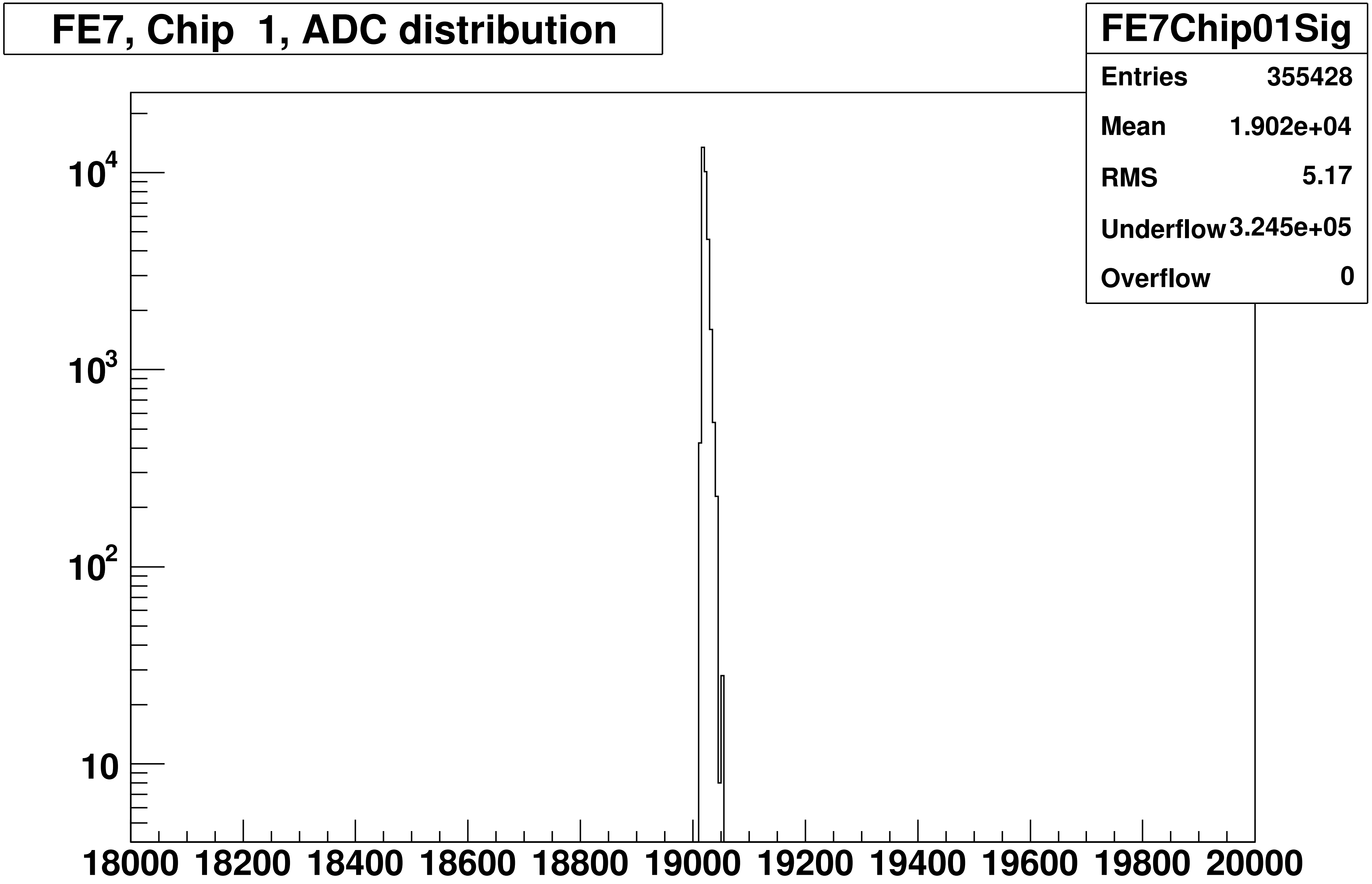
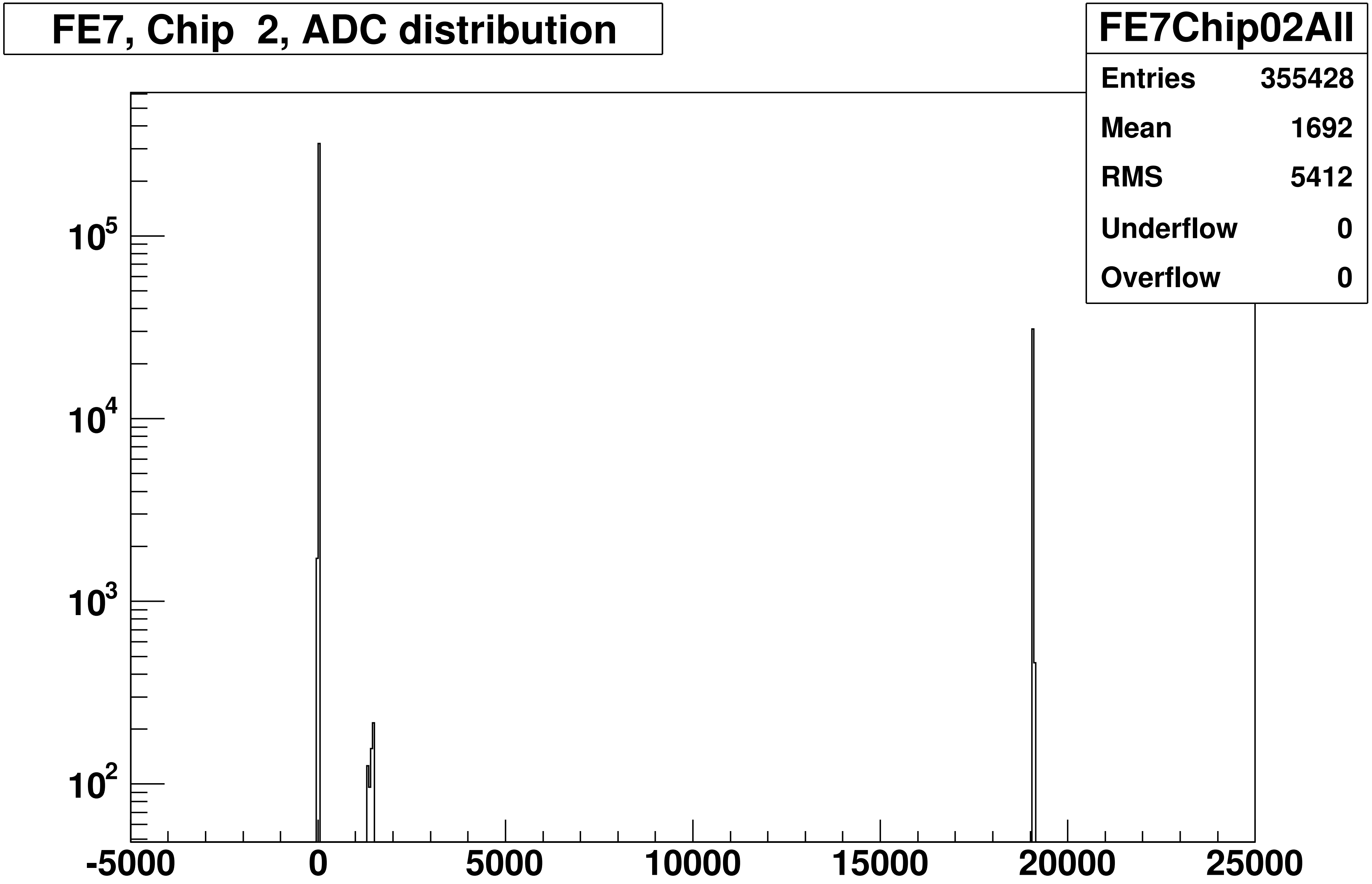
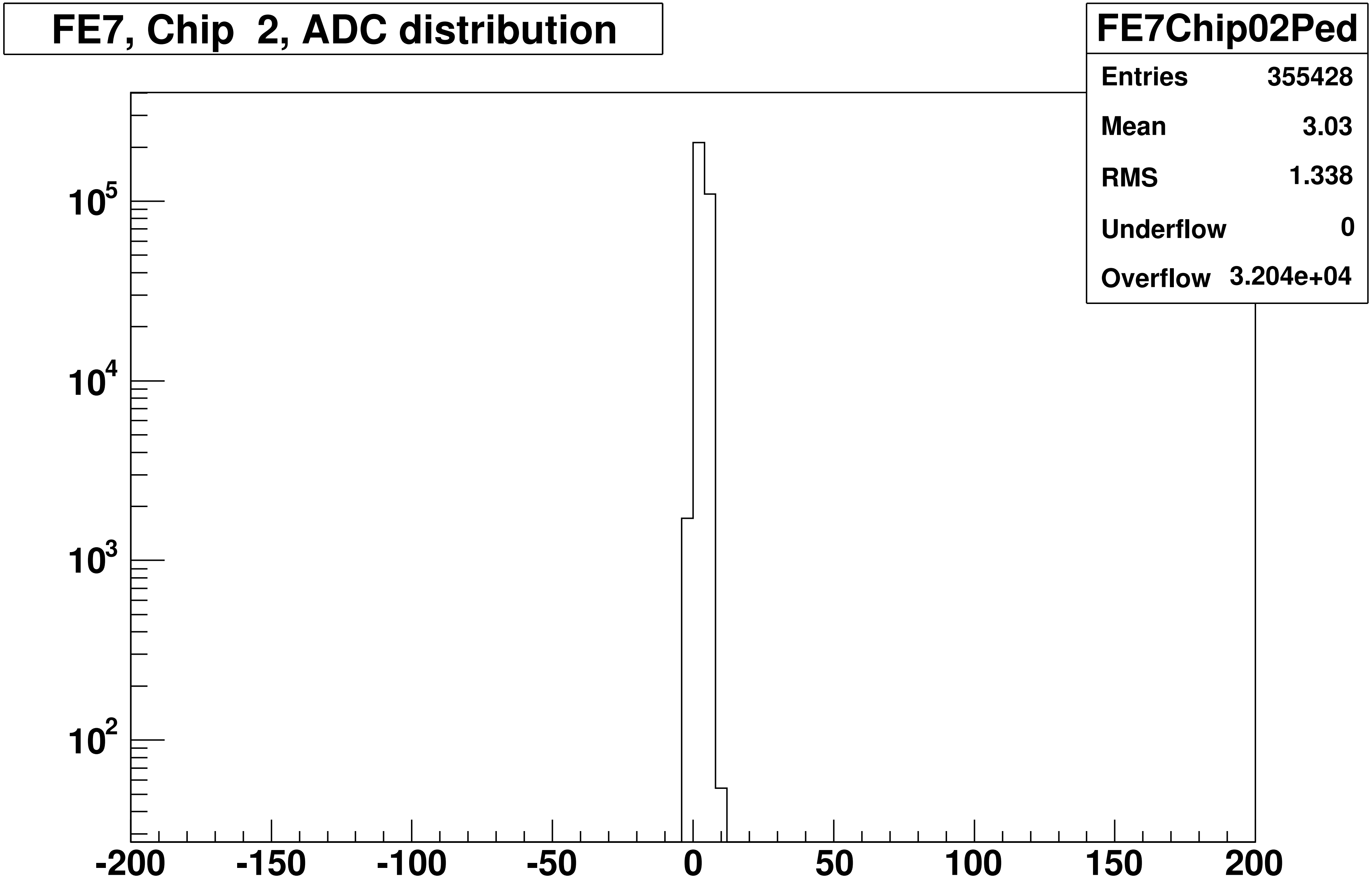
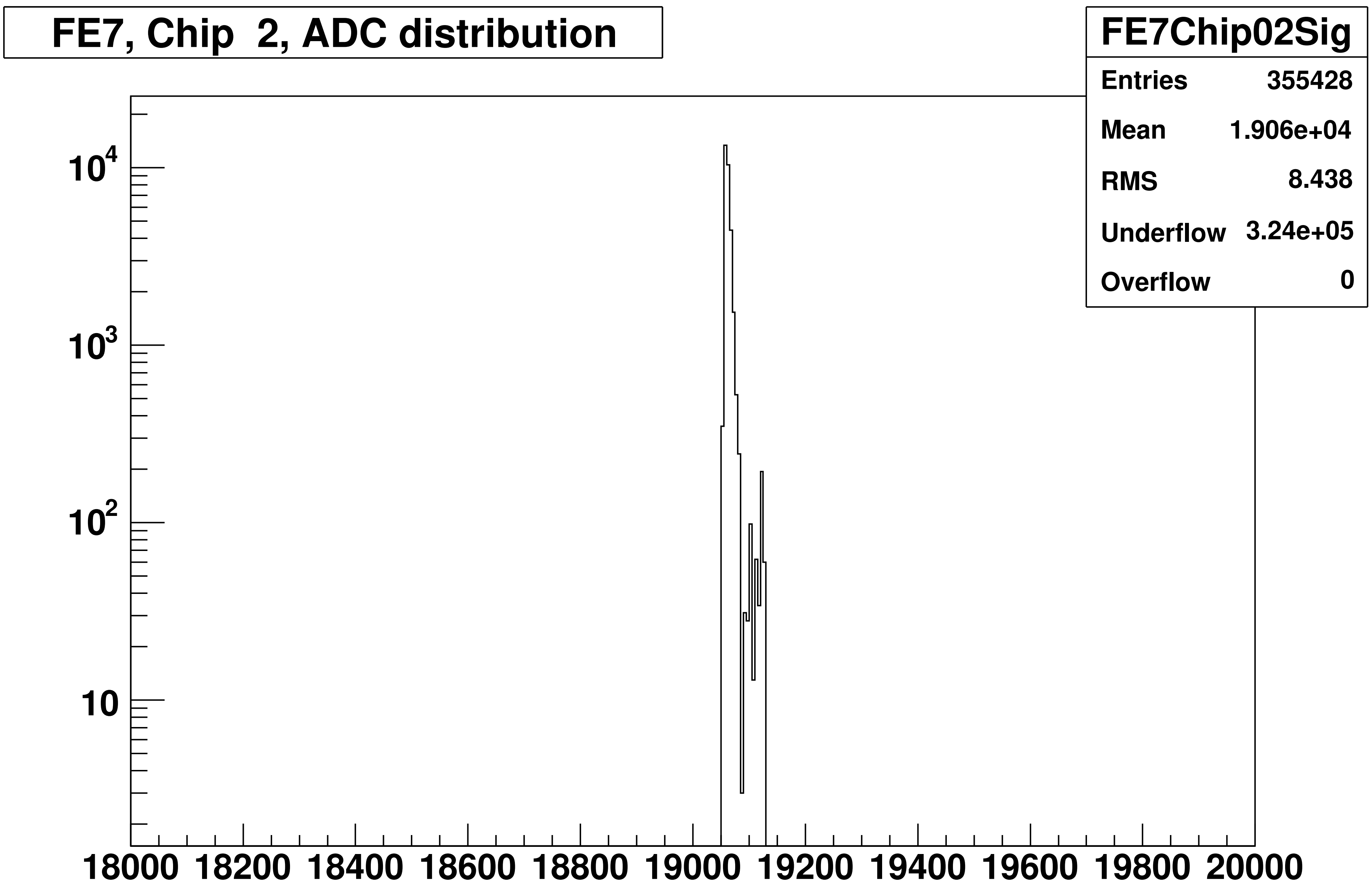
0
50
100
150
200

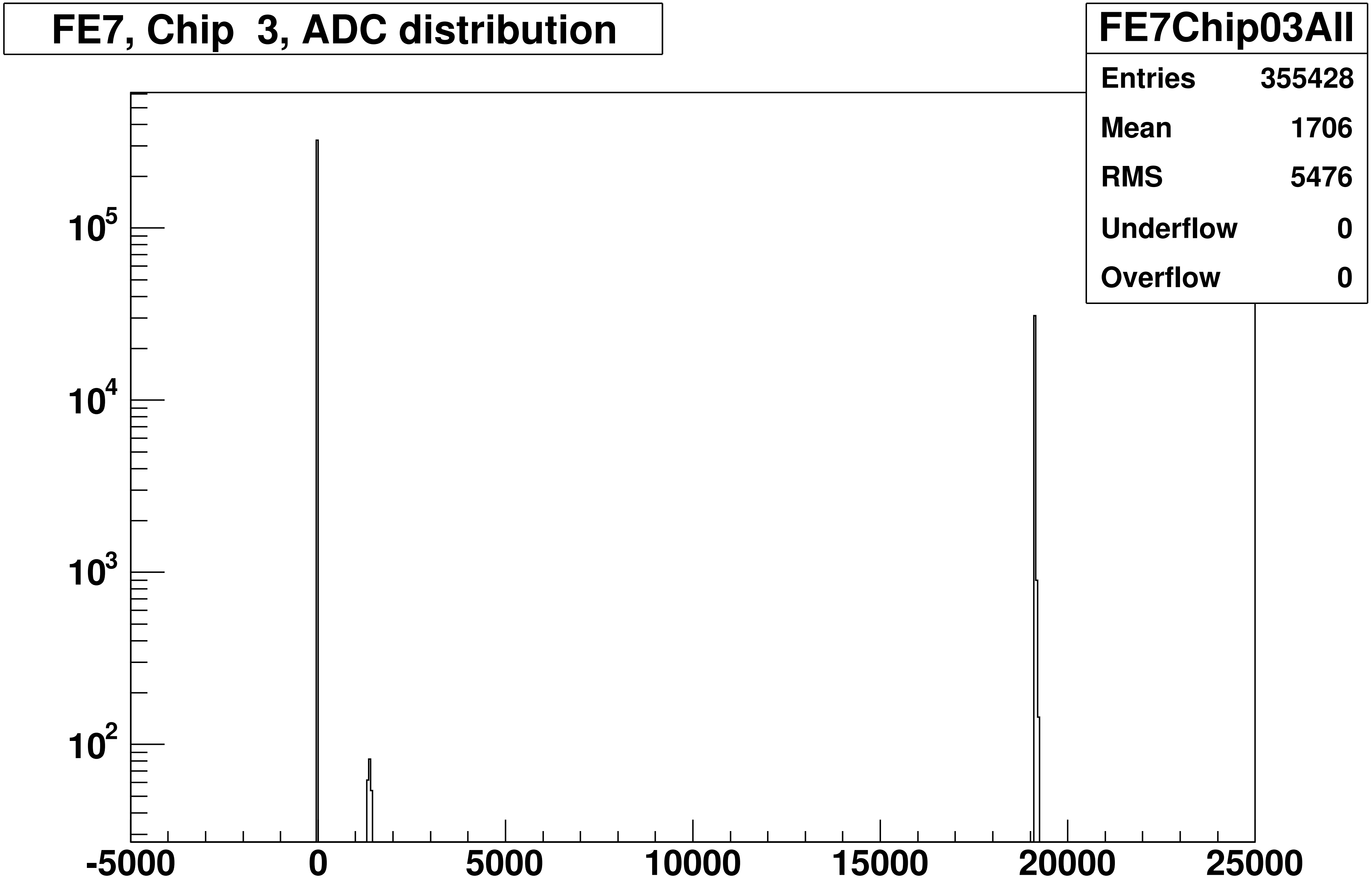

FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 355428
Mean 1.902e+04
RMS 5.17
Underflow 3.245e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



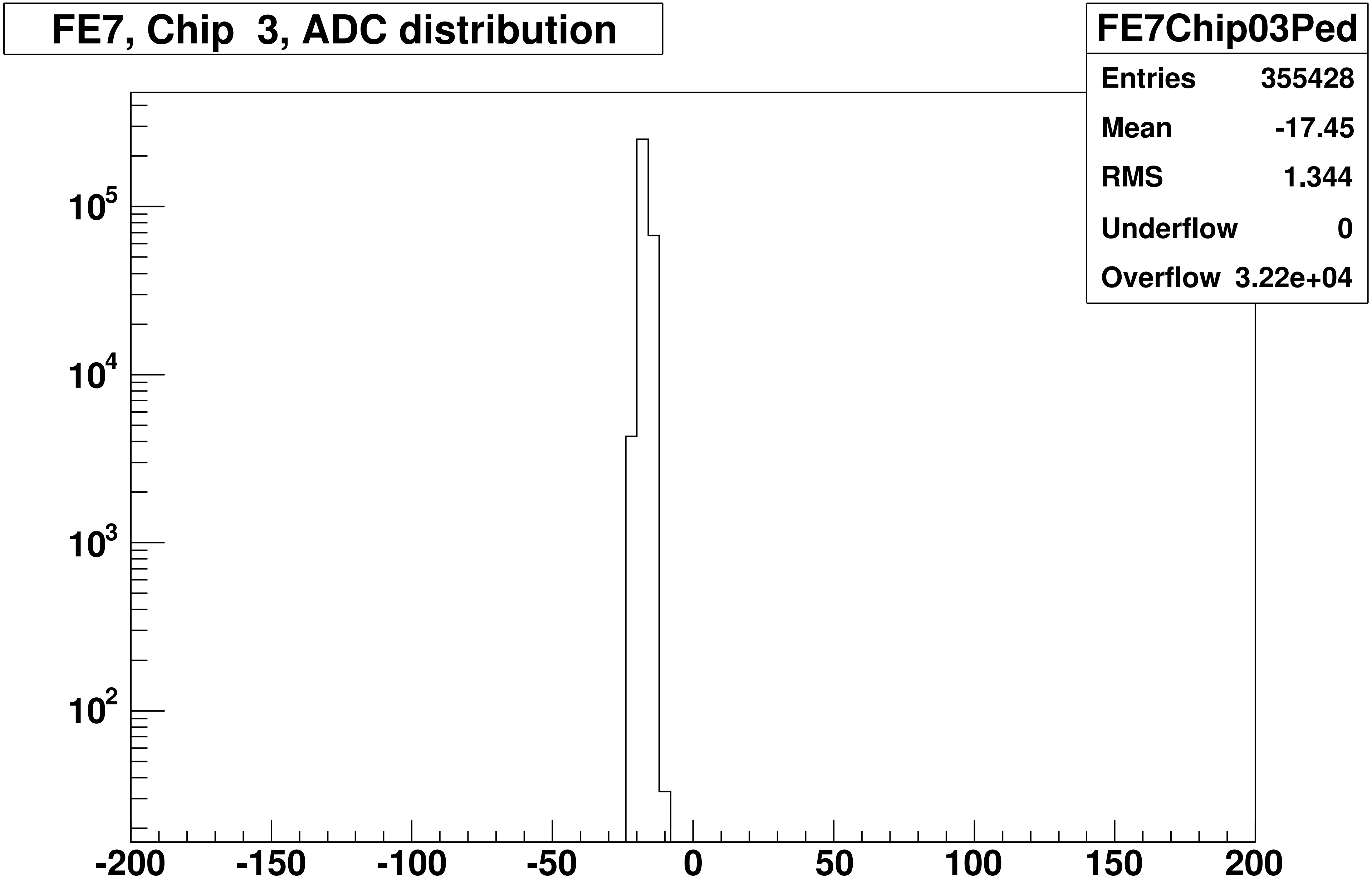
FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 355428
Mean 1692
RMS 5412
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 355428
Mean 3.03
RMS 1.338
Underflow 0
Overflow 3.204e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 355428
Mean 1.906e+04
RMS 8.438
Underflow 3.24e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 355428
Mean 1706
RMS 5476
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 355428
Mean -17.45
RMS 1.344
Underflow 0
Overflow 3.22e+04
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10^4
10^3
10^2
-200
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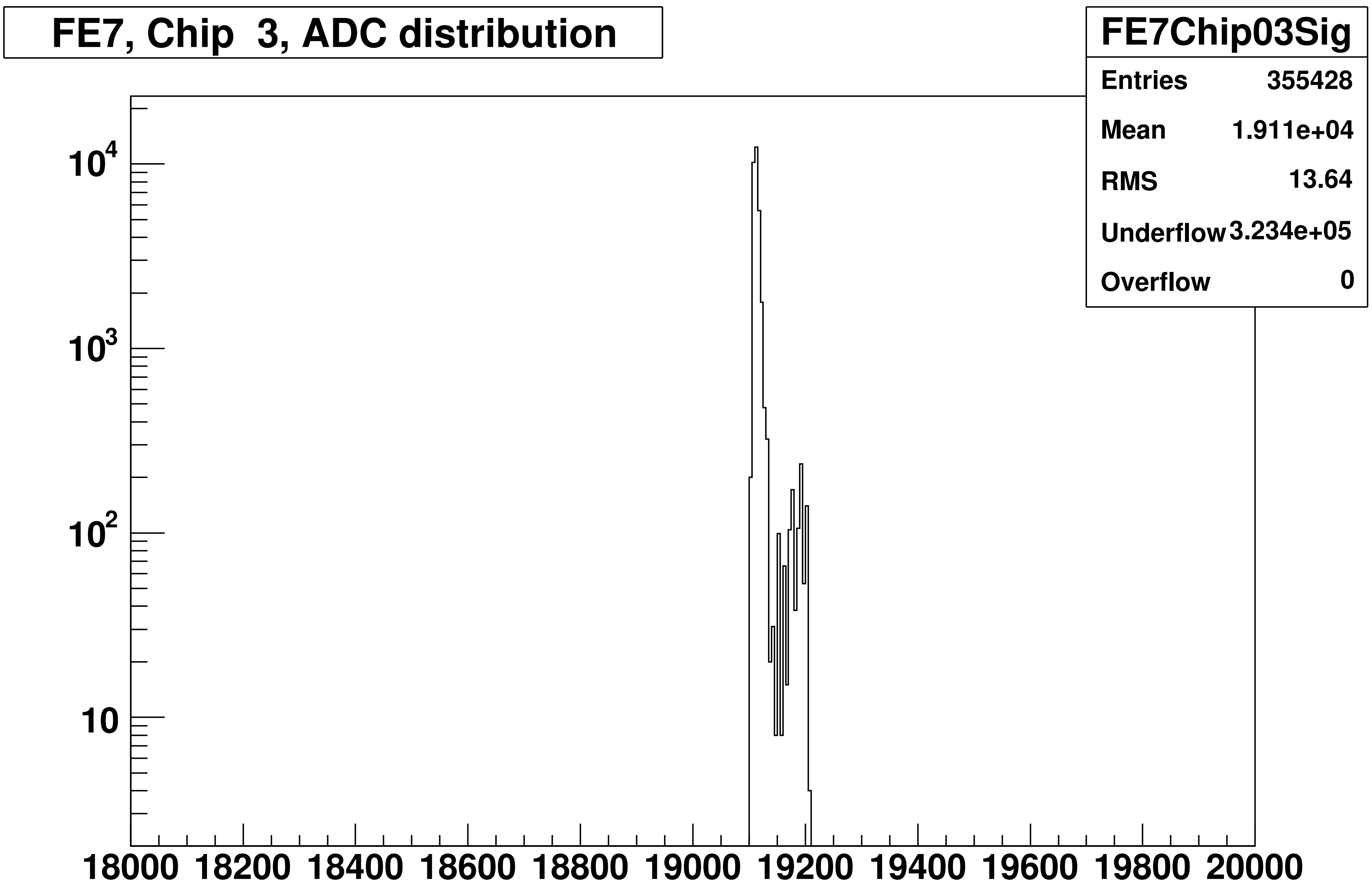
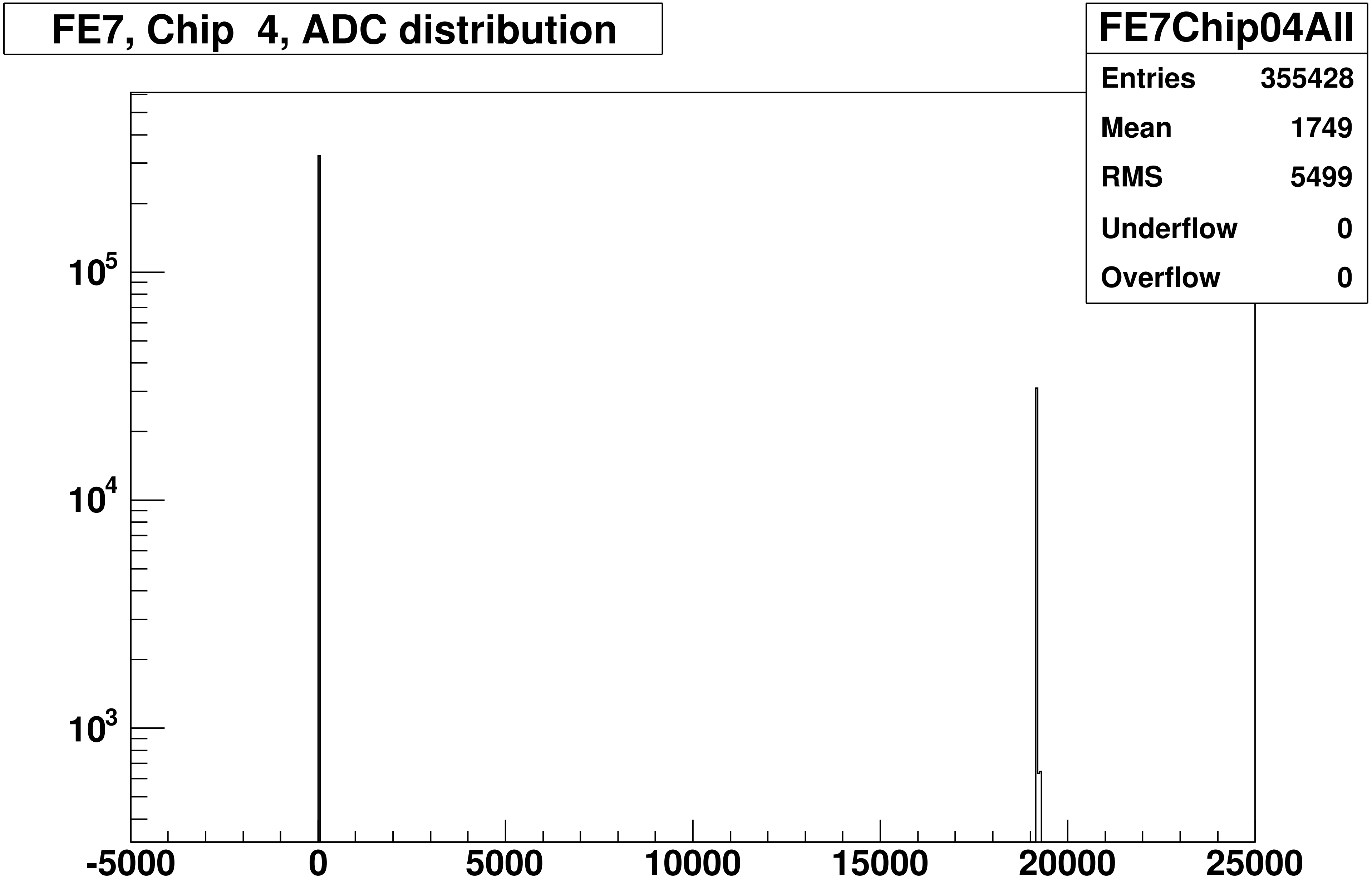
-100
-50
0
50
100
150
200



FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 355428
Mean 1.911e+04
RMS 13.64
Underflow 3.234e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 355428
Mean 1749
RMS 5499
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
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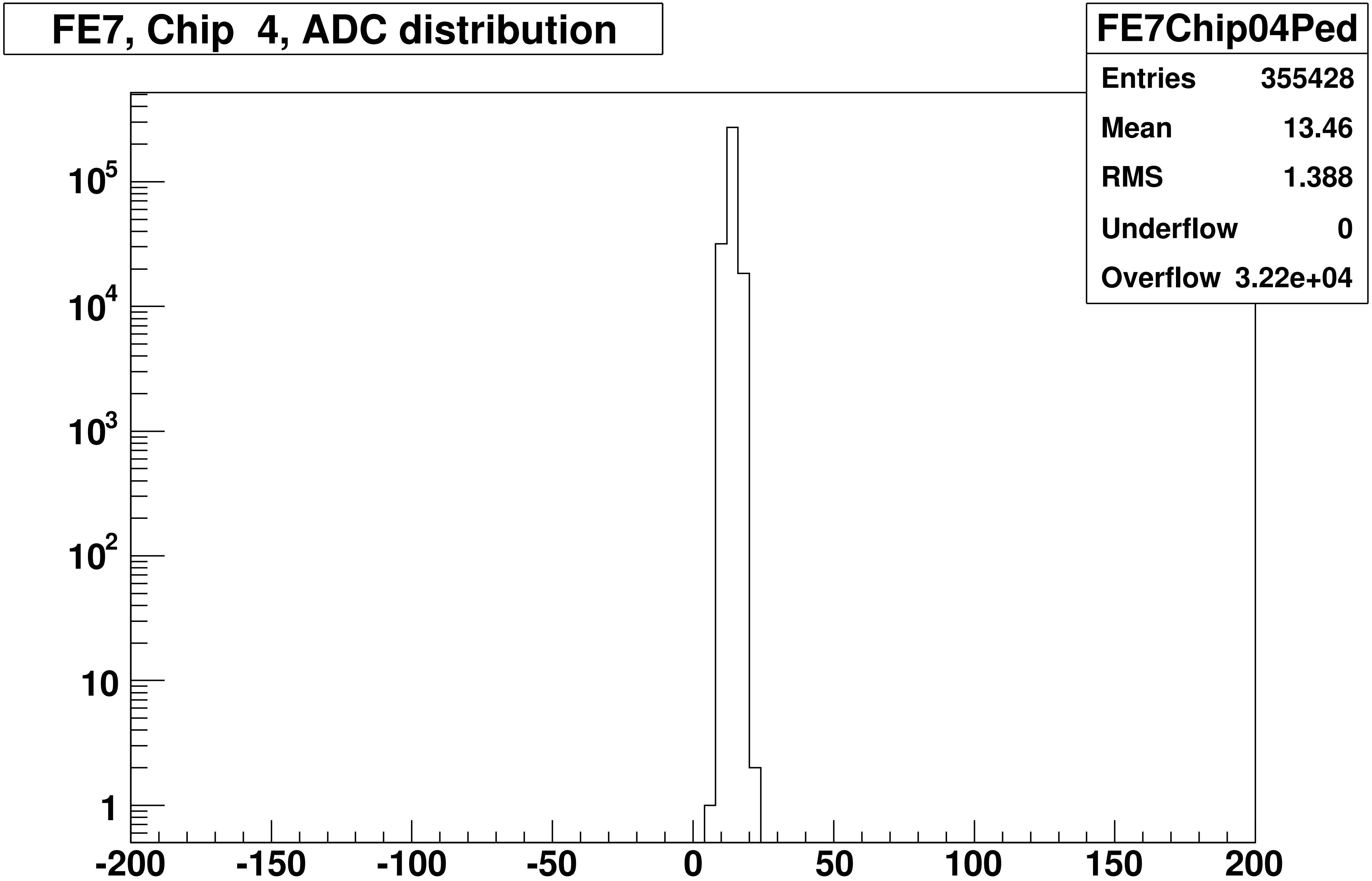
10000
15000
20000
25000


FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 355428
Mean 13.46
RMS 1.388
Underflow 0
Overflow 3.22e+04
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10^4
10^3
10^2
10
1
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-150
-100
-50
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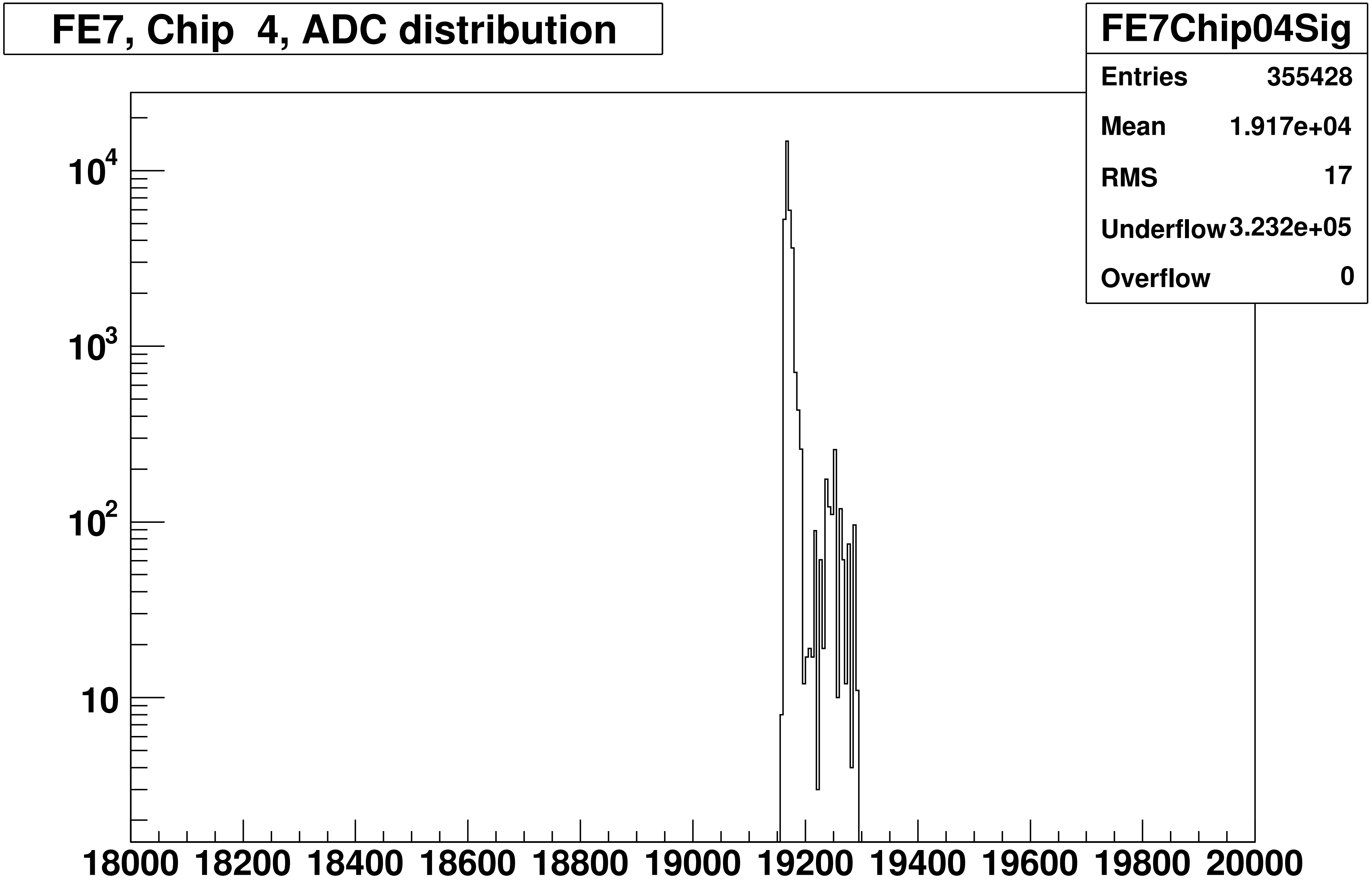
100
150
200



FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 355428
Mean 1.917e+04
RMS 17
Underflow 3.232e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
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19000
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19600
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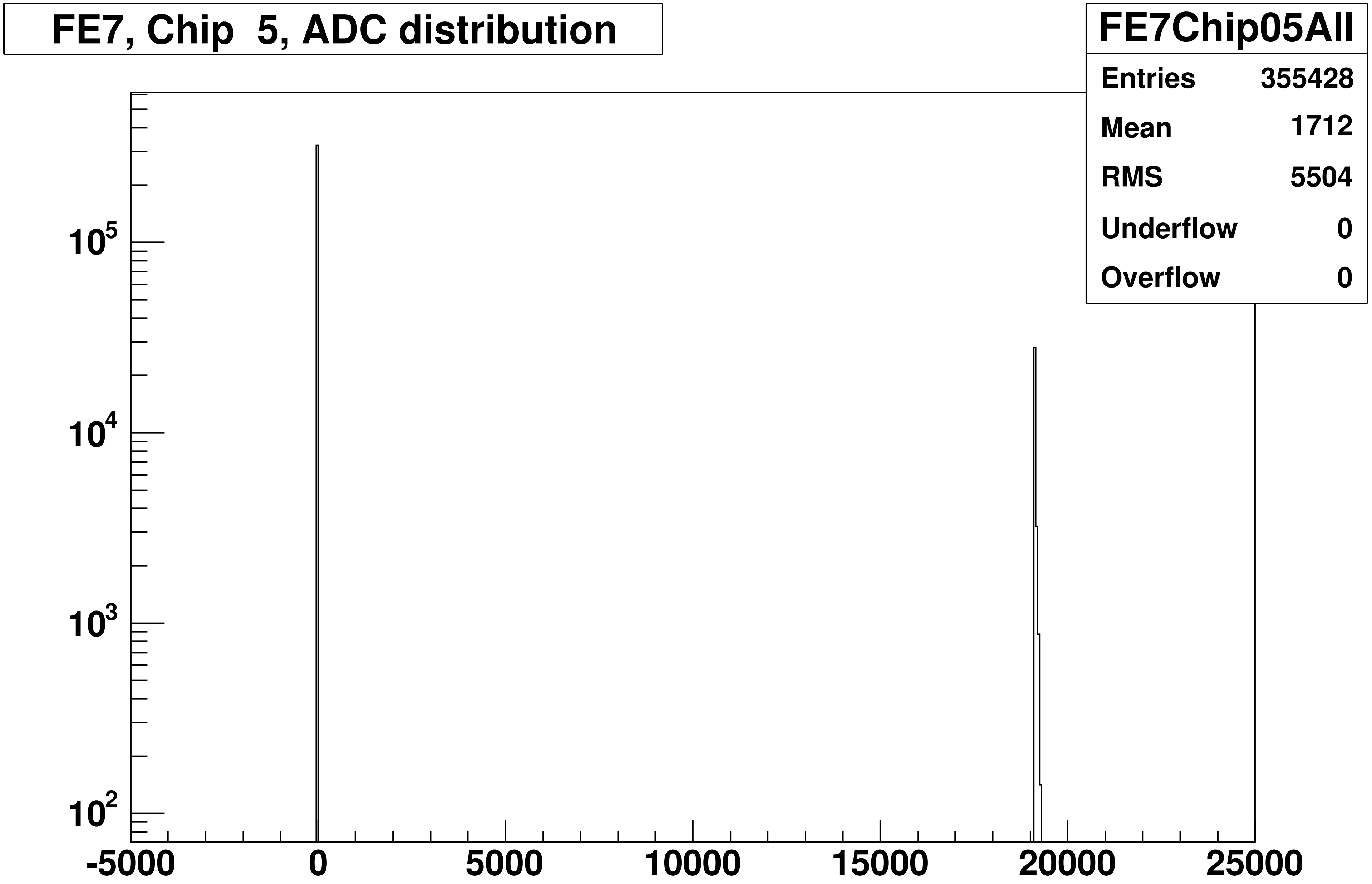
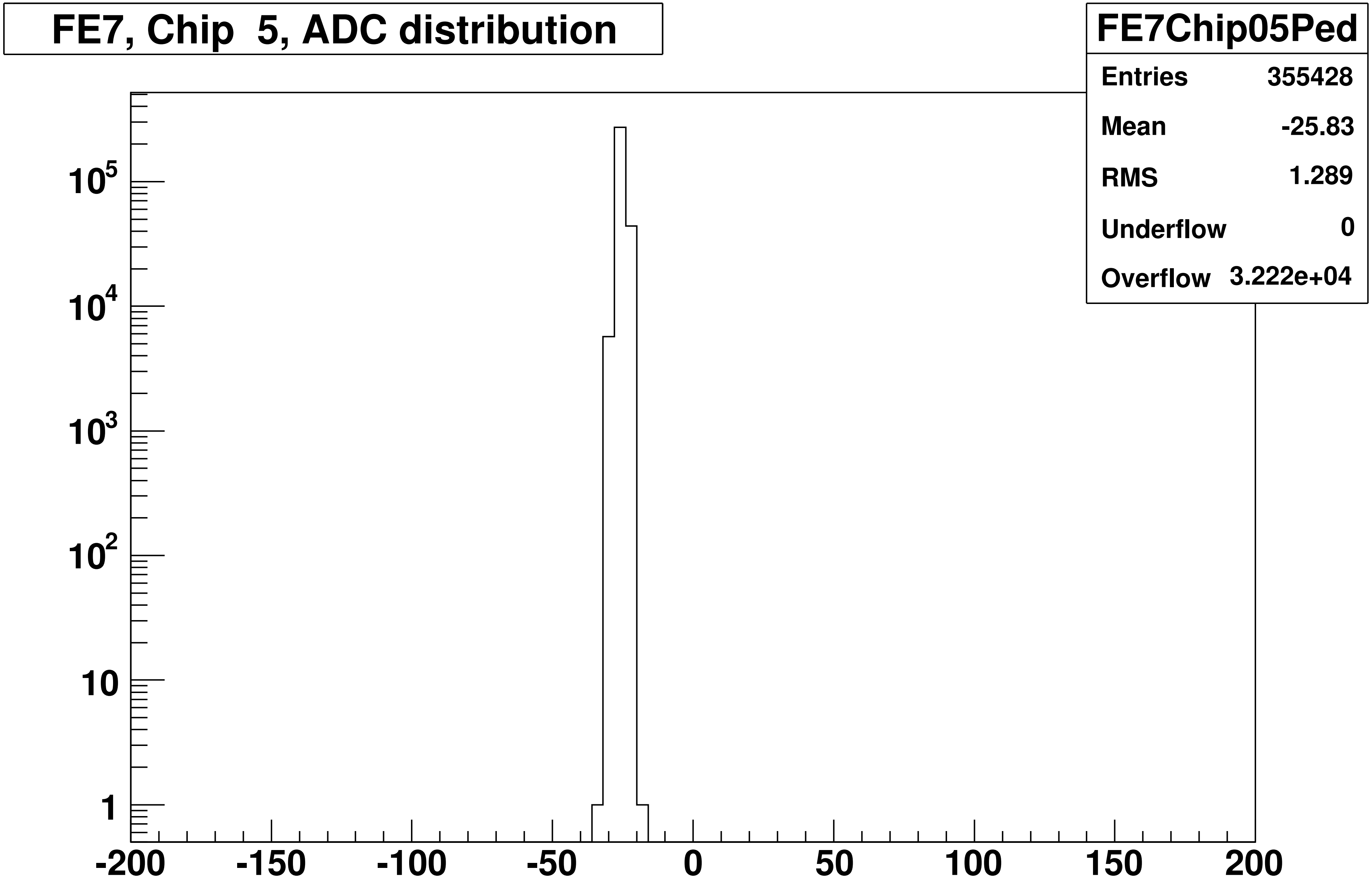
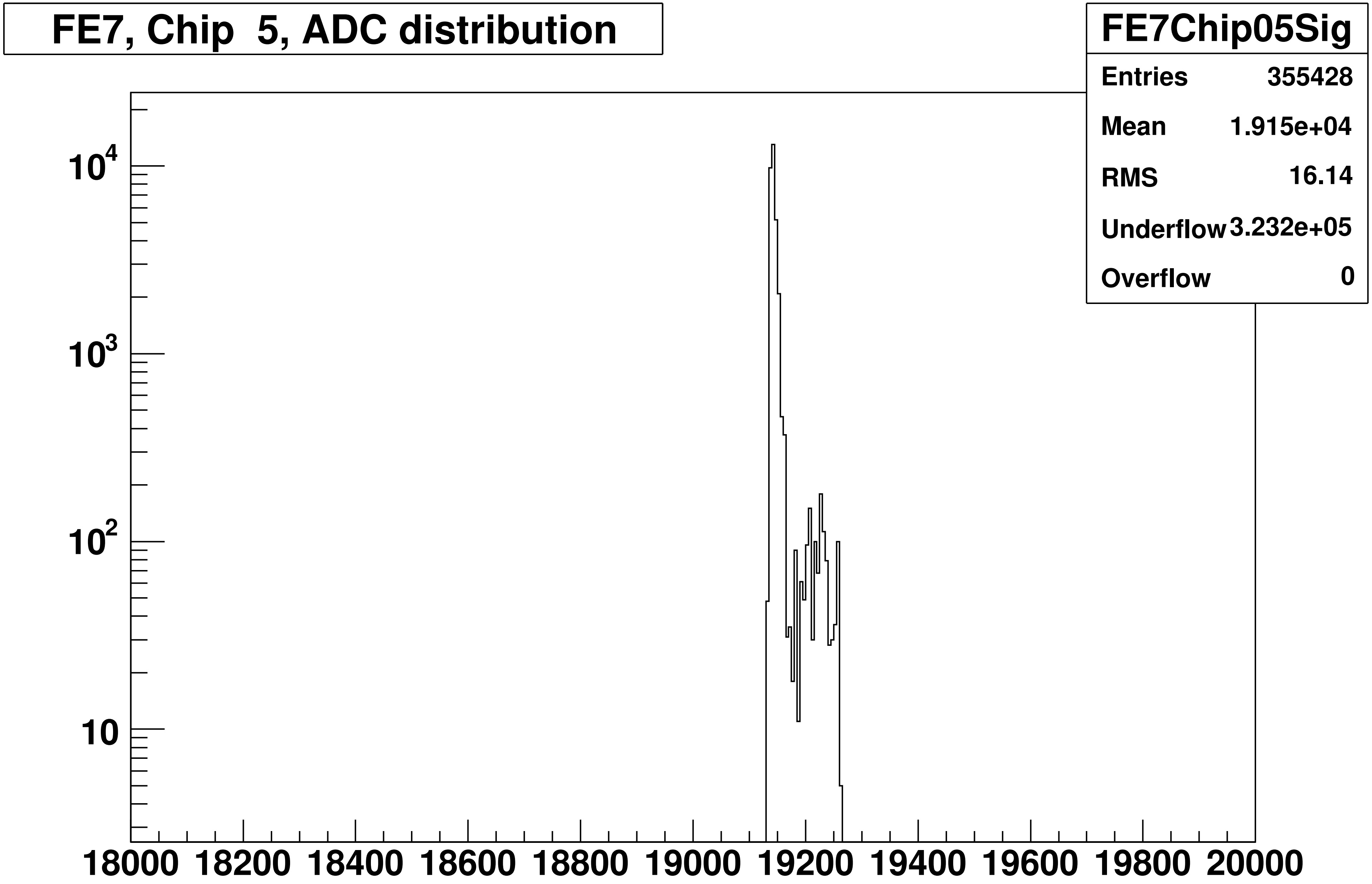
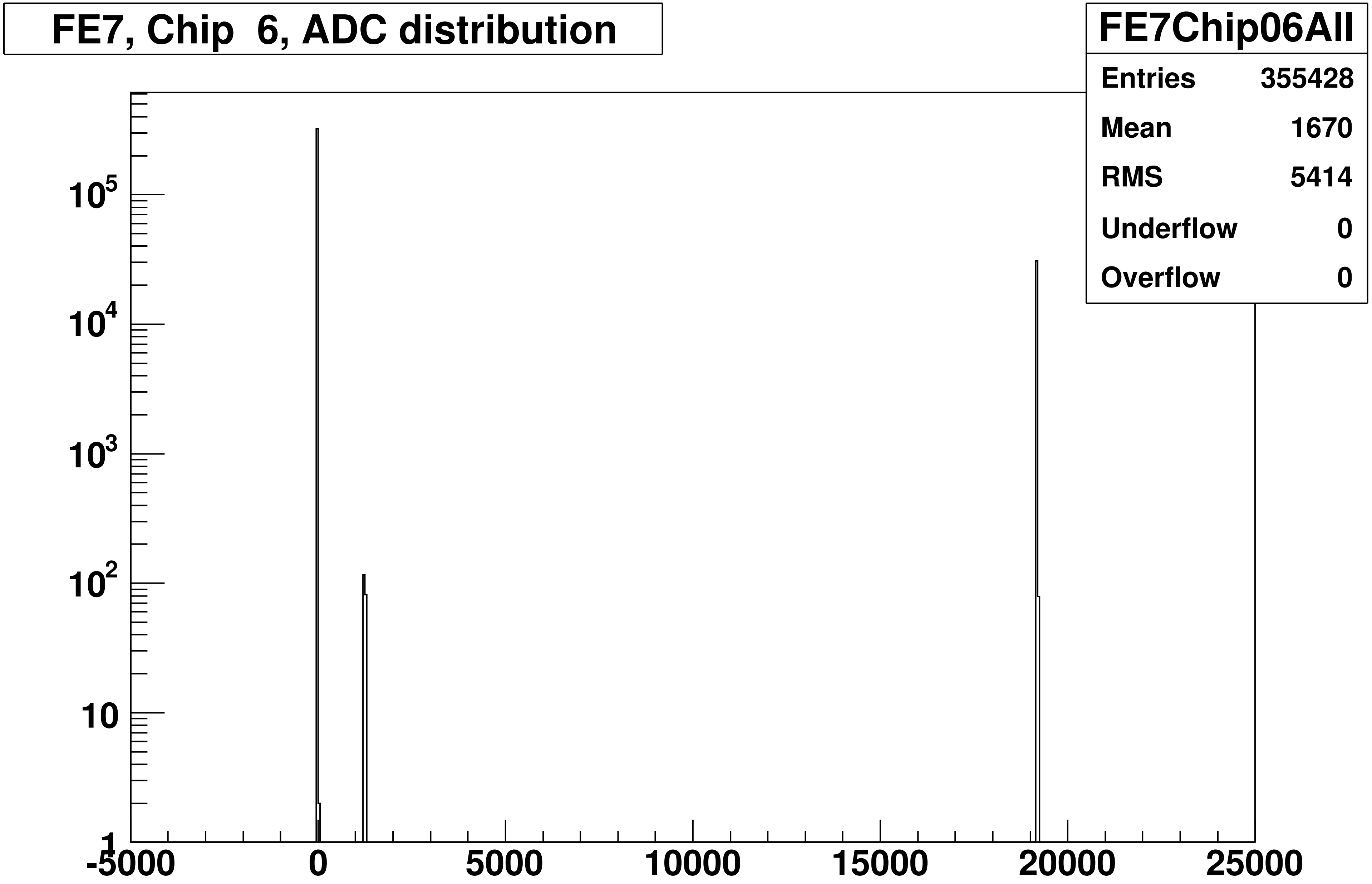

FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 355428
Mean 1712
RMS 5504
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 355428
Mean -25.83
RMS 1.289
Underflow 0
Overflow 3.222e+04
1
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 355428
Mean 1.915e+04
RMS 16.14
Underflow 3.232e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 355428
Mean 1670
RMS 5414
Underflow 0
Overflow 0
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10^4
10^3
10^2
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1
-5000
0
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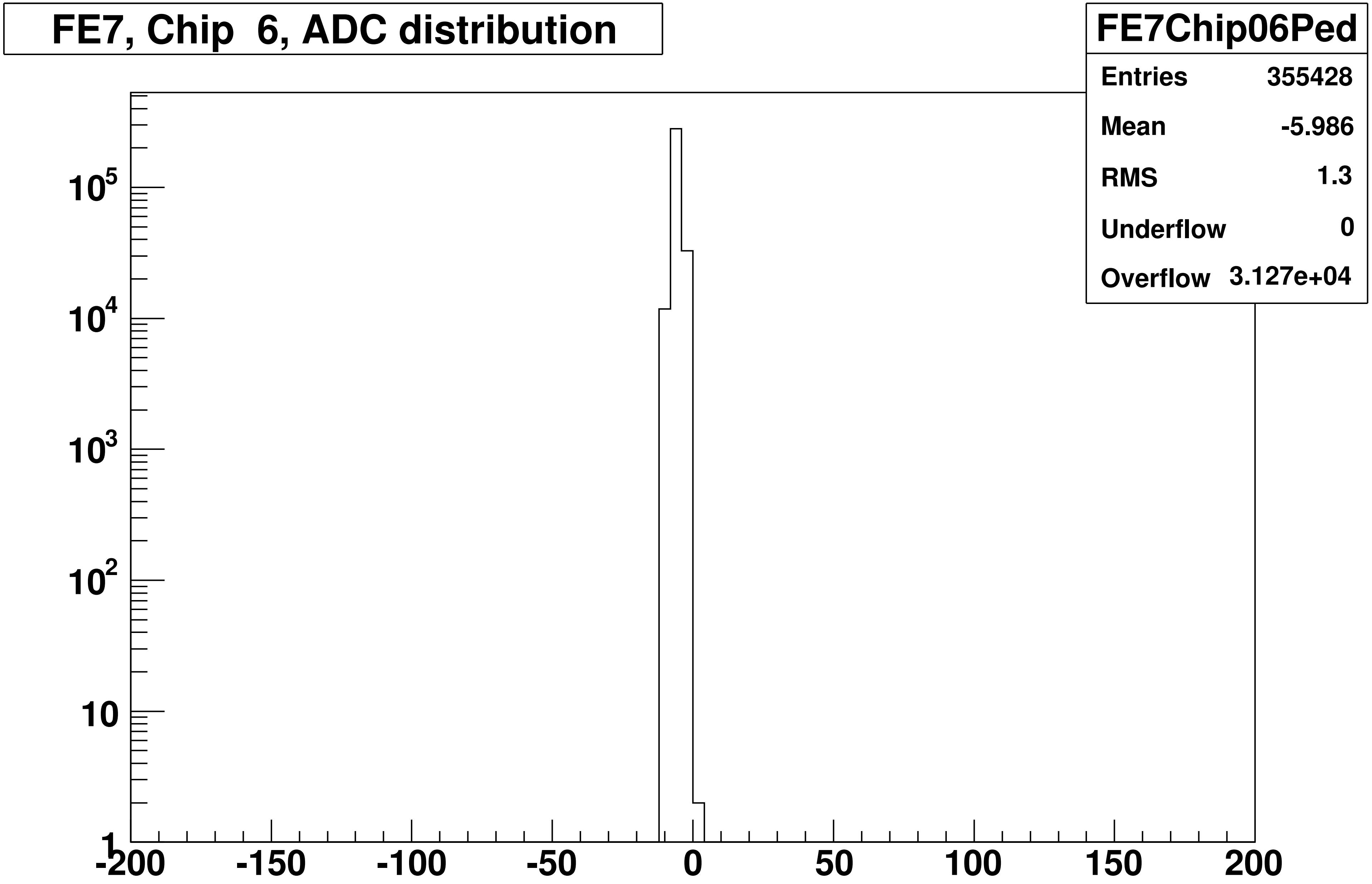
10000
15000
20000
25000



FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 355428
Mean -5.986
RMS 1.3
Underflow 0
Overflow 3.127e+04
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10^4
10^3
10^2
10
1
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-100
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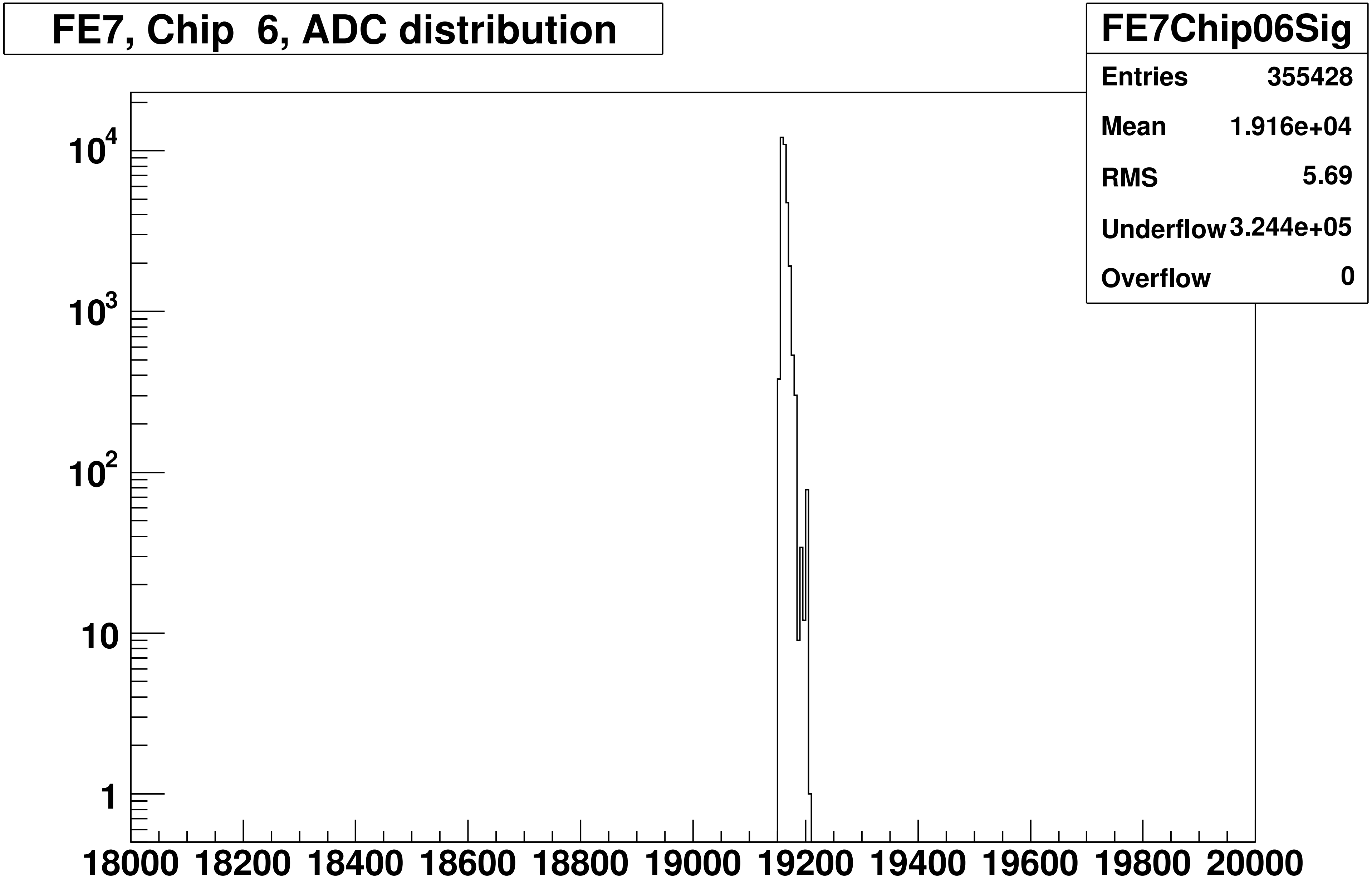
50
100
150
200


FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 355428
Mean 1.916e+04
RMS 5.69
Underflow 3.244e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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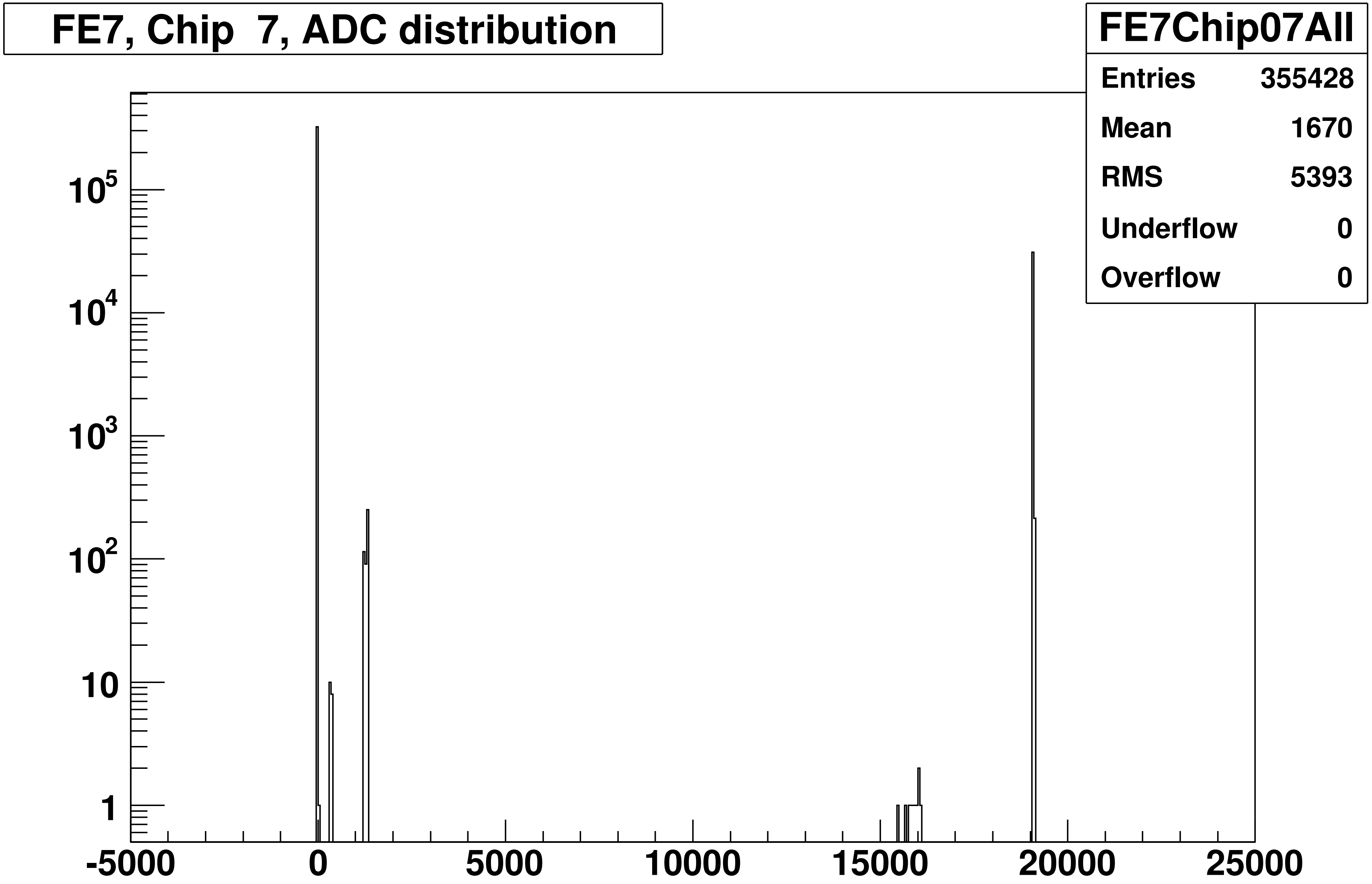
19600
19800
20000


FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 355428
Mean 1670
RMS 5393
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
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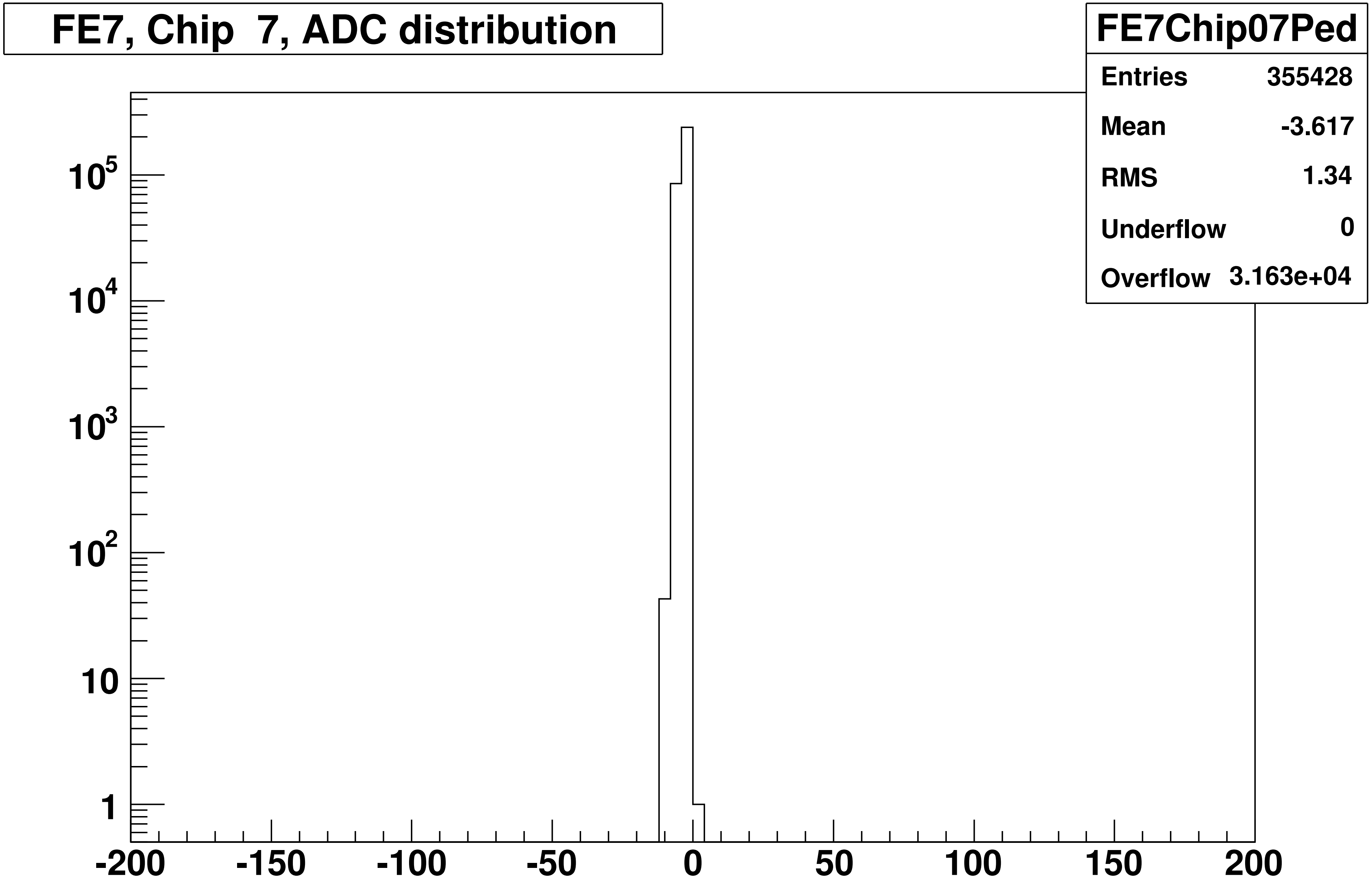
10000
15000
20000
25000


FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 355428
Mean -3.617
RMS 1.34
Underflow 0
Overflow 3.163e+04
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10^3
10^2
10
1
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-100
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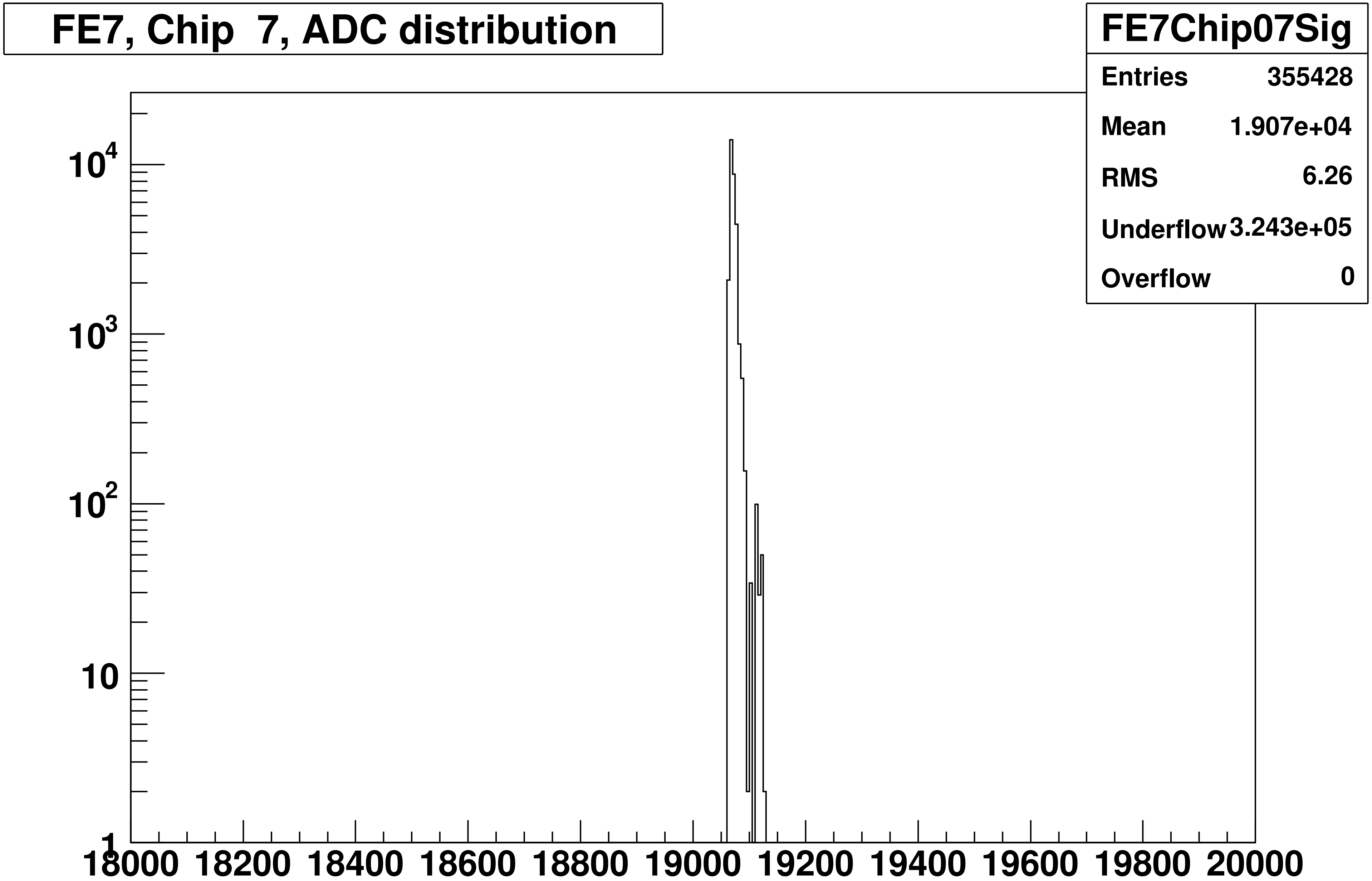
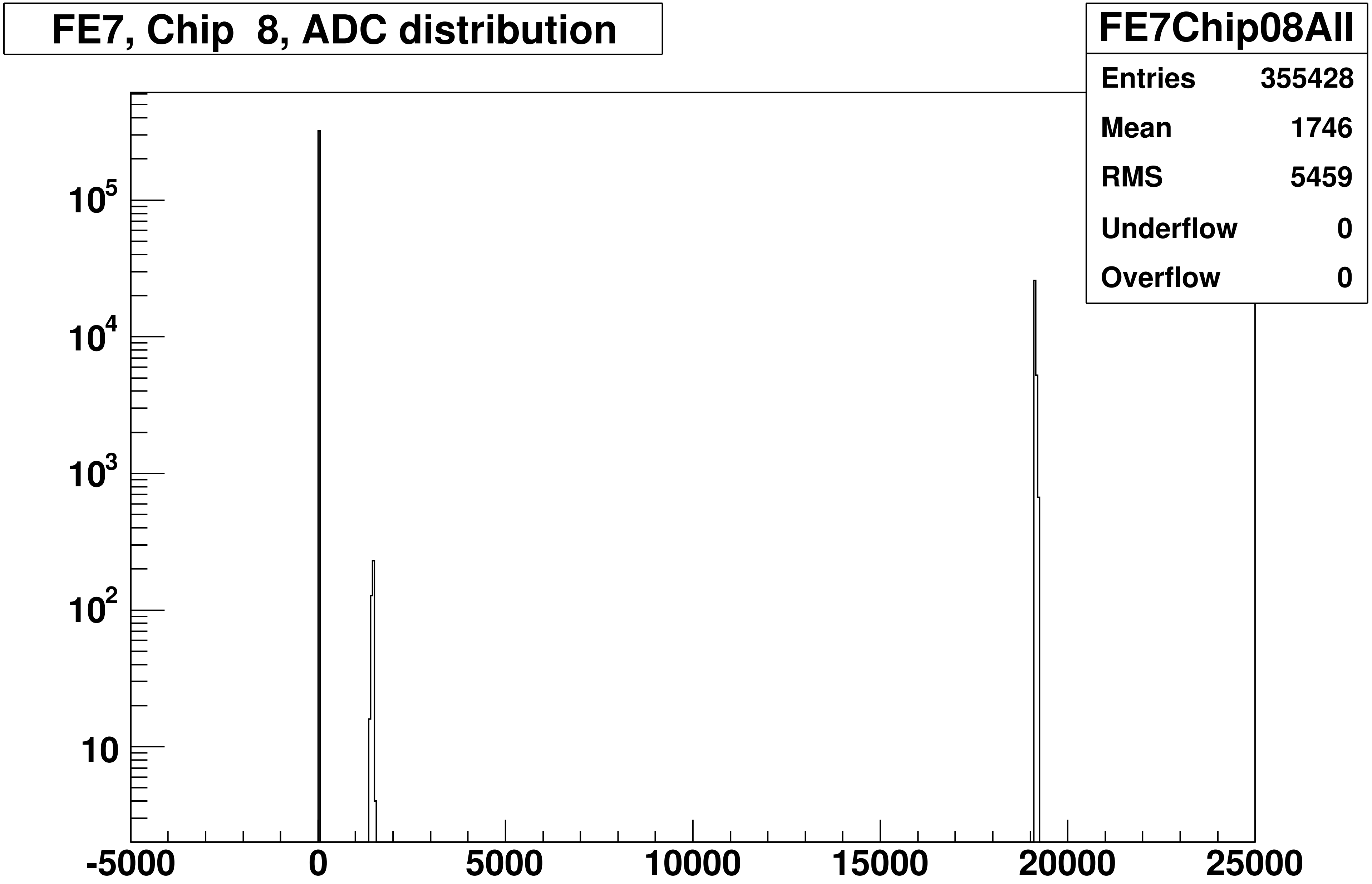
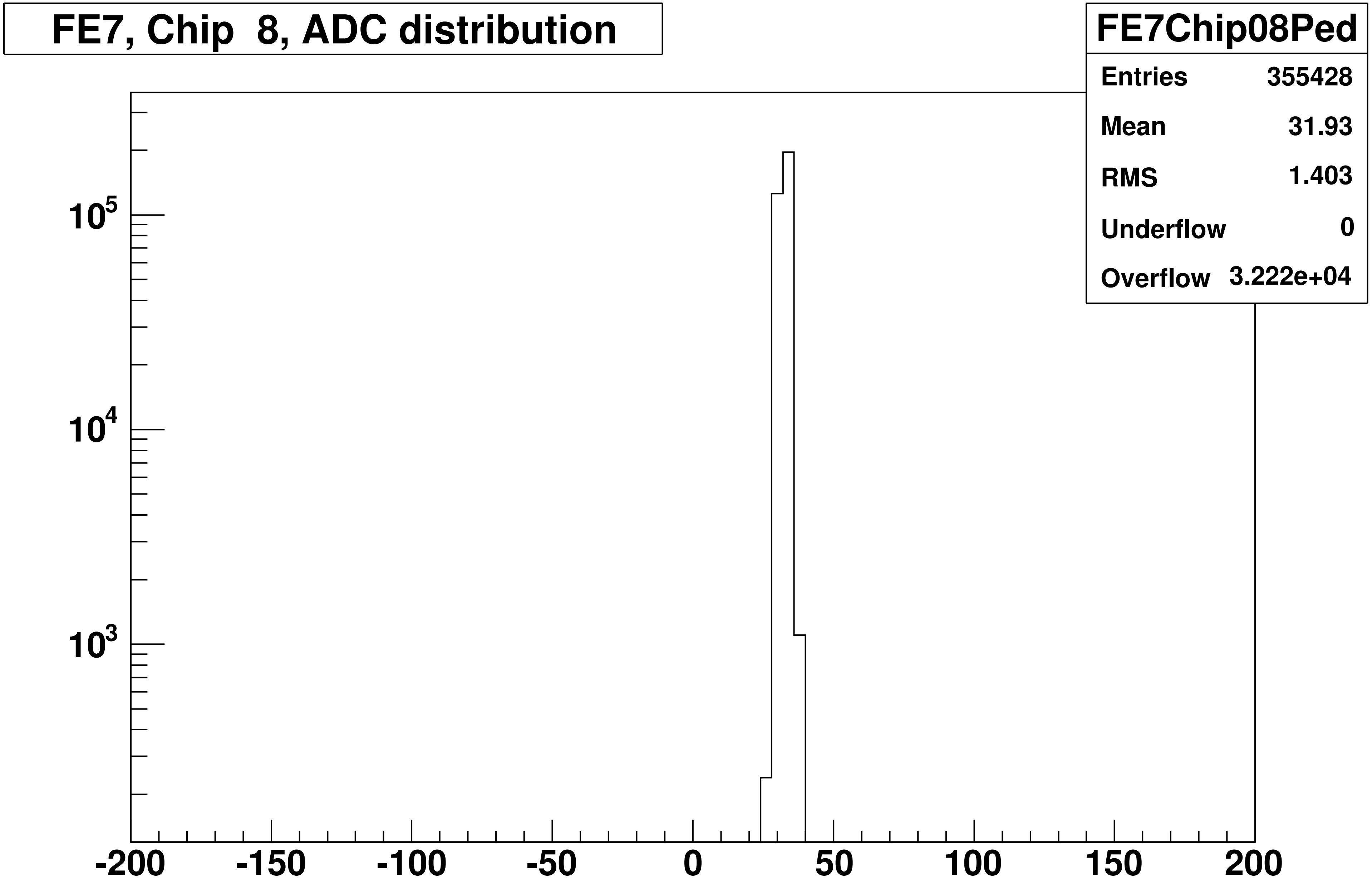
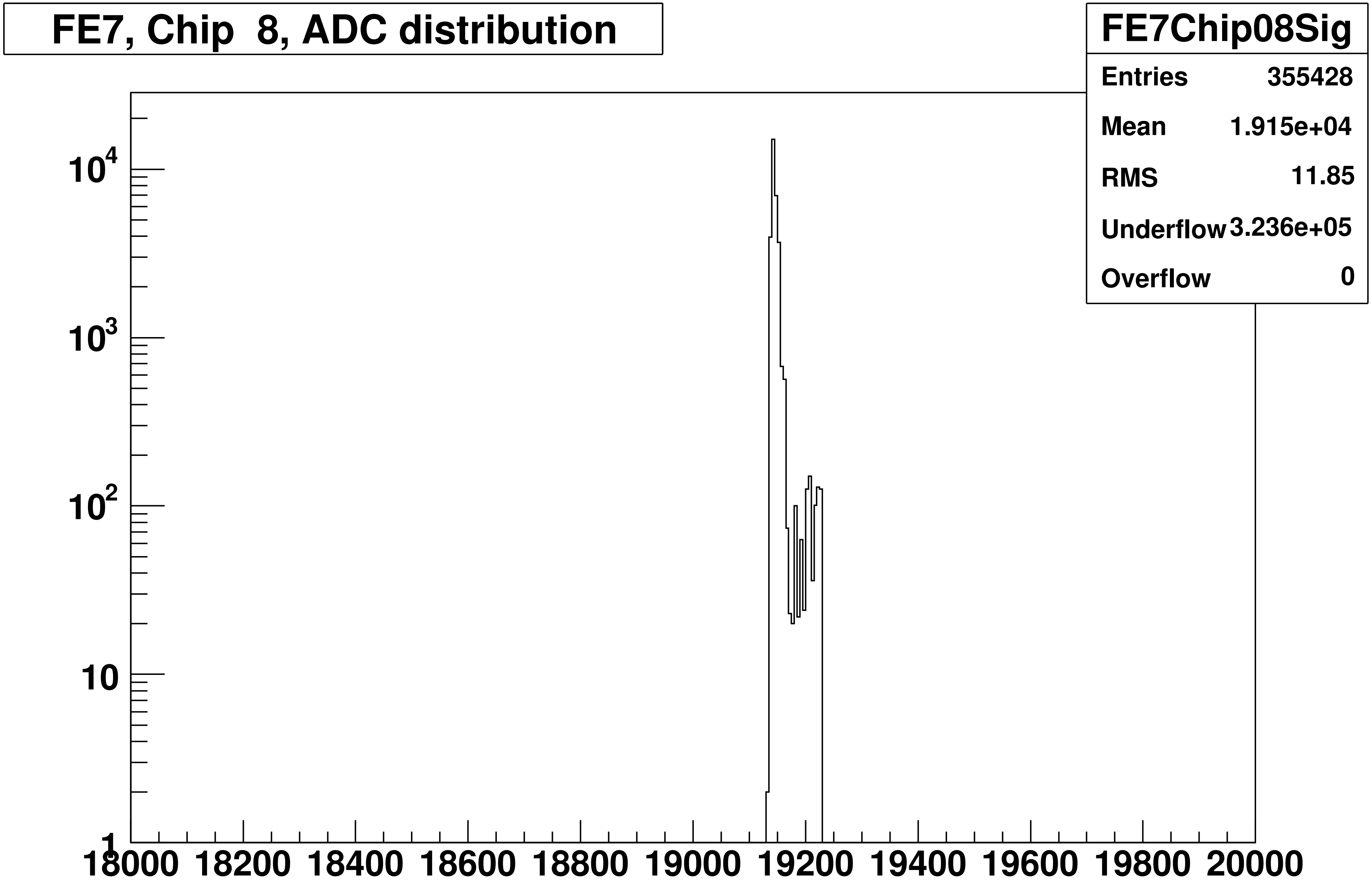
100
150
200



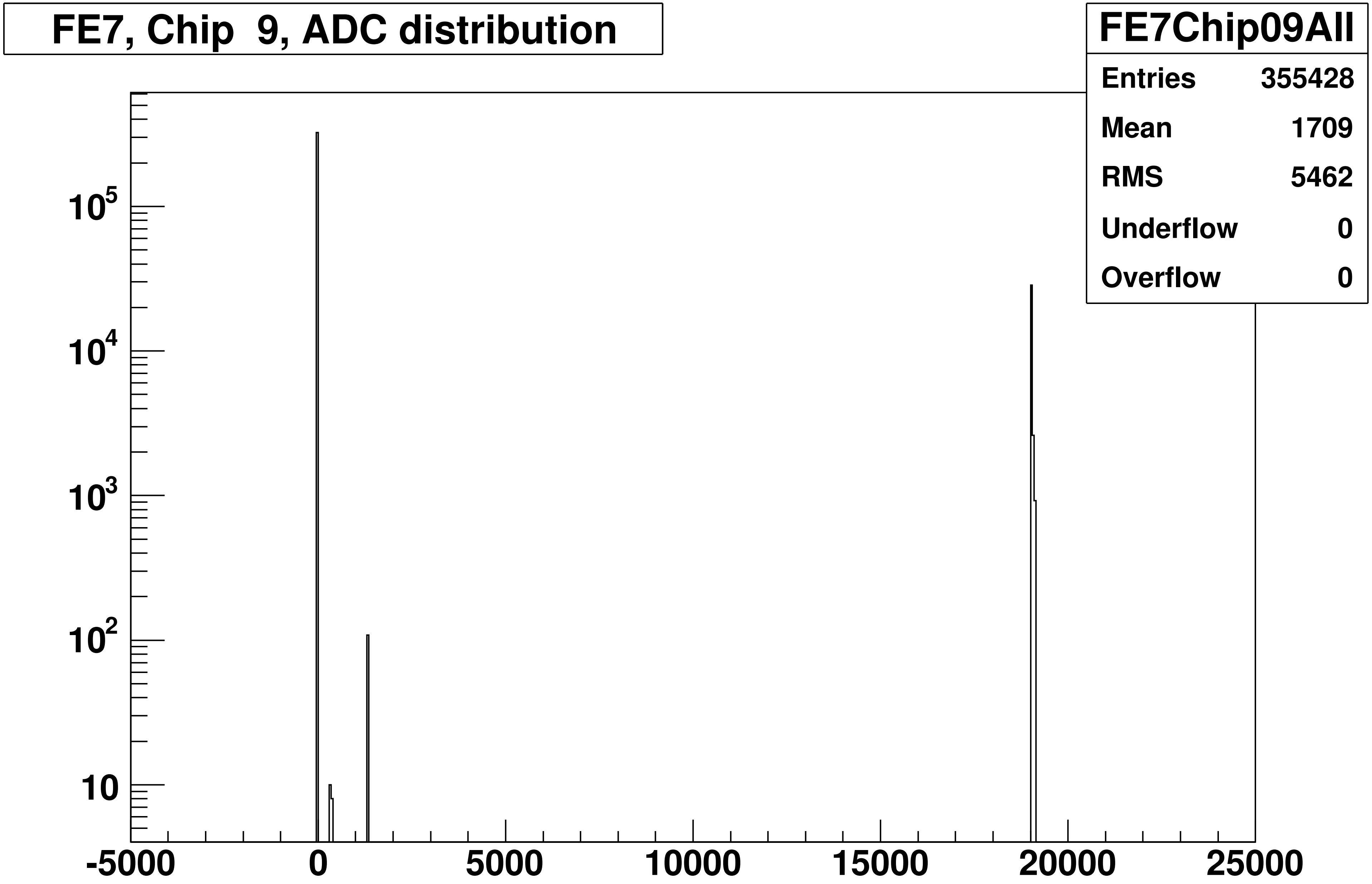
FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 355428
Mean 1.907e+04
RMS 6.26
Underflow 3.243e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

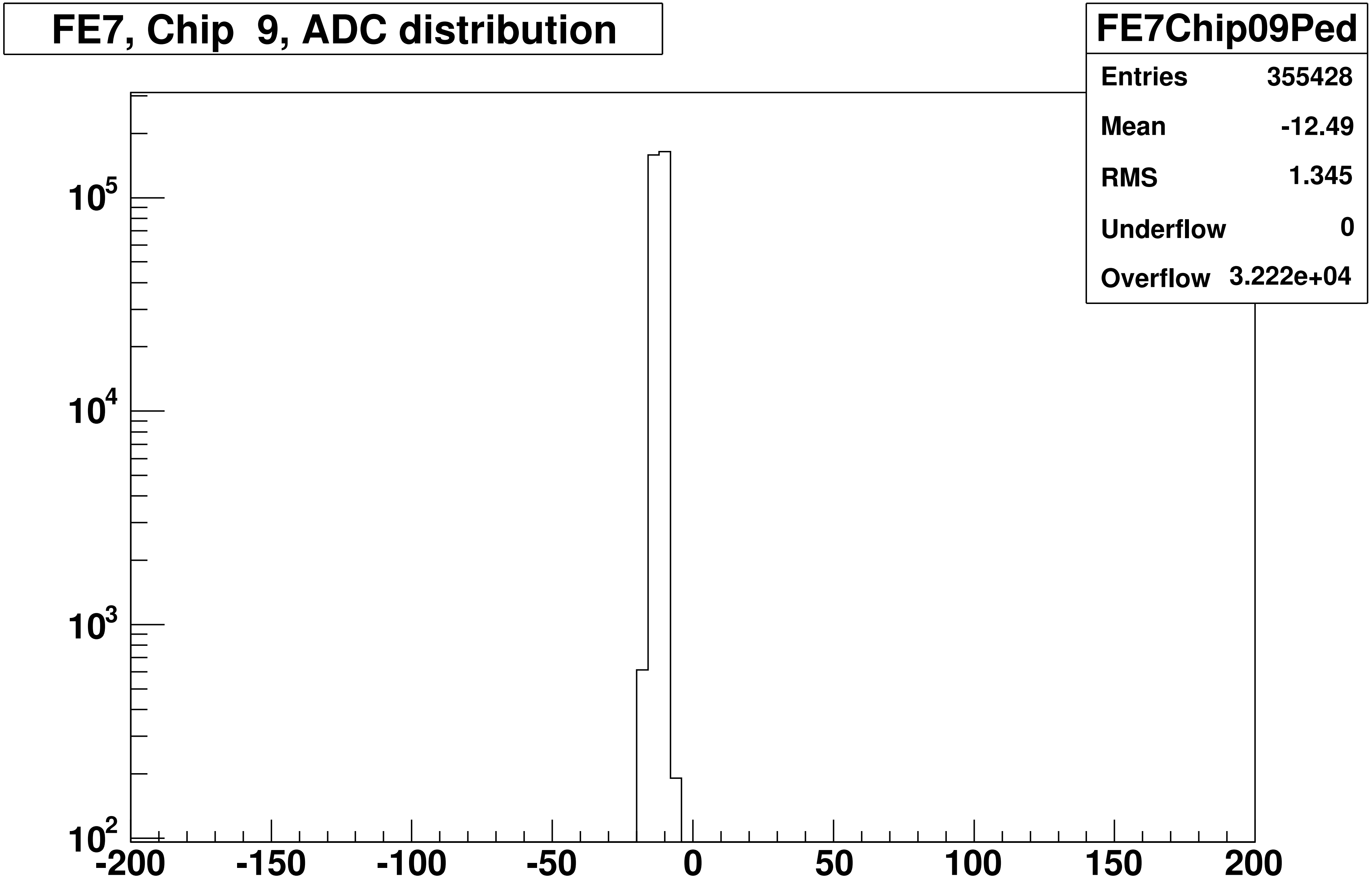

FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 355428
Mean 1746
RMS 5459
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 355428
Mean 31.93
RMS 1.403
Underflow 0
Overflow 3.222e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

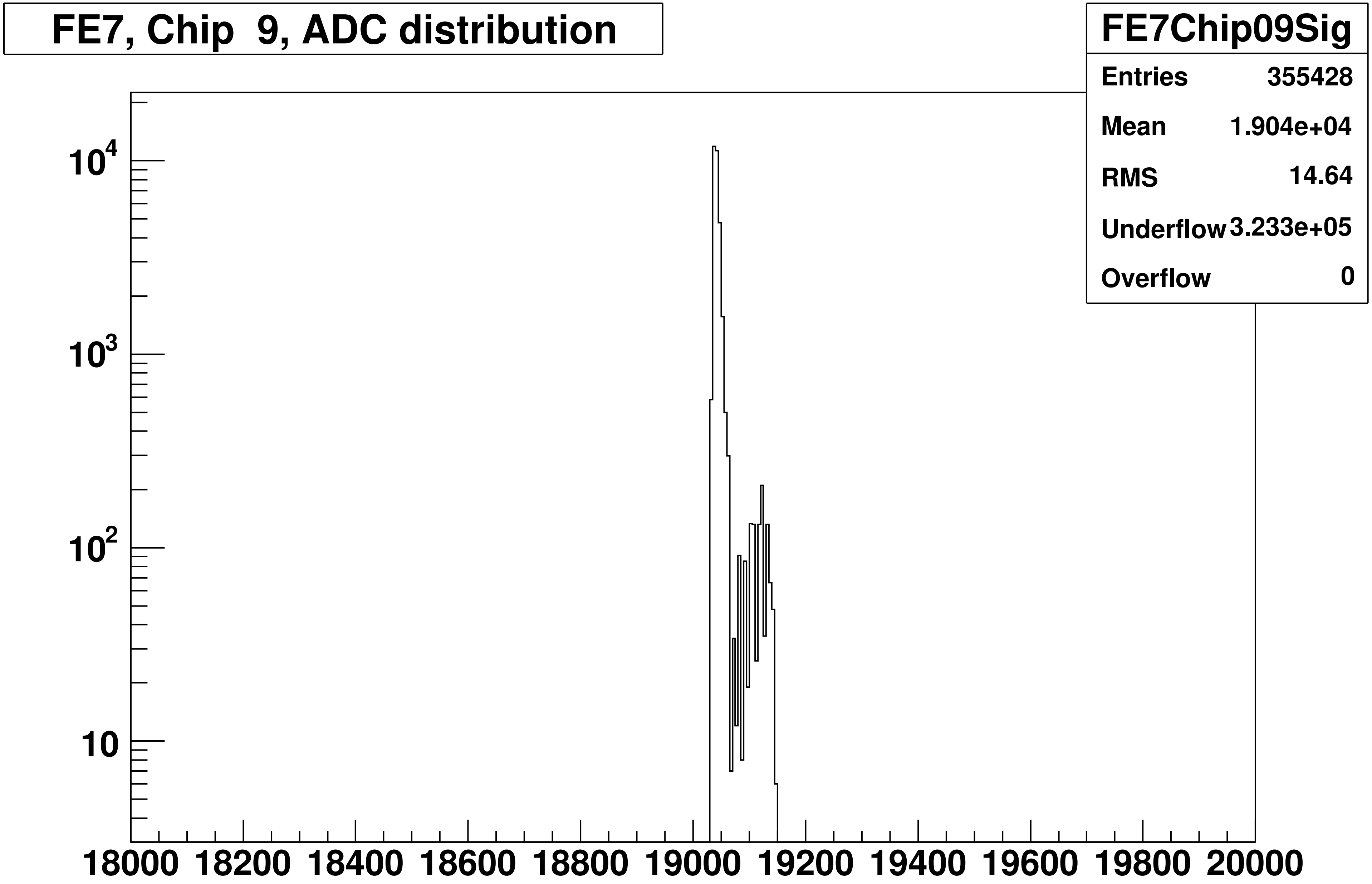


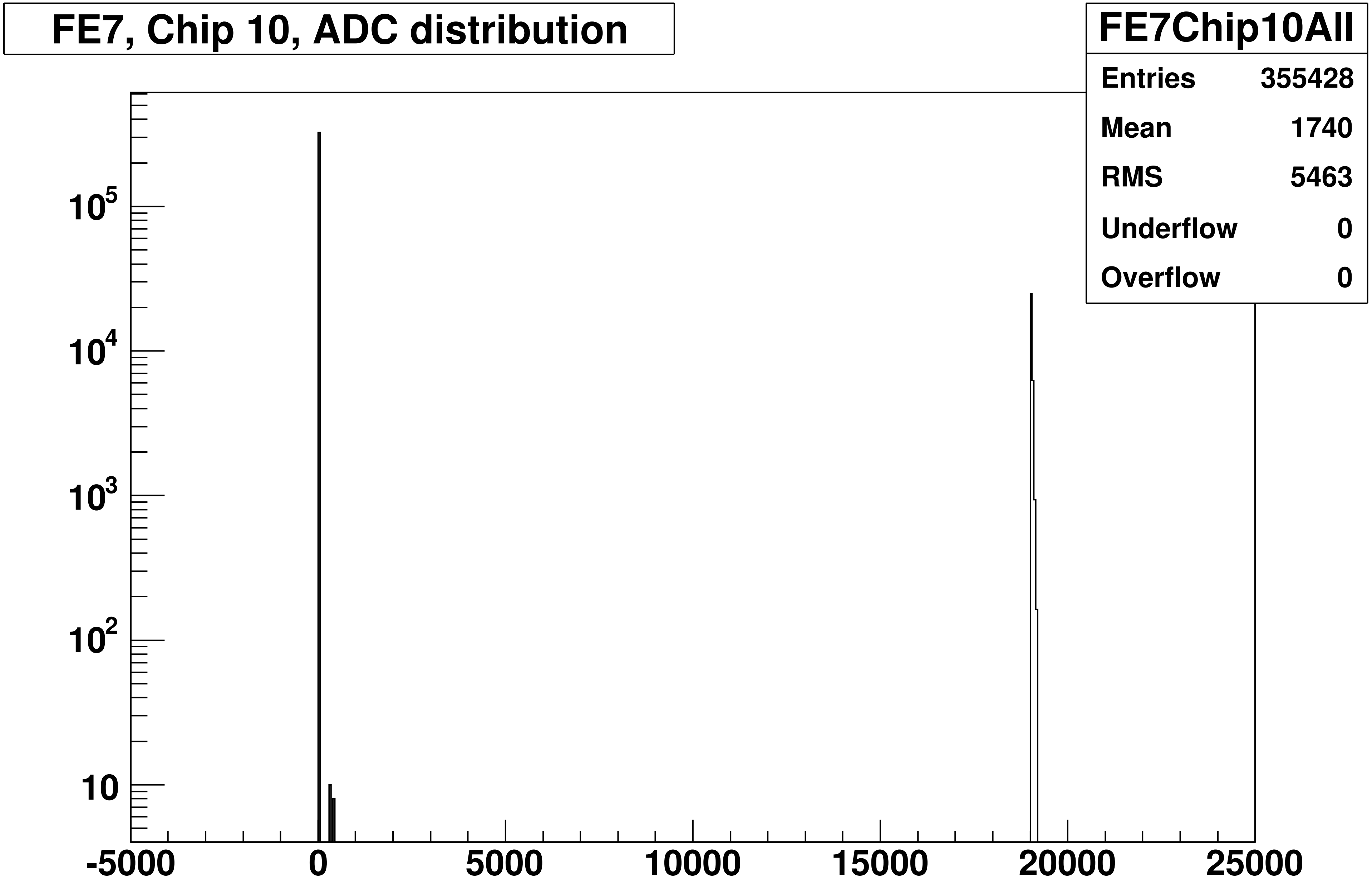
FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 355428
Mean 1.915e+04
RMS 11.85
Underflow 3.236e+05
Overflow 0
1
10
10^2
10^3
10^4
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 355428
Mean 1709
RMS 5462
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

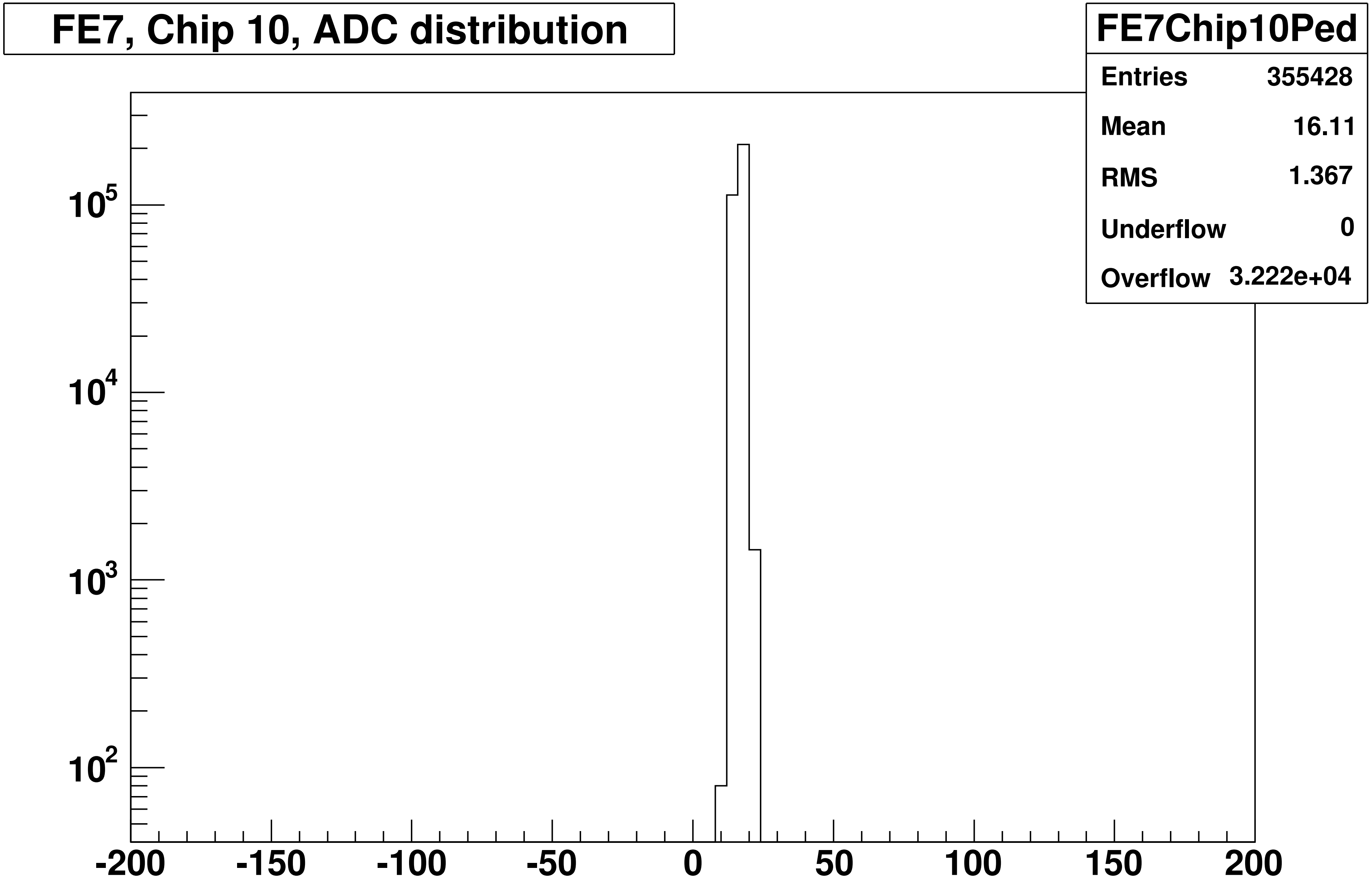


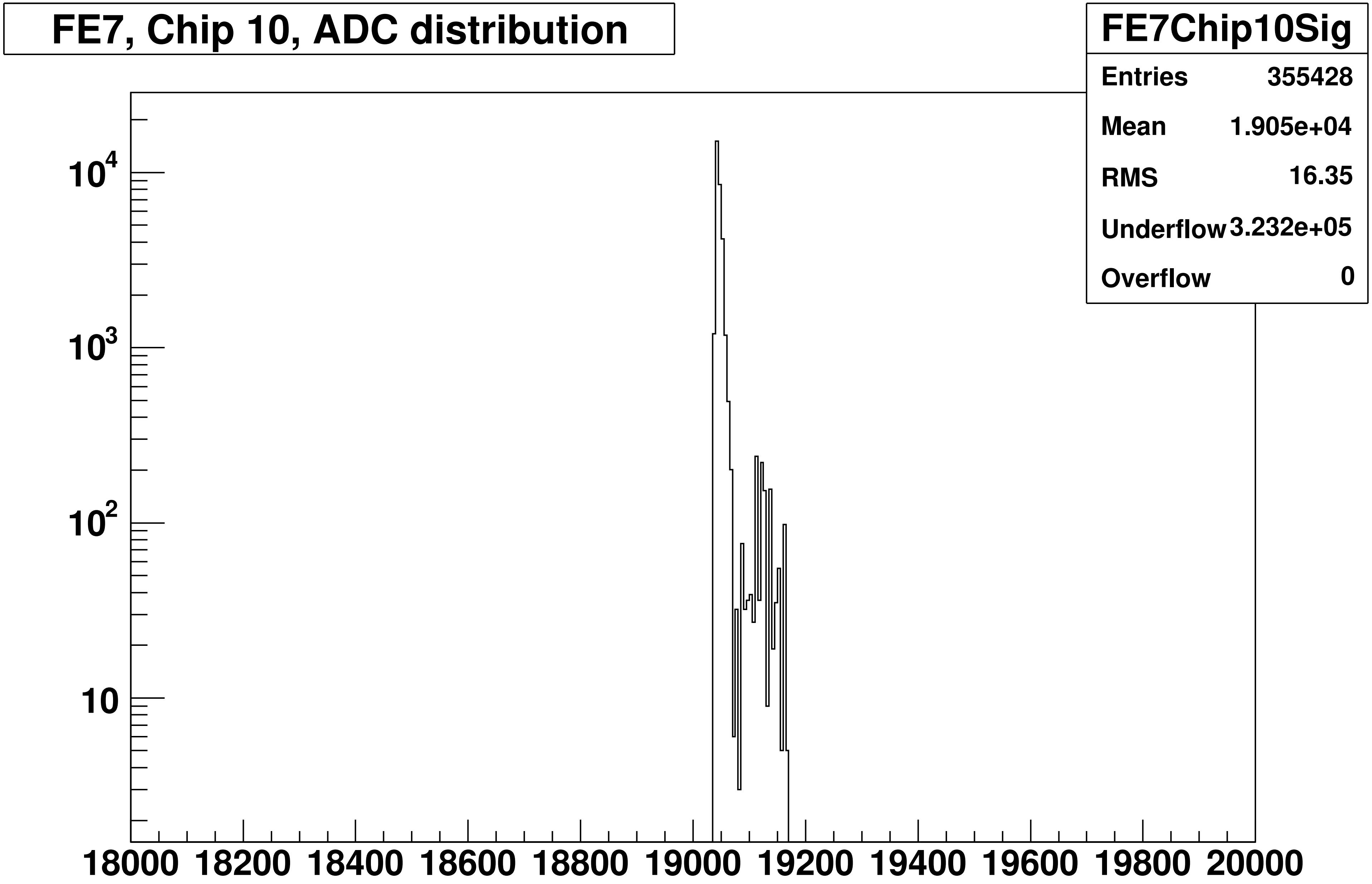
FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 355428
Mean -12.49
RMS 1.345
Underflow 0
Overflow 3.222e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

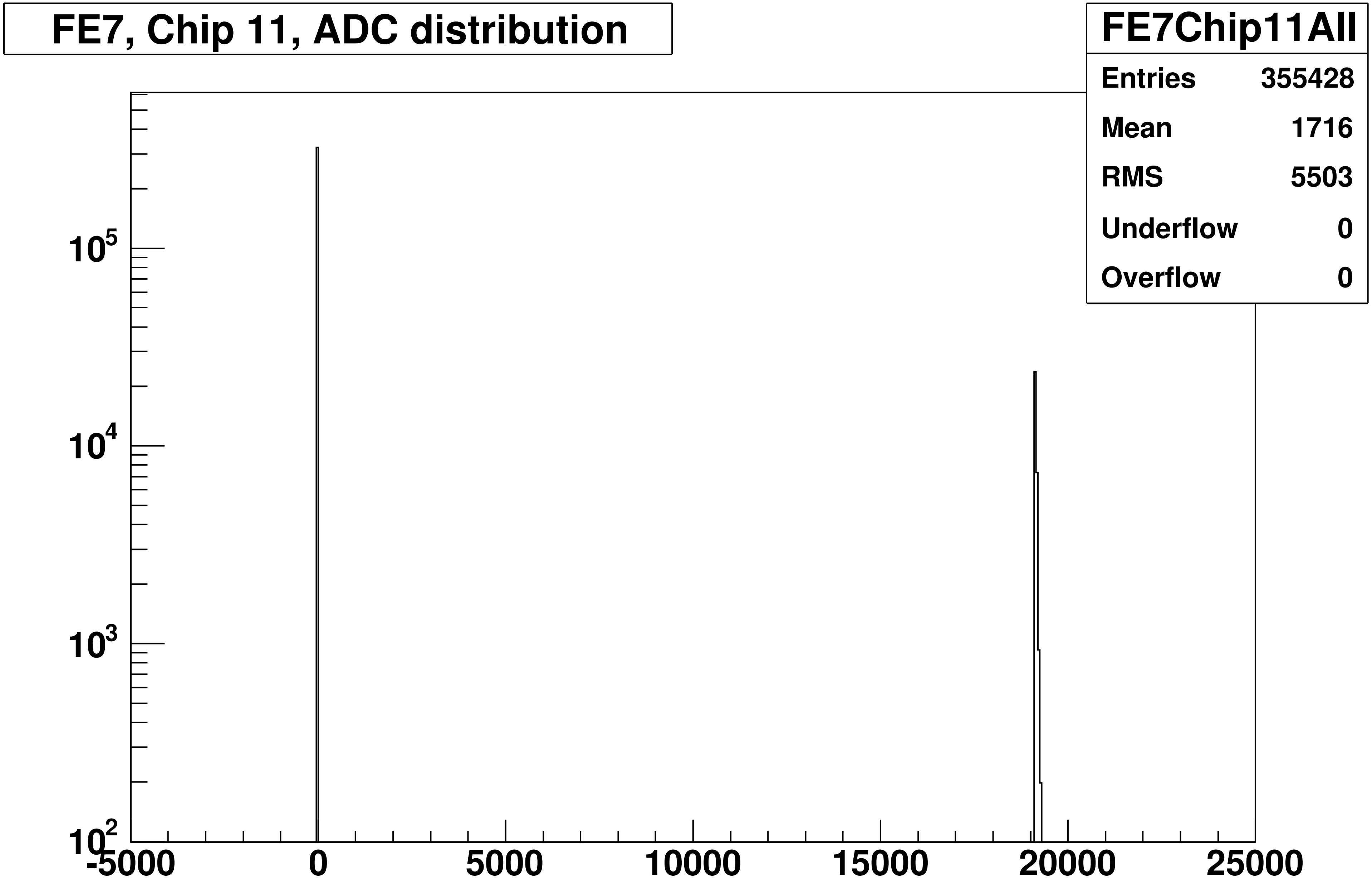

FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 355428
Mean 1.904e+04
RMS 14.64
Underflow 3.233e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

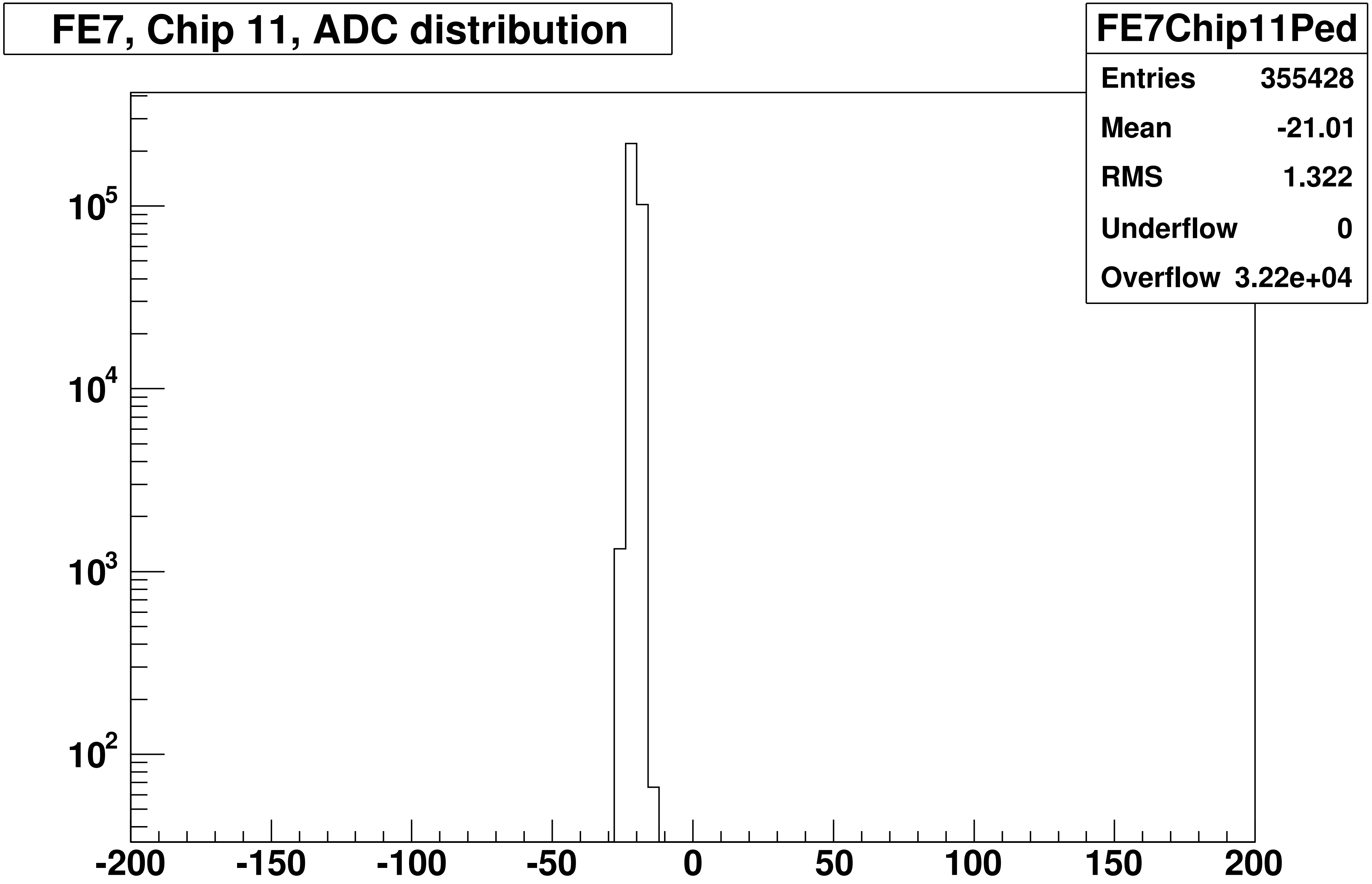


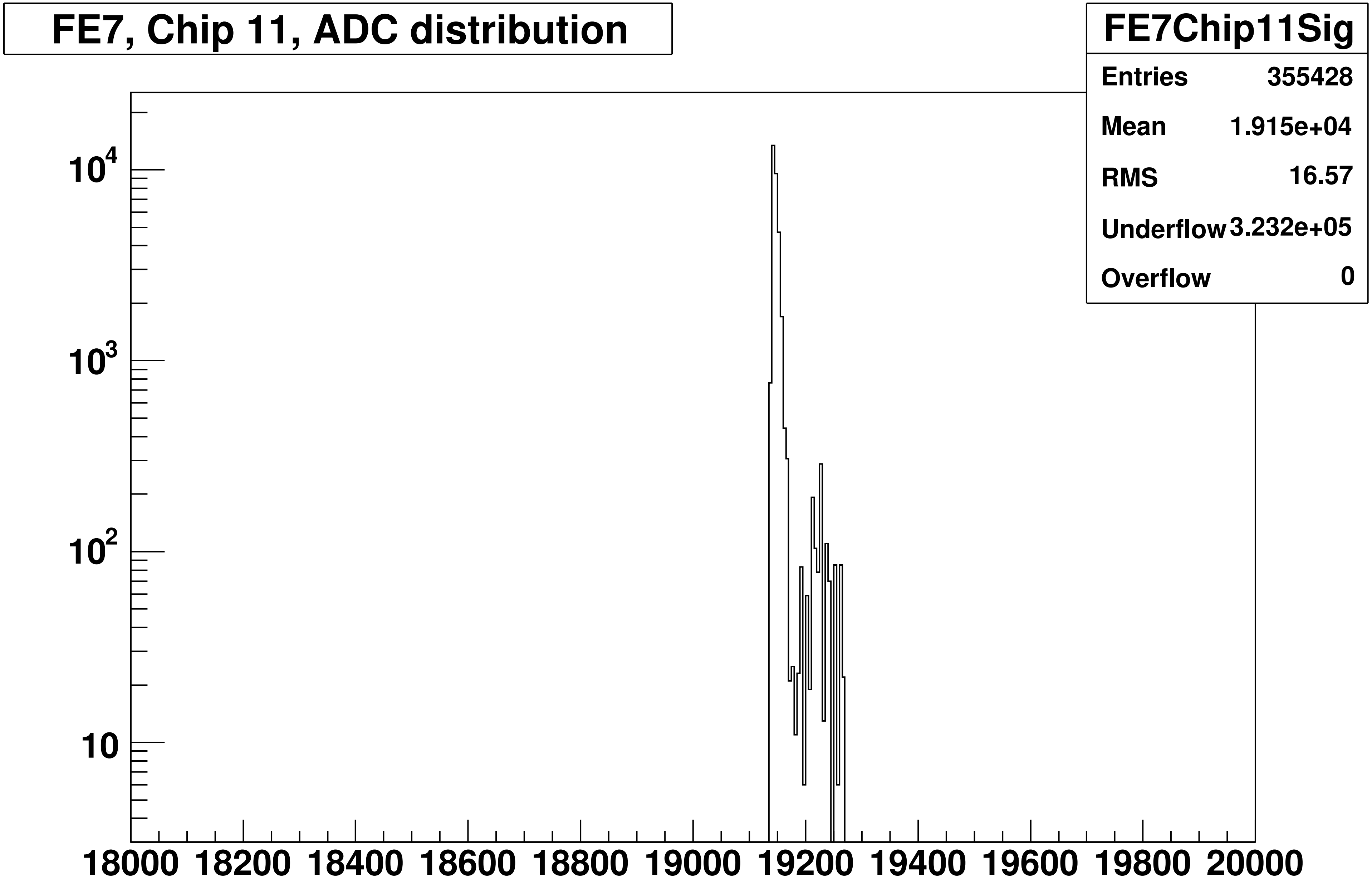
FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 355428
Mean 1740
RMS 5463
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 355428
Mean 16.11
RMS 1.367
Underflow 0
Overflow 3.222e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

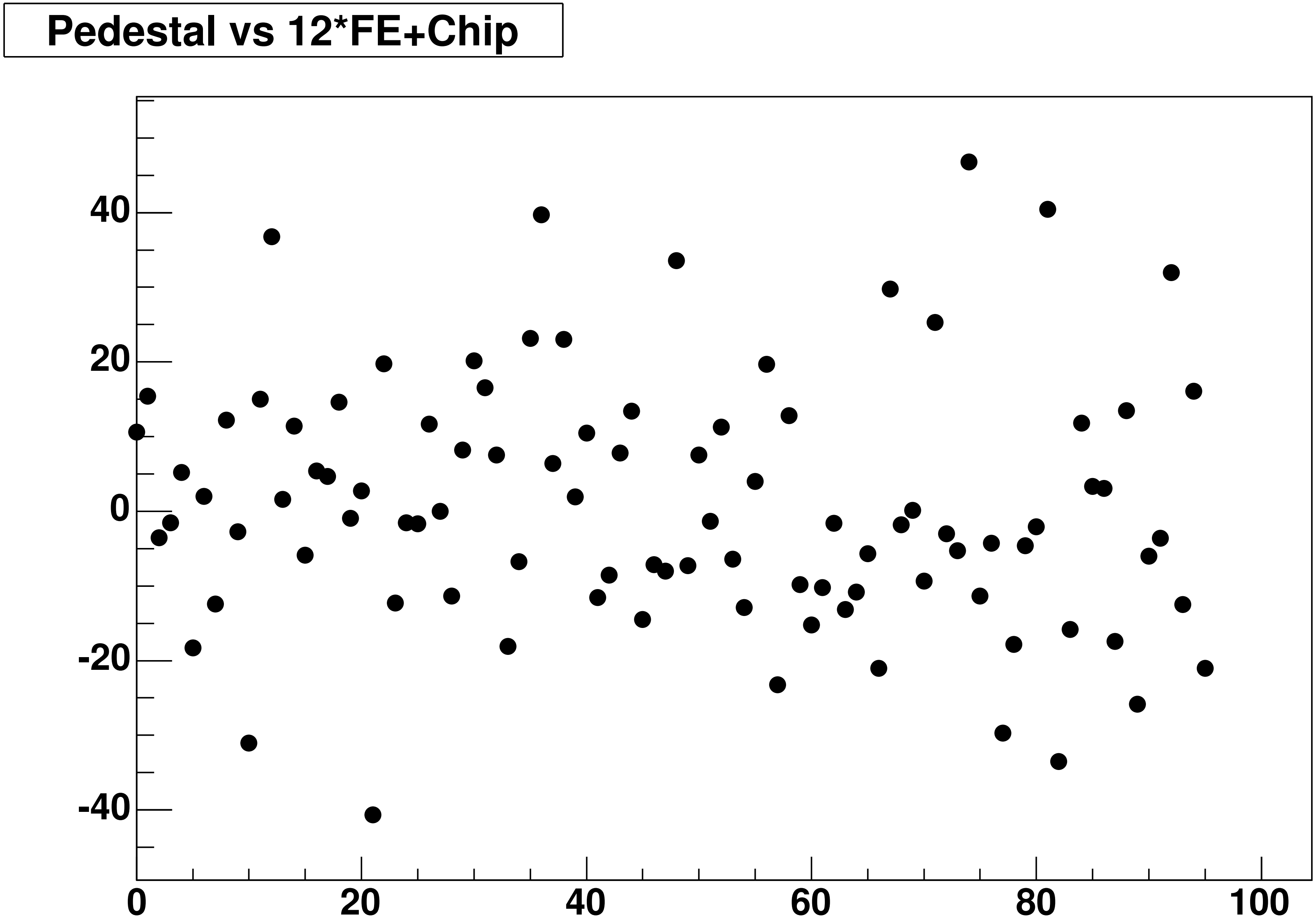

FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 355428
Mean 1.905e+04
RMS 16.35
Underflow 3.232e+05
Overflow 0
10
10^2
10^3
10^4
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

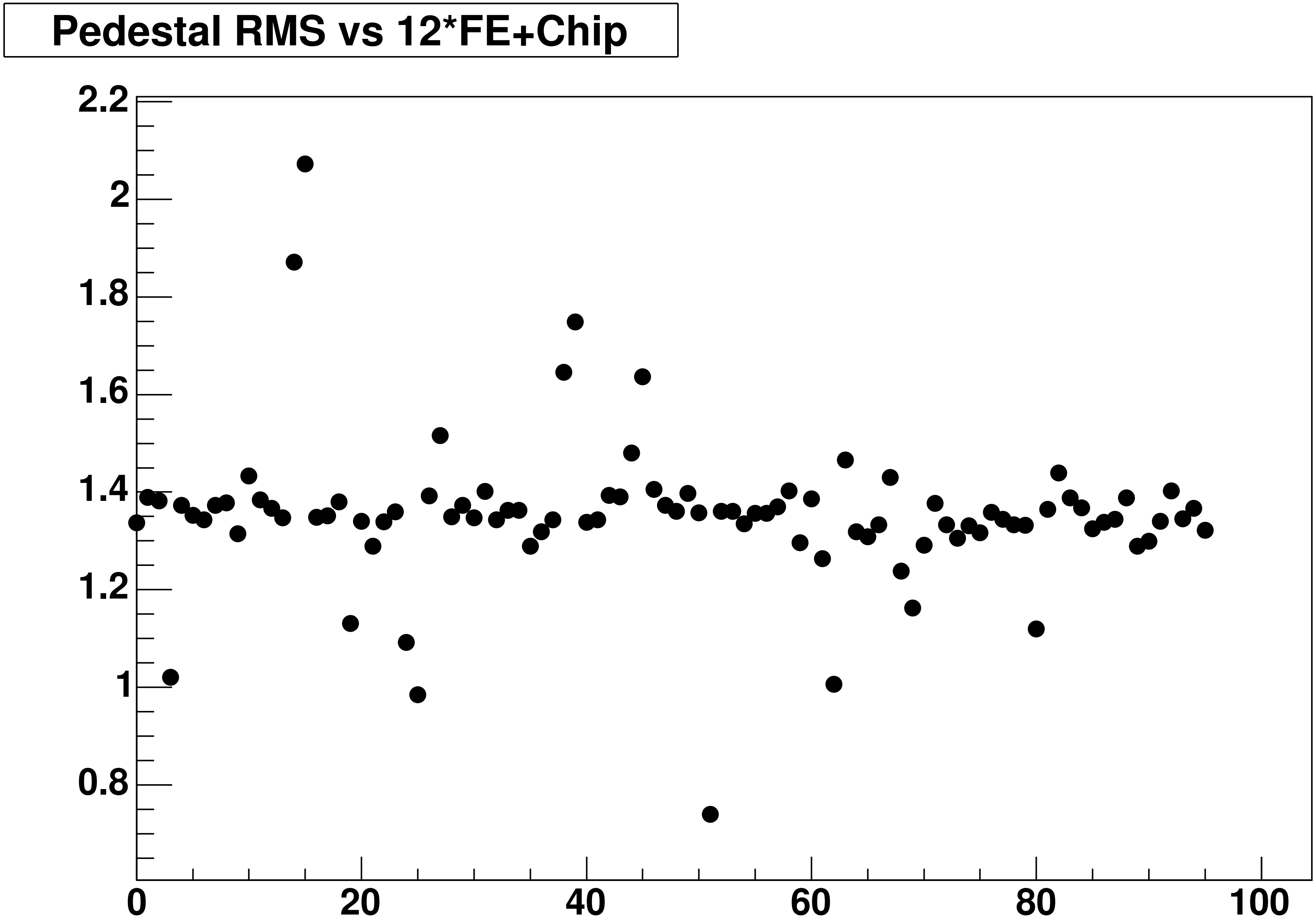

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 355428
Mean 1716
RMS 5503
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

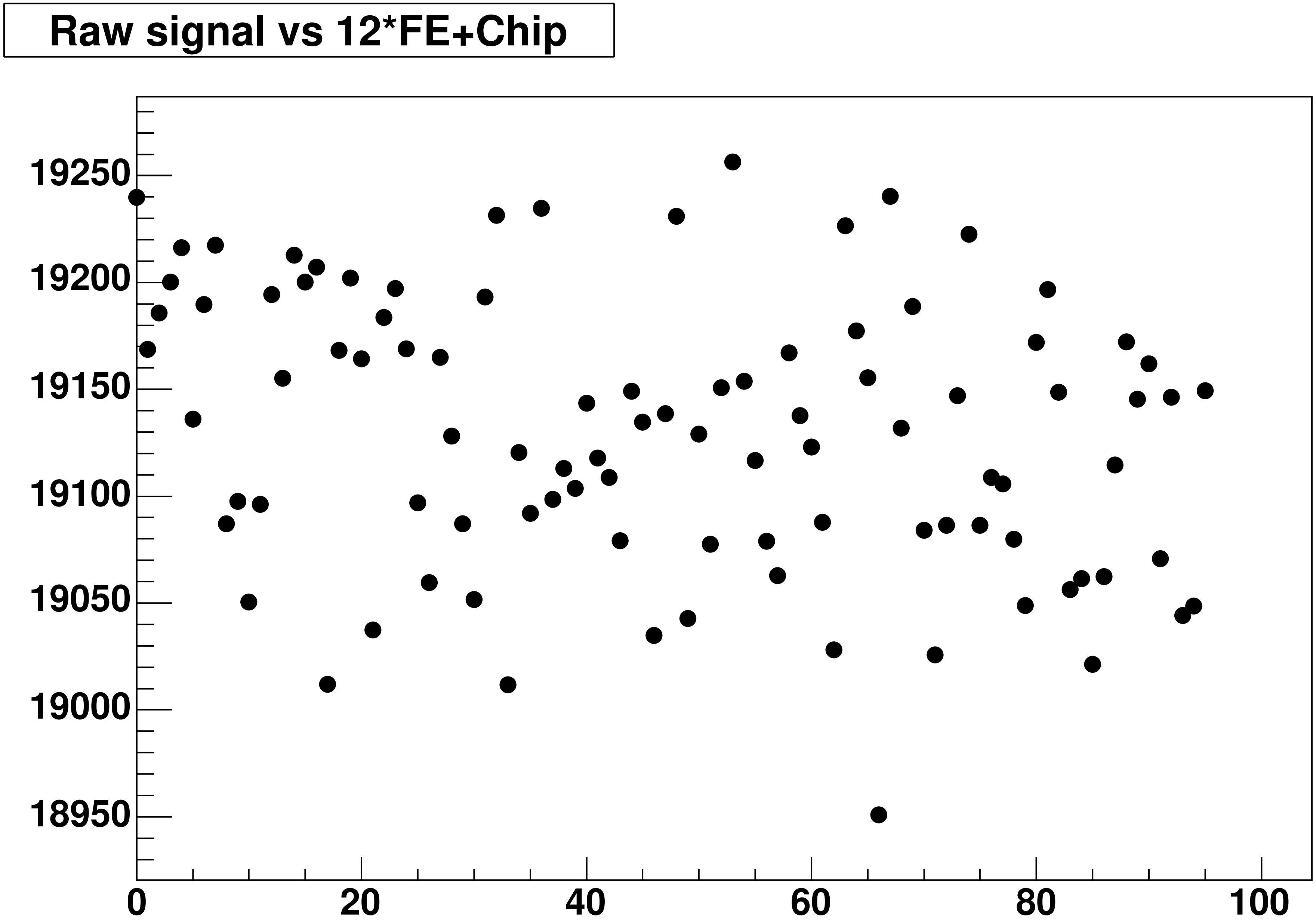

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 355428
Mean -21.01
RMS 1.322
Underflow 0
Overflow 3.22e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



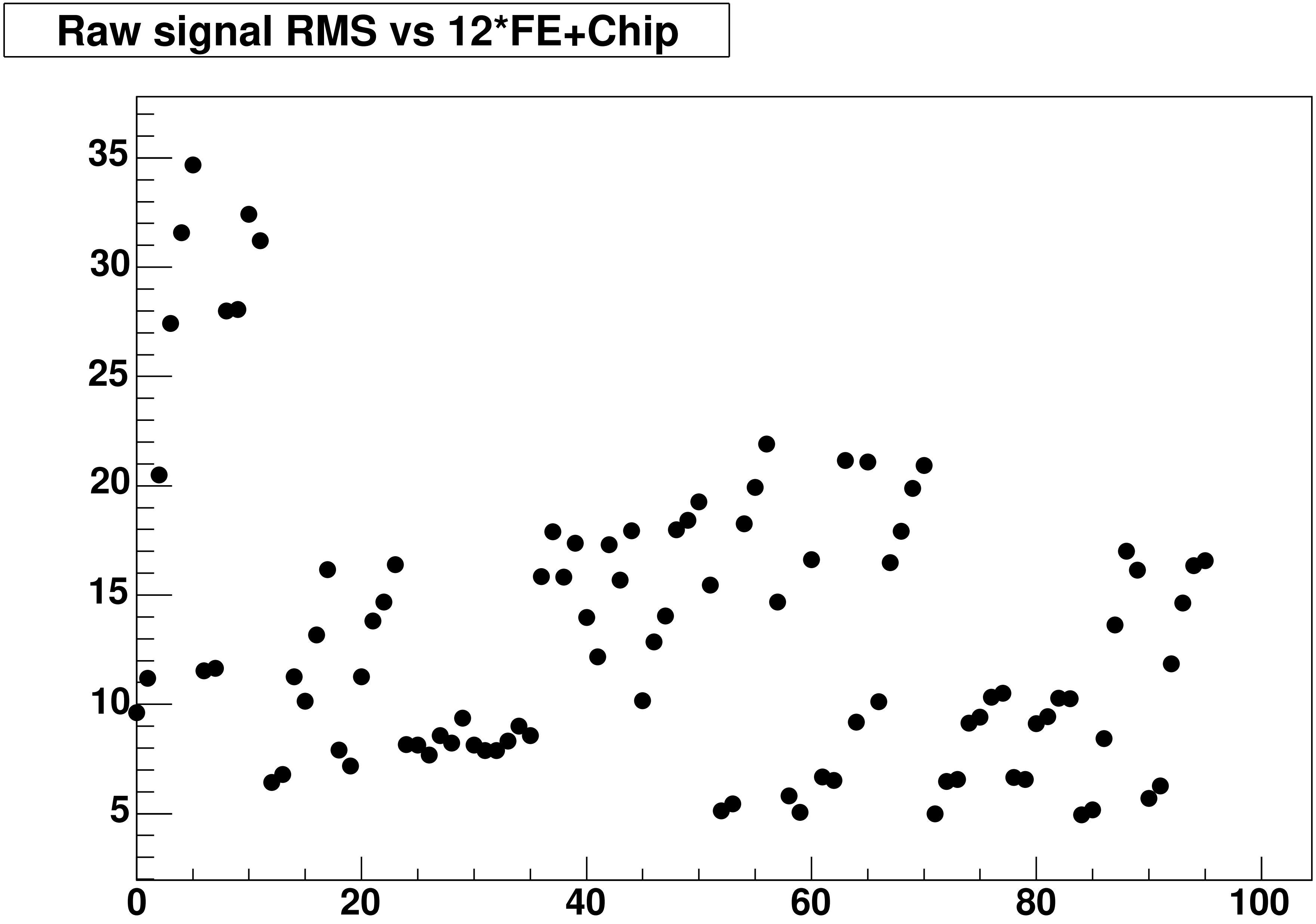
FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 355428
Mean 1.915e+04
RMS 16.57
Underflow 3.232e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

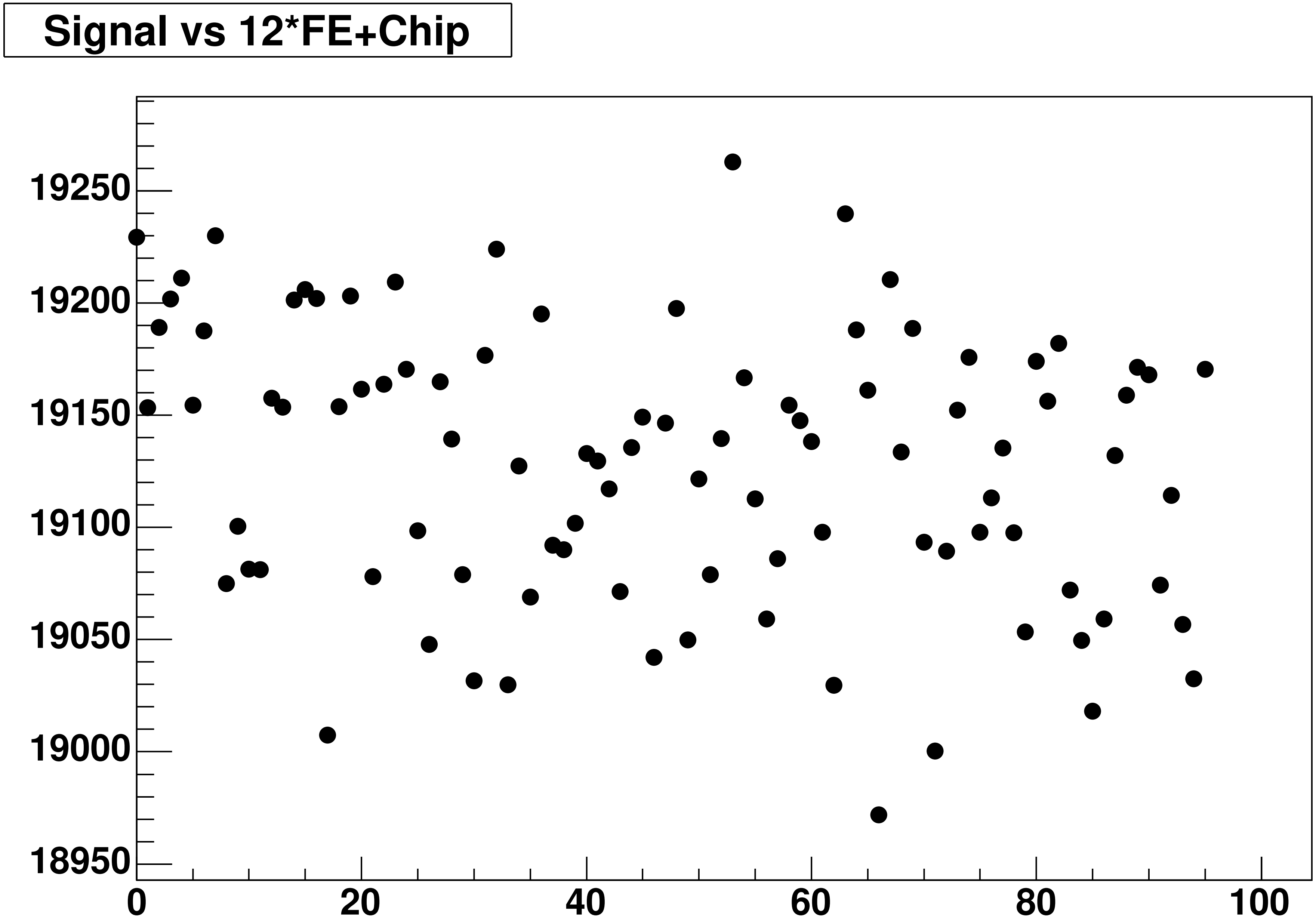

Pedestal vs 12*FE+Chip
40
20
0
-20
-40
0
20
40
60
80
100


Pedestal RMS vs 12*FE+Chip
2.2
2
1.8
1.6
1.4
1.2
1
0.8
0
20
40
60
80
100

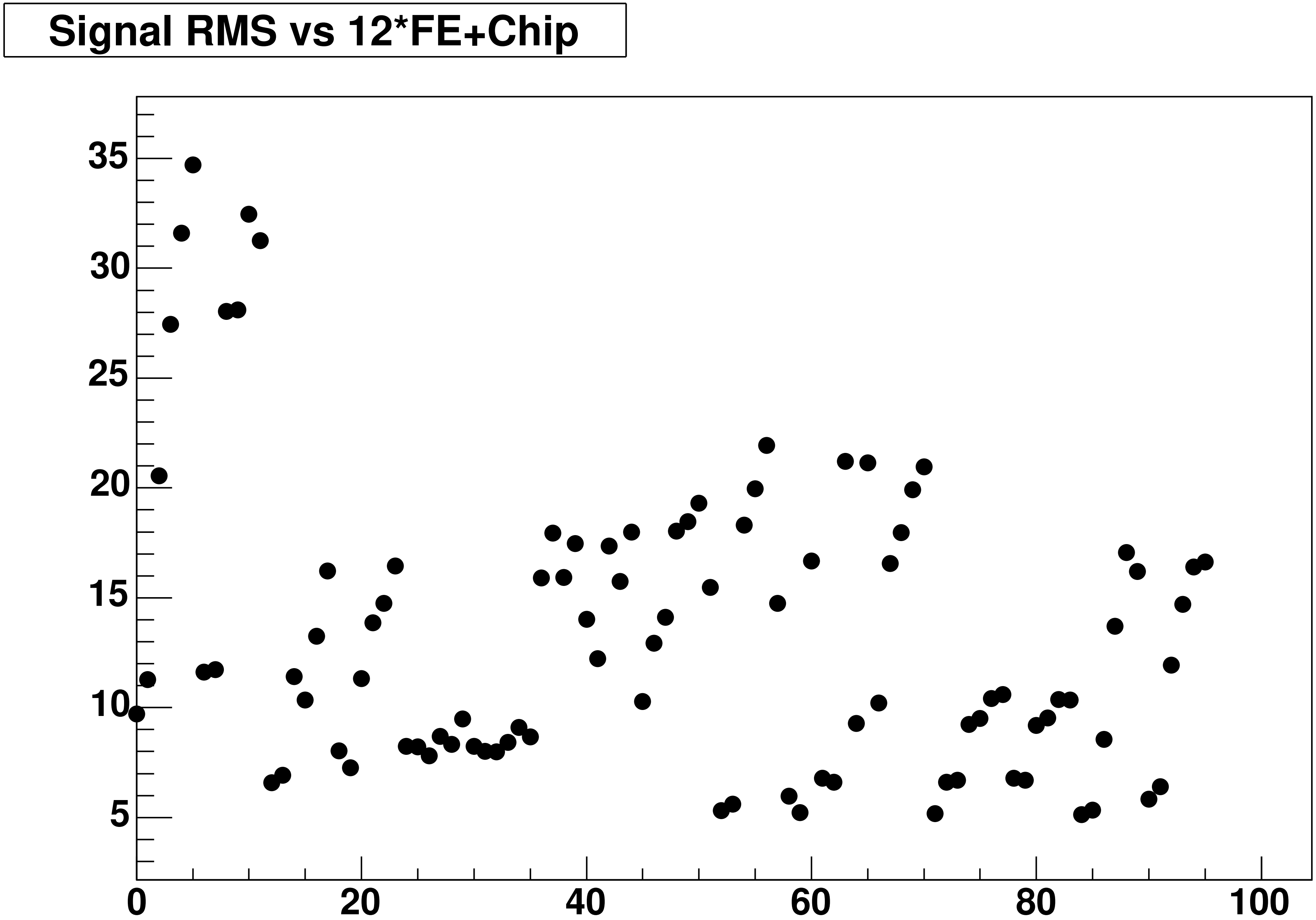


Raw signal vs 12*FE+Chip
19250
19200
19150
19100
19050
19000
18950
0
20
40
60
80
100


Raw signal RMS vs 12*FE+Chip
35
30
25
20
15
10
5
0
20
40
60
80
100



Signal vs 12*FE+Chip
19250
19200
19150
19100
19050
19000
18950
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
35
30
25
20
15
10
5
0
20
40
60
80
100