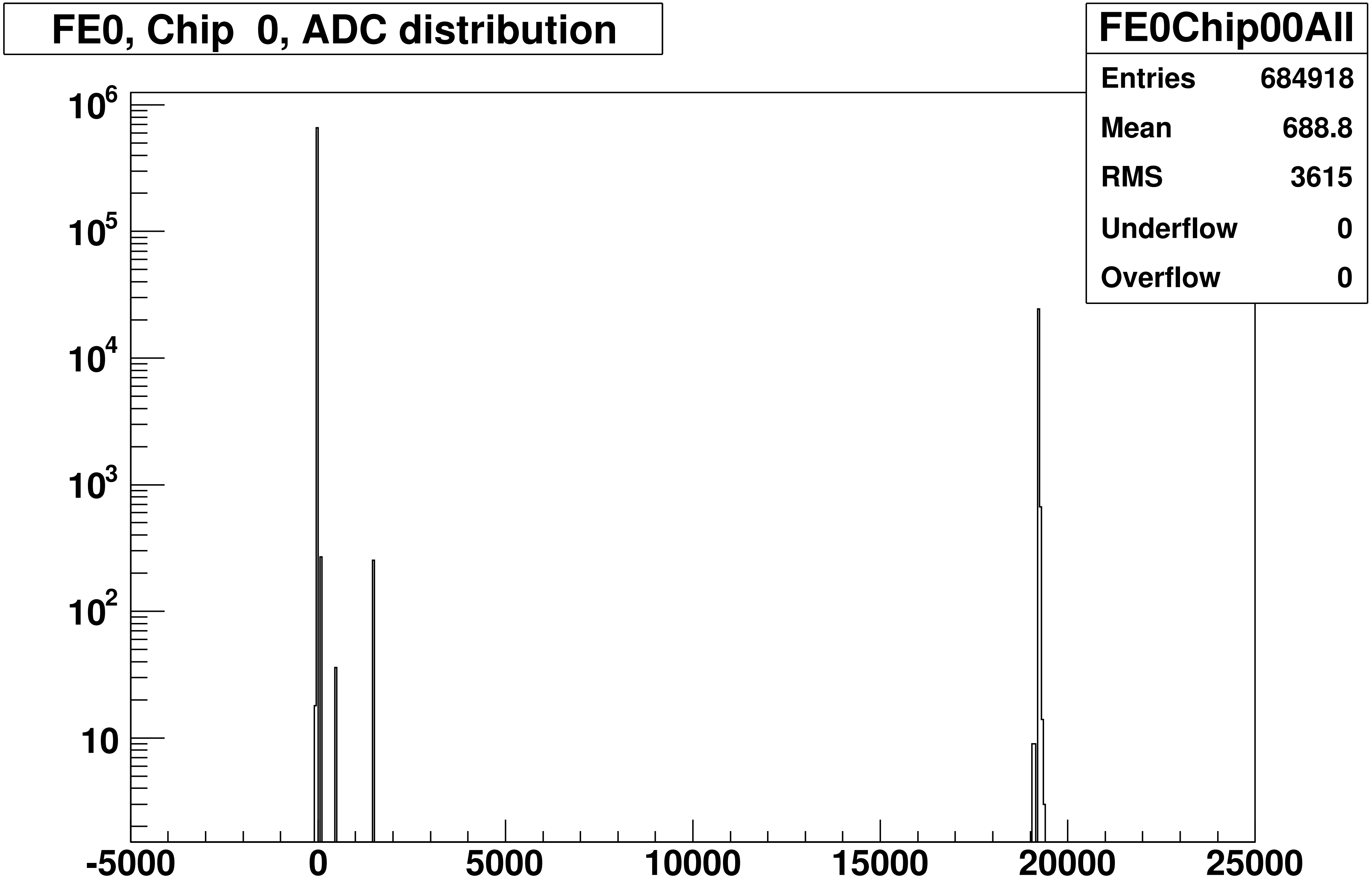
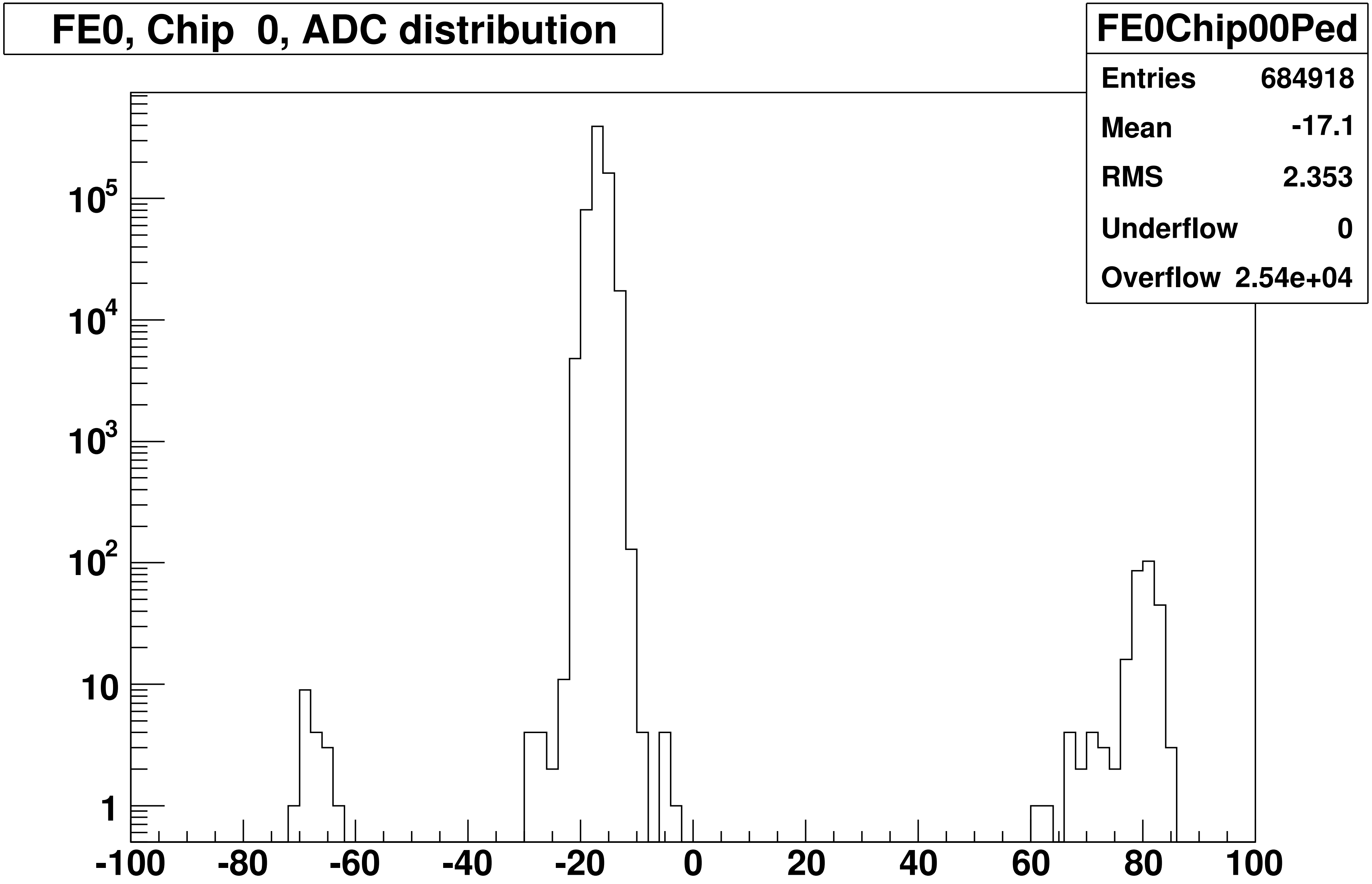
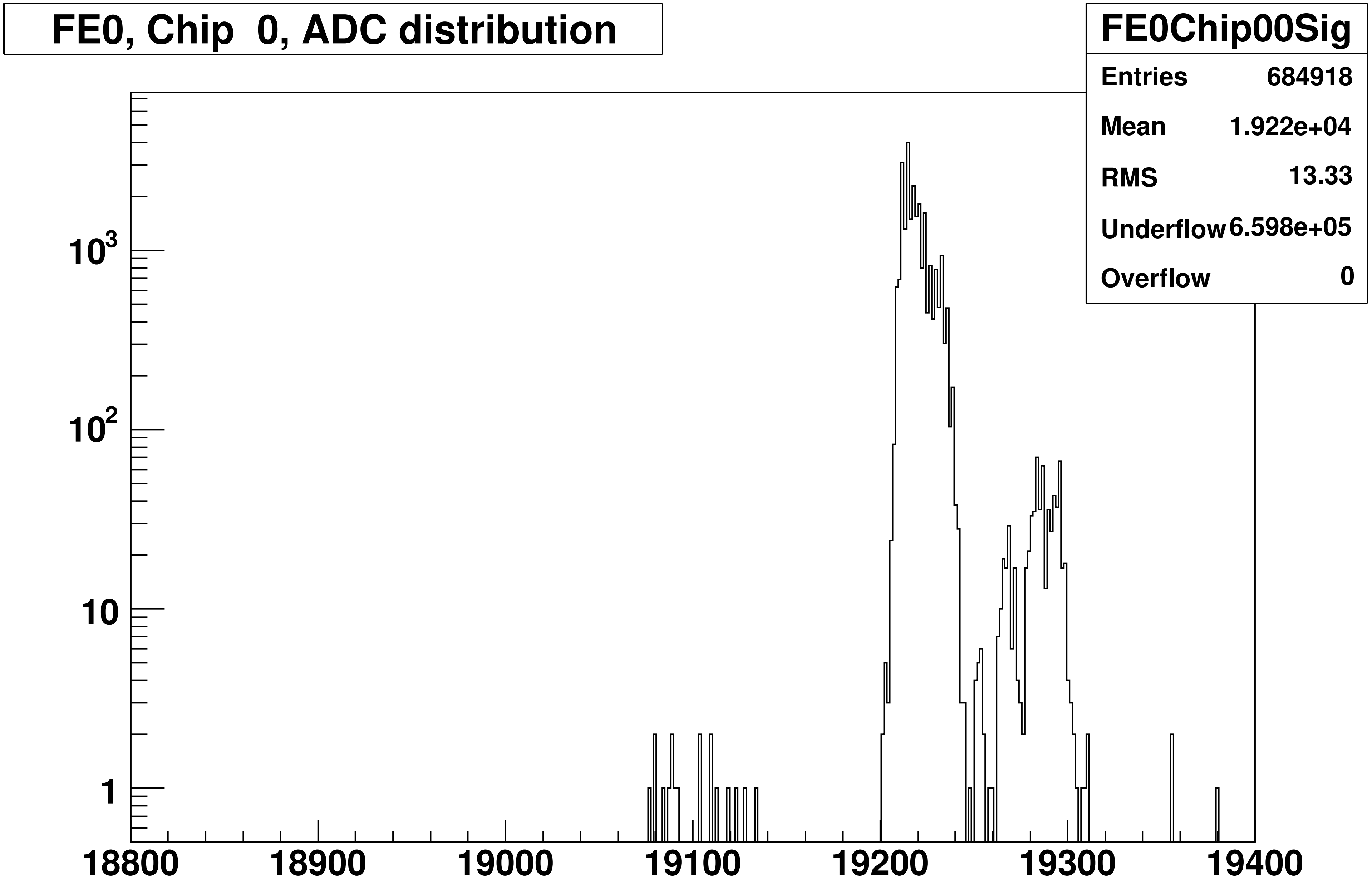
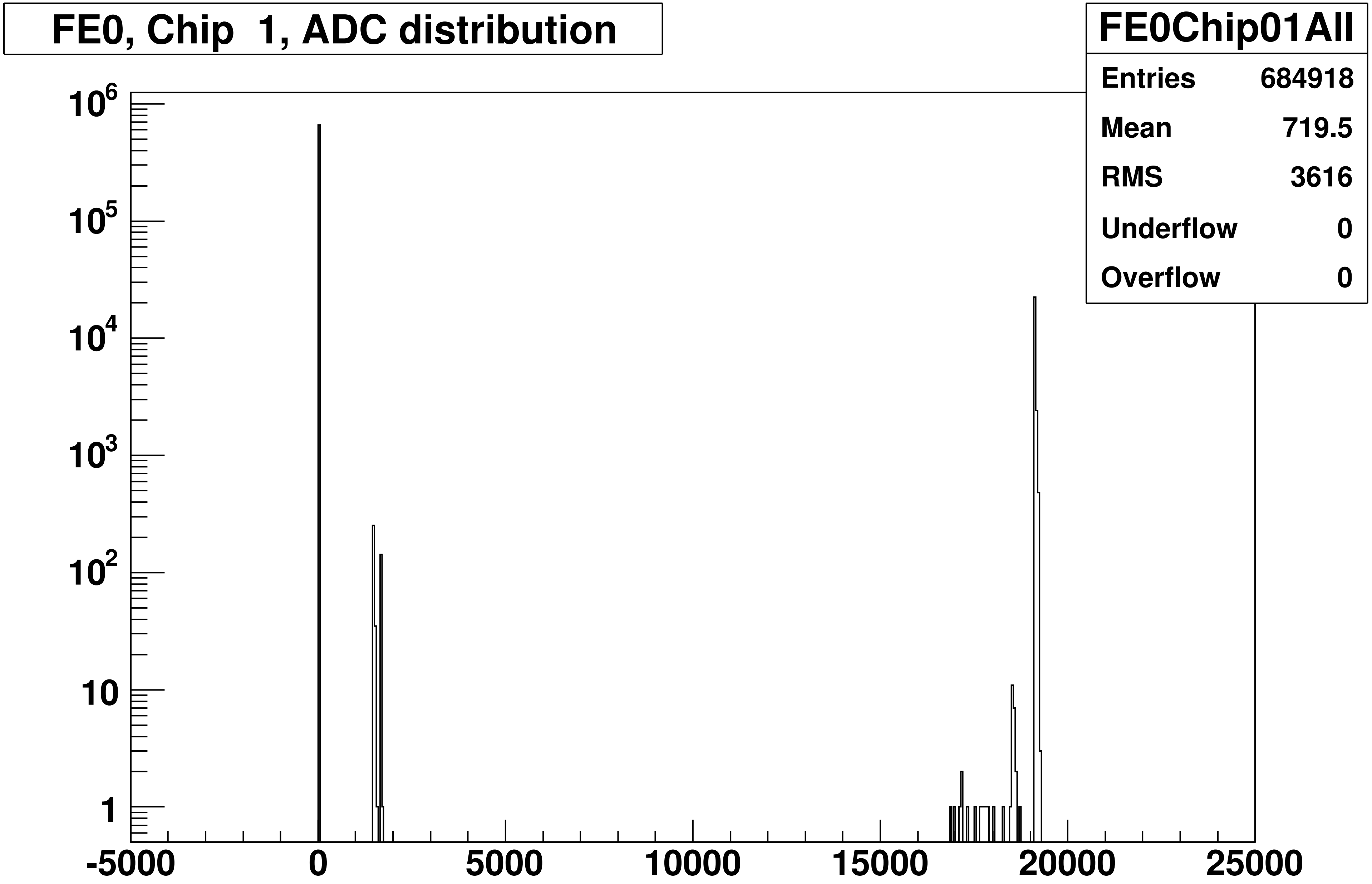
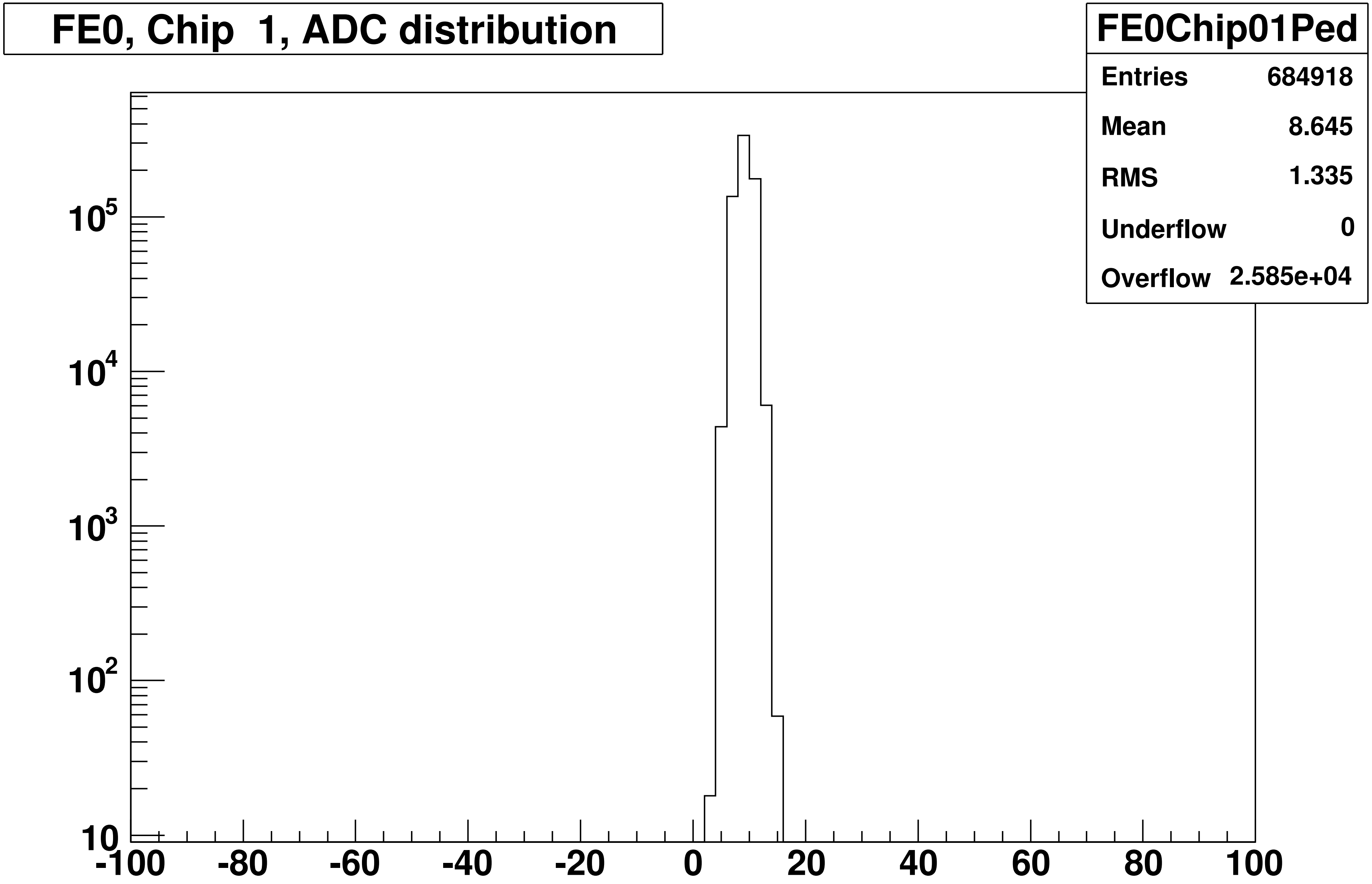
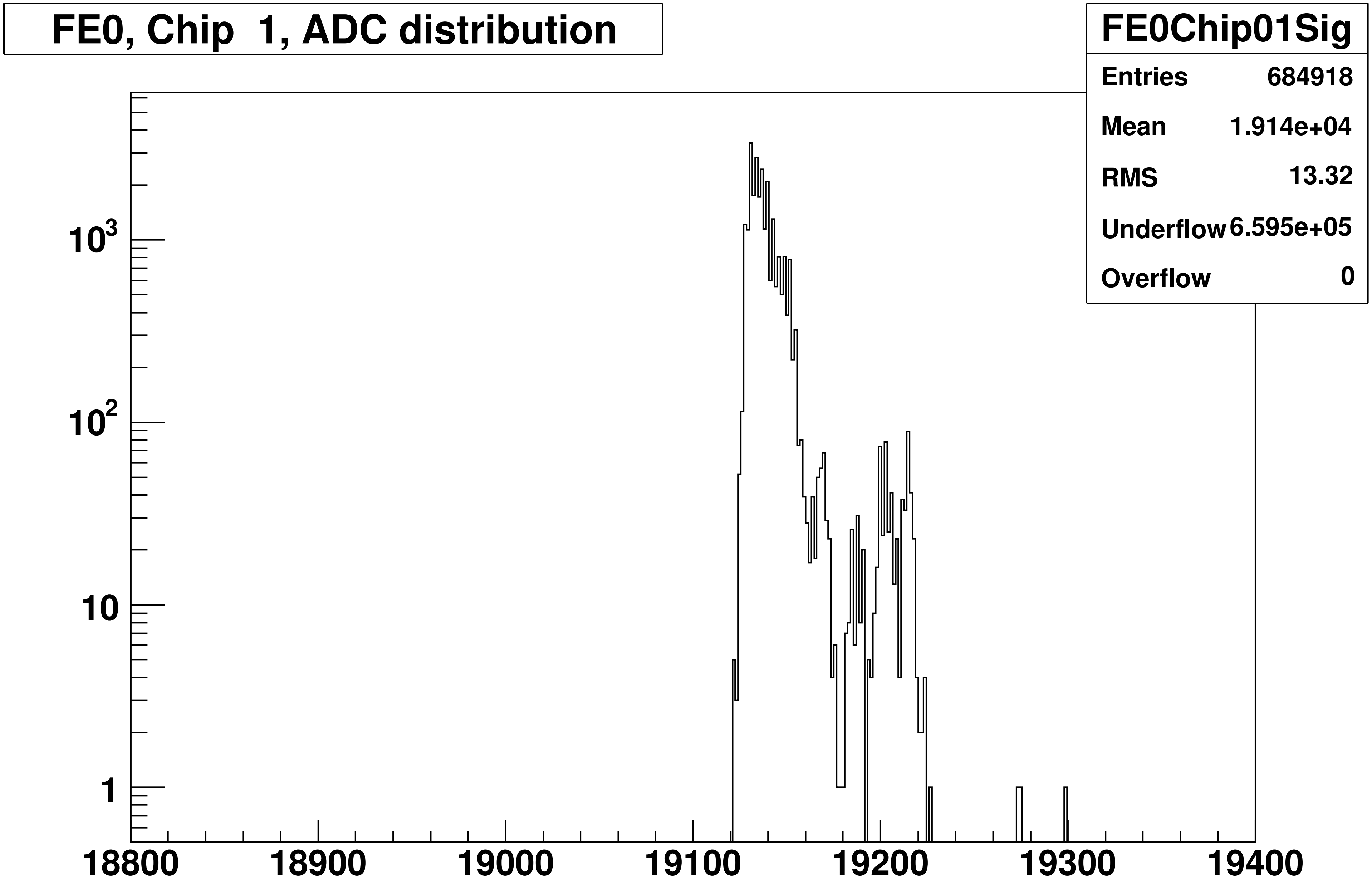

FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 684918
Mean 688.8
RMS 3615
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 684918
Mean -17.1
RMS 2.353
Underflow 0
Overflow 2.54e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

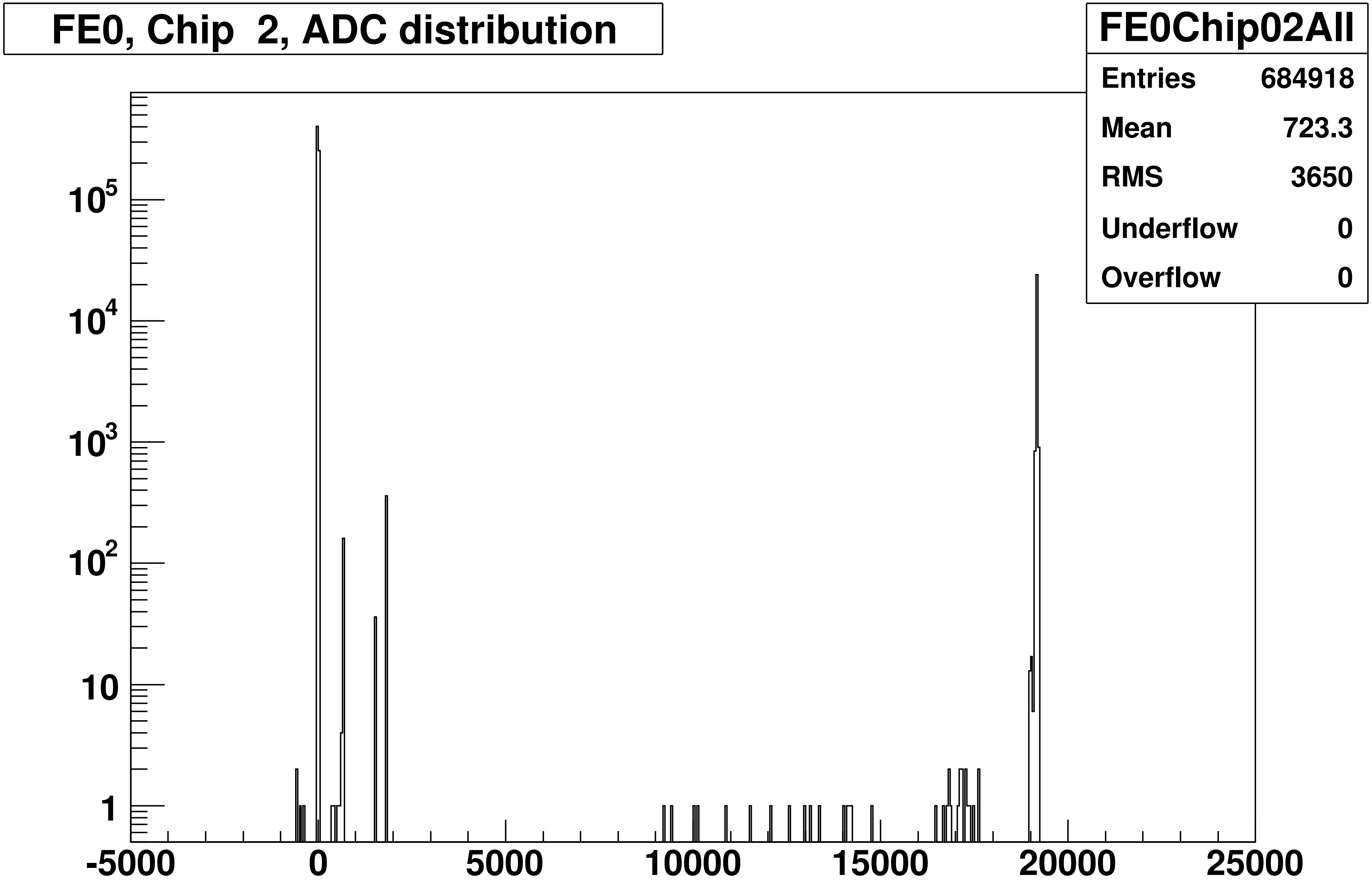

FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 684918
Mean 1.922e+04
RMS 13.33
Underflow 6.598e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

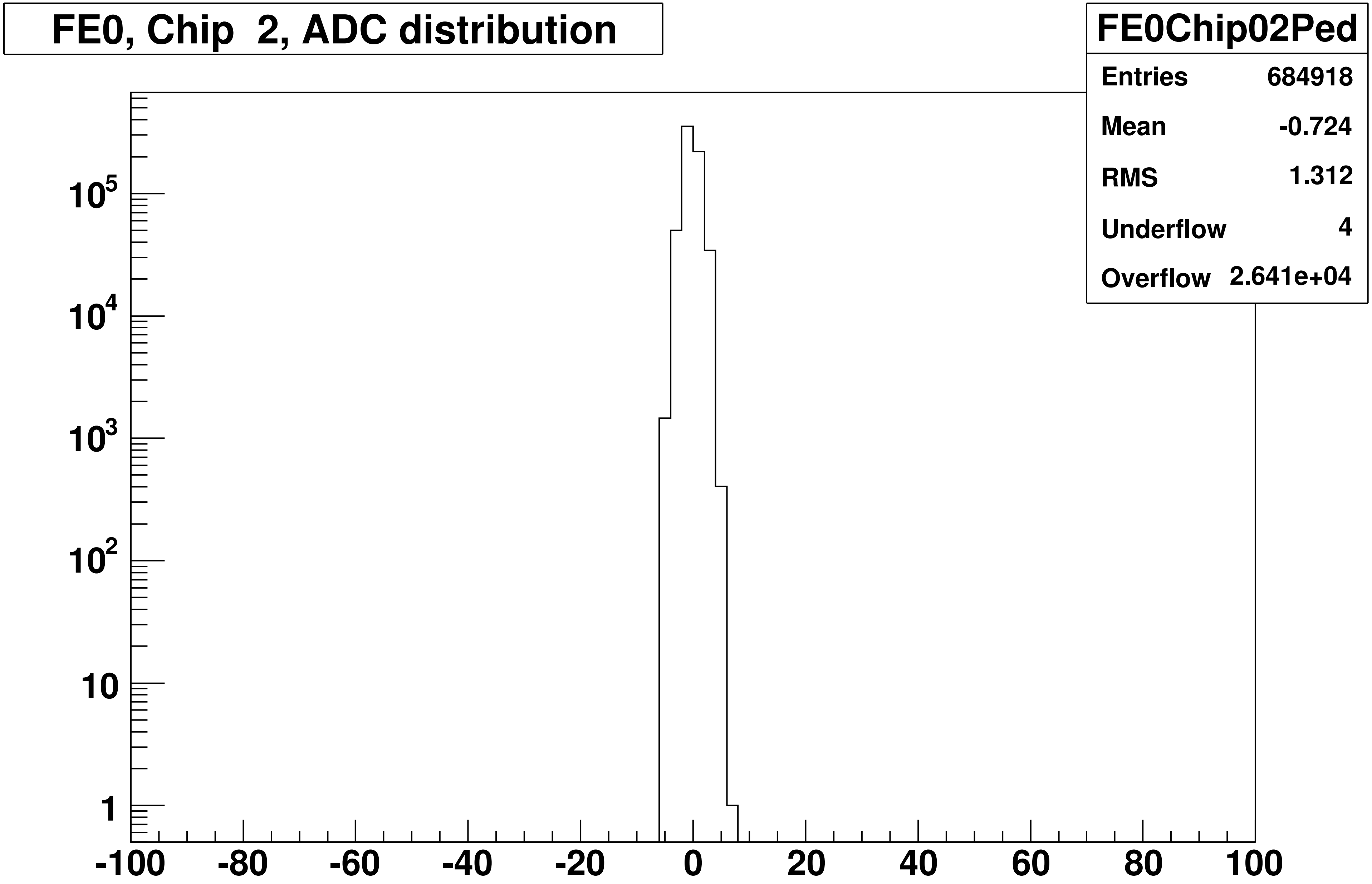

FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 684918
Mean 719.5
RMS 3616
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

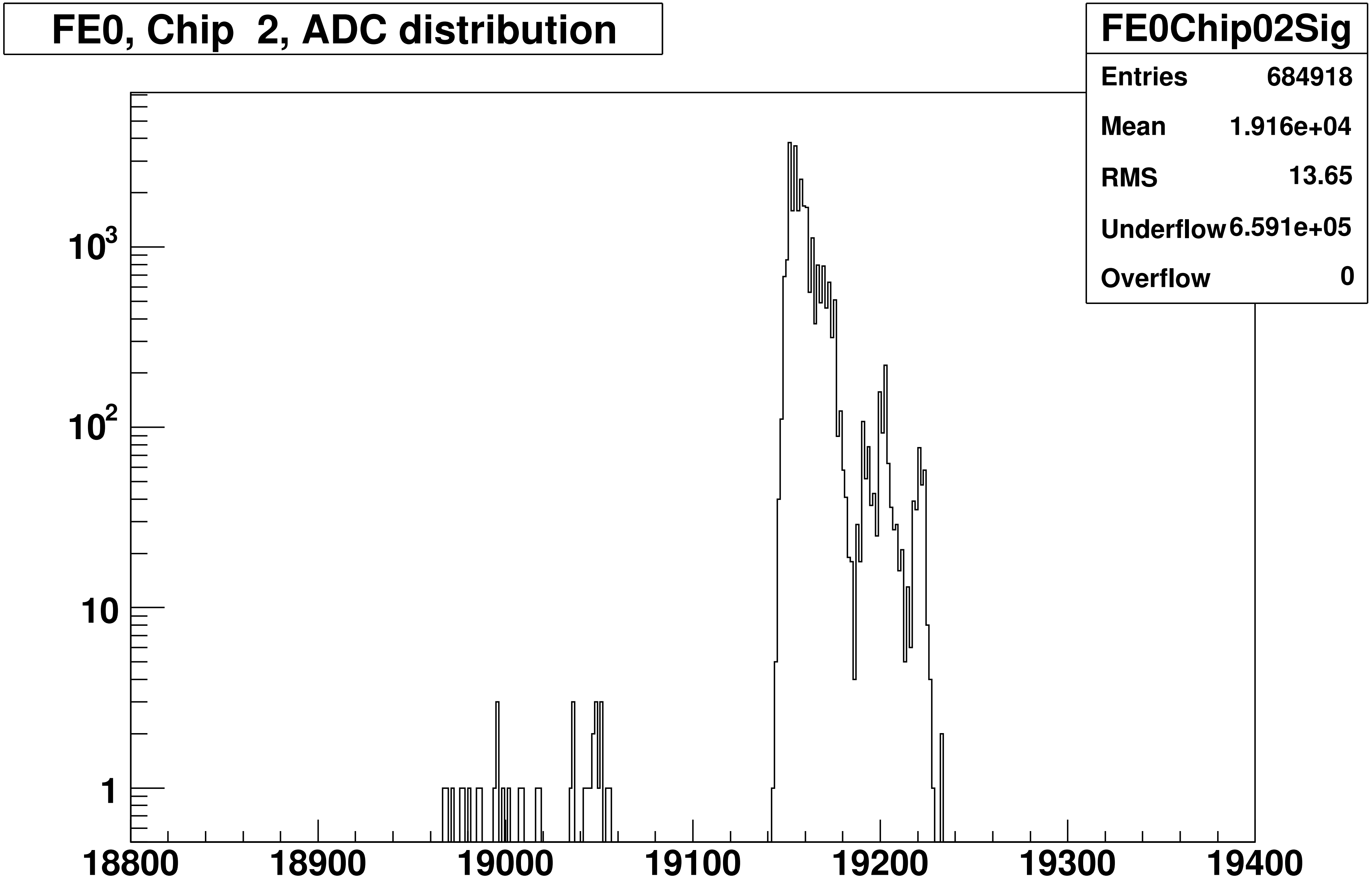

FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 684918
Mean 8.645
RMS 1.335
Underflow 0
Overflow 2.585e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

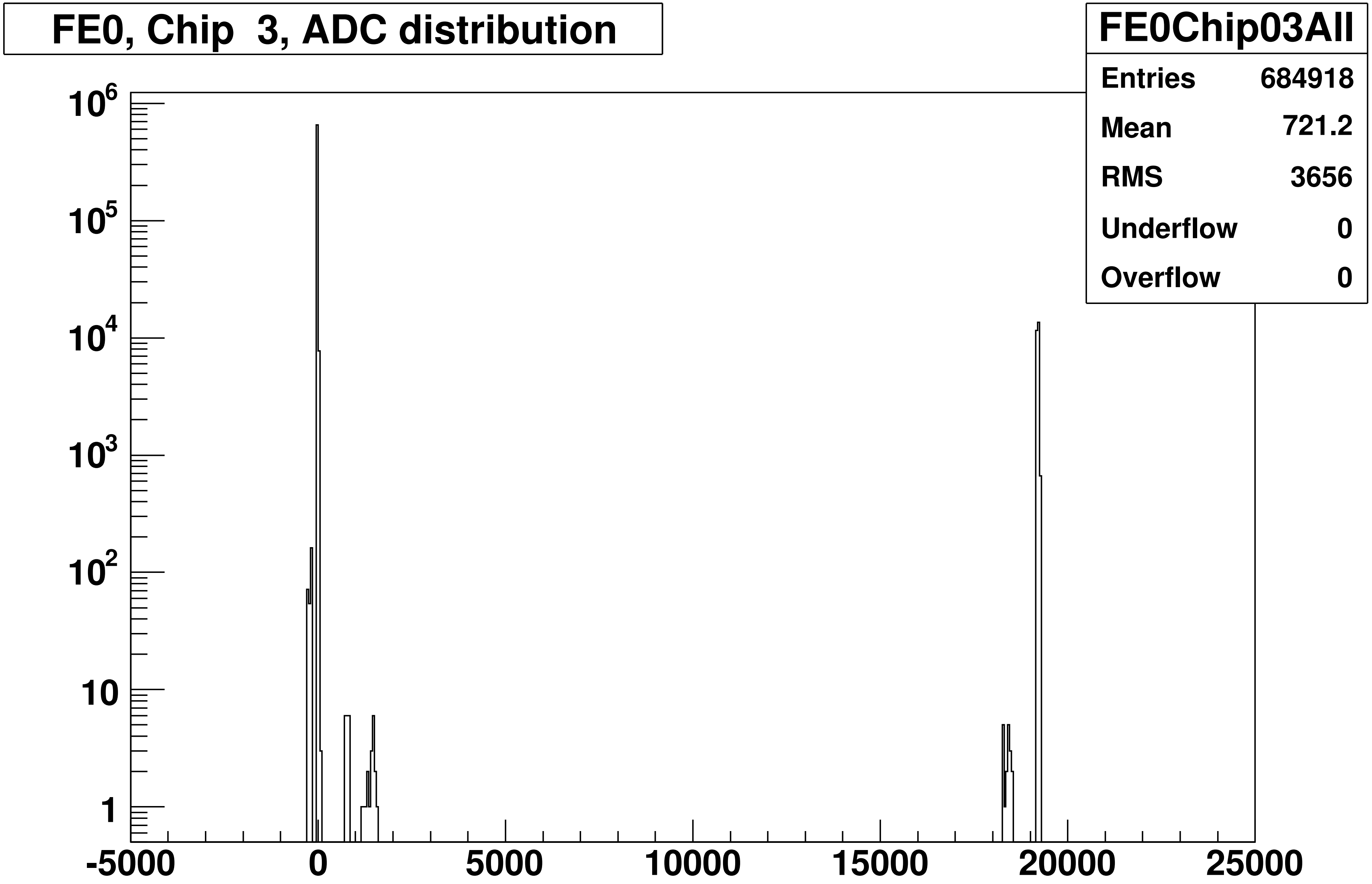


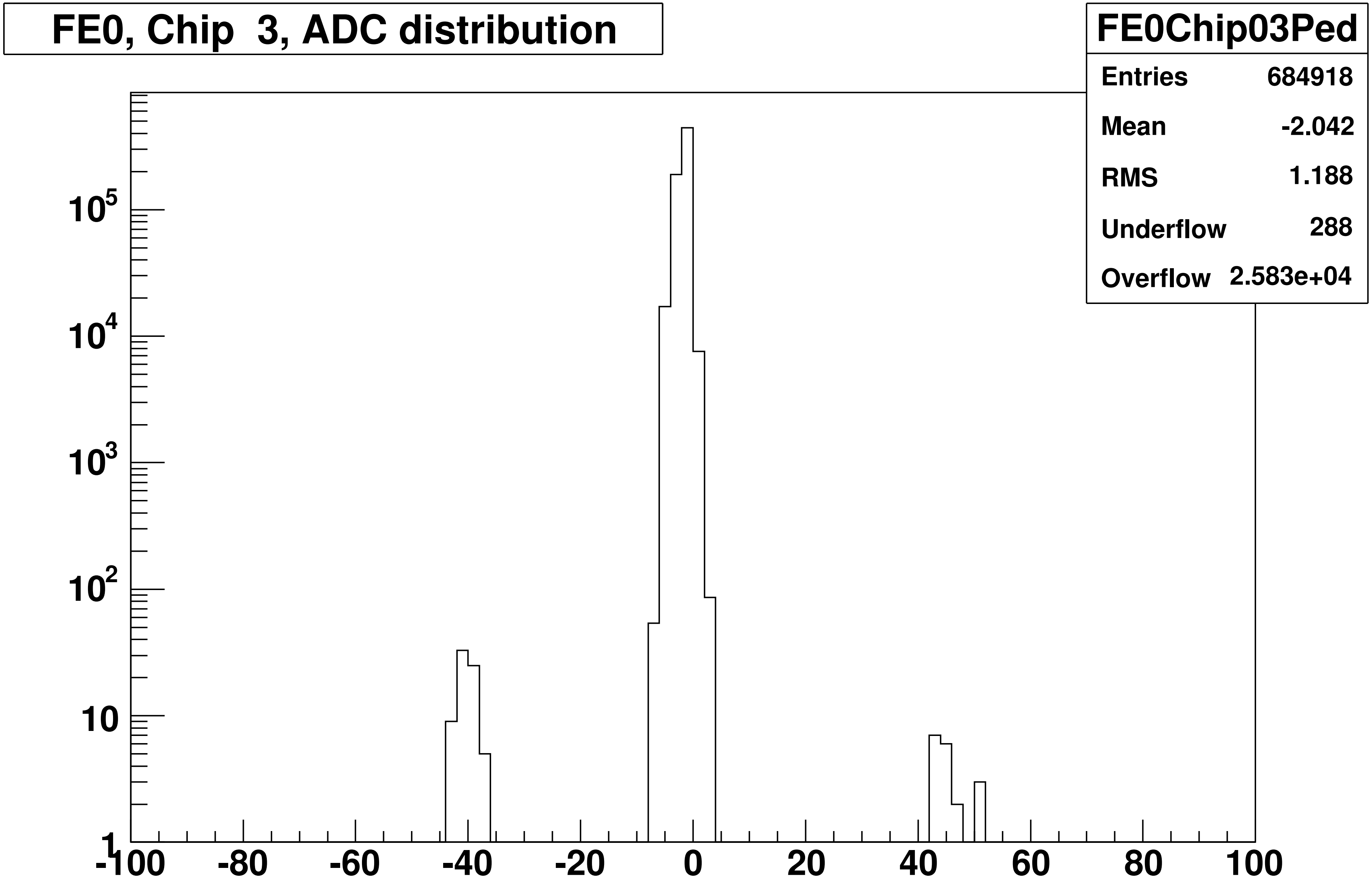
FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 684918
Mean 1.914e+04
RMS 13.32
Underflow 6.595e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

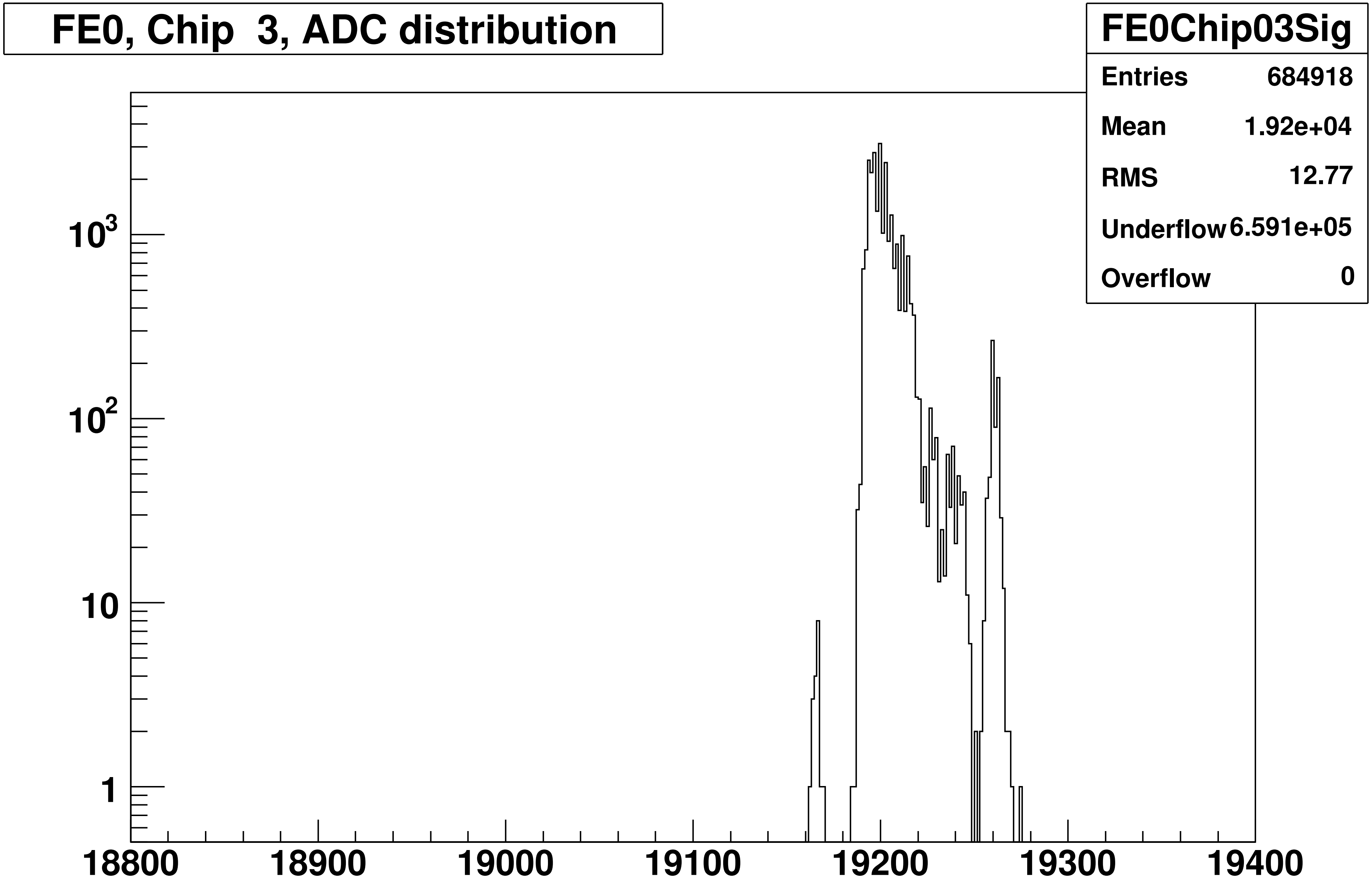

FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 684918
Mean 723.3
RMS 3650
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

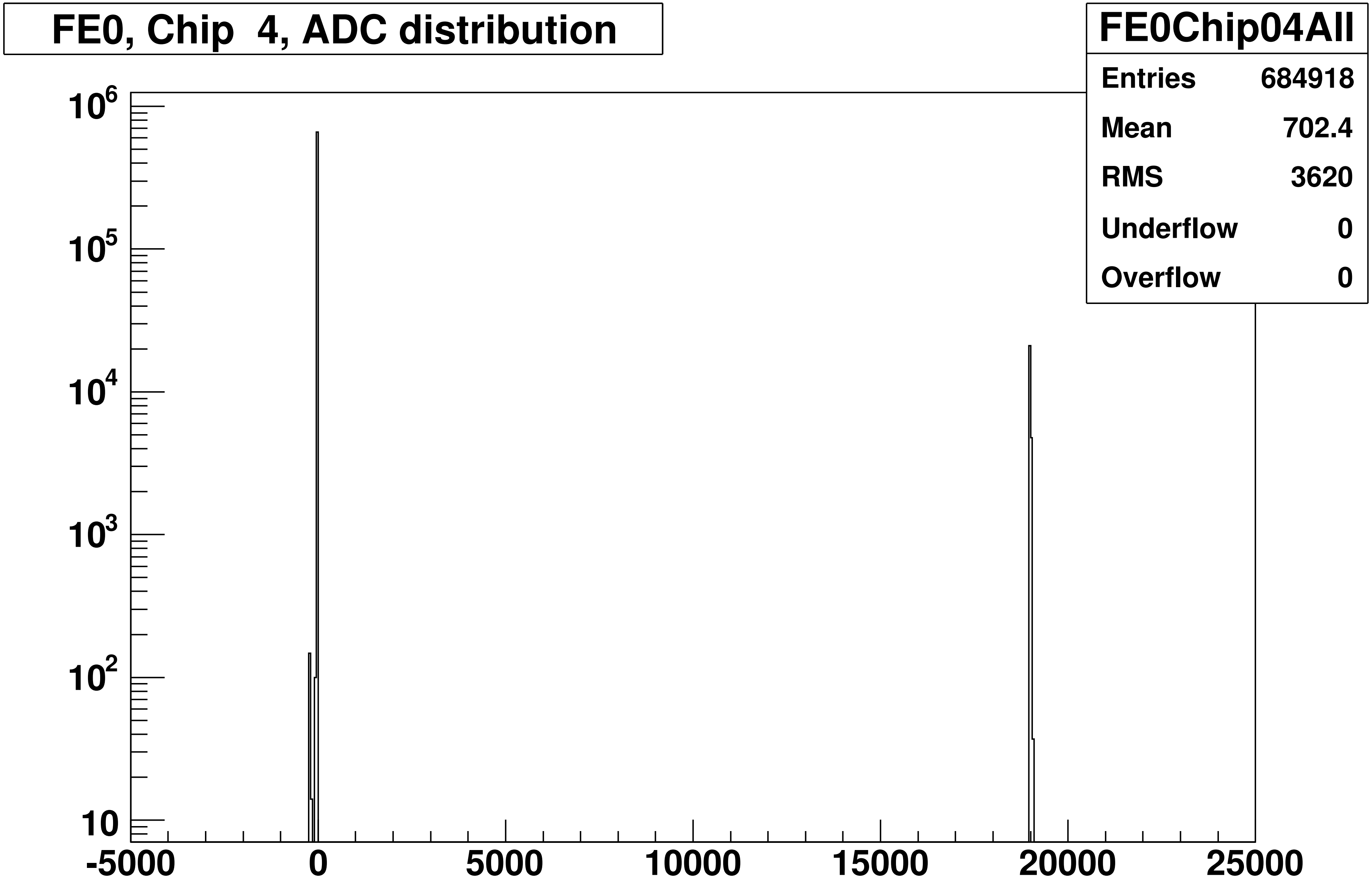

FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 684918
Mean -0.724
RMS 1.312
Underflow 4
Overflow 2.641e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

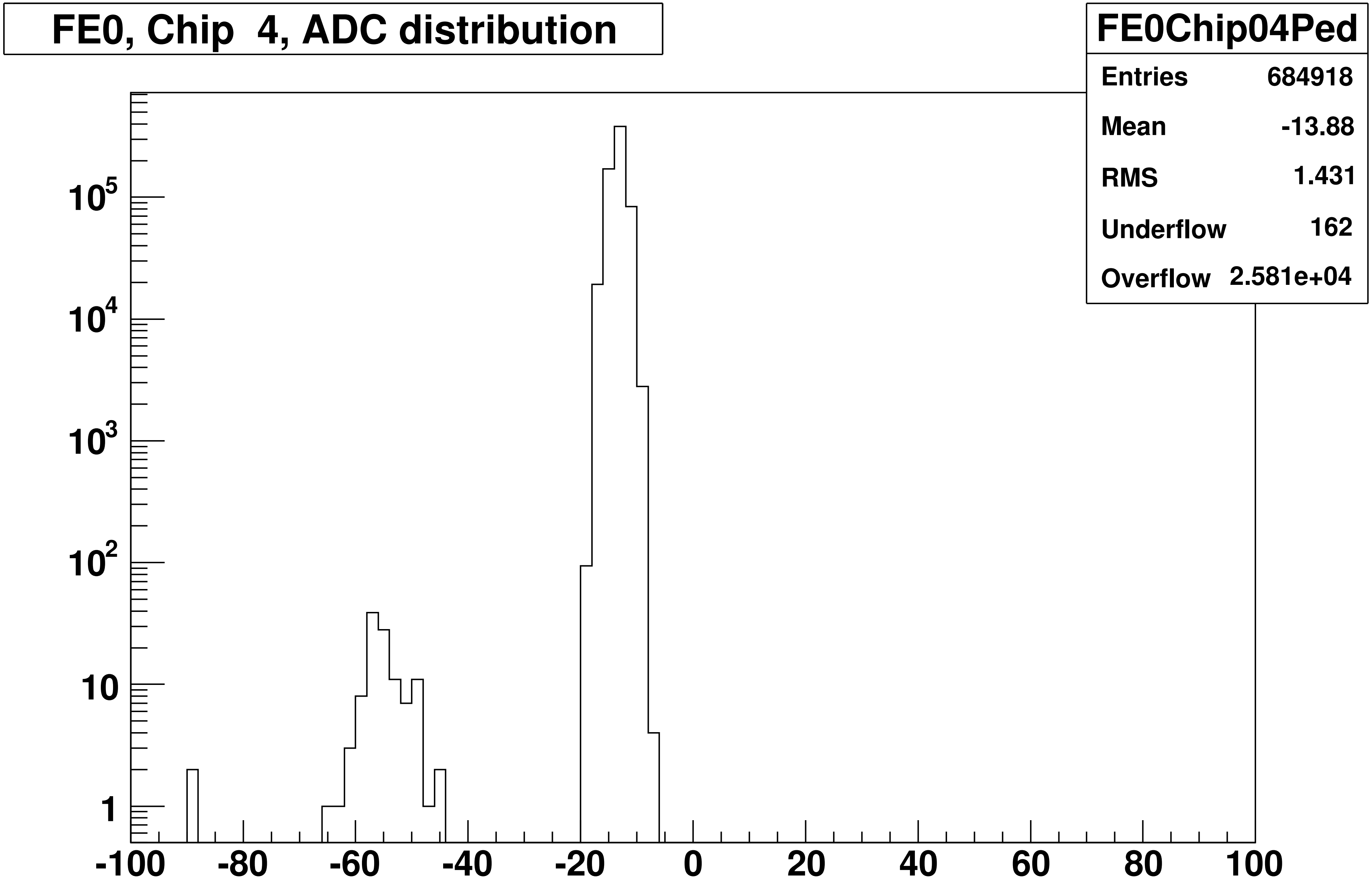

FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 684918
Mean 1.916e+04
RMS 13.65
Underflow 6.591e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

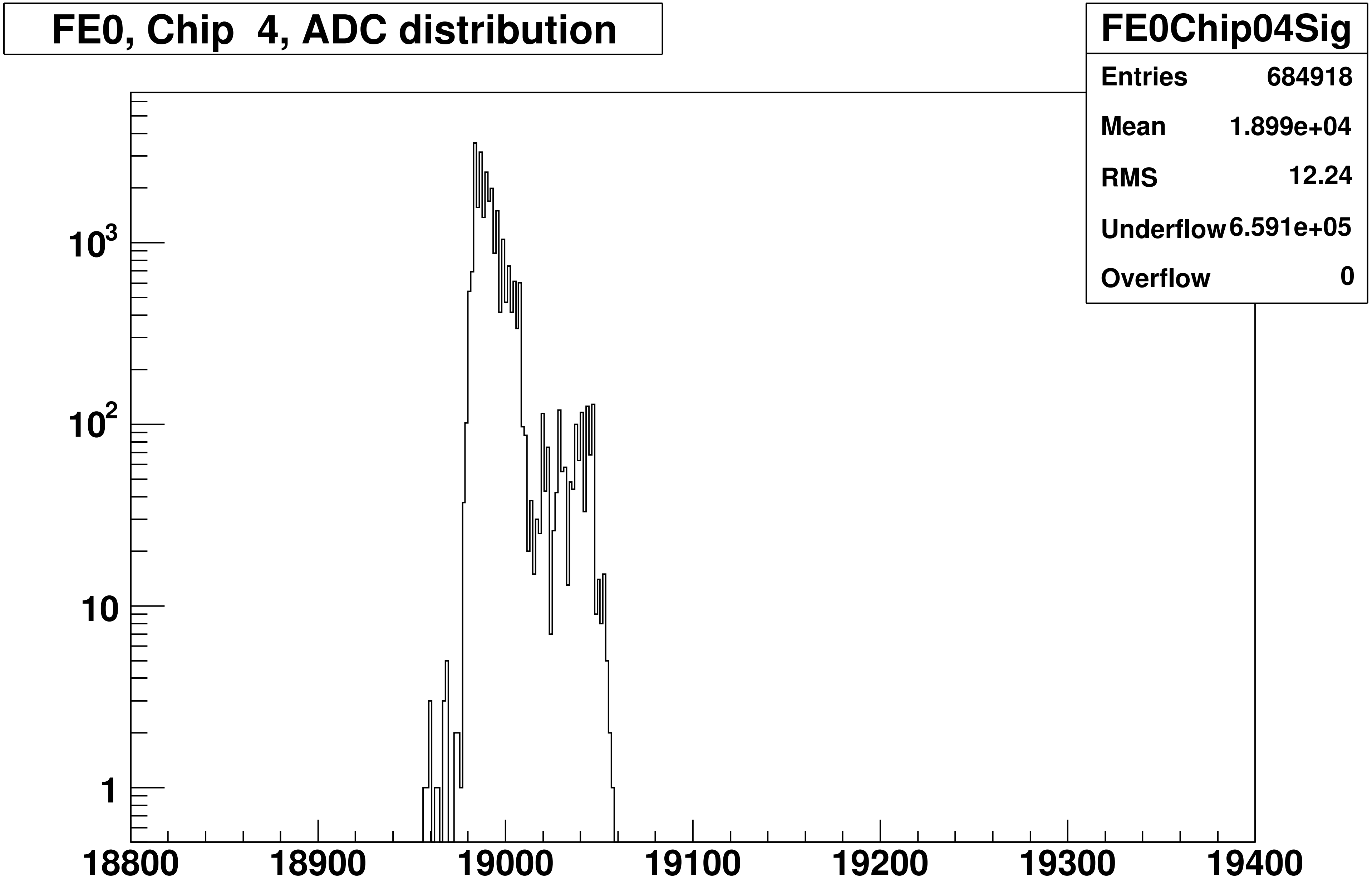

FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 684918
Mean 721.2
RMS 3656
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

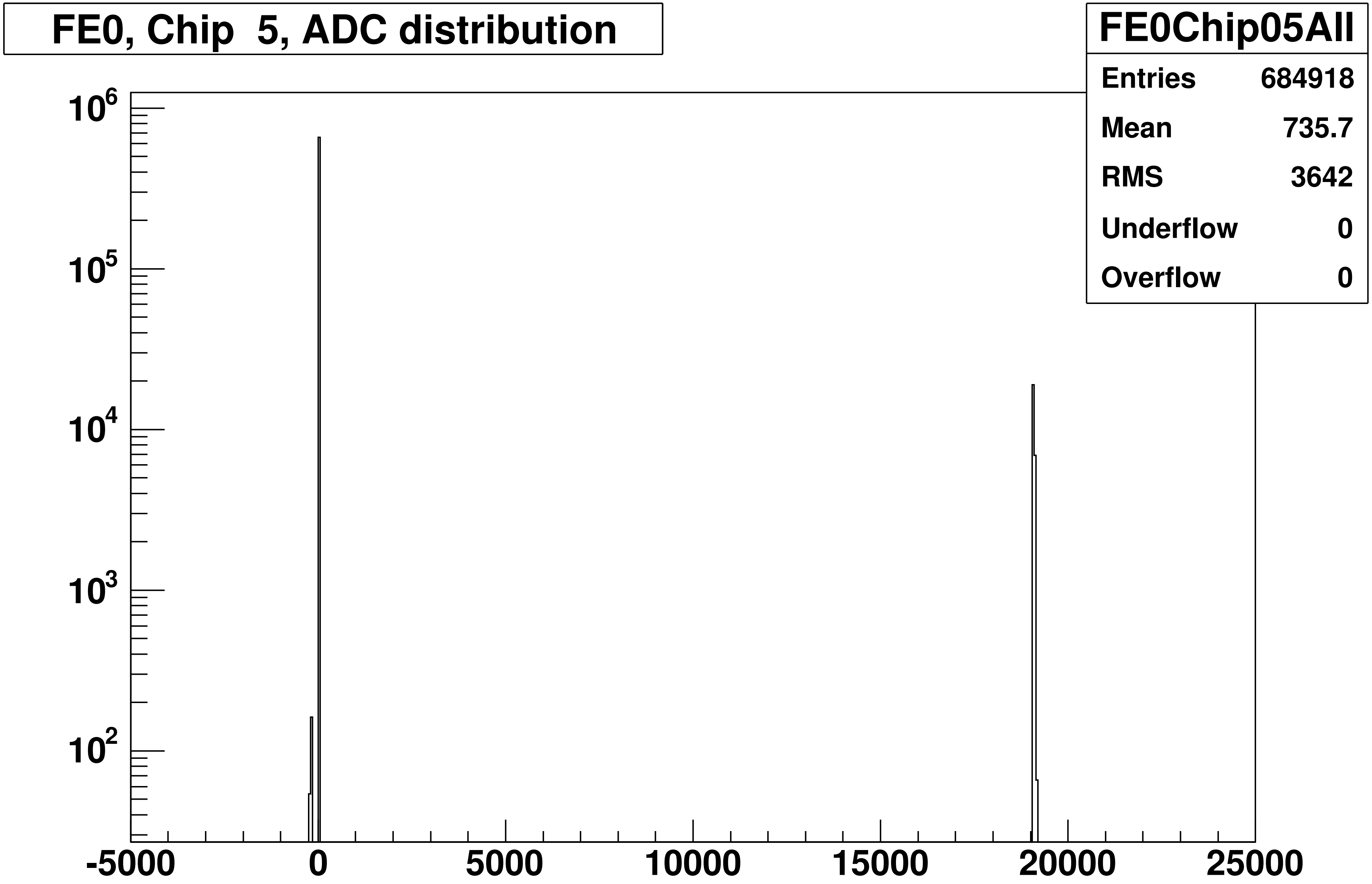

FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 684918
Mean -2.042
RMS 1.188
Underflow 288
Overflow 2.583e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

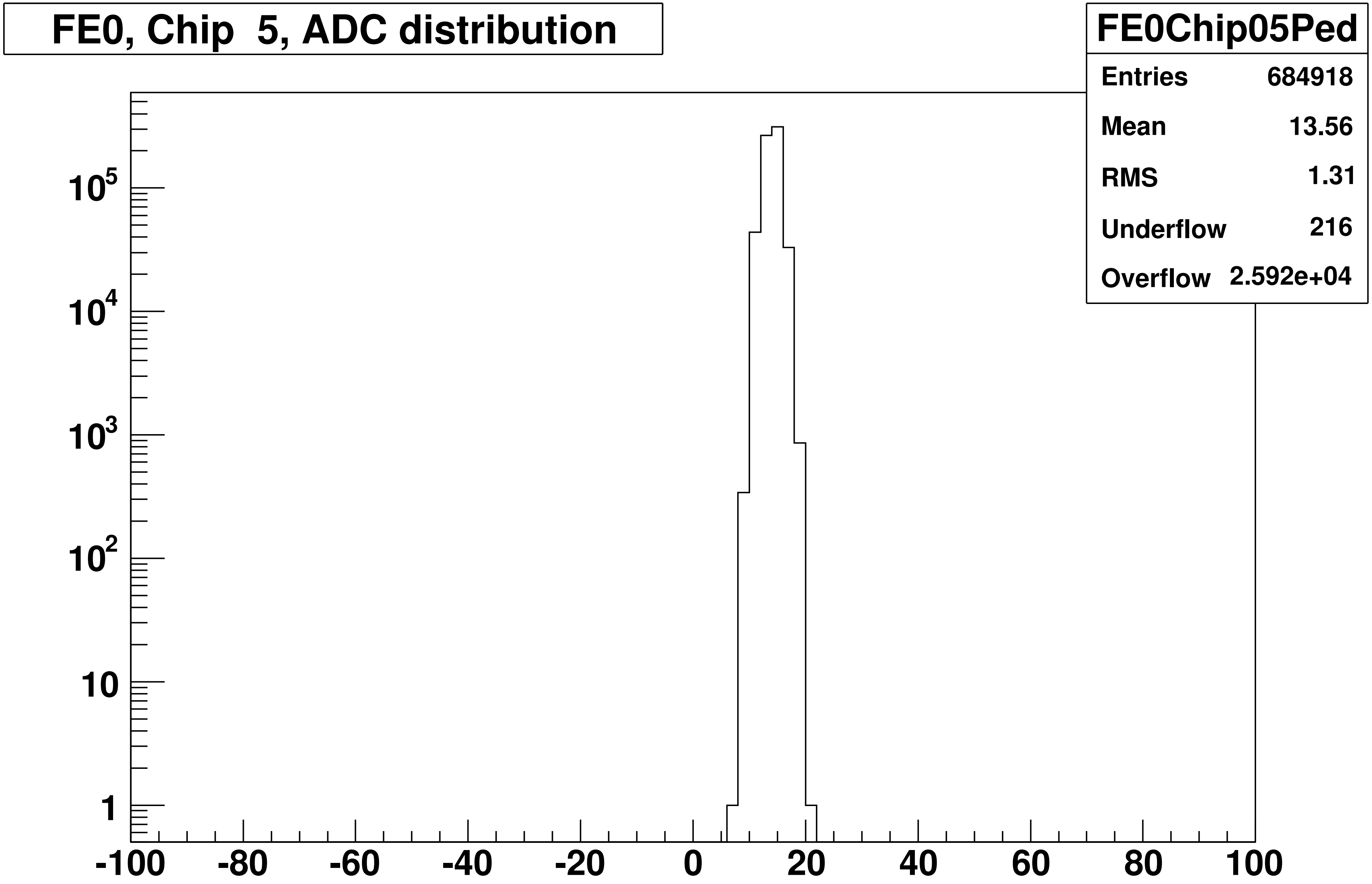

FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 684918
Mean 1.92e+04
RMS 12.77
Underflow 6.591e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 684918
Mean 702.4
RMS 3620
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

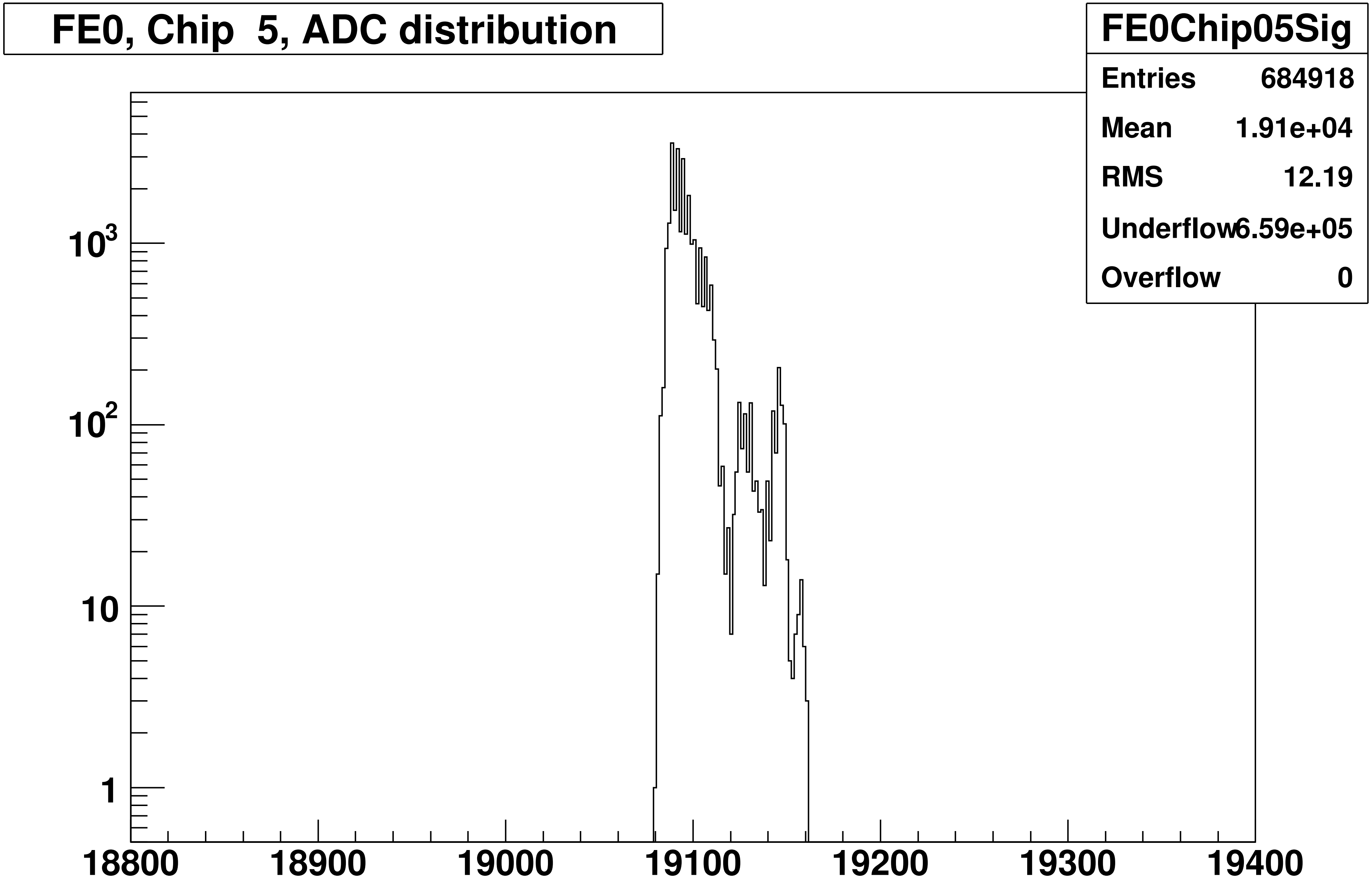

FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 684918
Mean -13.88
RMS 1.431
Underflow 162
Overflow 2.581e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 684918
Mean 1.899e+04
RMS 12.24
Underflow 6.591e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

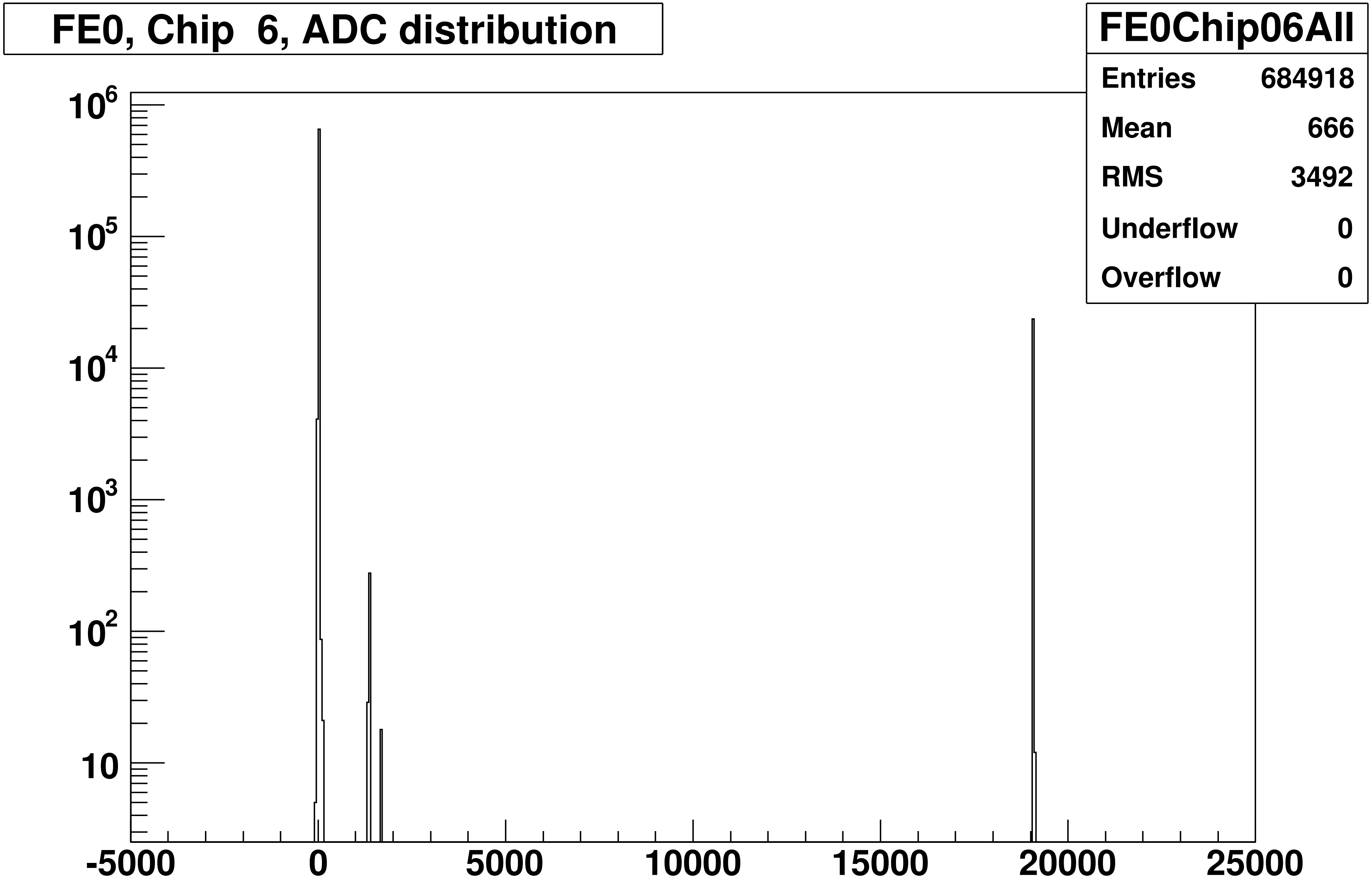

FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 684918
Mean 735.7
RMS 3642
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



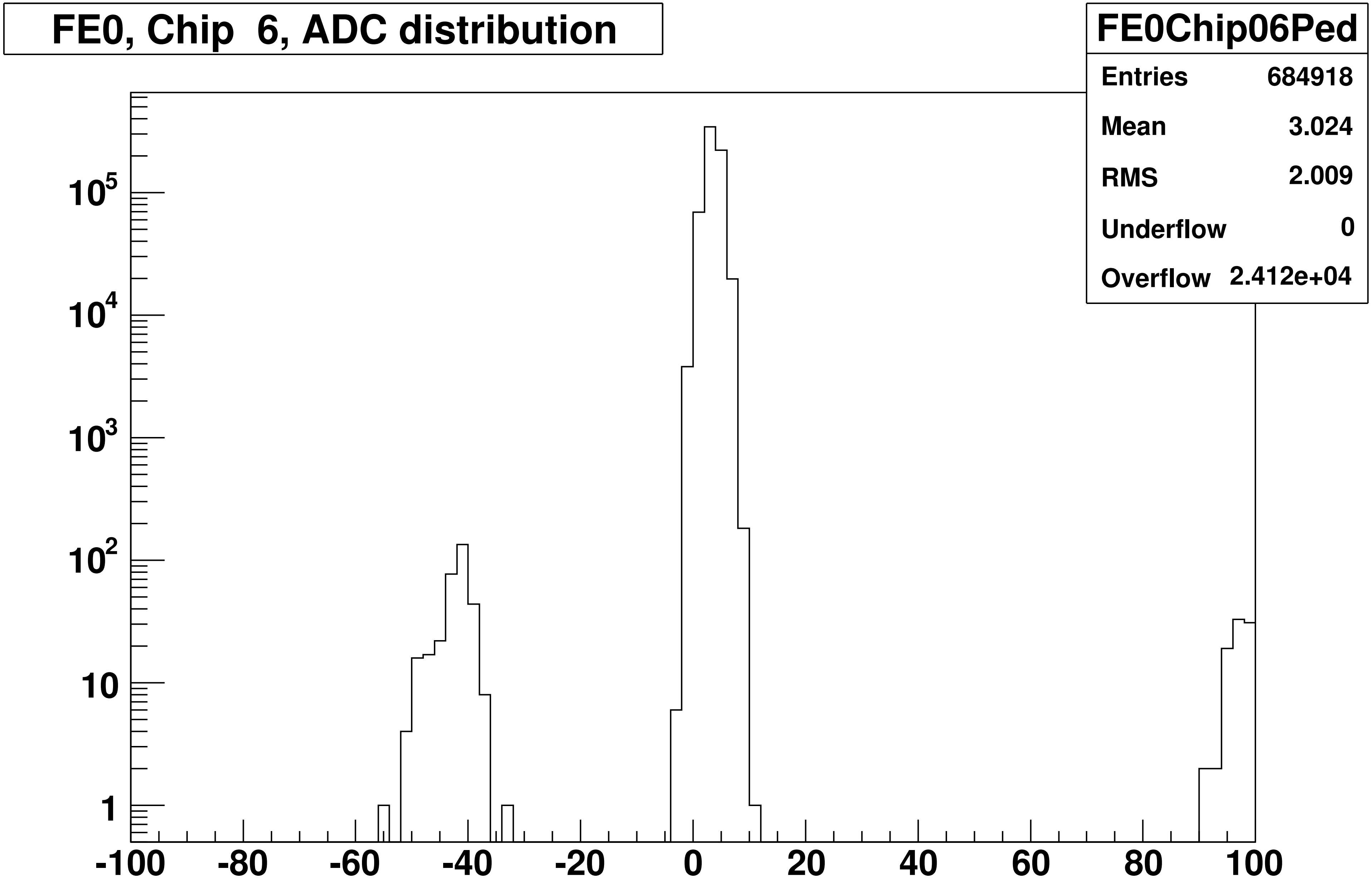
FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 684918
Mean 13.56
RMS 1.31
Underflow 216
Overflow 2.592e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



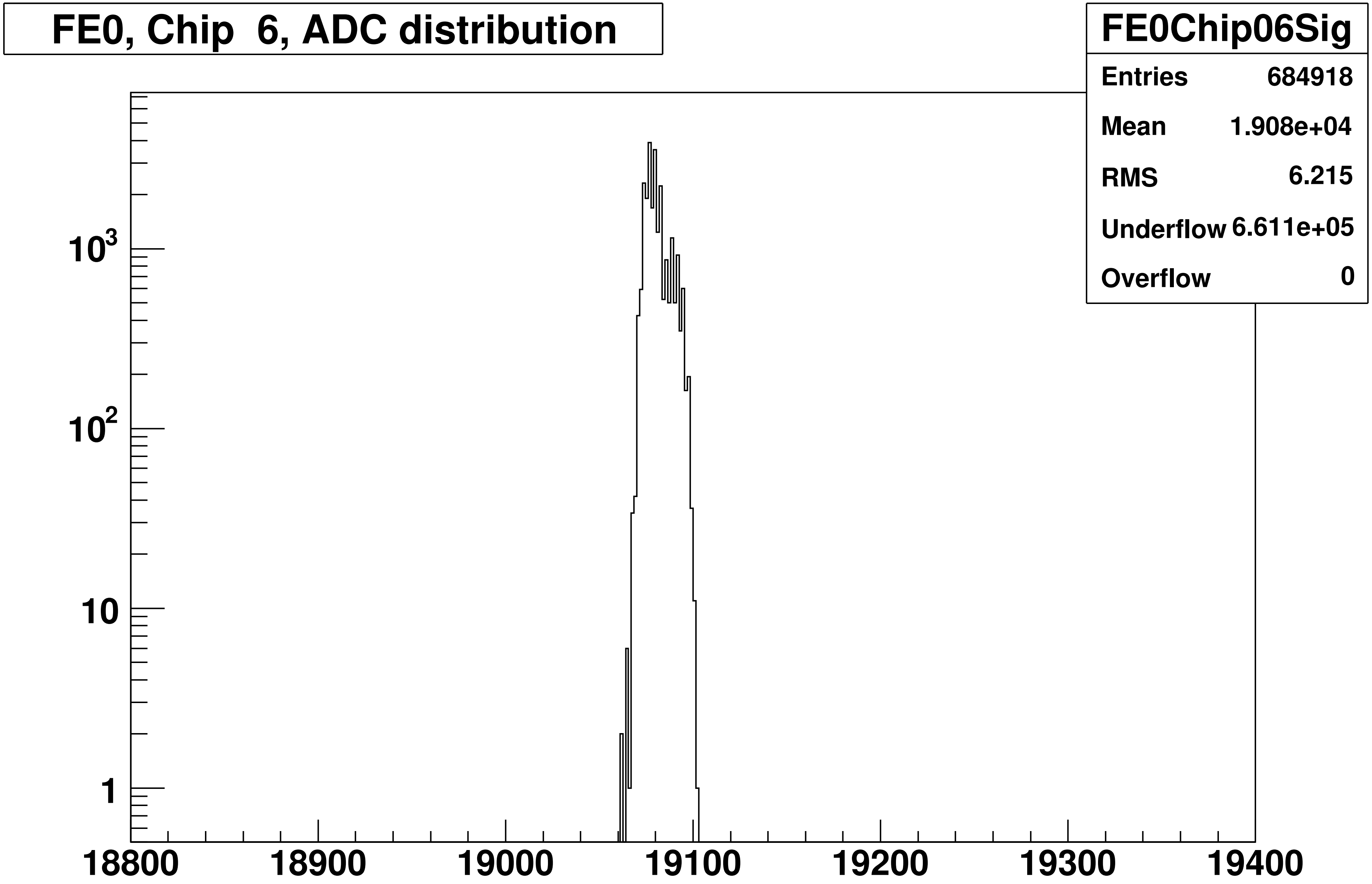
FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 684918
Mean 1.91e+04
RMS 12.19
Underflow 6.59e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

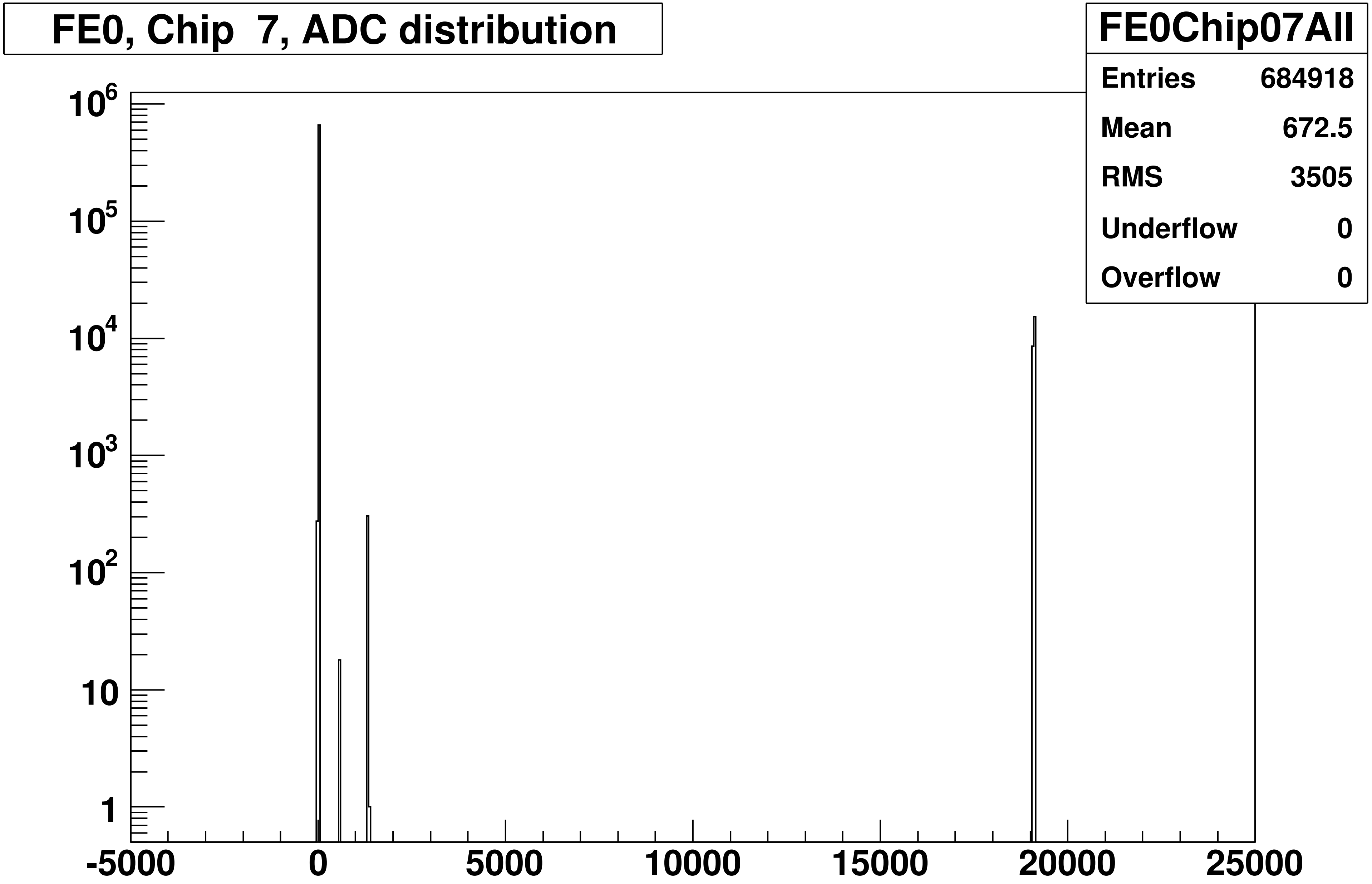


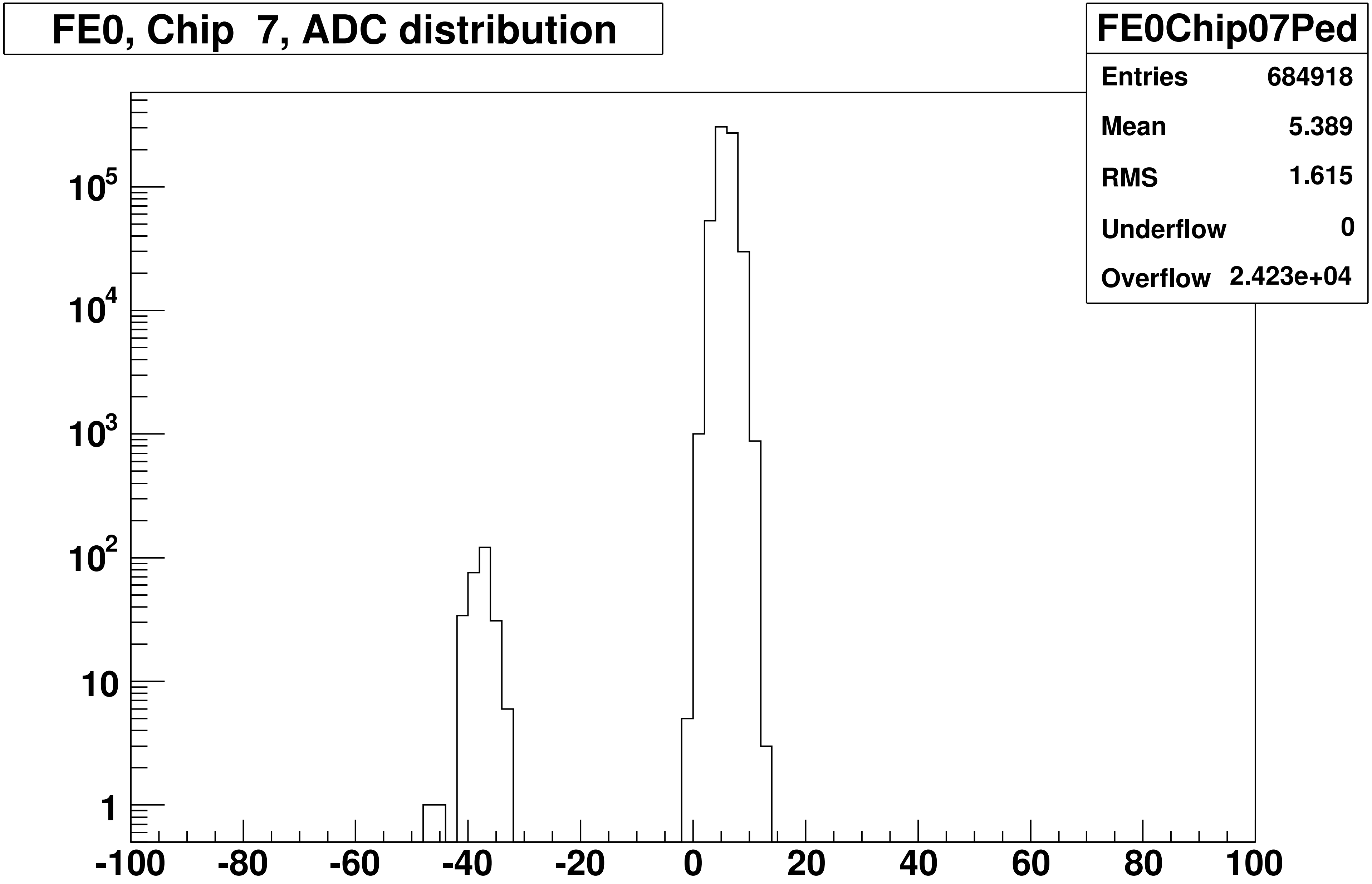
FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 684918
Mean 666
RMS 3492
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



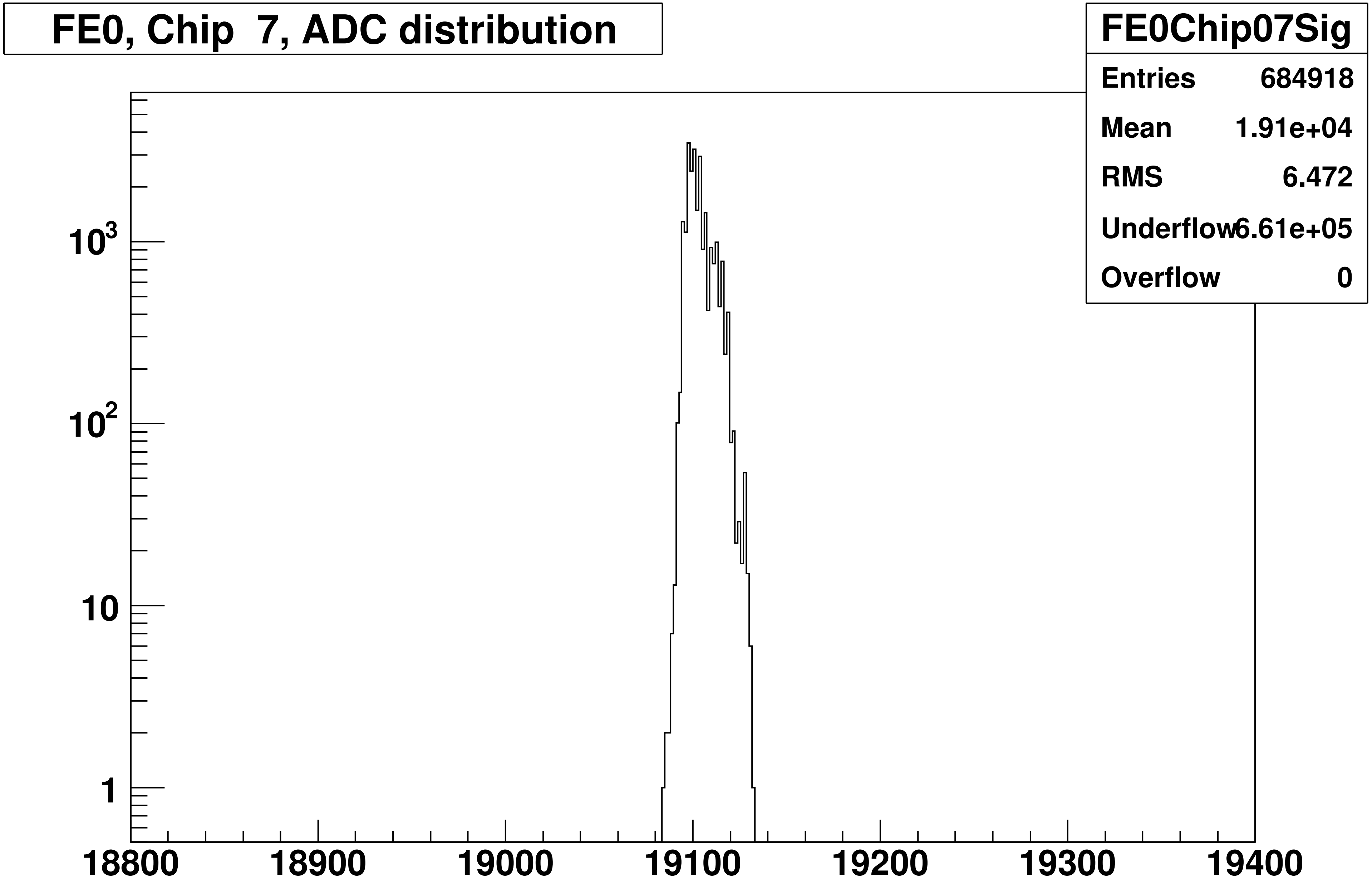
FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 684918
Mean 3.024
RMS 2.009
Underflow 0
Overflow 2.412e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

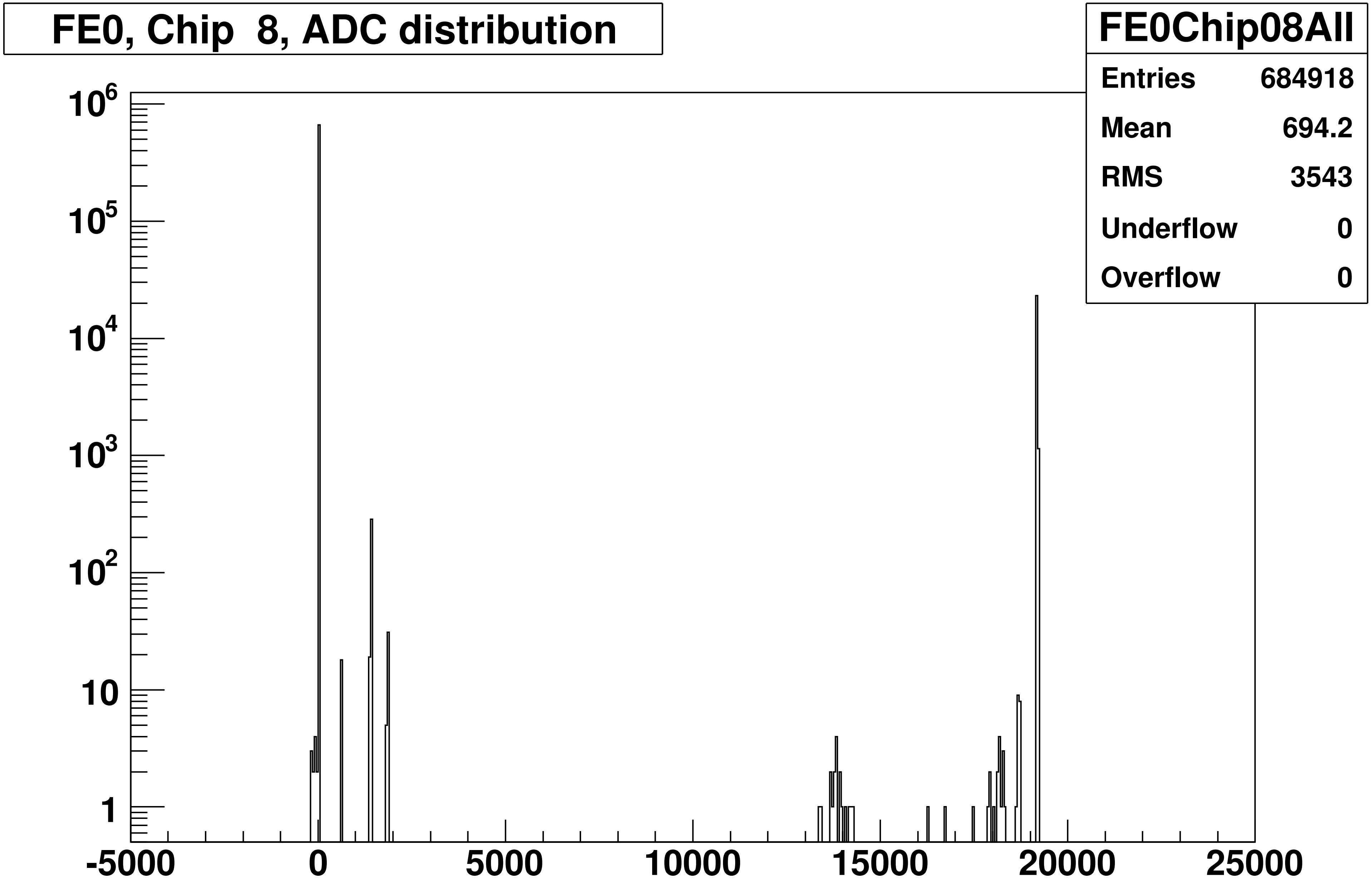

FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 684918
Mean 1.908e+04
RMS 6.215
Underflow 6.611e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

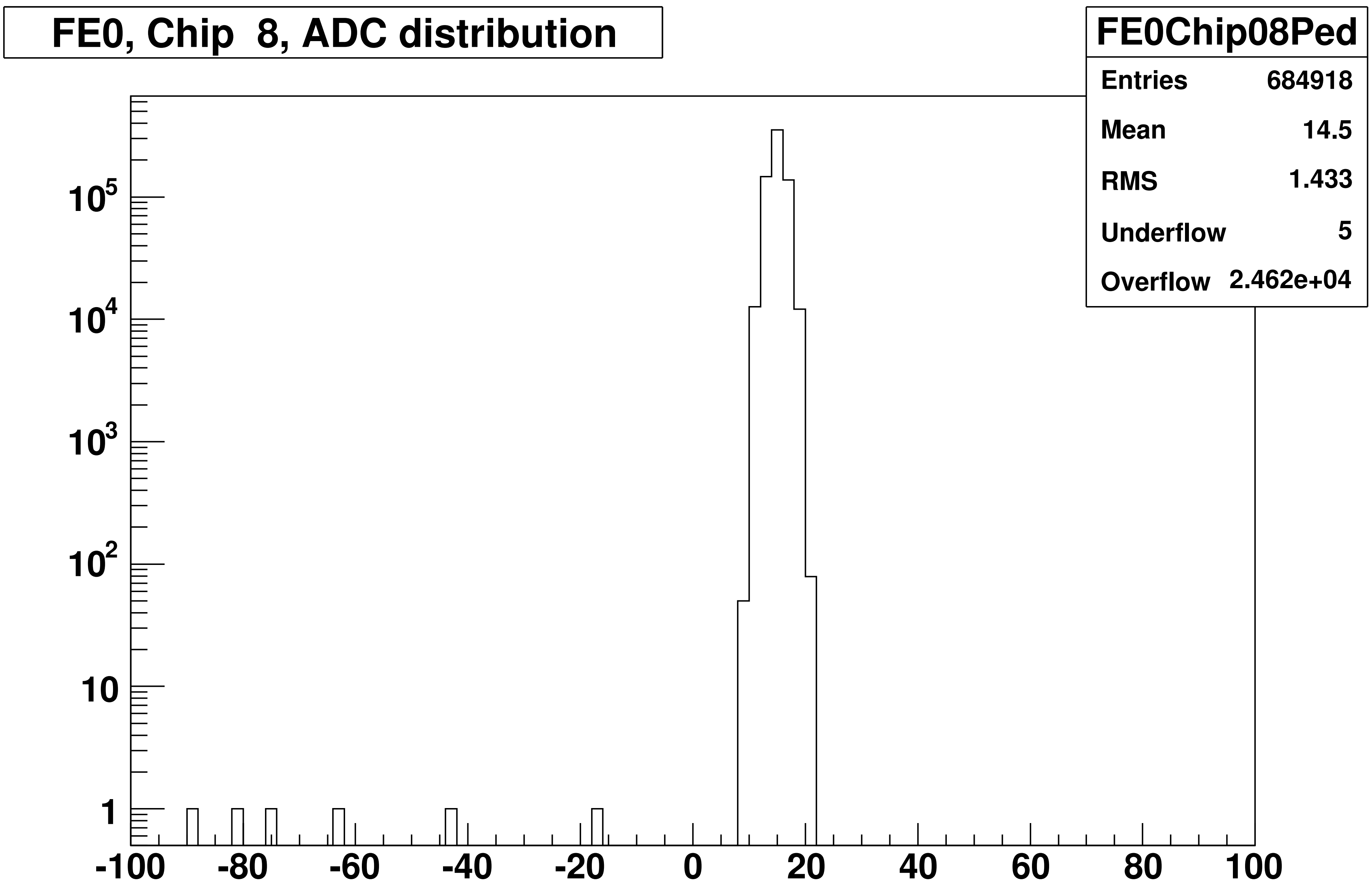

FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 684918
Mean 672.5
RMS 3505
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

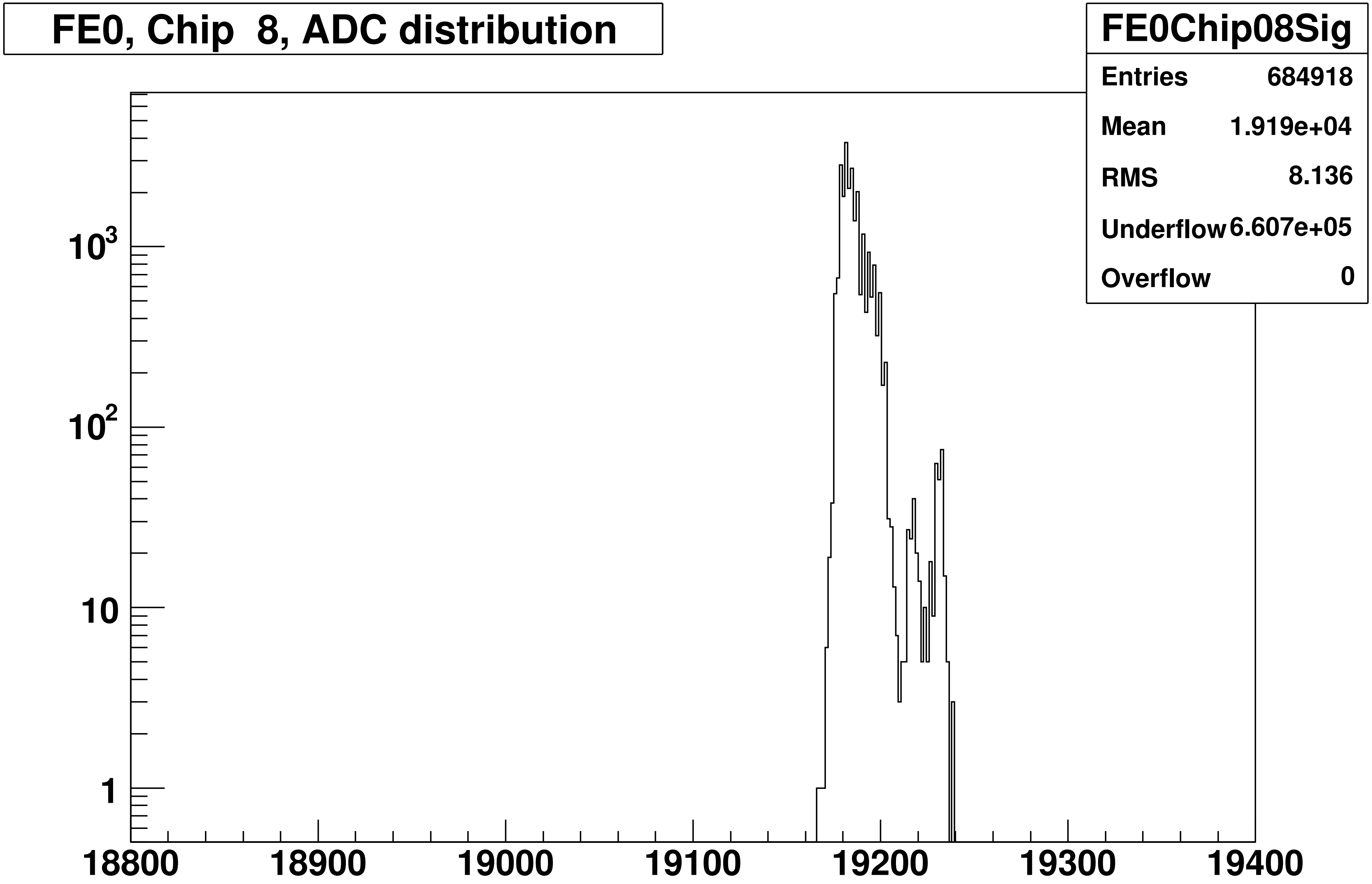


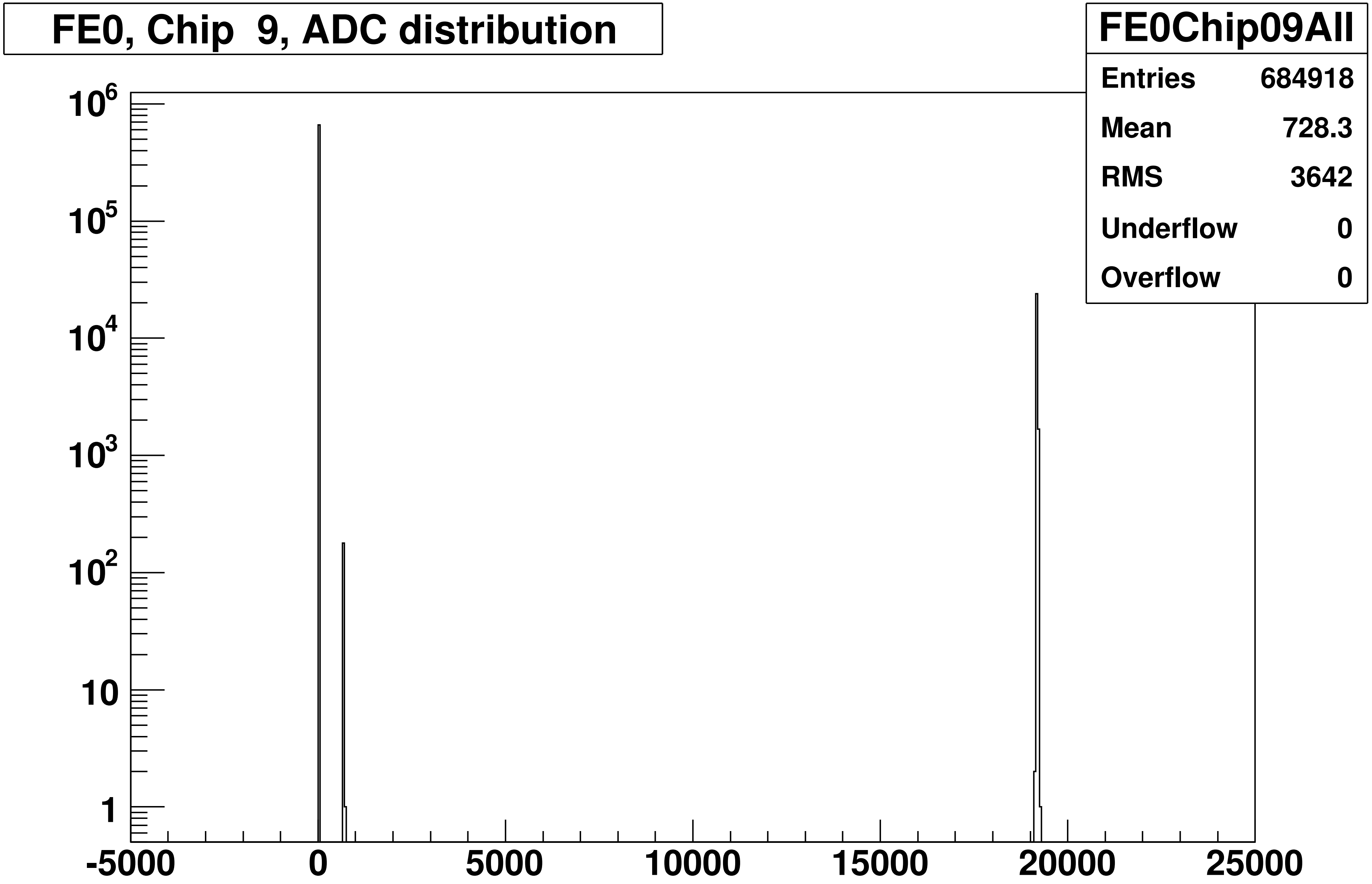
FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 684918
Mean 5.389
RMS 1.615
Underflow 0
Overflow 2.423e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 684918
Mean 1.91e+04
RMS 6.472
Underflow 6.61e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

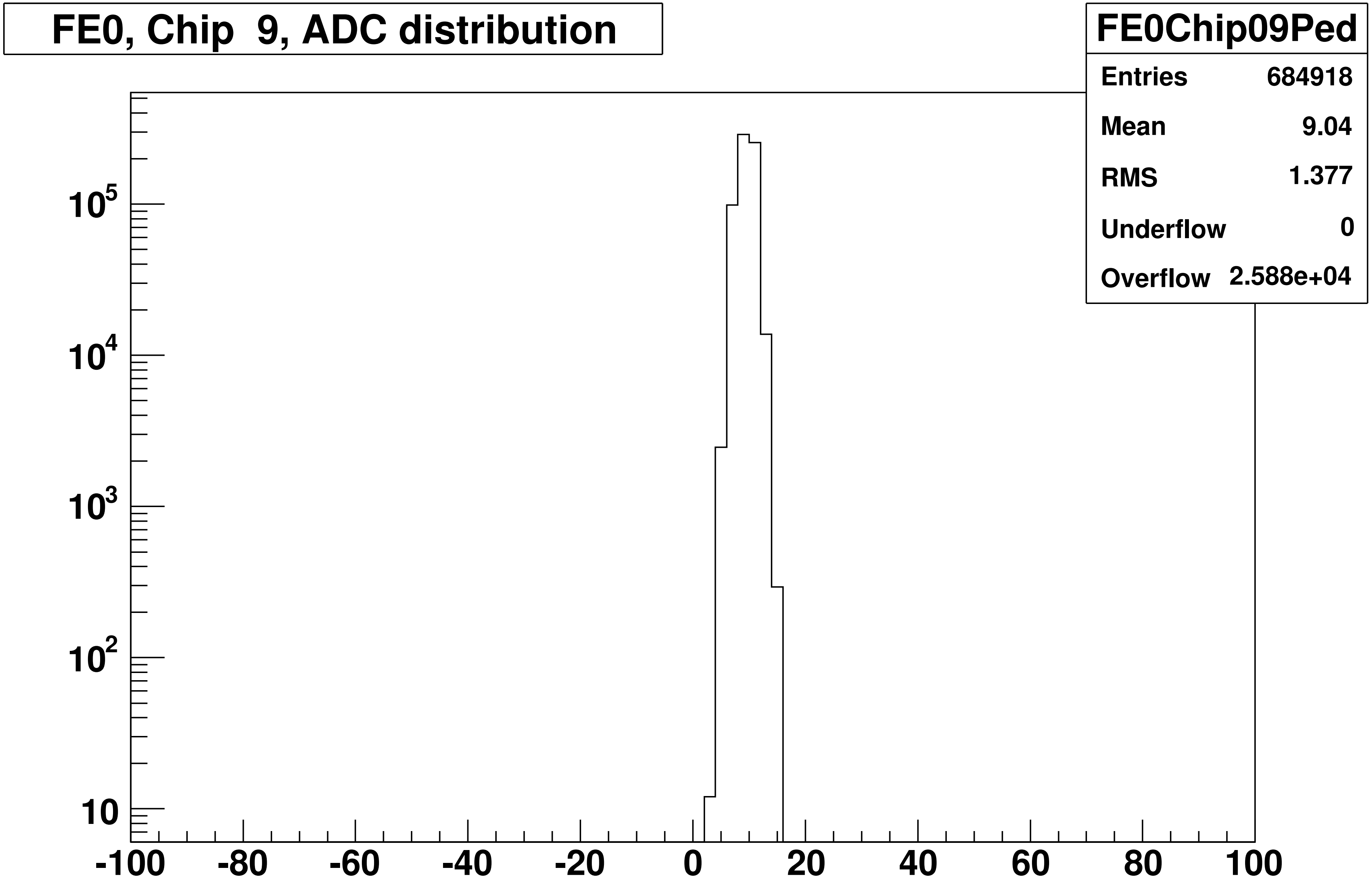

FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 684918
Mean 694.2
RMS 3543
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

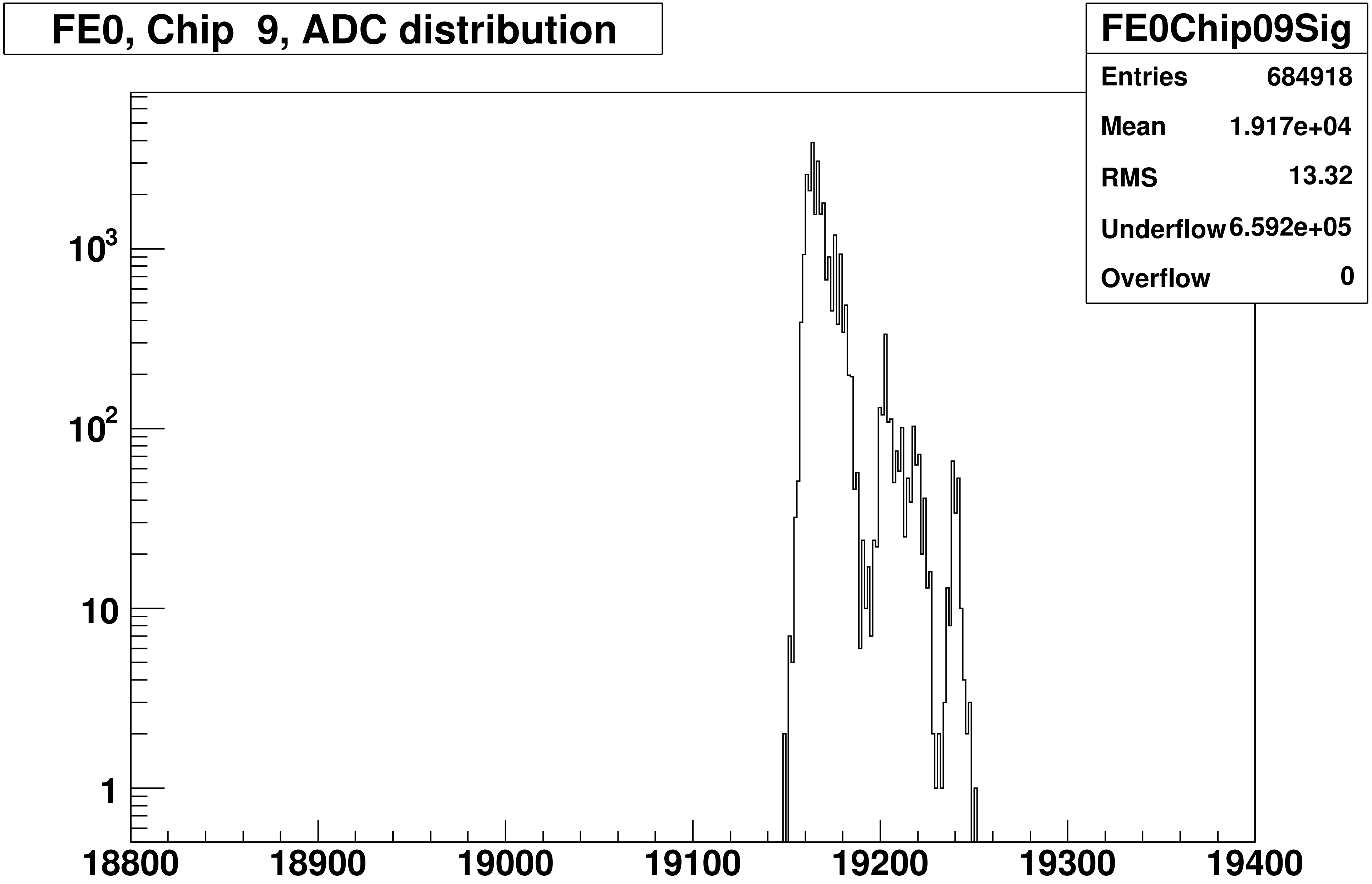

FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 684918
Mean 14.5
RMS 1.433
Underflow 5
Overflow 2.462e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 684918
Mean 1.919e+04
RMS 8.136
Underflow 6.607e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

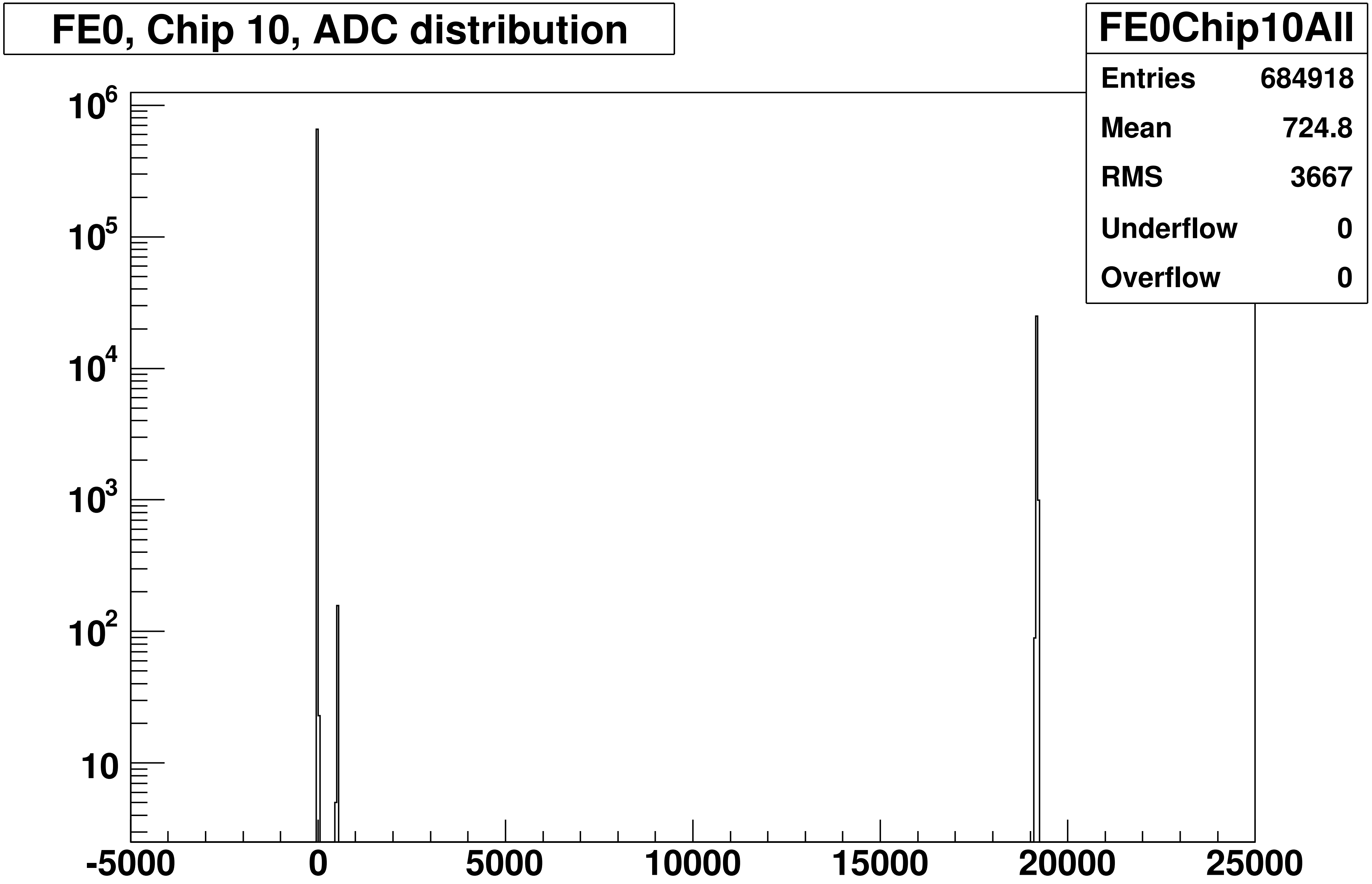


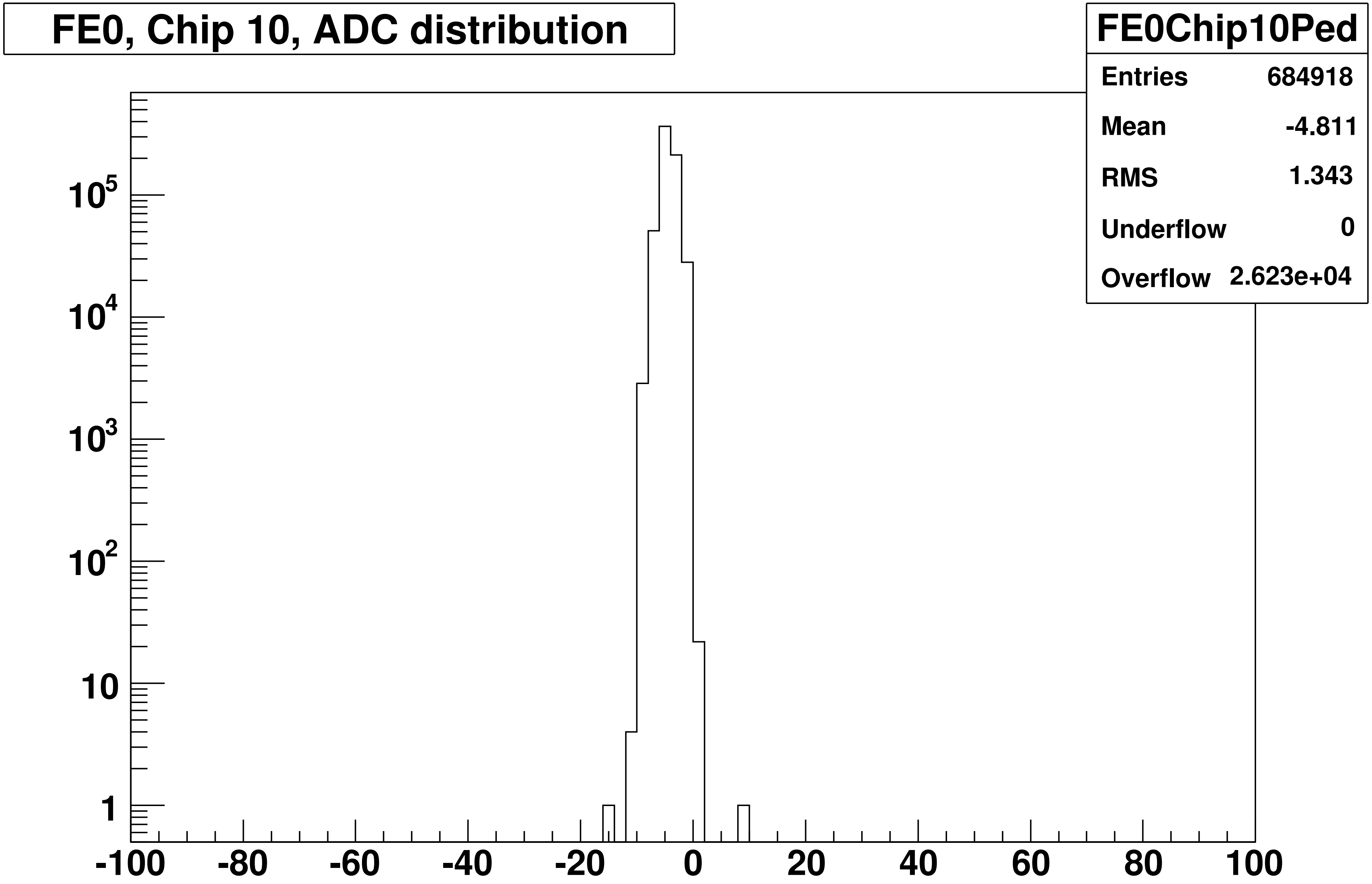
FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 684918
Mean 728.3
RMS 3642
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 684918
Mean 9.04
RMS 1.377
Underflow 0
Overflow 2.588e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

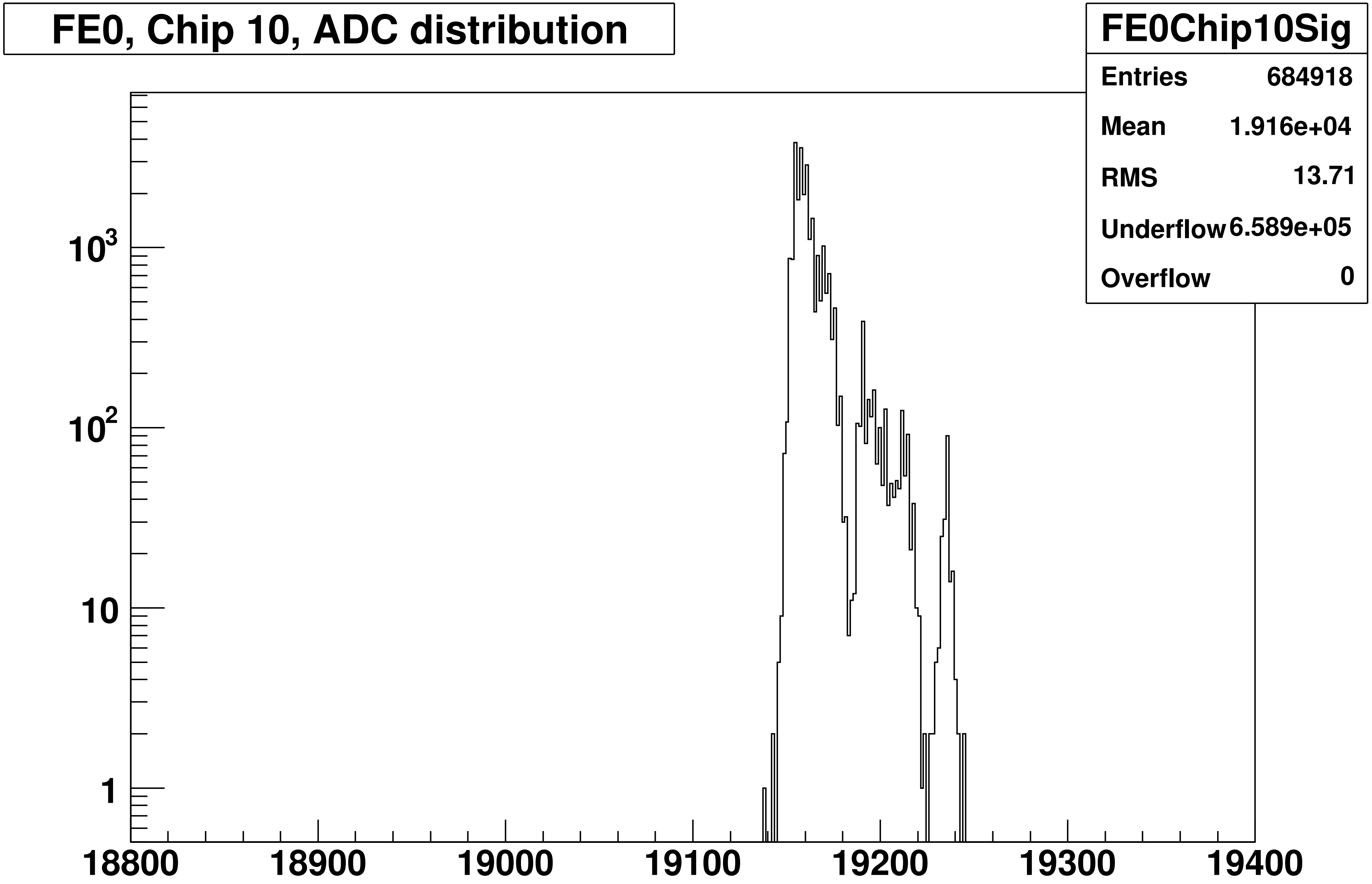


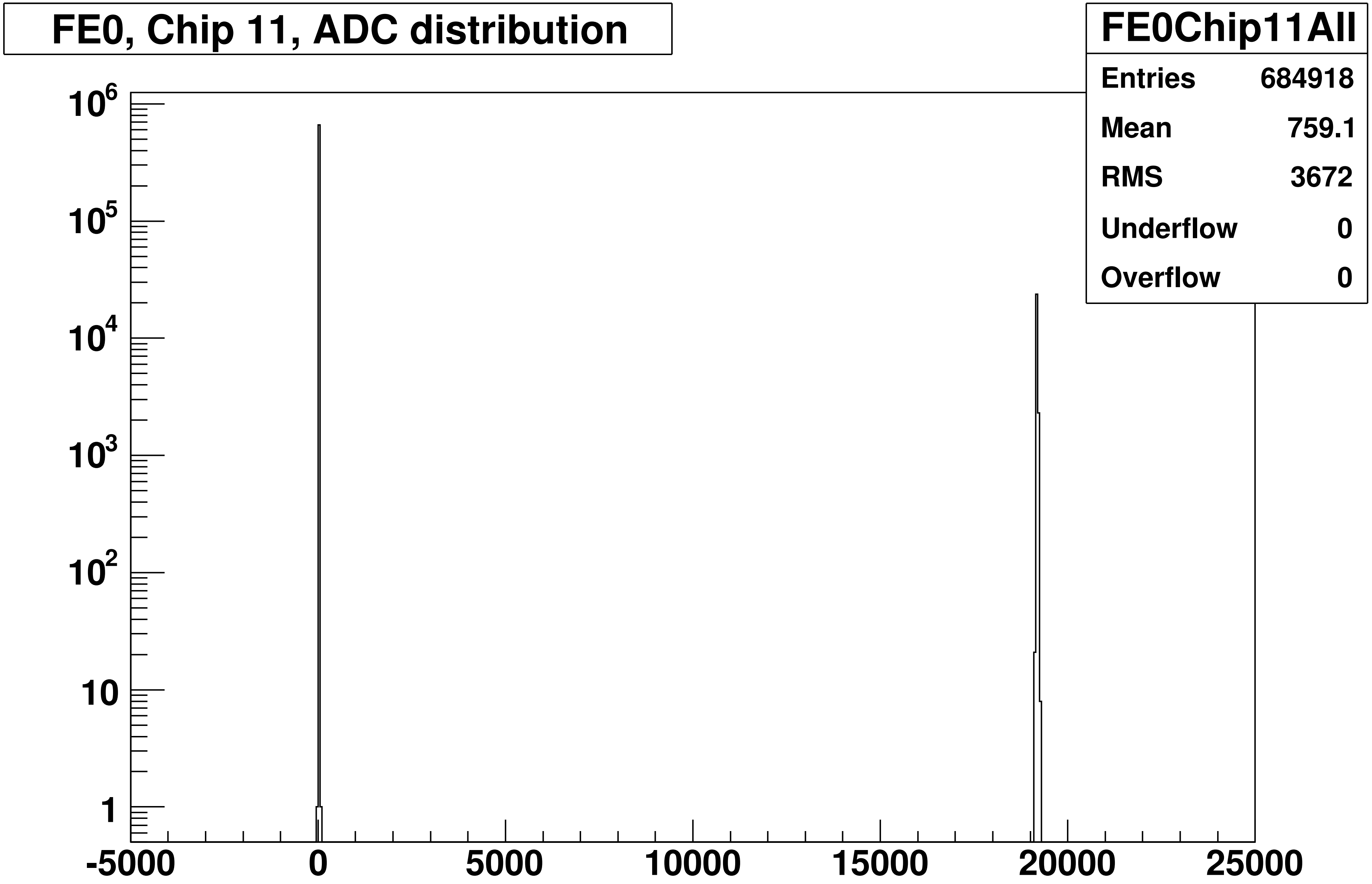
FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 684918
Mean 1.917e+04
RMS 13.32
Underflow 6.592e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

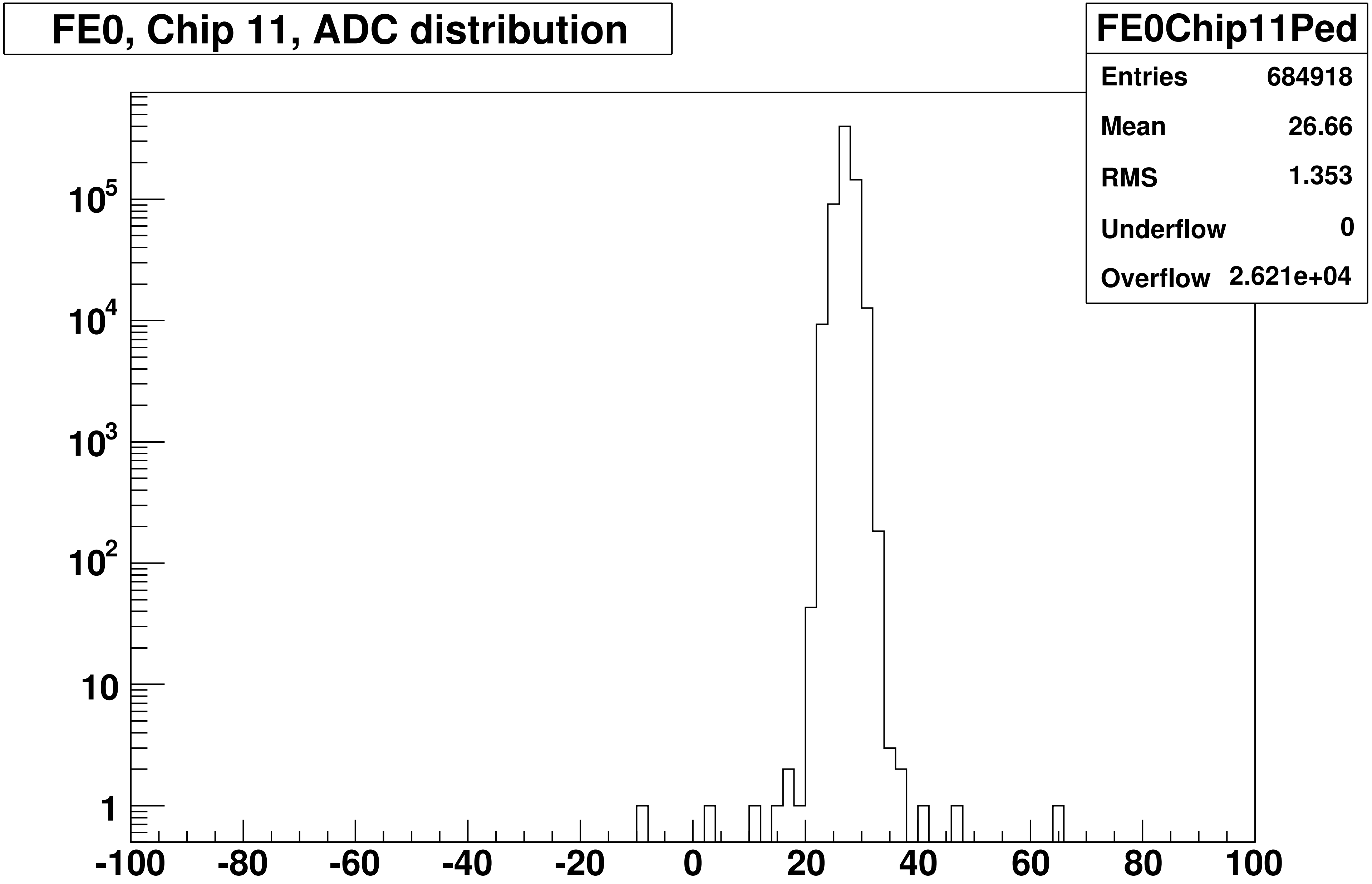

FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 684918
Mean 724.8
RMS 3667
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

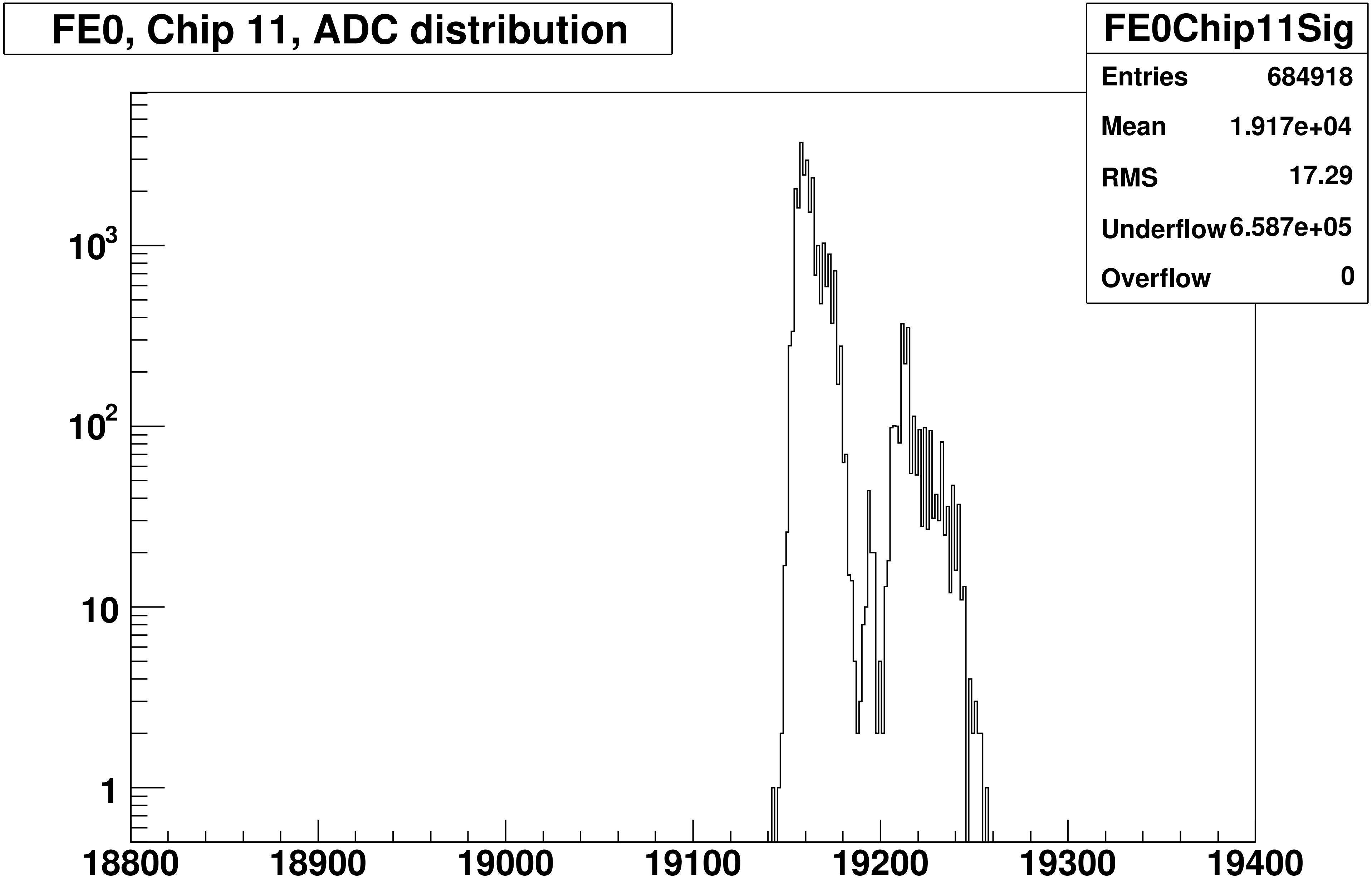


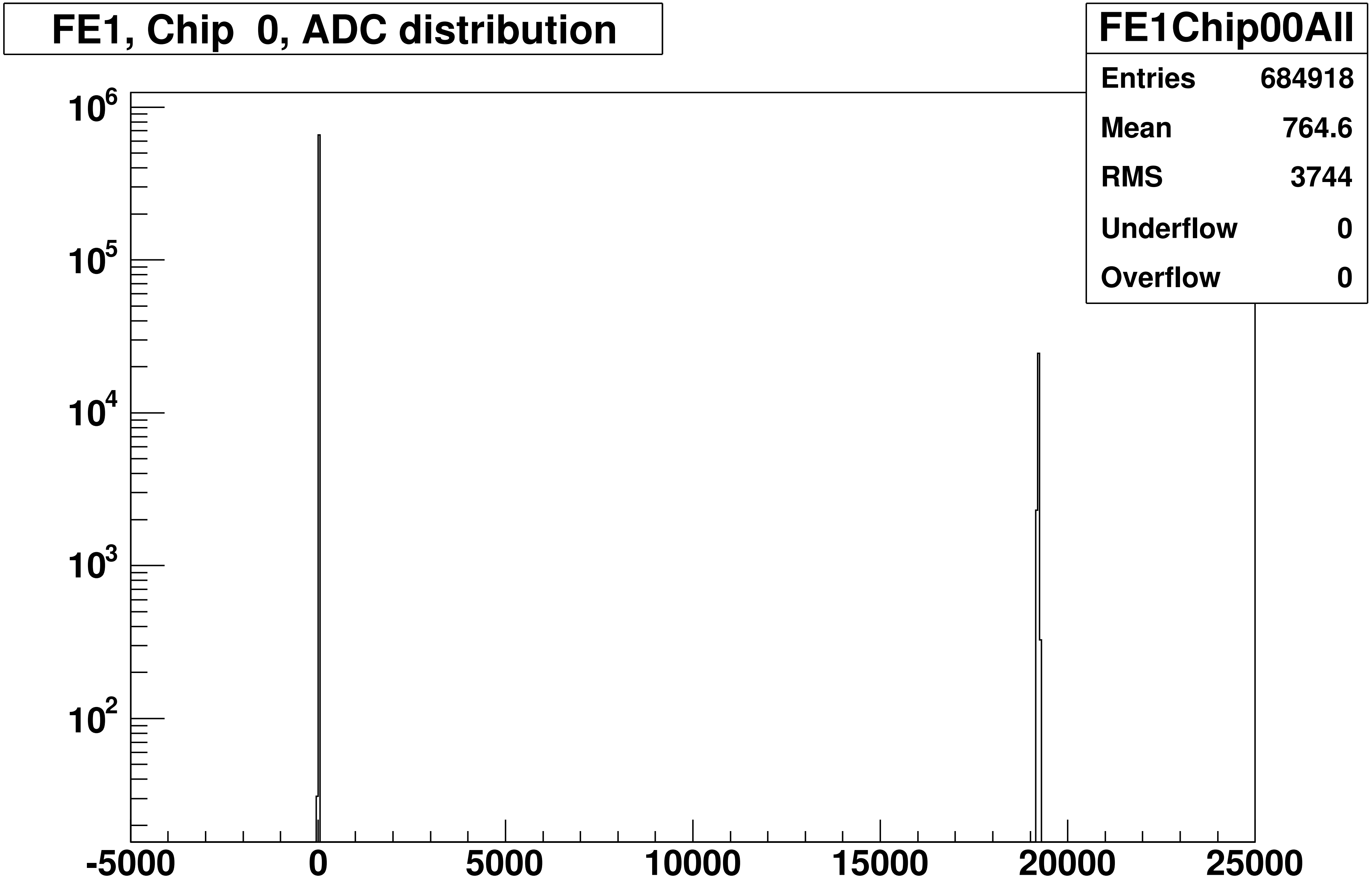
FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 684918
Mean -4.811
RMS 1.343
Underflow 0
Overflow 2.623e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

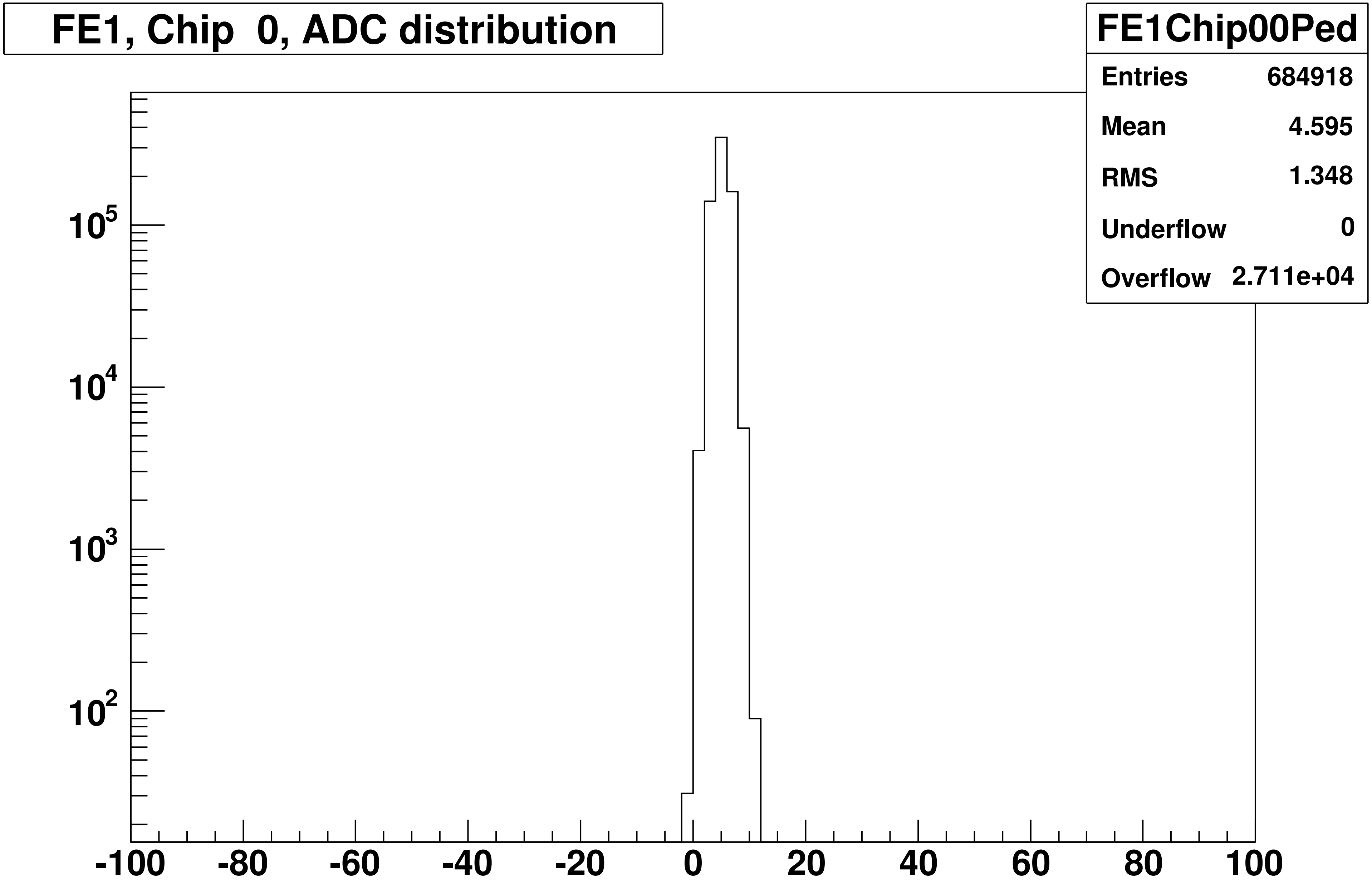

FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 684918
Mean 1.916e+04
RMS 13.71
Underflow 6.589e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

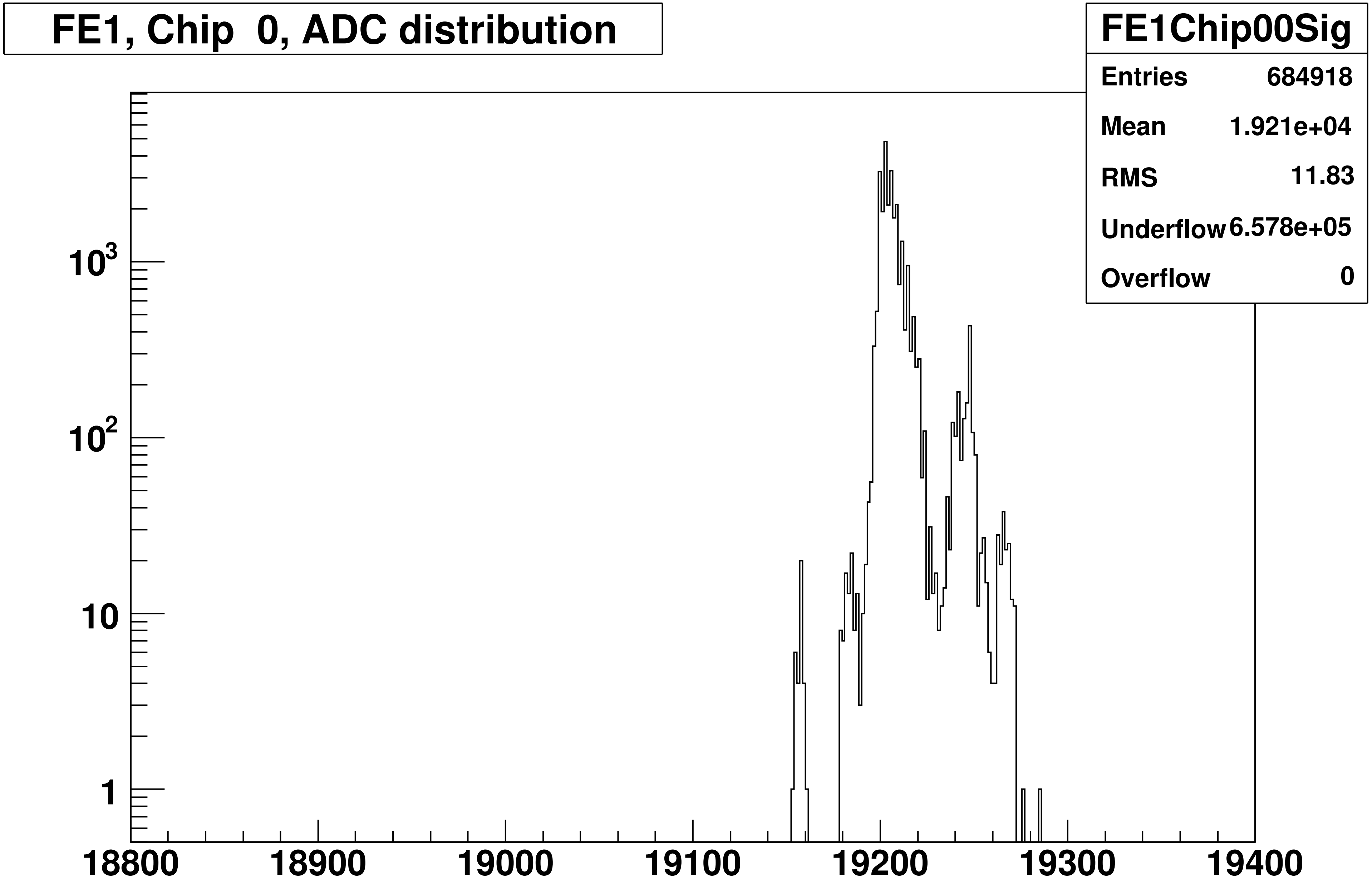

FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 684918
Mean 759.1
RMS 3672
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

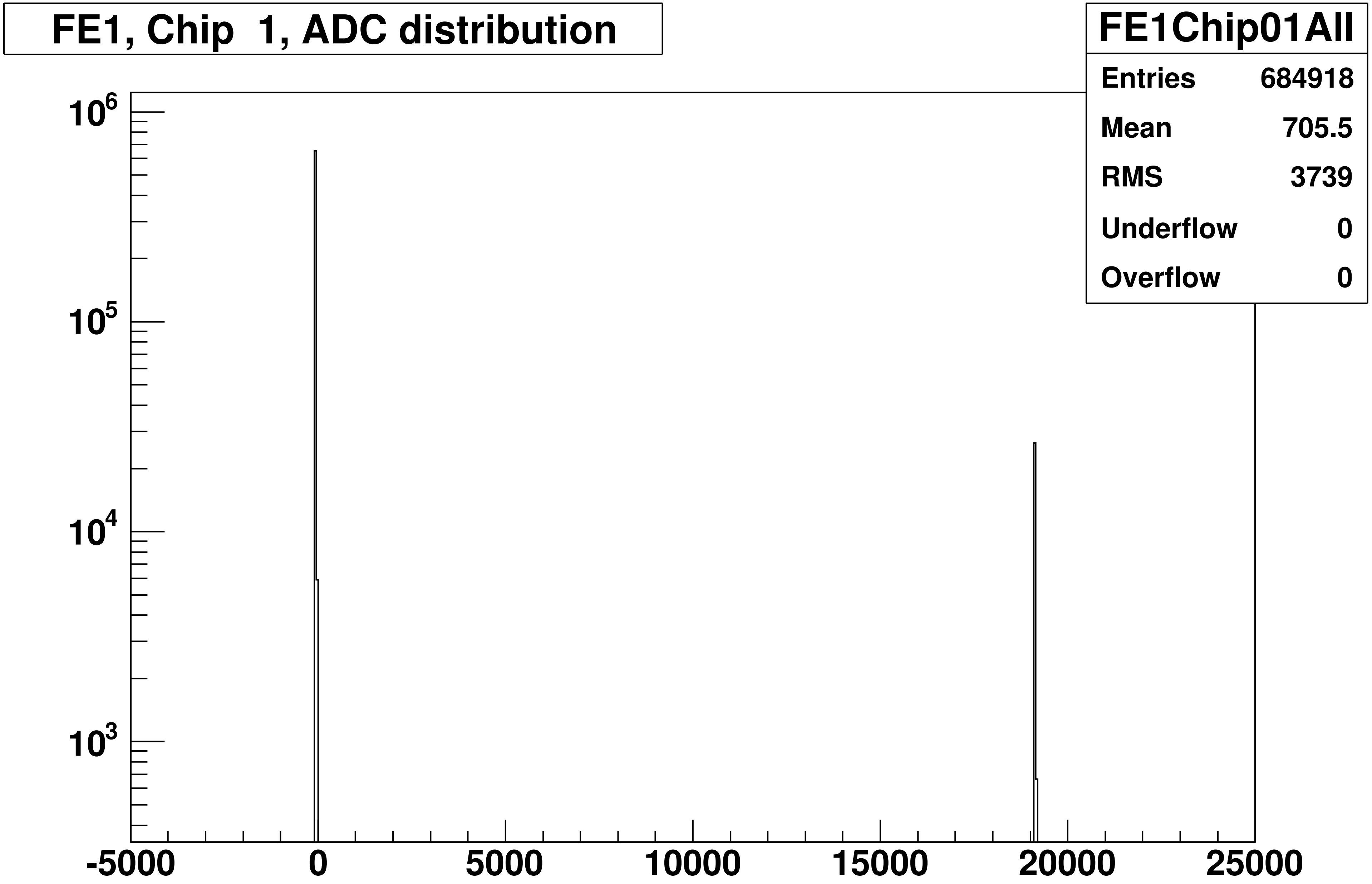

FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 684918
Mean 26.66
RMS 1.353
Underflow 0
Overflow 2.621e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

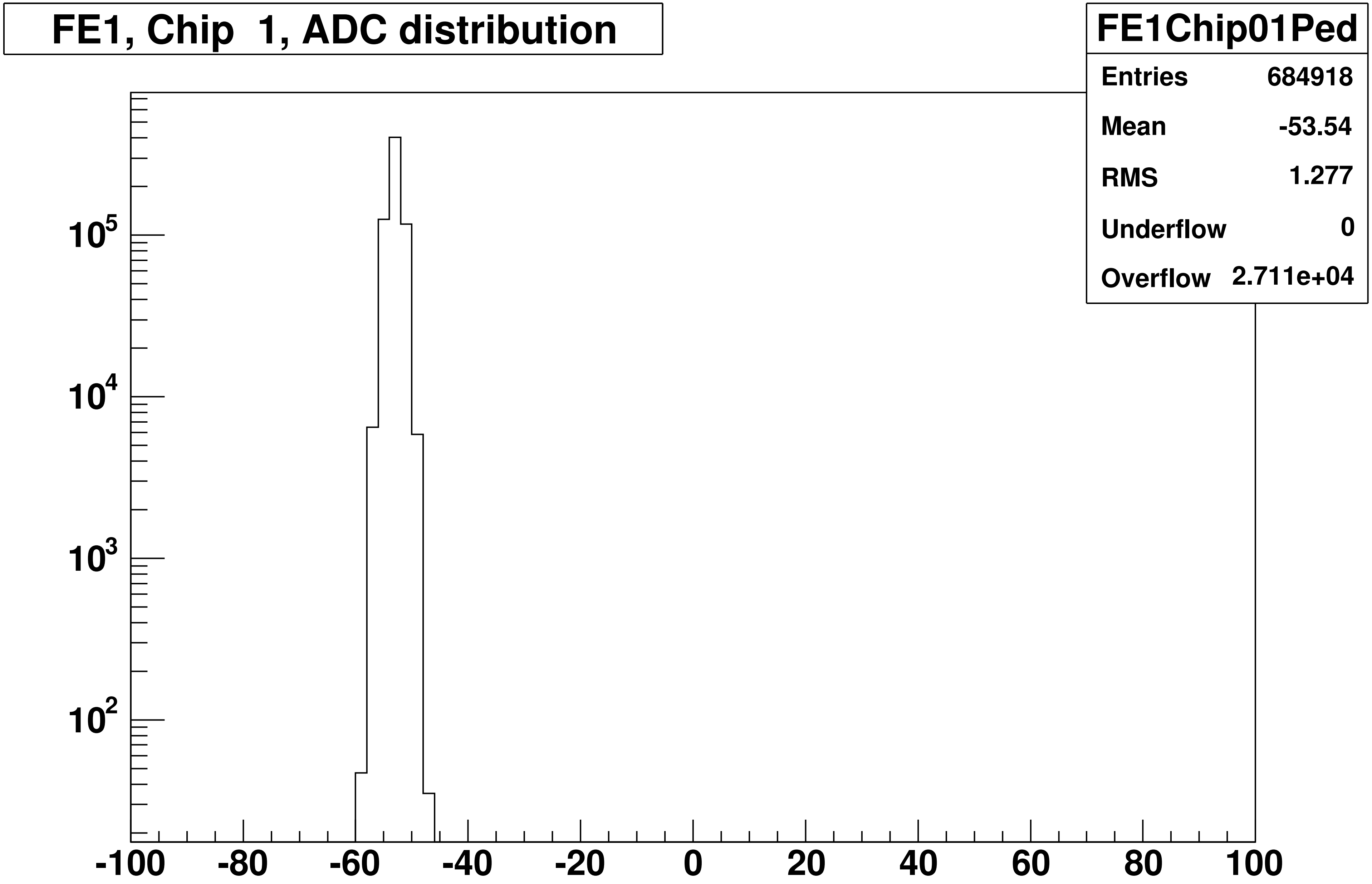

FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 684918
Mean 1.917e+04
RMS 17.29
Underflow 6.587e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400

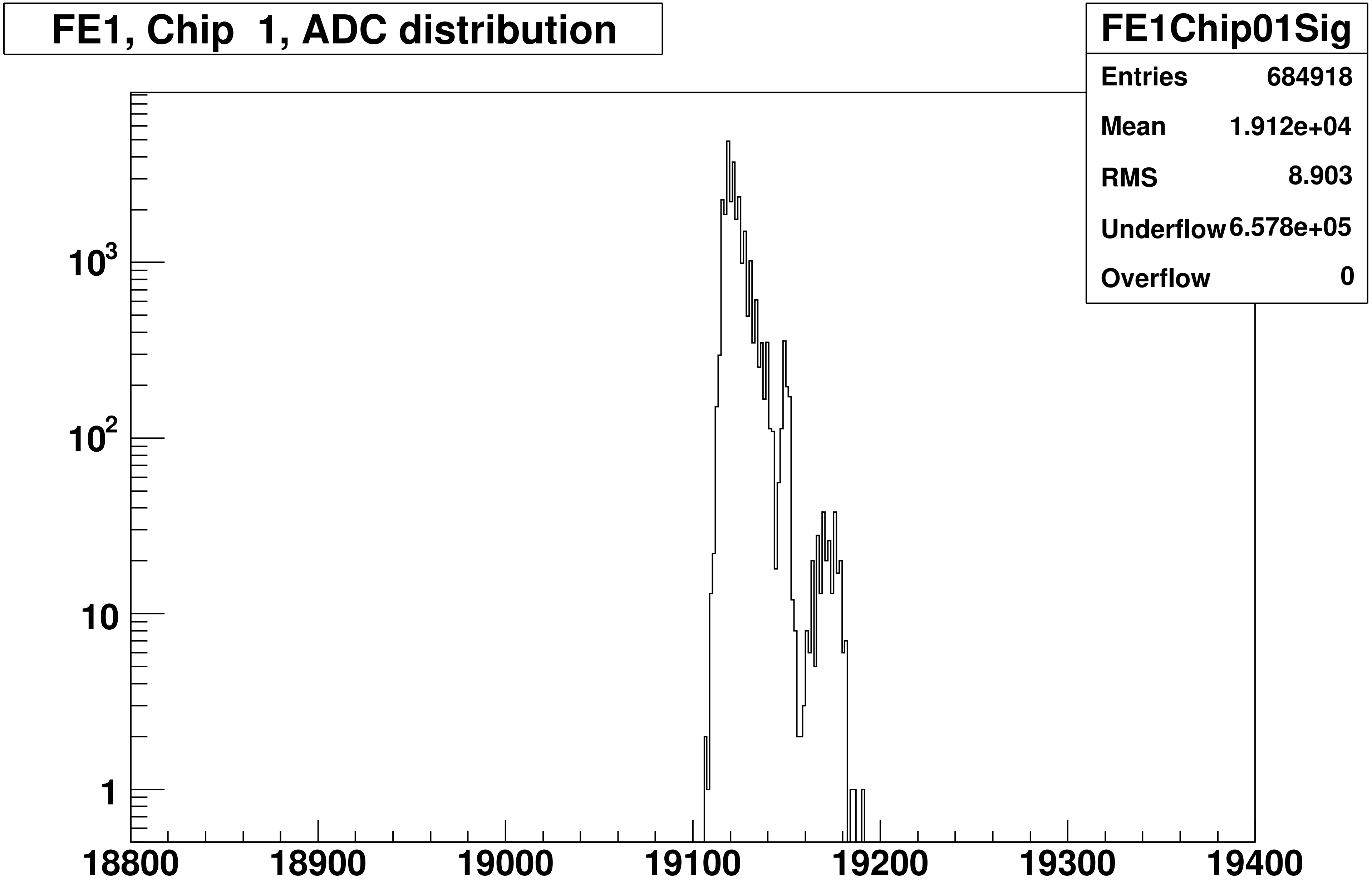

FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 684918
Mean 764.6
RMS 3744
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

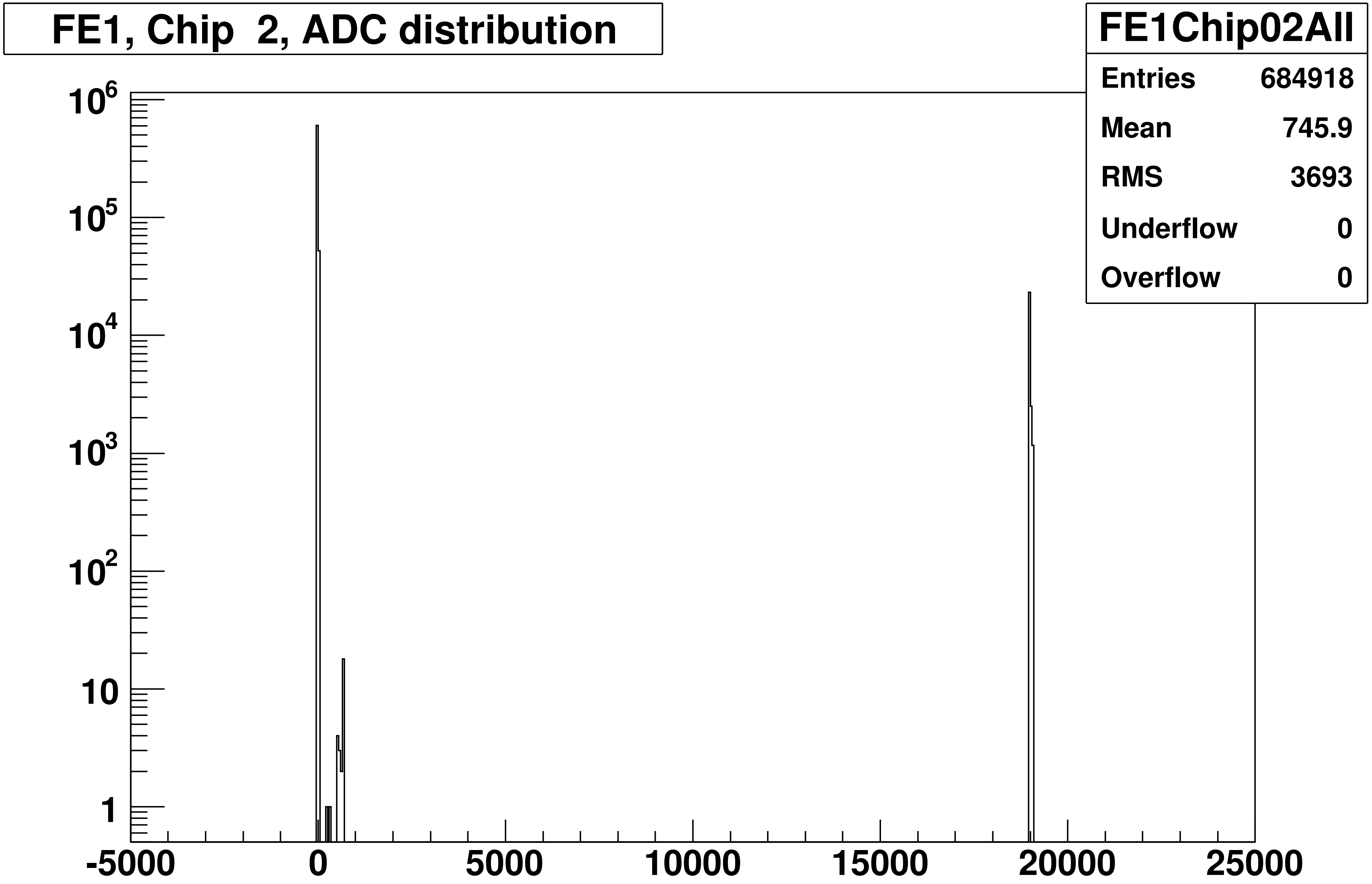

FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 684918
Mean 4.595
RMS 1.348
Underflow 0
Overflow 2.711e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

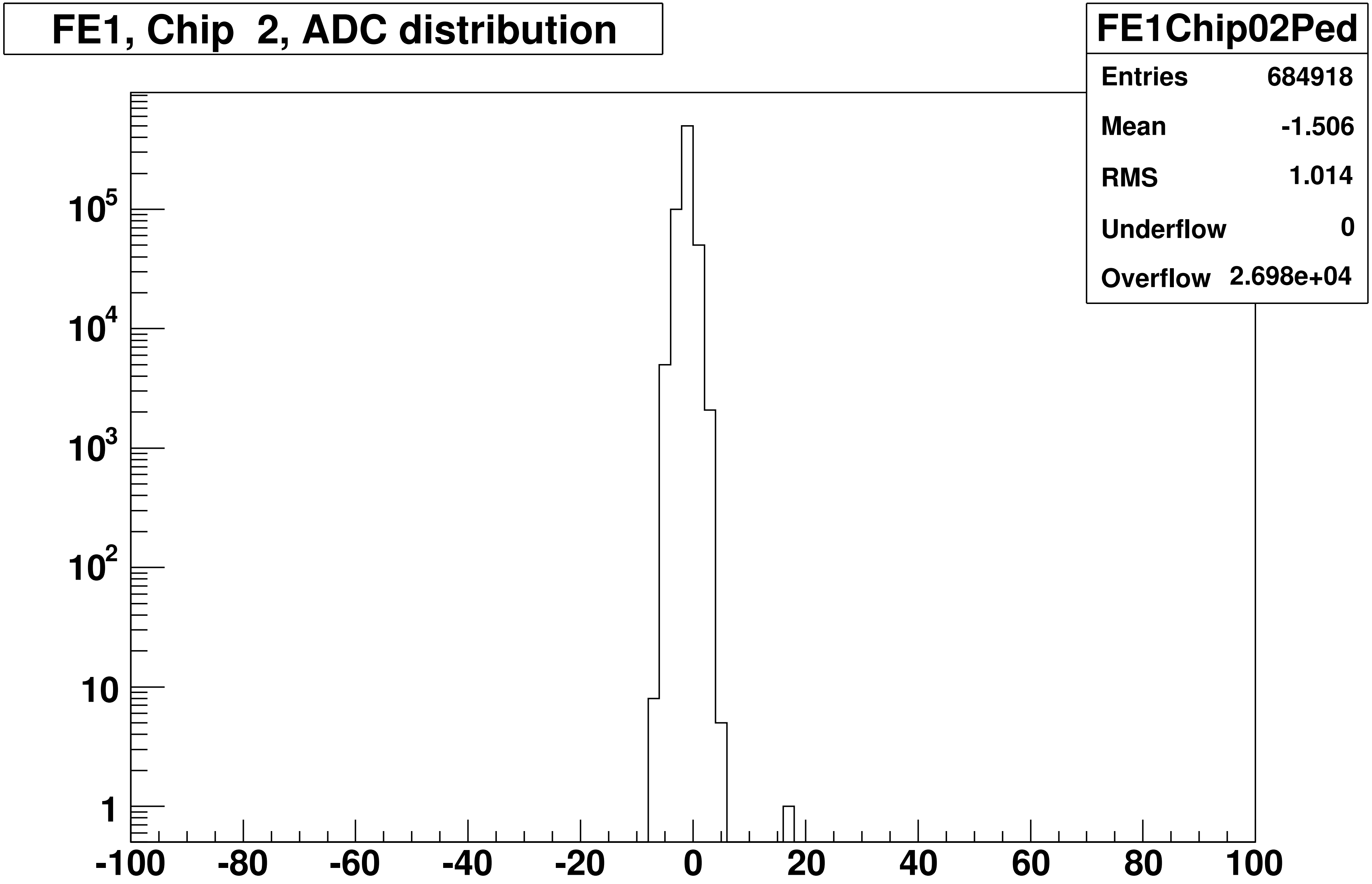

FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 684918
Mean 1.921e+04
RMS 11.83
Underflow 6.578e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 684918
Mean 705.5
RMS 3739
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

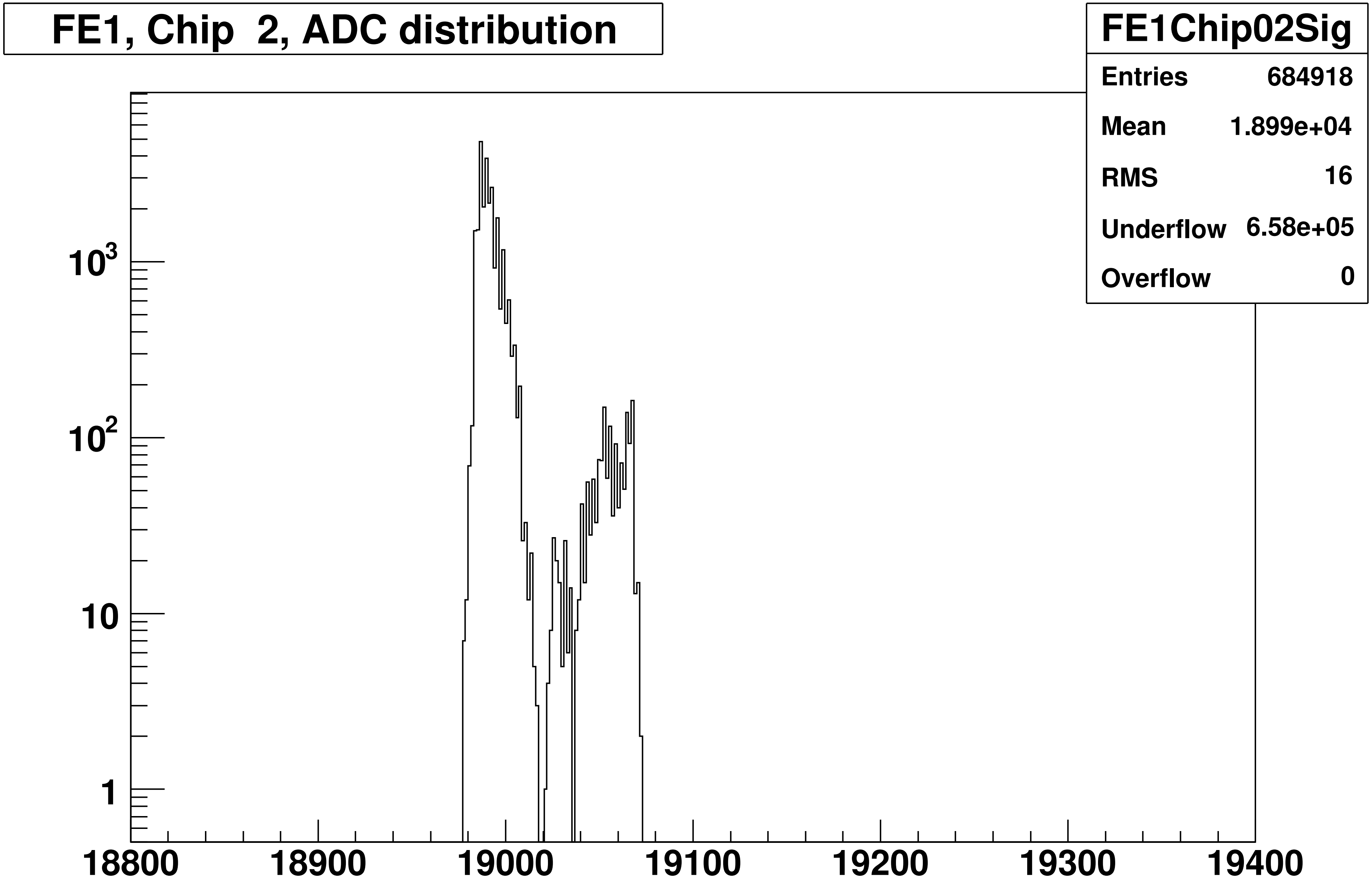

FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 684918
Mean -53.54
RMS 1.277
Underflow 0
Overflow 2.711e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

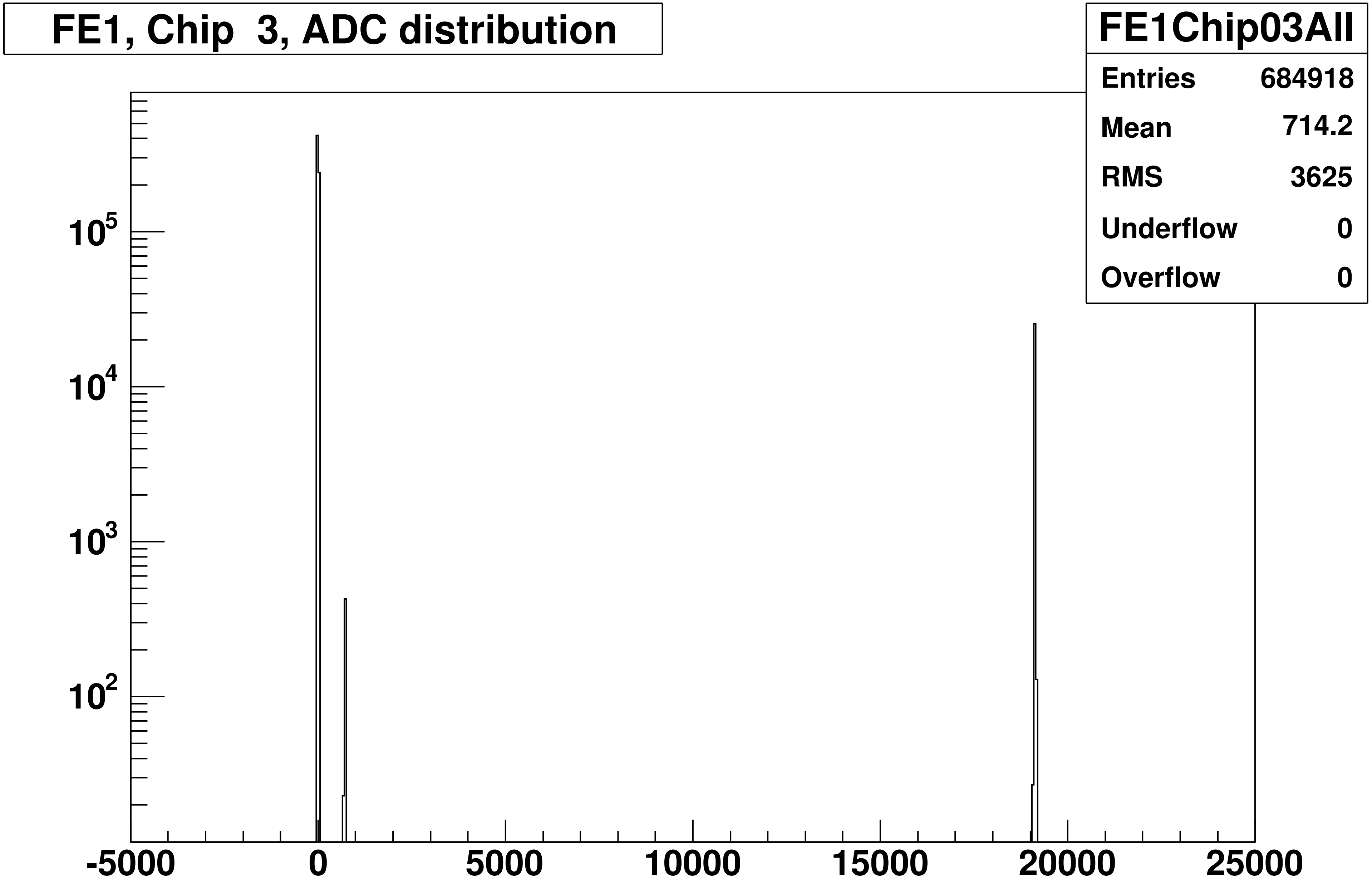

FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 684918
Mean 1.912e+04
RMS 8.903
Underflow 6.578e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400

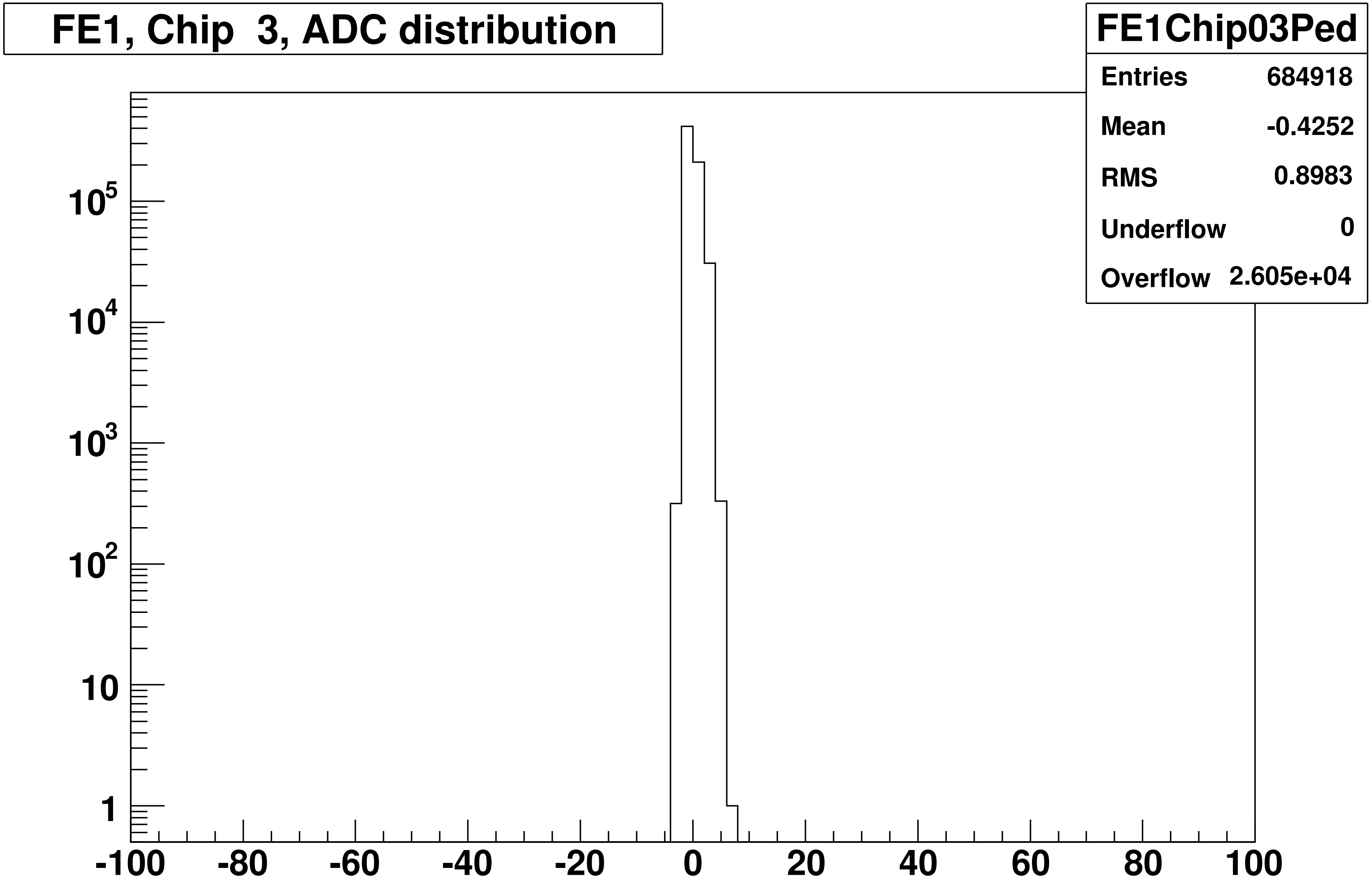

FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 684918
Mean 745.9
RMS 3693
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



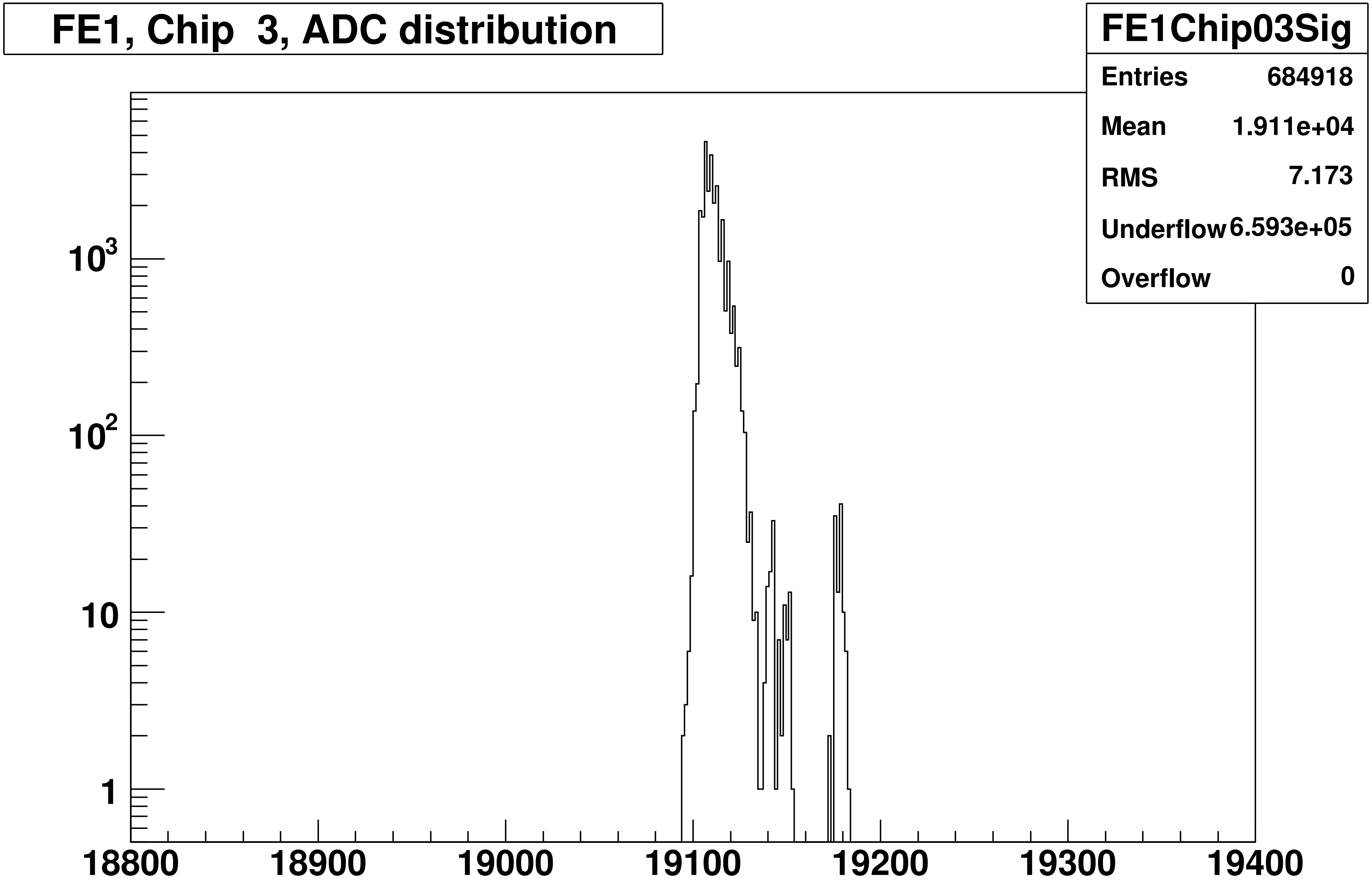
FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 684918
Mean -1.506
RMS 1.014
Underflow 0
Overflow 2.698e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

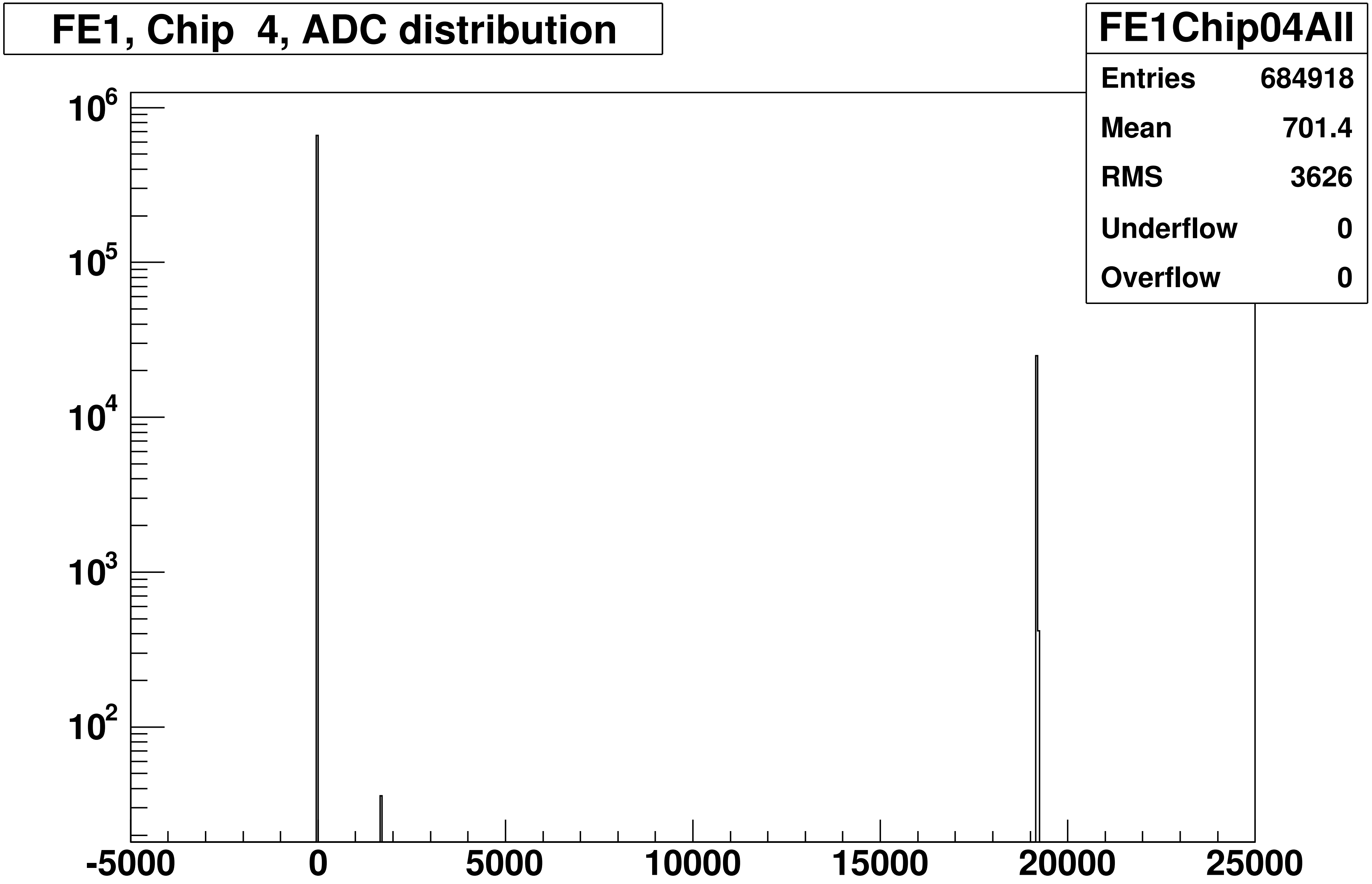

FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 684918
Mean 1.899e+04
RMS 16
Underflow 6.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

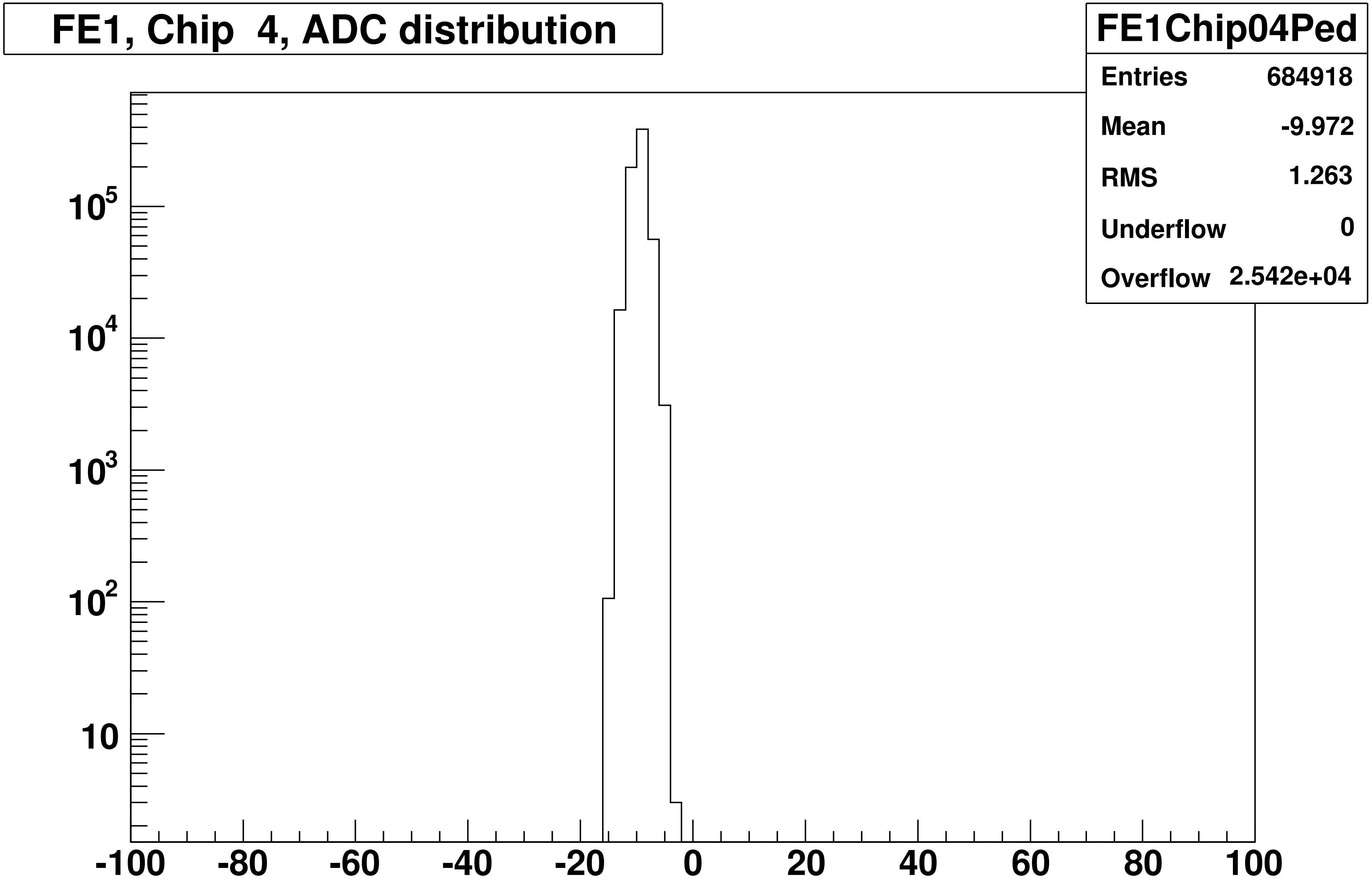

FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 684918
Mean 714.2
RMS 3625
Underflow 0
Overflow 0
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000



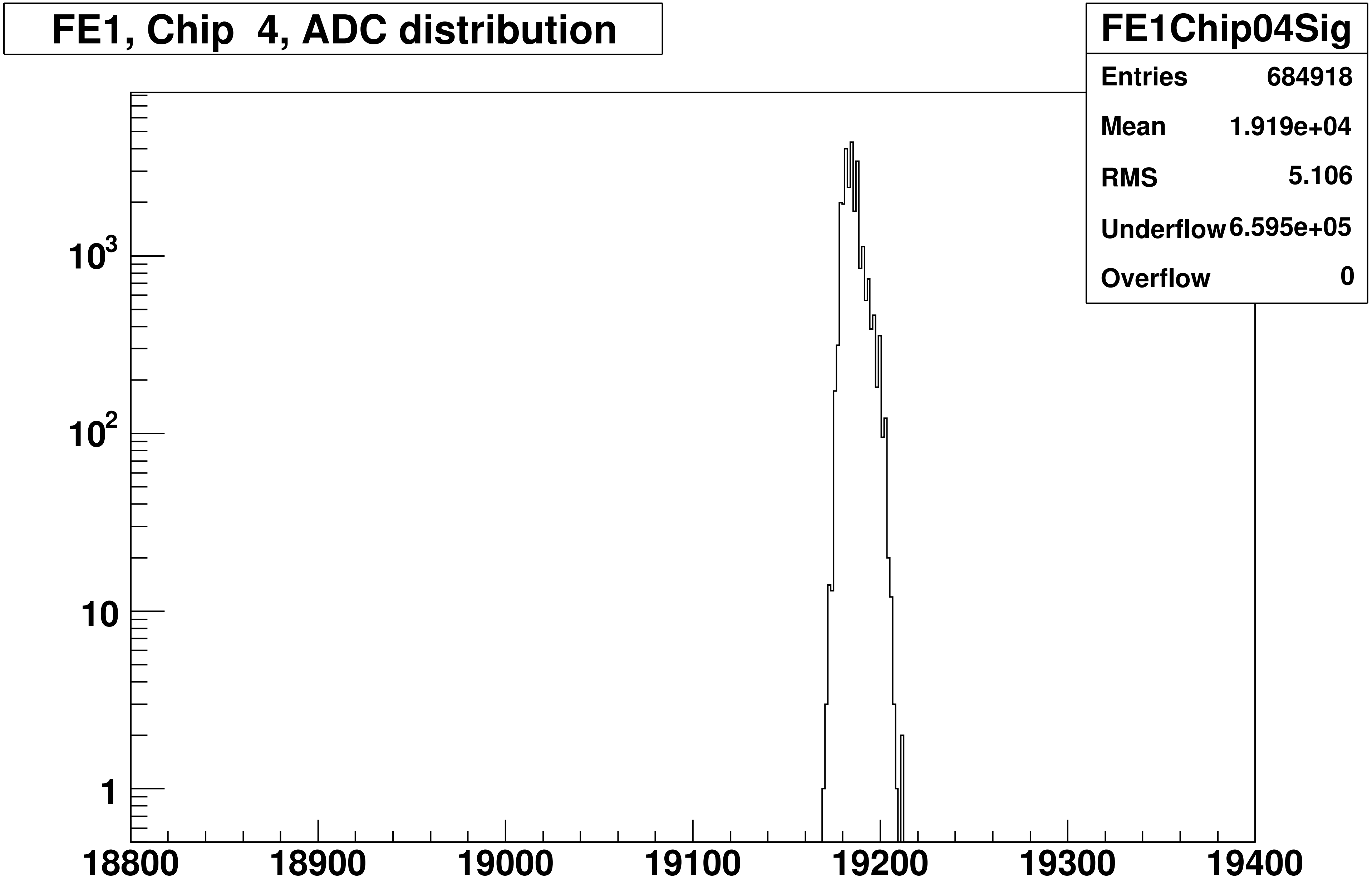
FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 684918
Mean -0.4252
RMS 0.8983
Underflow 0
Overflow 2.605e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

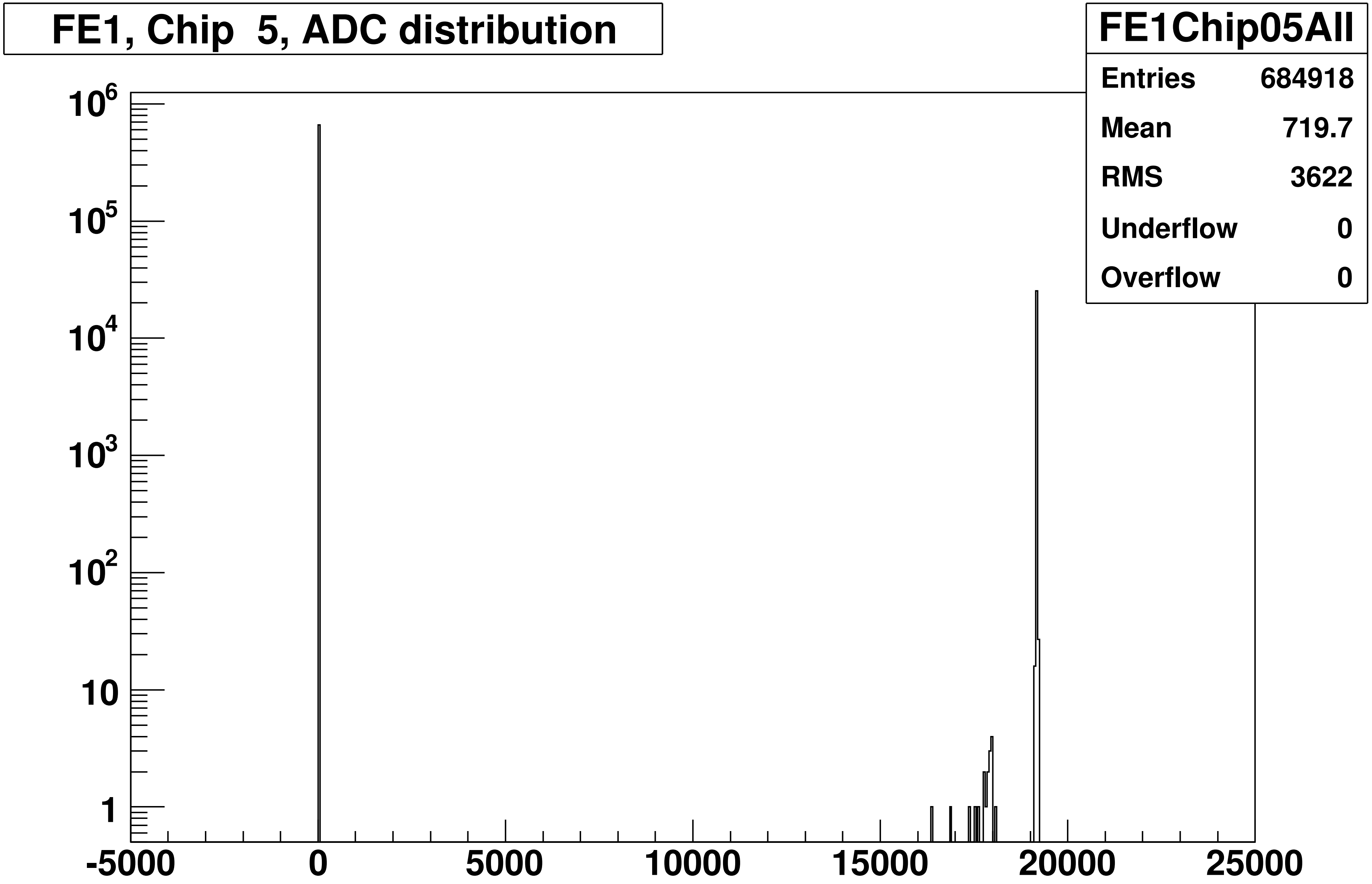

FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 684918
Mean 1.911e+04
RMS 7.173
Underflow 6.593e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

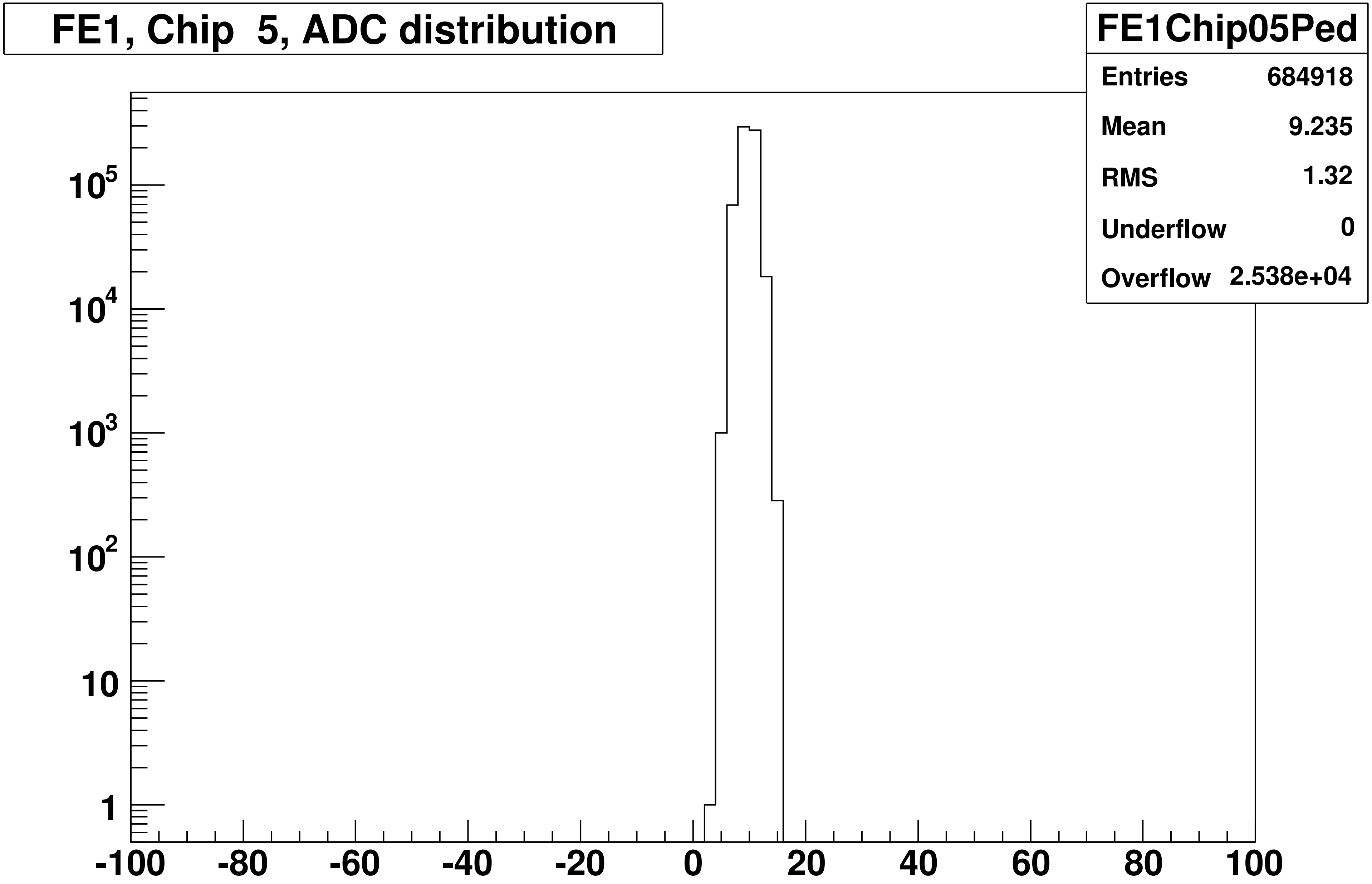

FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 684918
Mean 701.4
RMS 3626
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



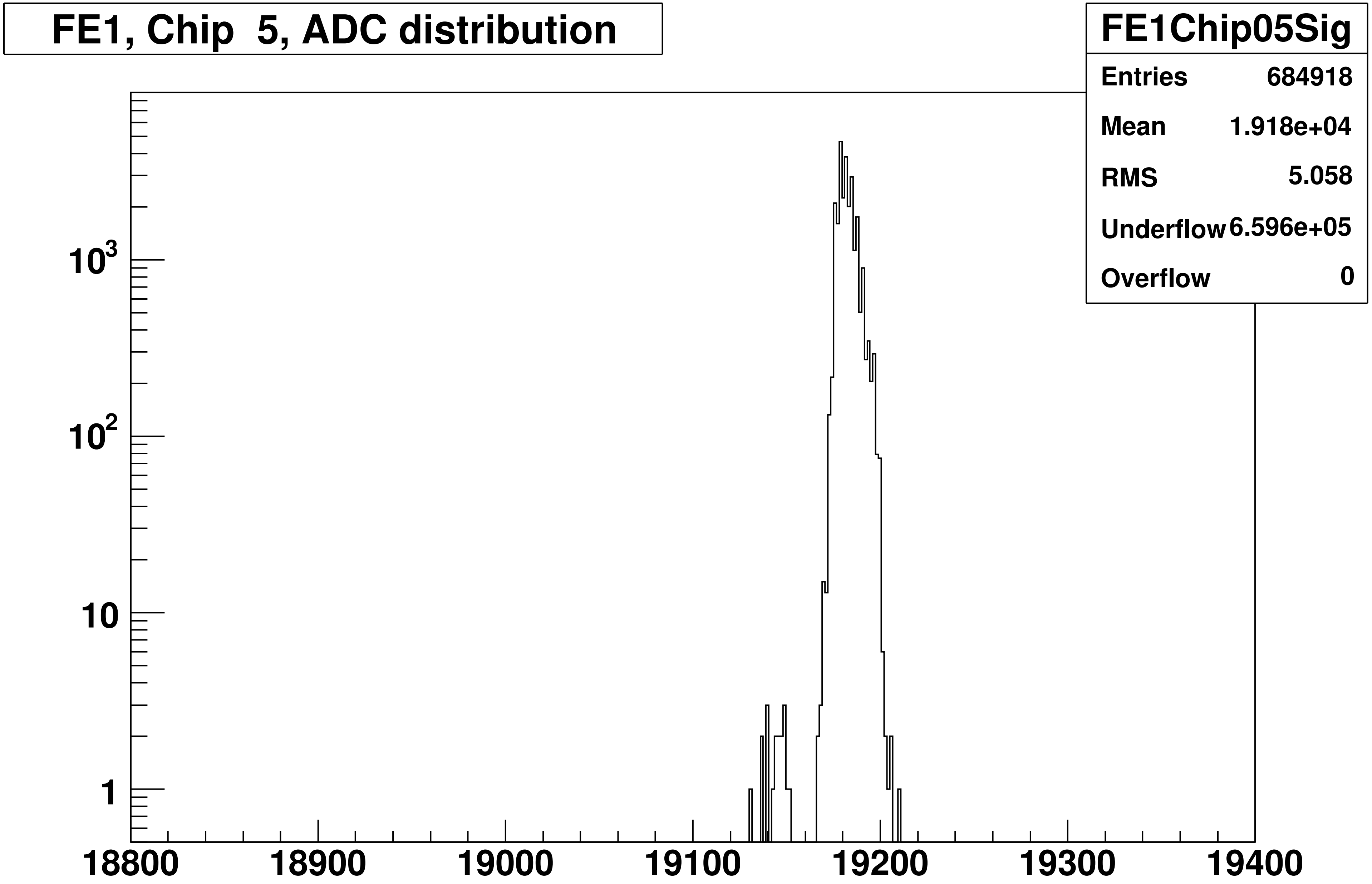
FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 684918
Mean -9.972
RMS 1.263
Underflow 0
Overflow 2.542e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 684918
Mean 1.919e+04
RMS 5.106
Underflow 6.595e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

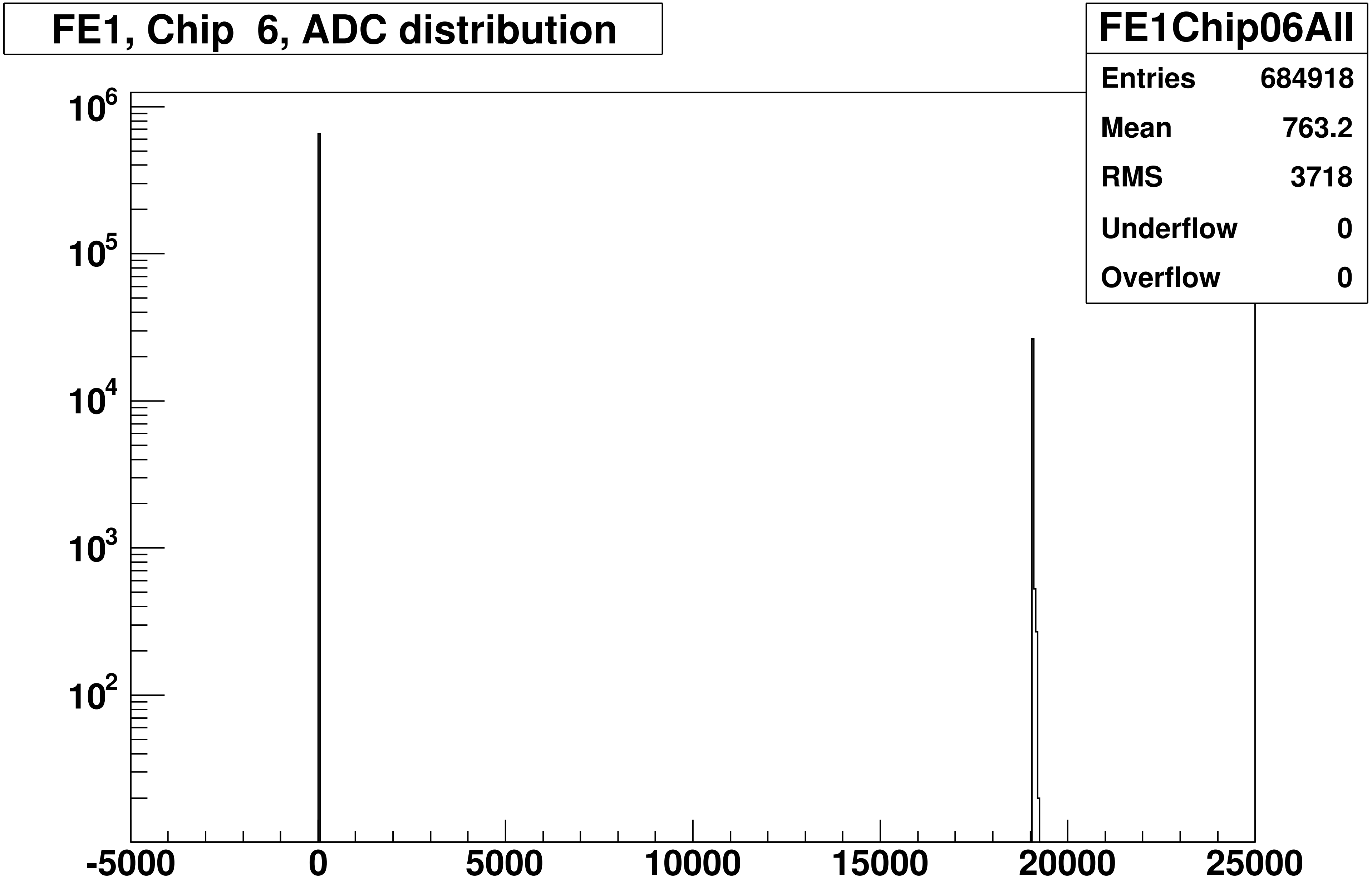

FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 684918
Mean 719.7
RMS 3622
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



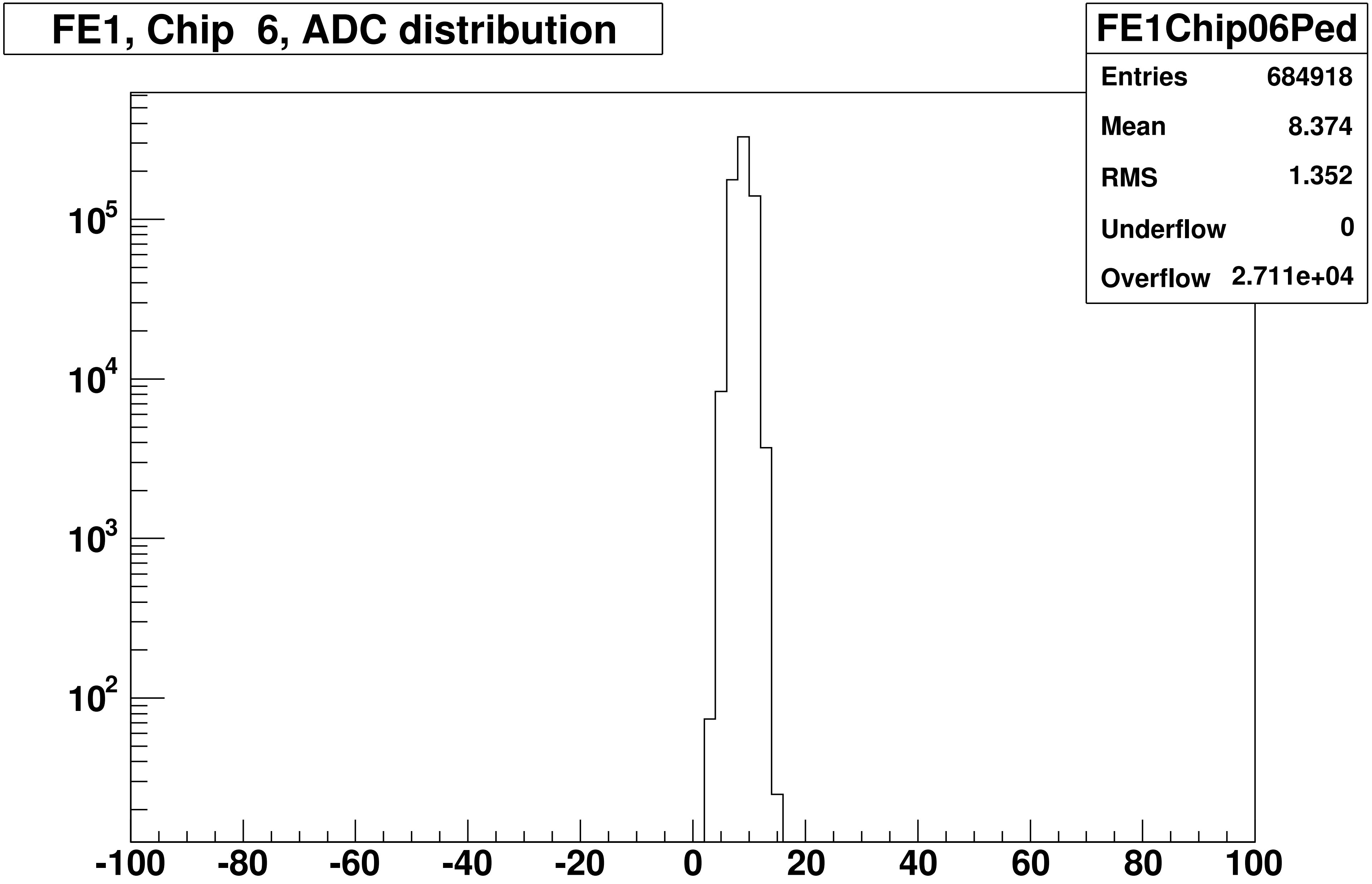
FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 684918
Mean 9.235
RMS 1.32
Underflow 0
Overflow 2.538e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

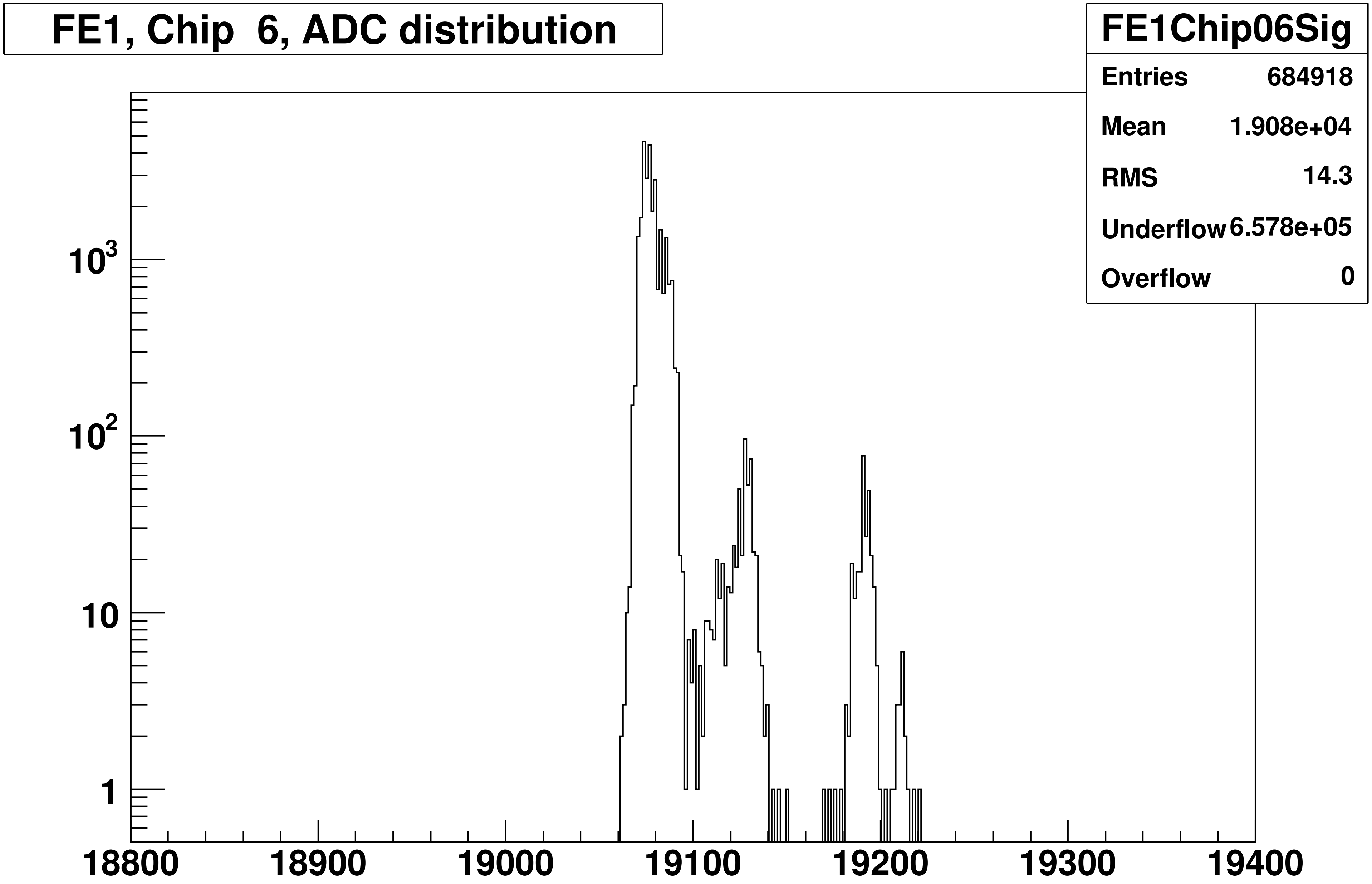


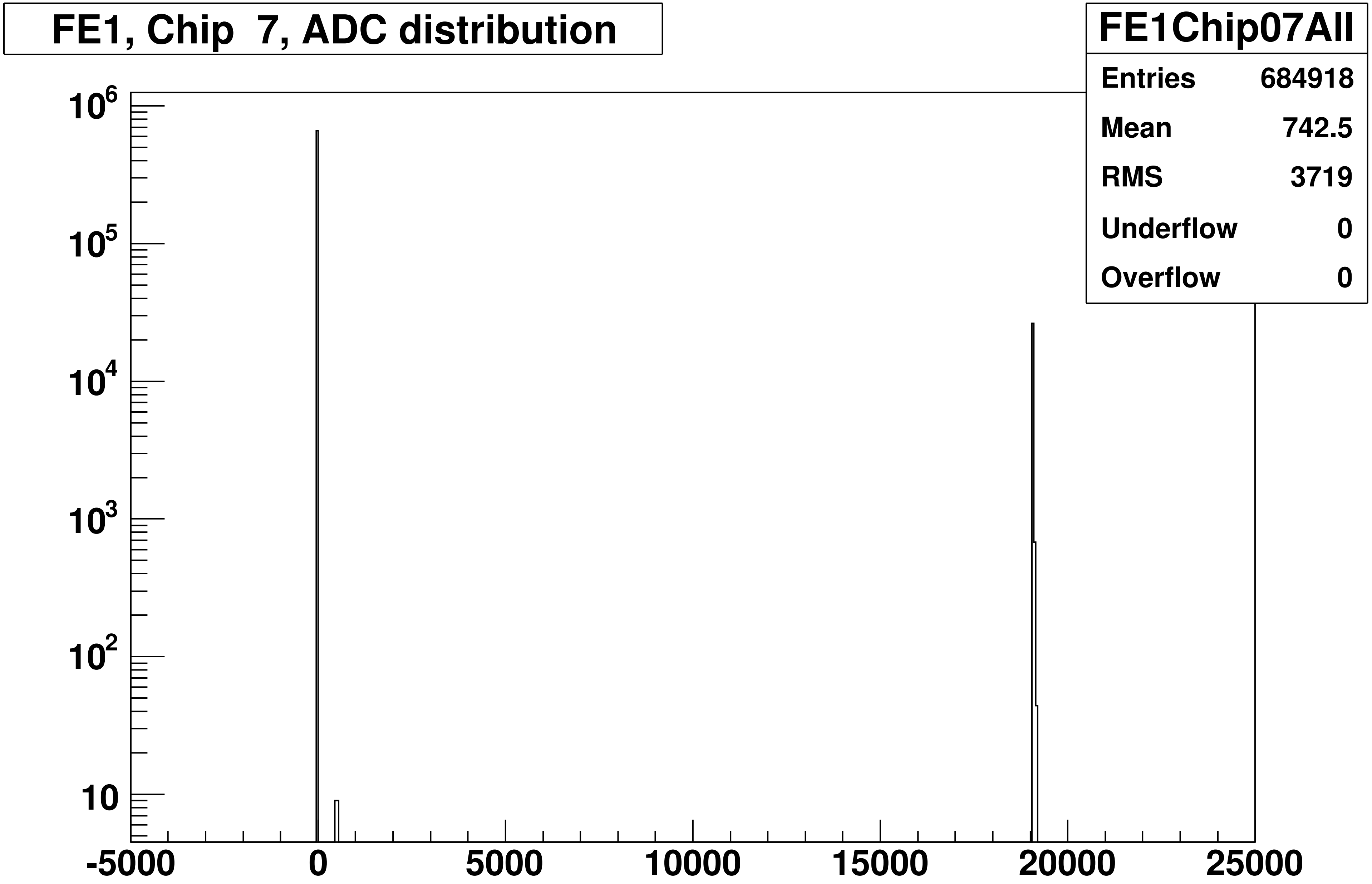
FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 684918
Mean 1.918e+04
RMS 5.058
Underflow 6.596e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

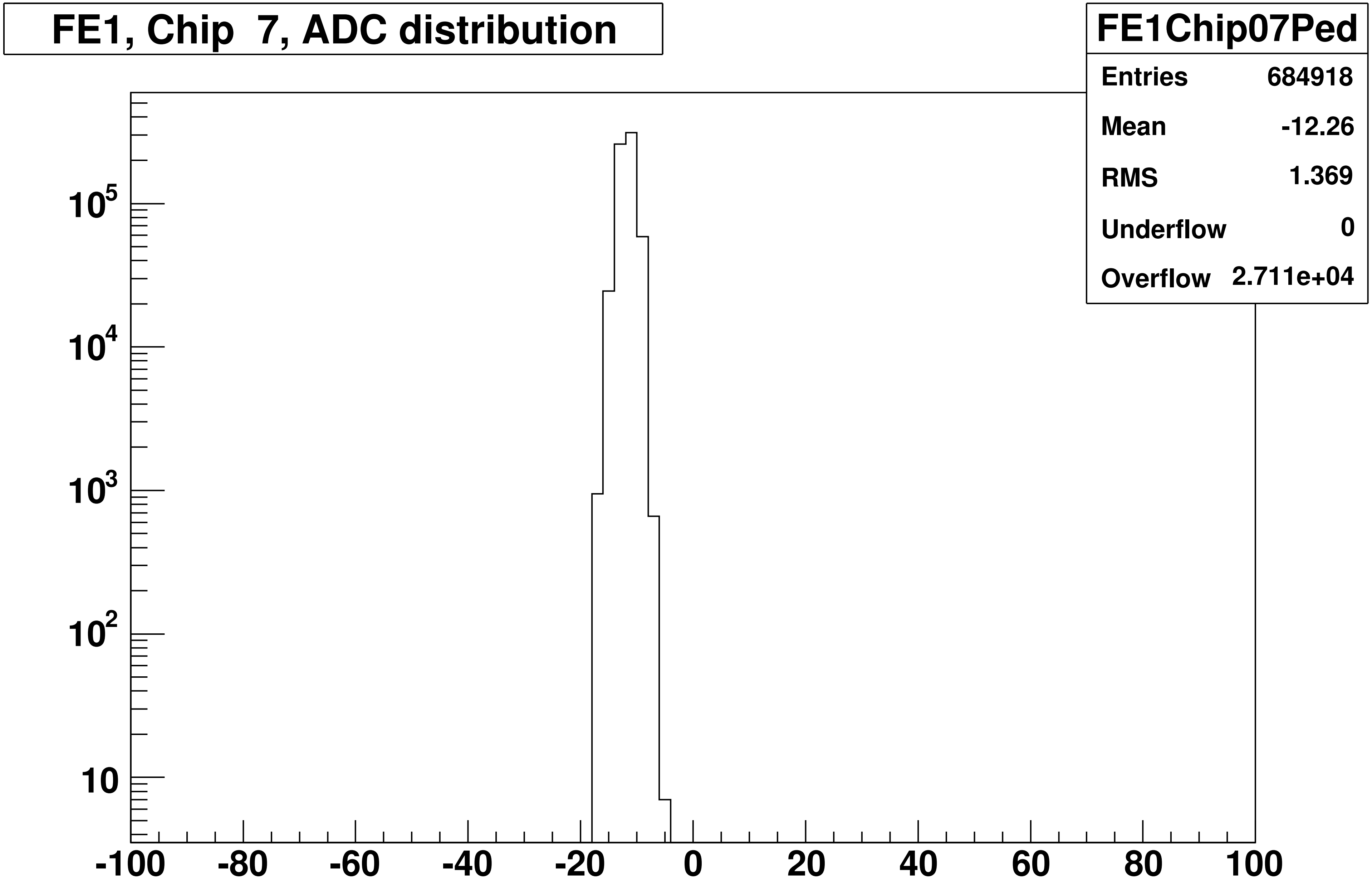


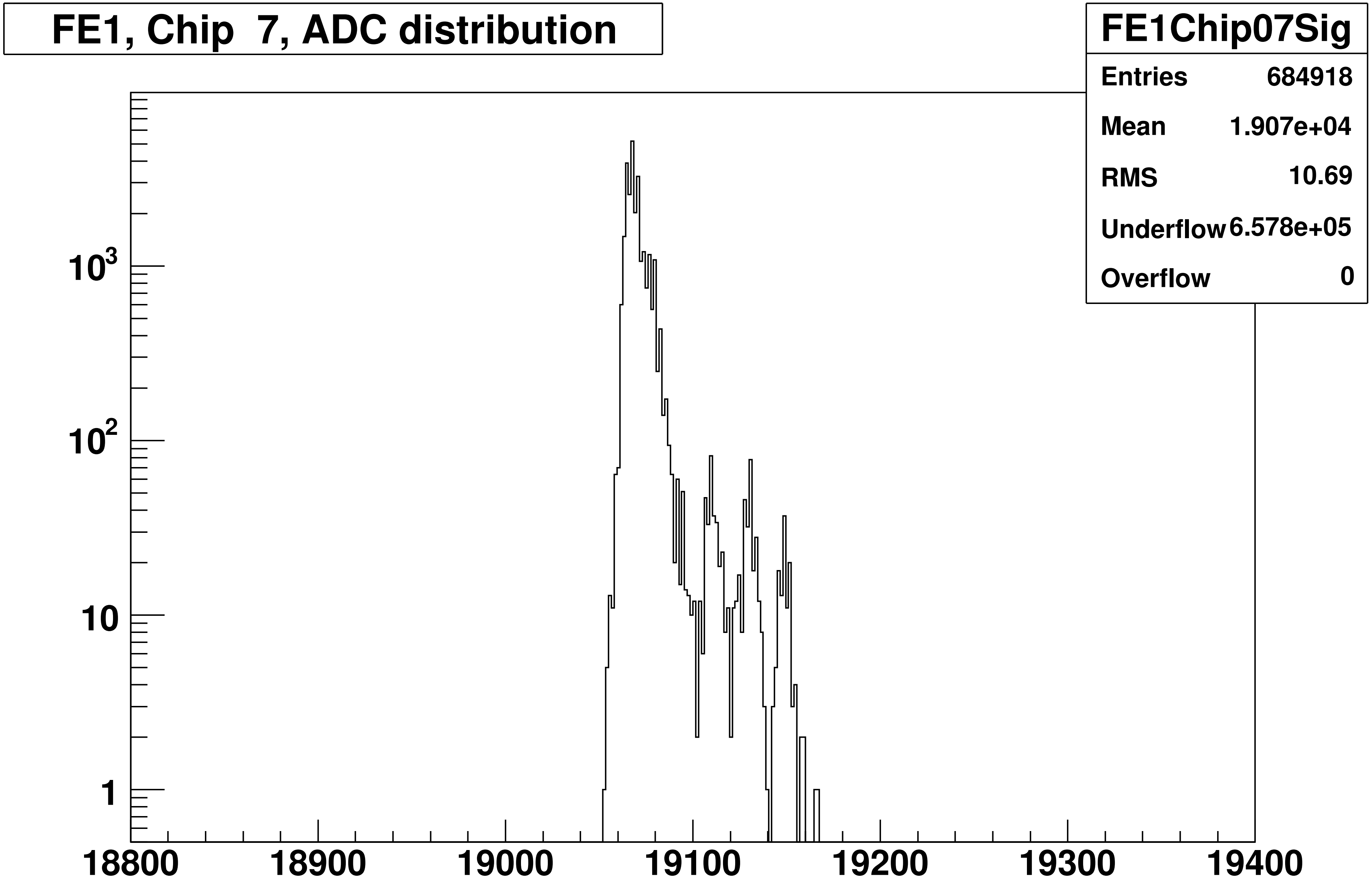
FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 684918
Mean 763.2
RMS 3718
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

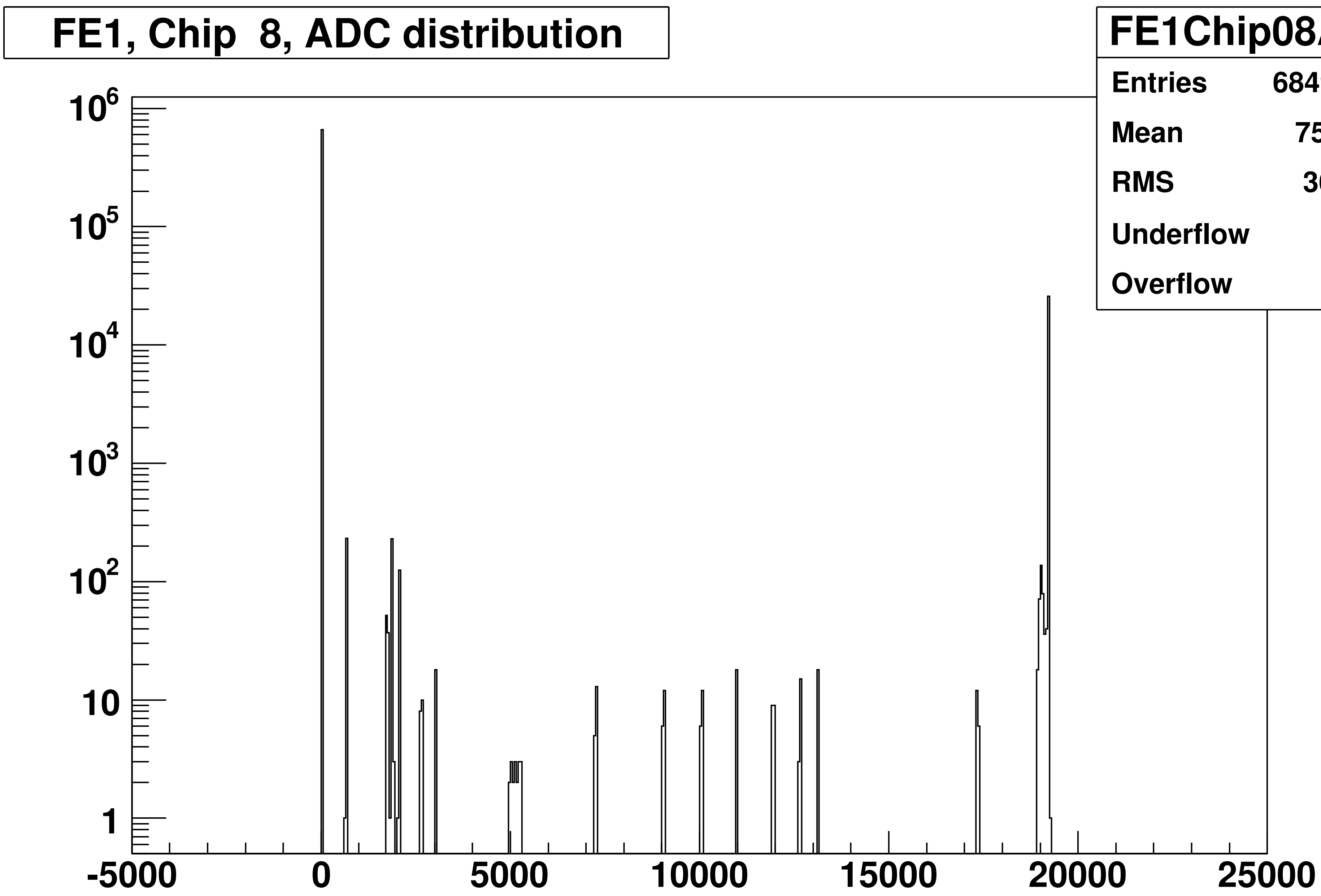

FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 684918
Mean 8.374
RMS 1.352
Underflow 0
Overflow 2.711e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 684918
Mean 1.908e+04
RMS 14.3
Underflow 6.578e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

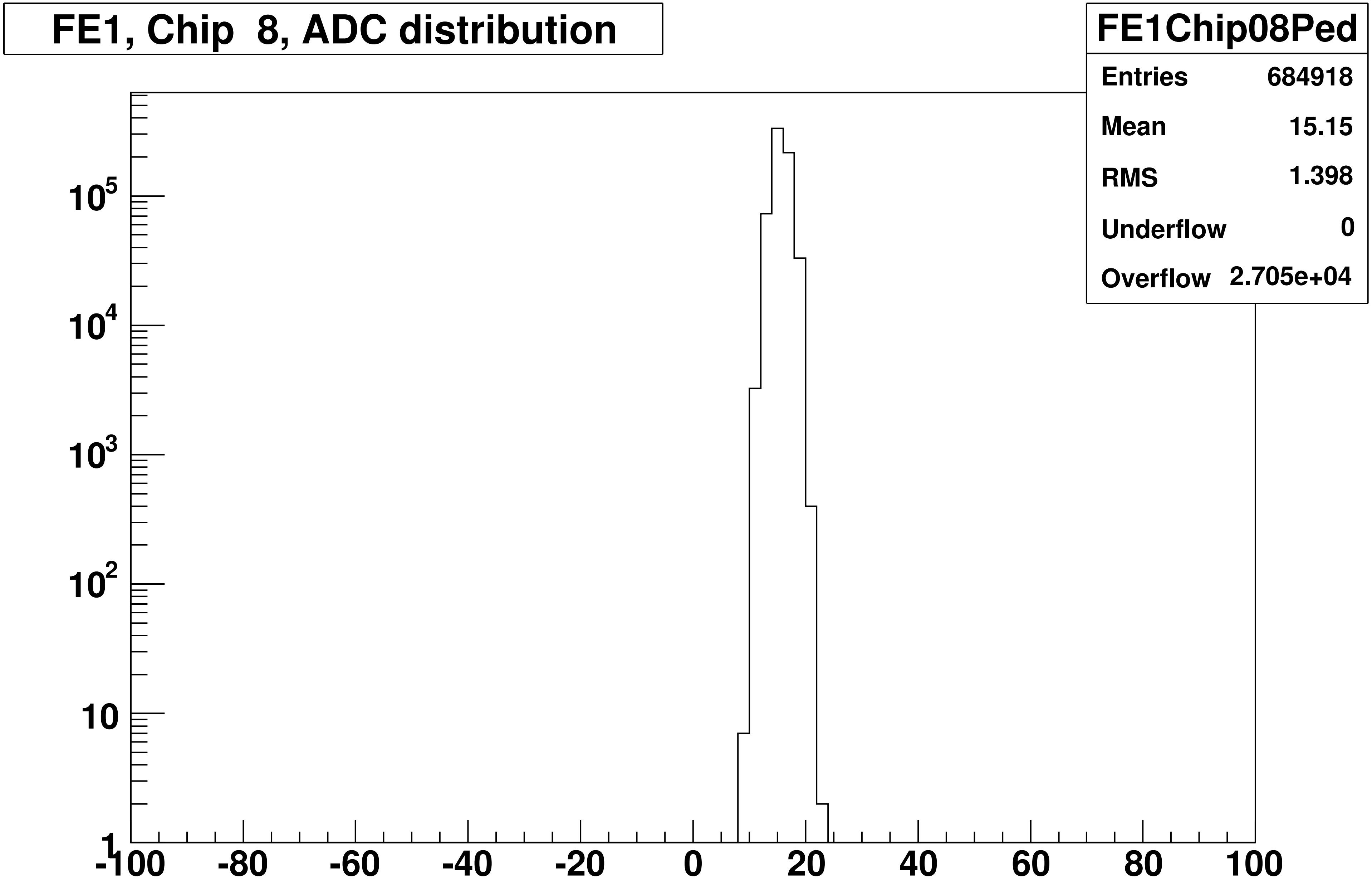

FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 684918
Mean 742.5
RMS 3719
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 684918
Mean -12.26
RMS 1.369
Underflow 0
Overflow 2.711e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

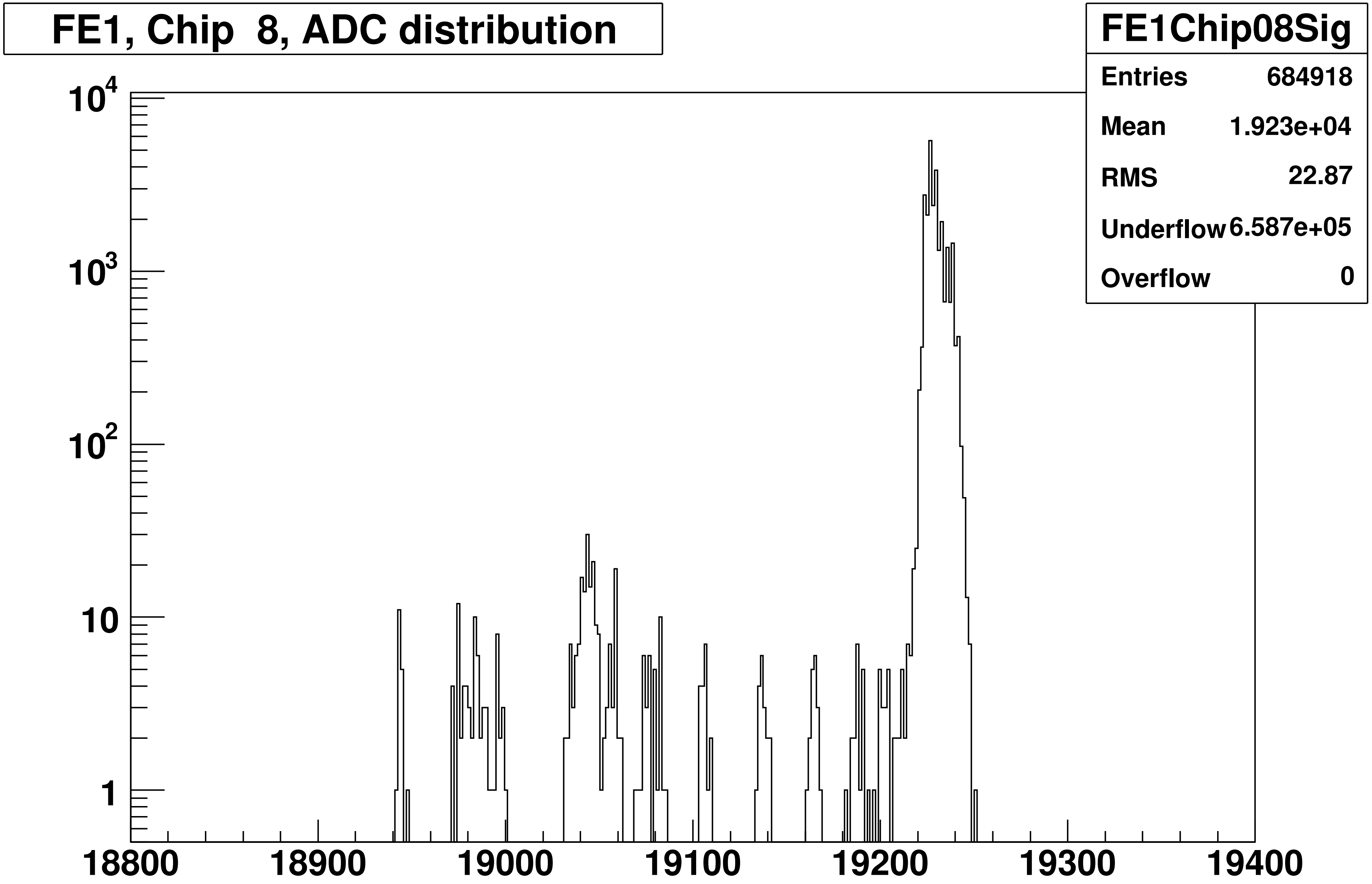

FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 684918
Mean 1.907e+04
RMS 10.69
Underflow 6.578e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

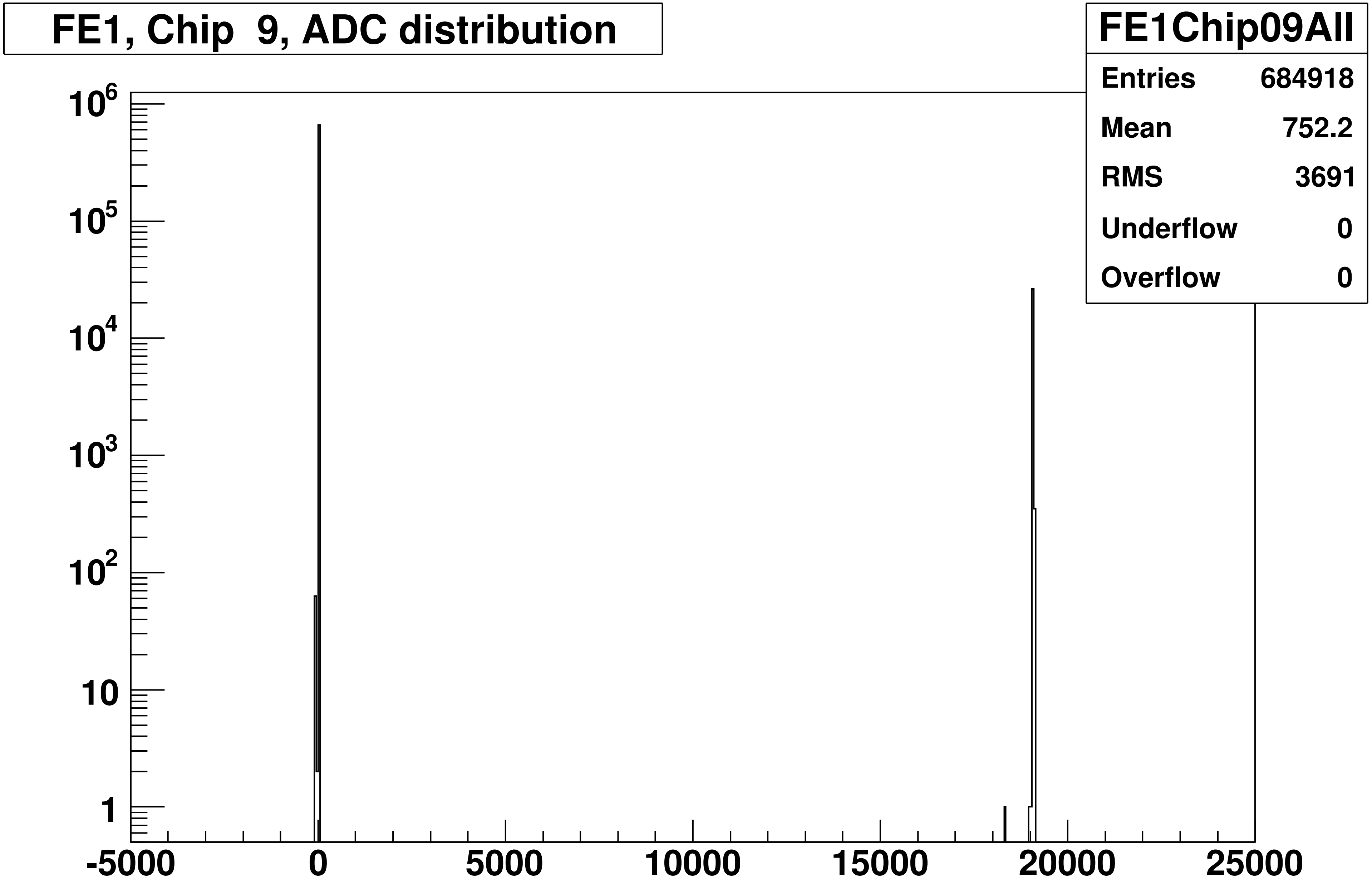


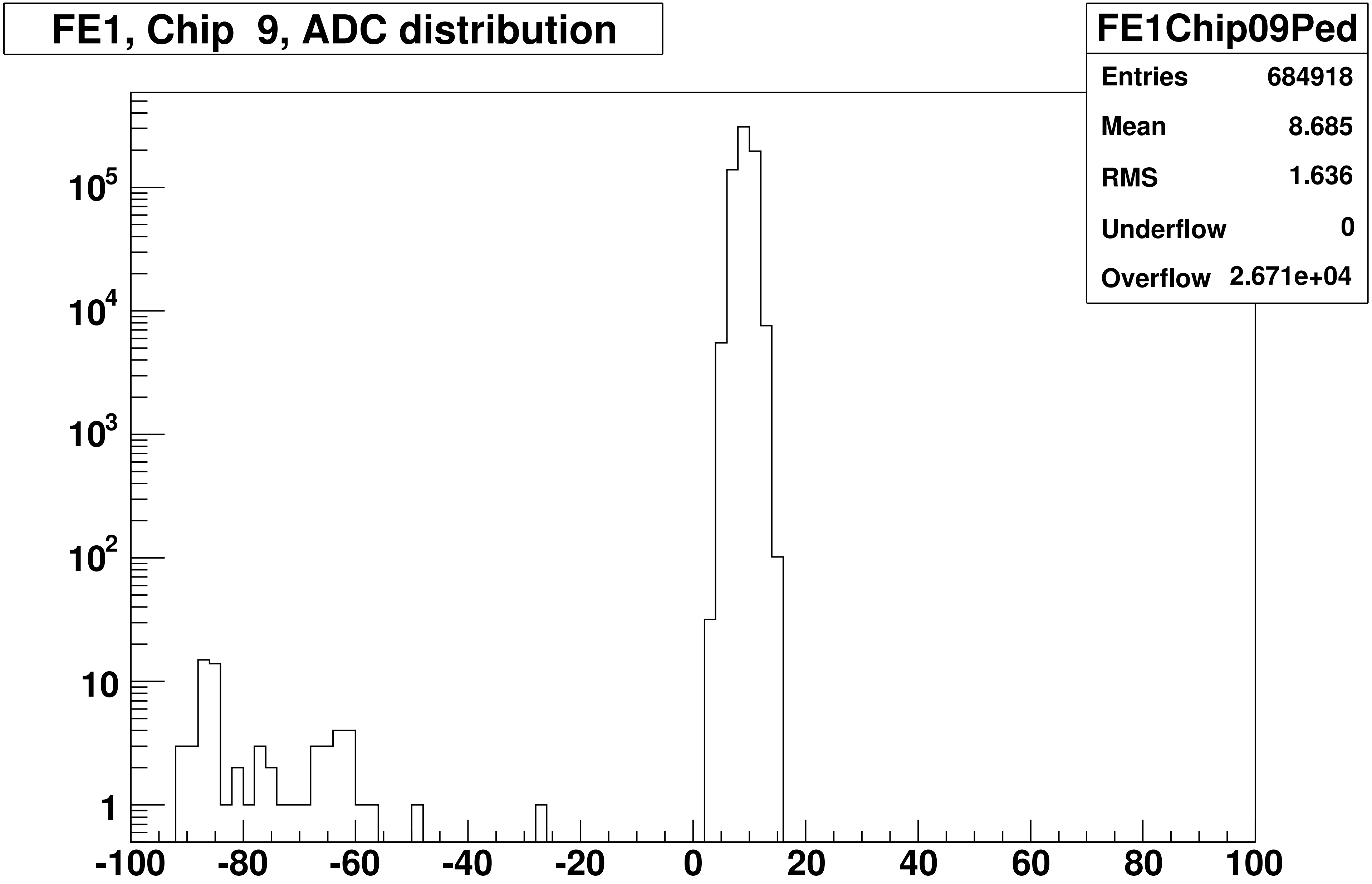
FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 684918
Mean 753.4
RMS 3687
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

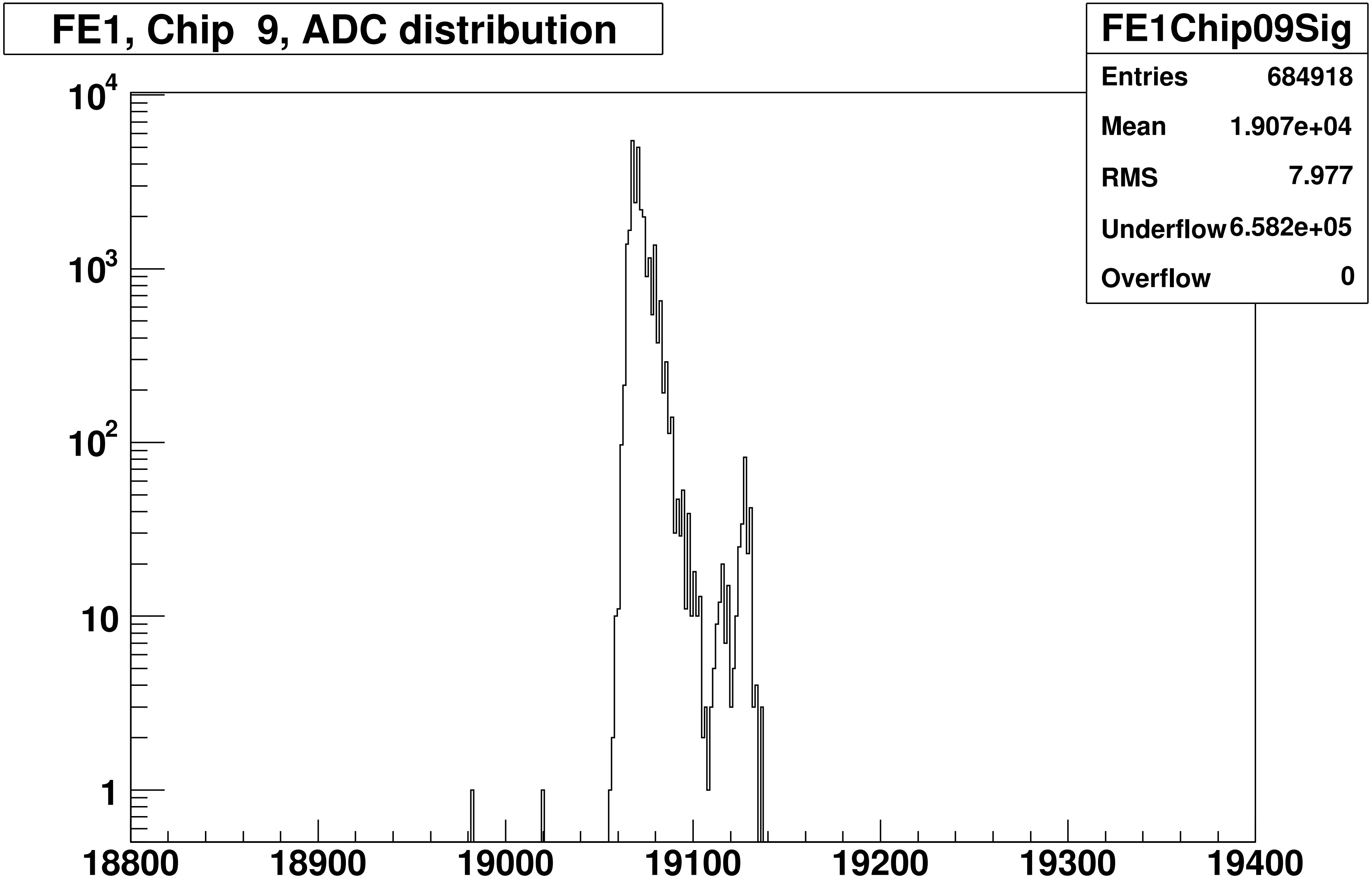


FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 684918
Mean 15.15
RMS 1.398
Underflow 0
Overflow 2.705e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

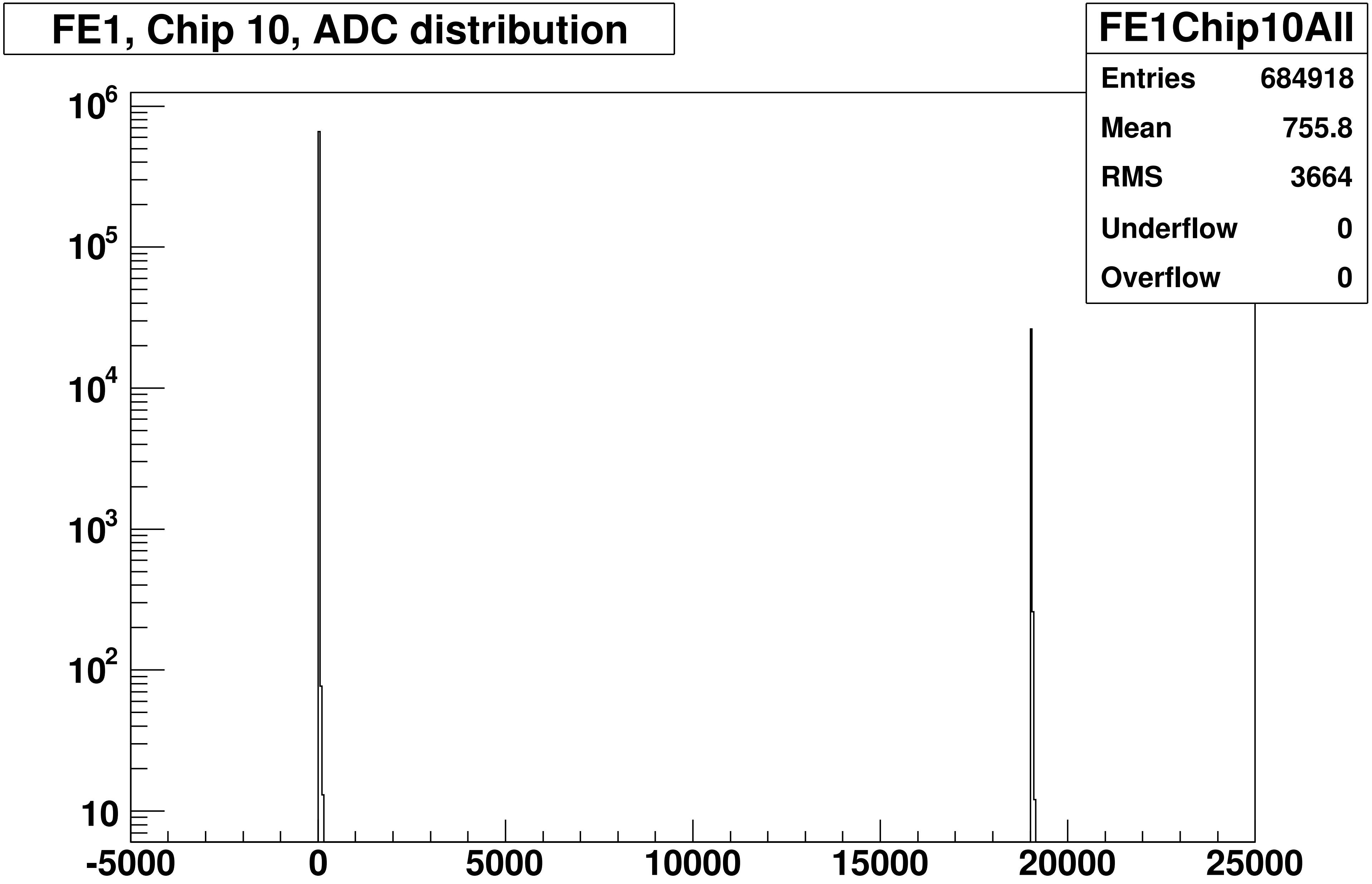

FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 684918
Mean 1.923e+04
RMS 22.87
Underflow 6.587e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 684918
Mean 752.2
RMS 3691
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

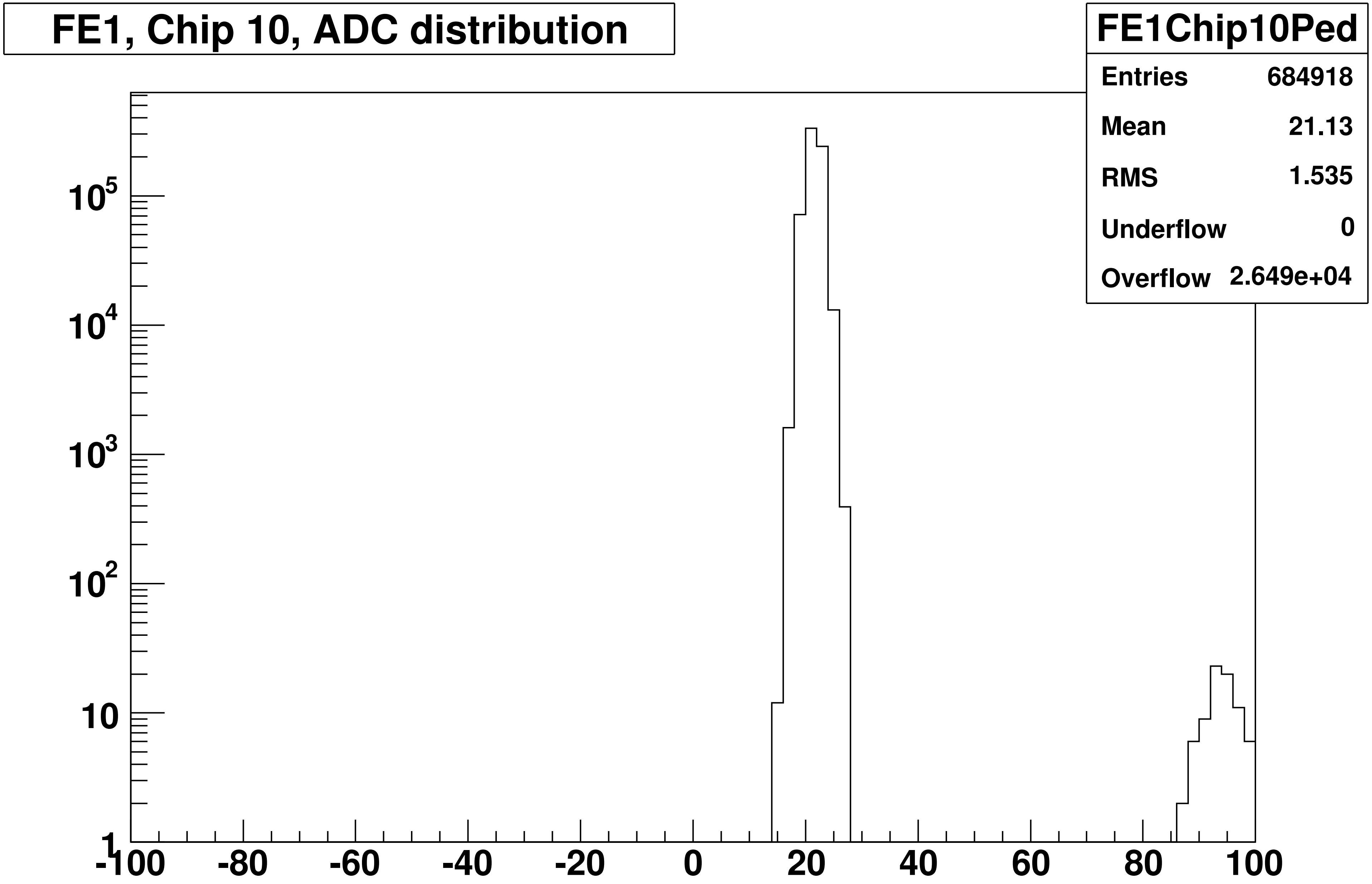

FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 684918
Mean 8.685
RMS 1.636
Underflow 0
Overflow 2.671e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

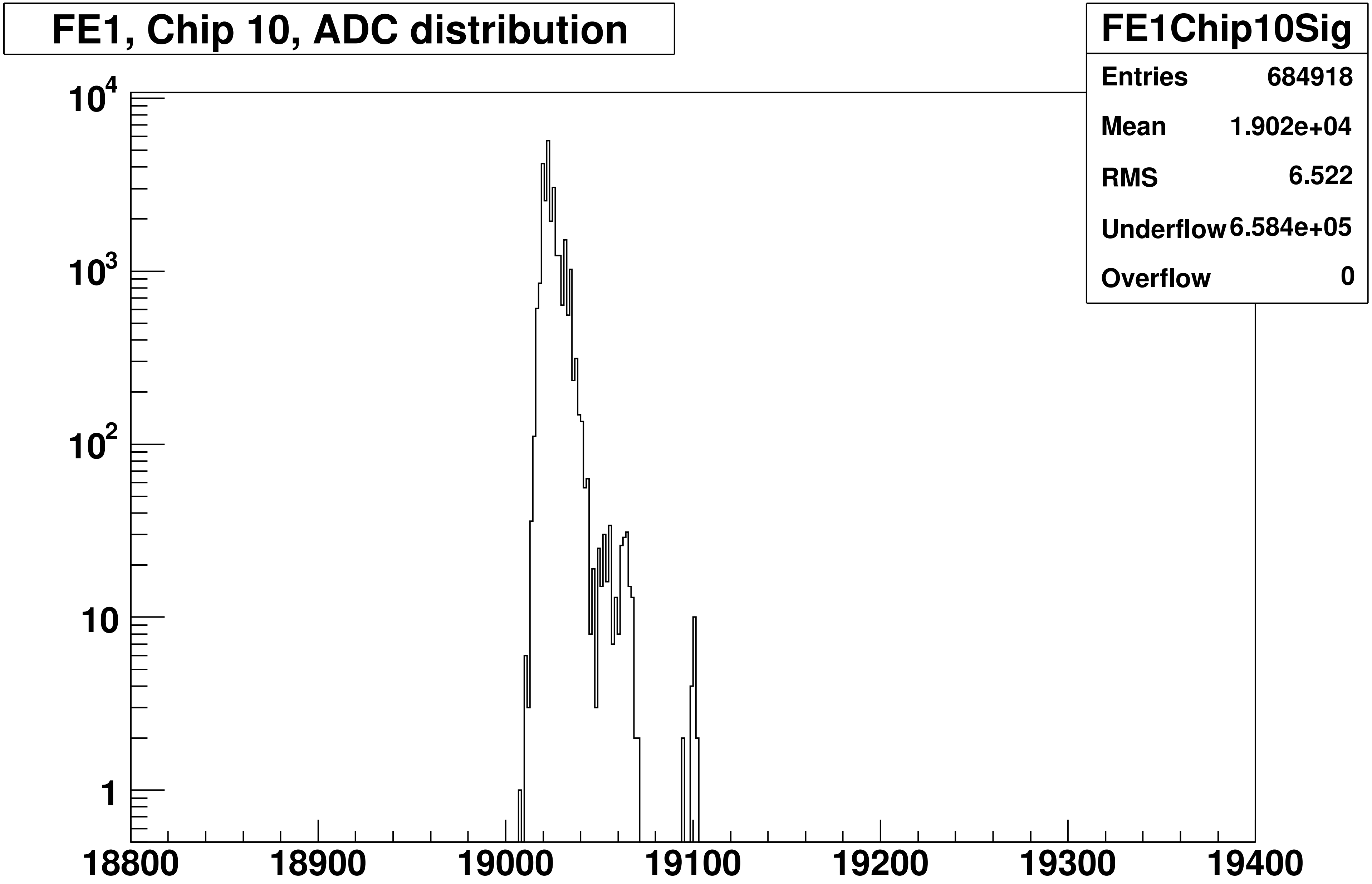


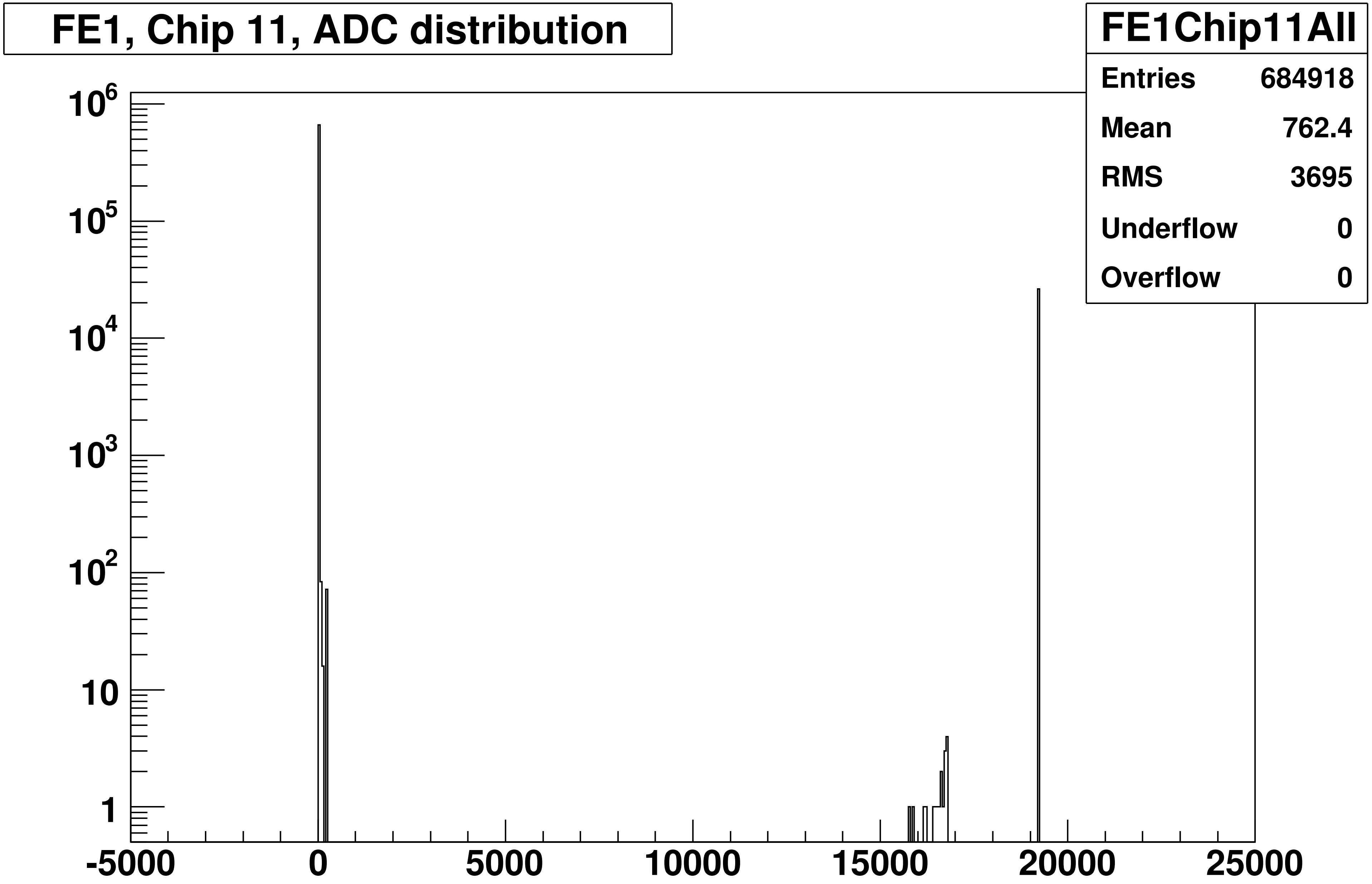
FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 684918
Mean 1.907e+04
RMS 7.977
Underflow 6.582e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



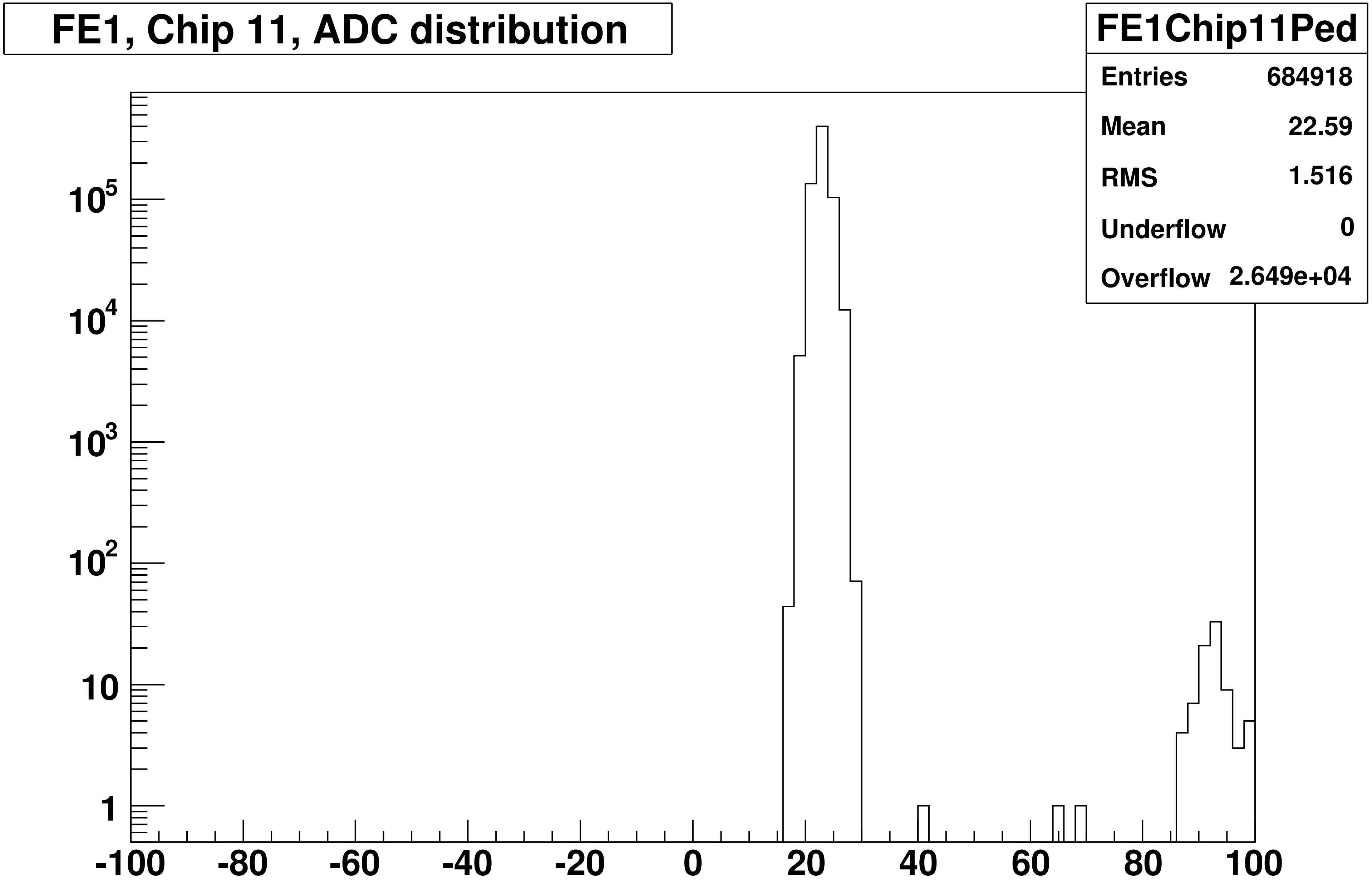
FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 684918
Mean 755.8
RMS 3664
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 684918
Mean 21.13
RMS 1.535
Underflow 0
Overflow 2.649e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

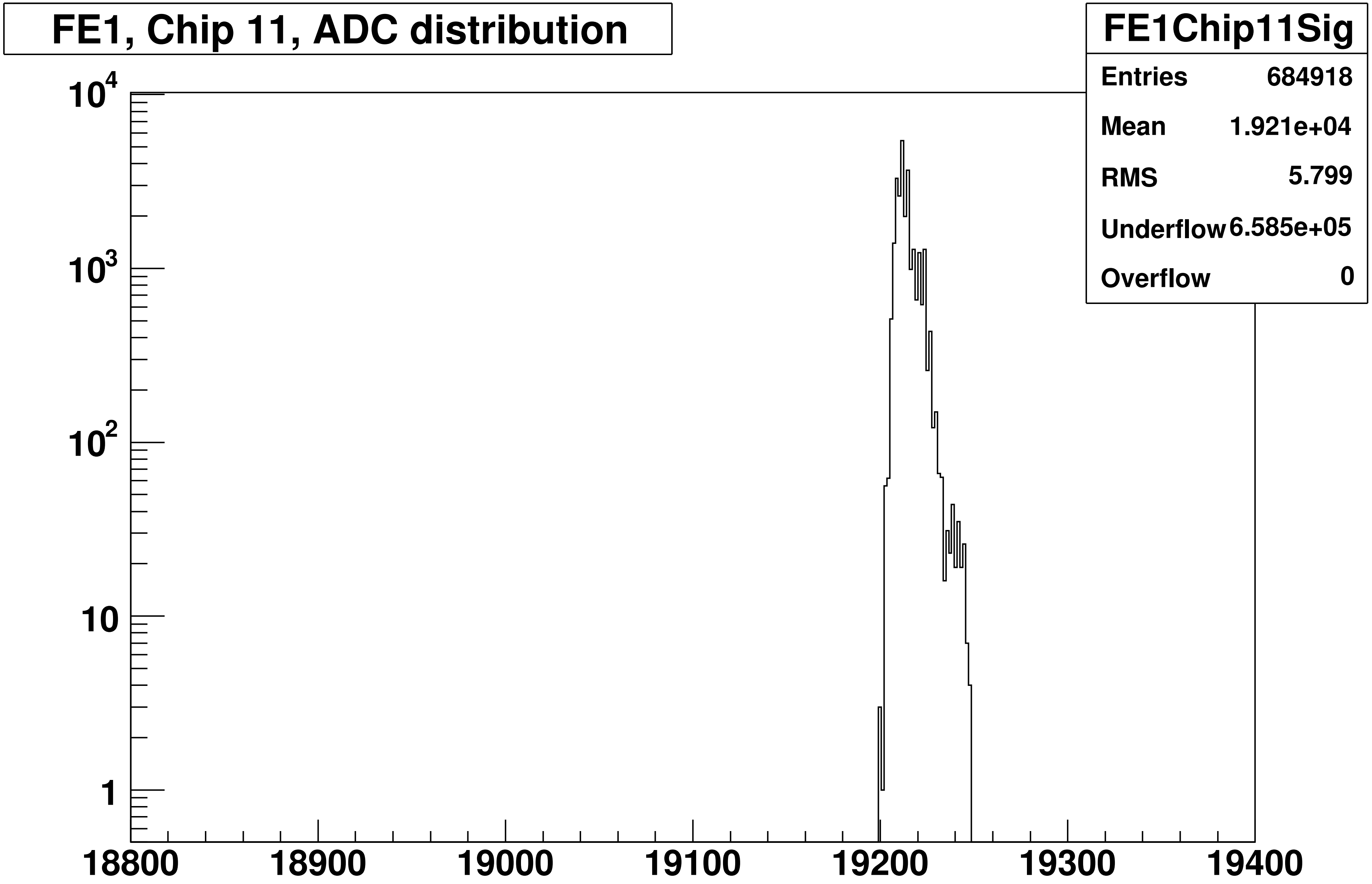

FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 684918
Mean 1.902e+04
RMS 6.522
Underflow 6.584e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

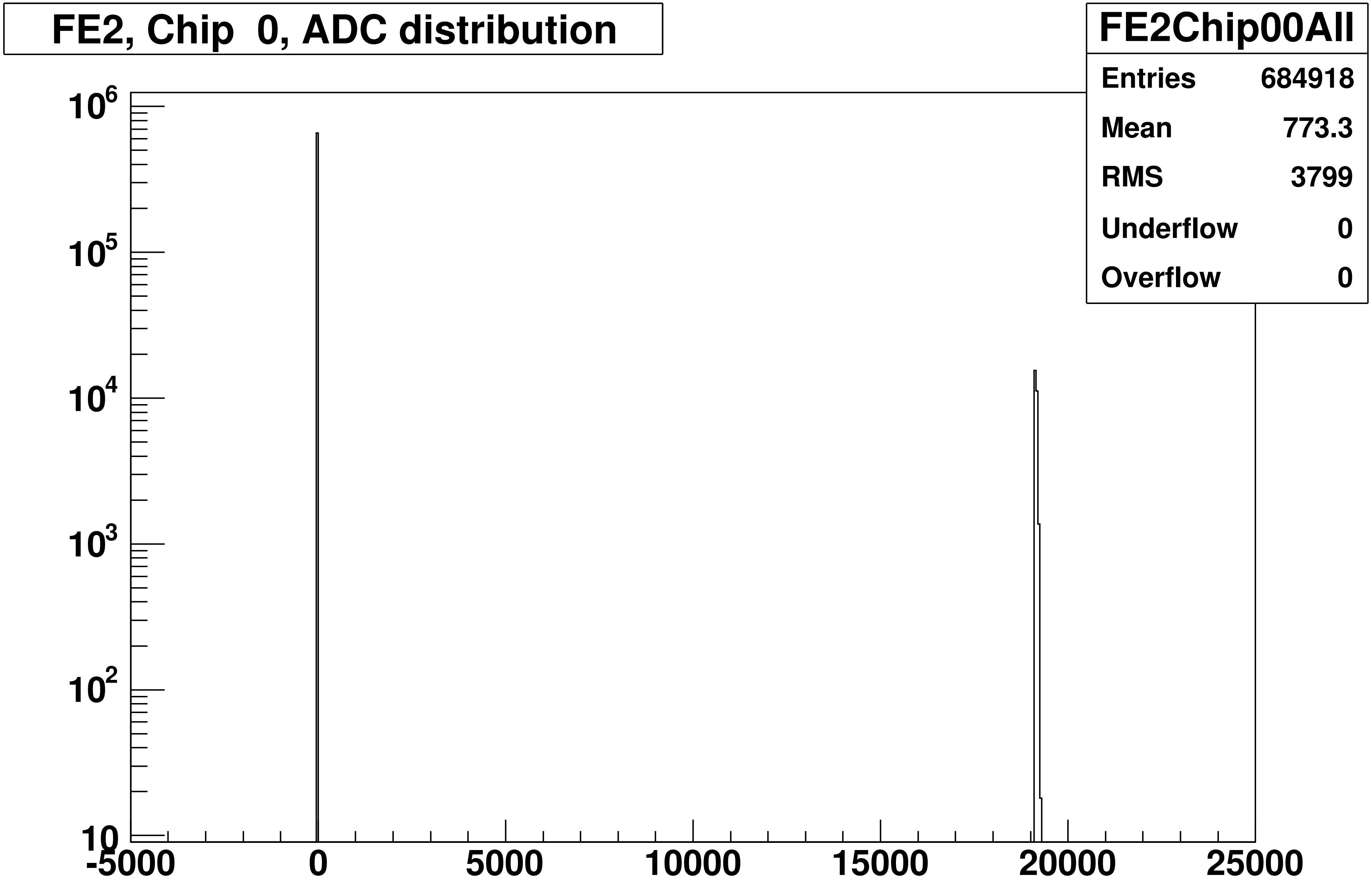


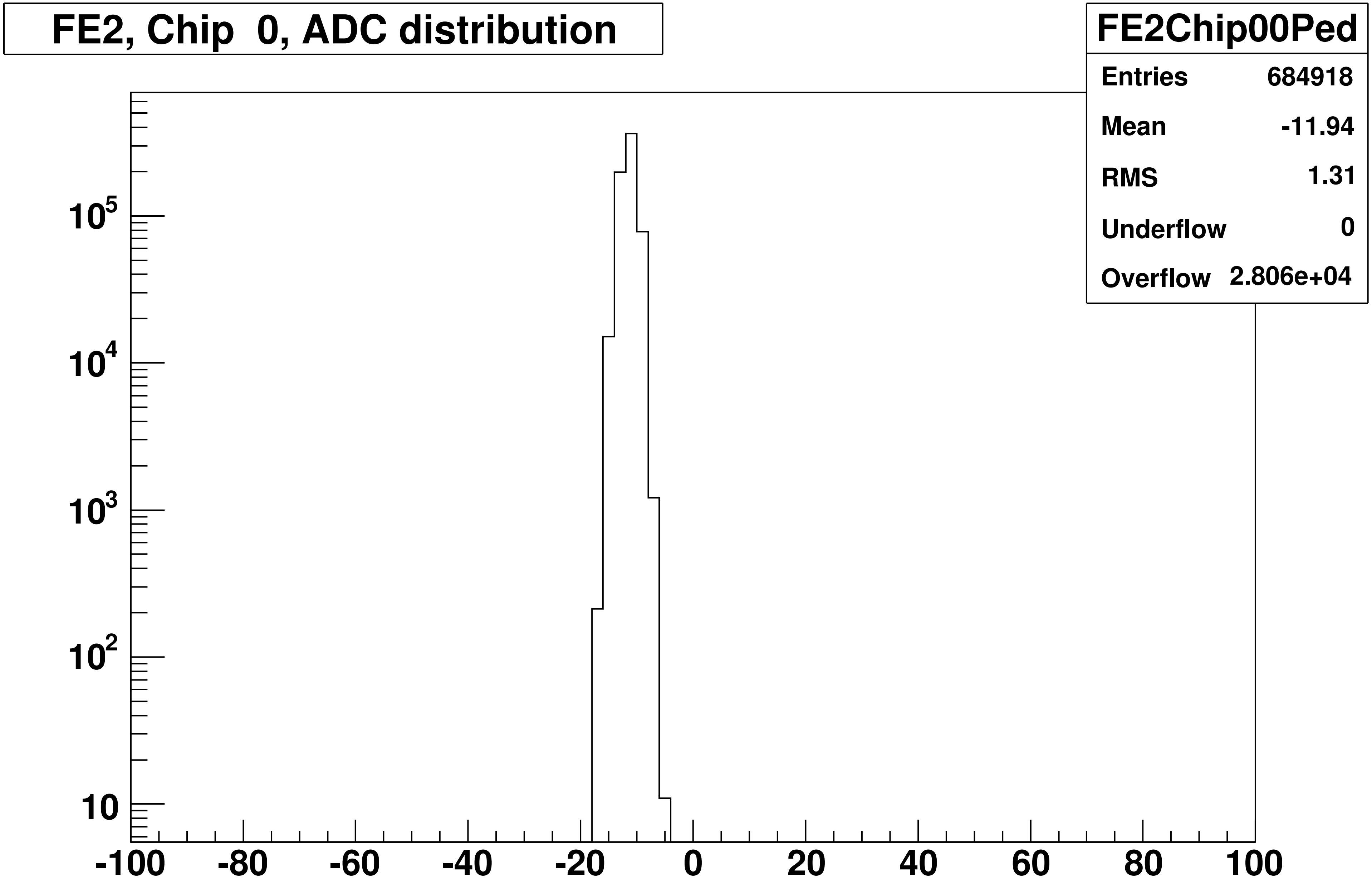
FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 684918
Mean 762.4
RMS 3695
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

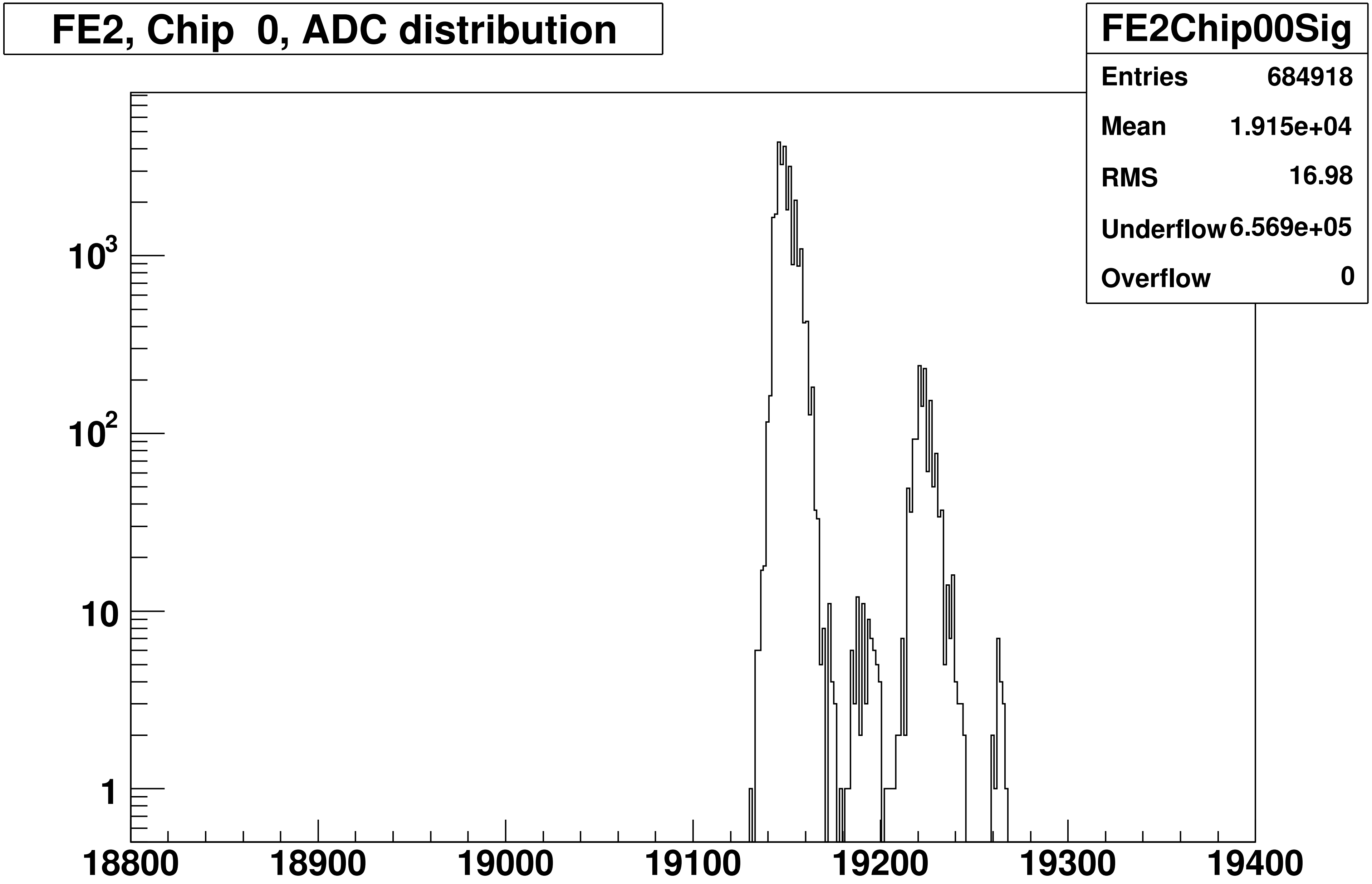


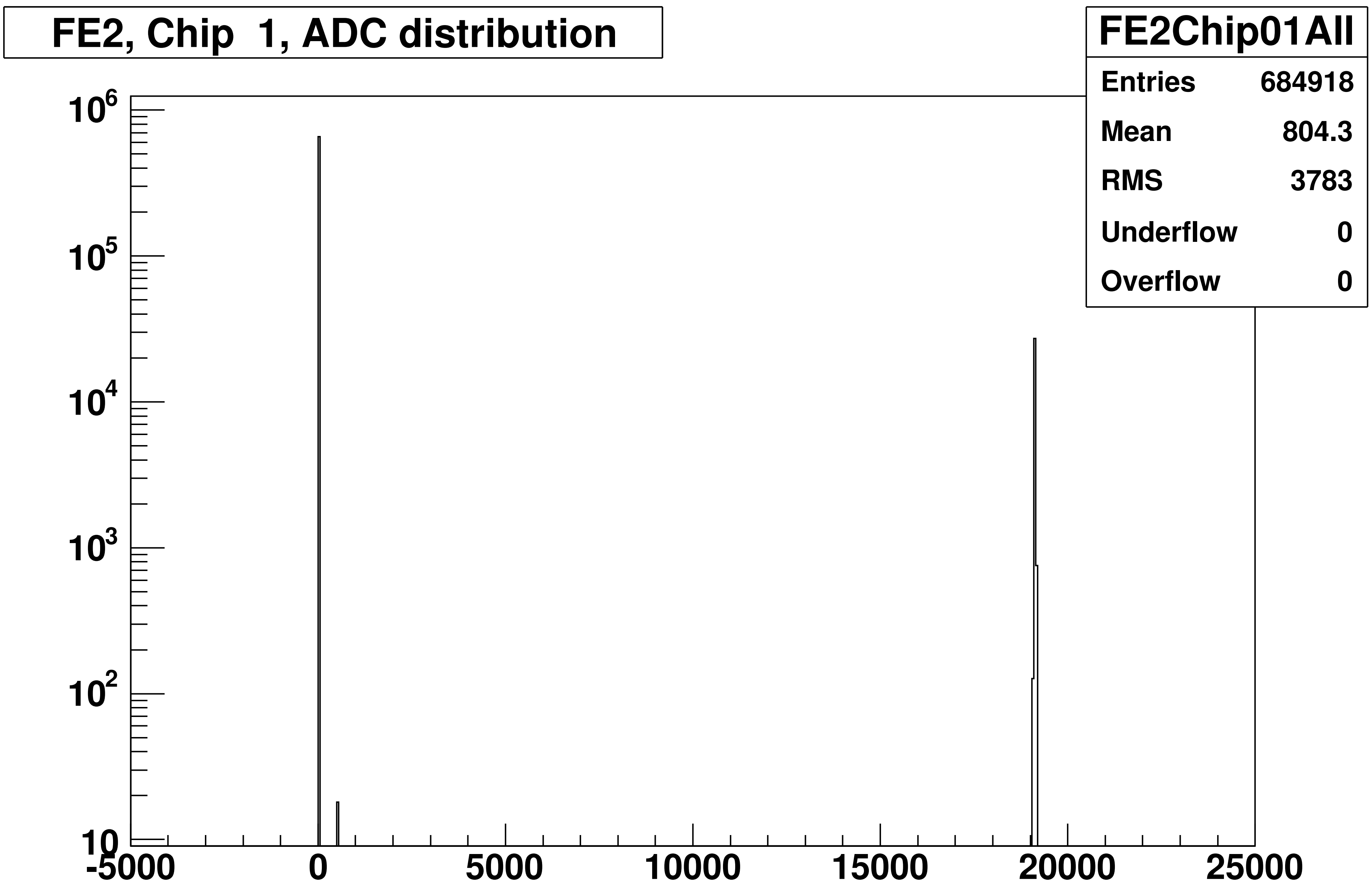
FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 684918
Mean 22.59
RMS 1.516
Underflow 0
Overflow 2.649e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

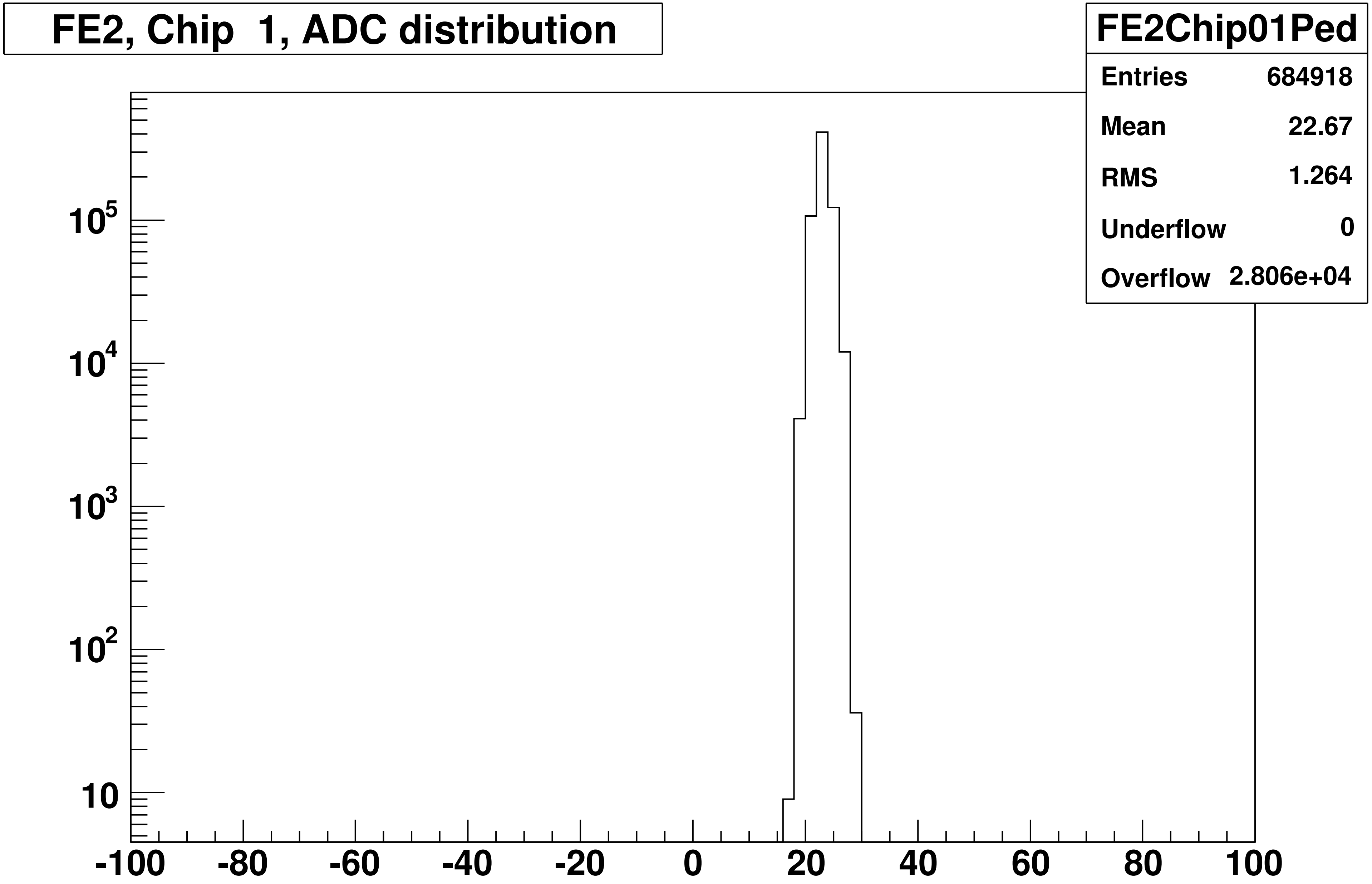

FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 684918
Mean 1.921e+04
RMS 5.799
Underflow 6.585e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

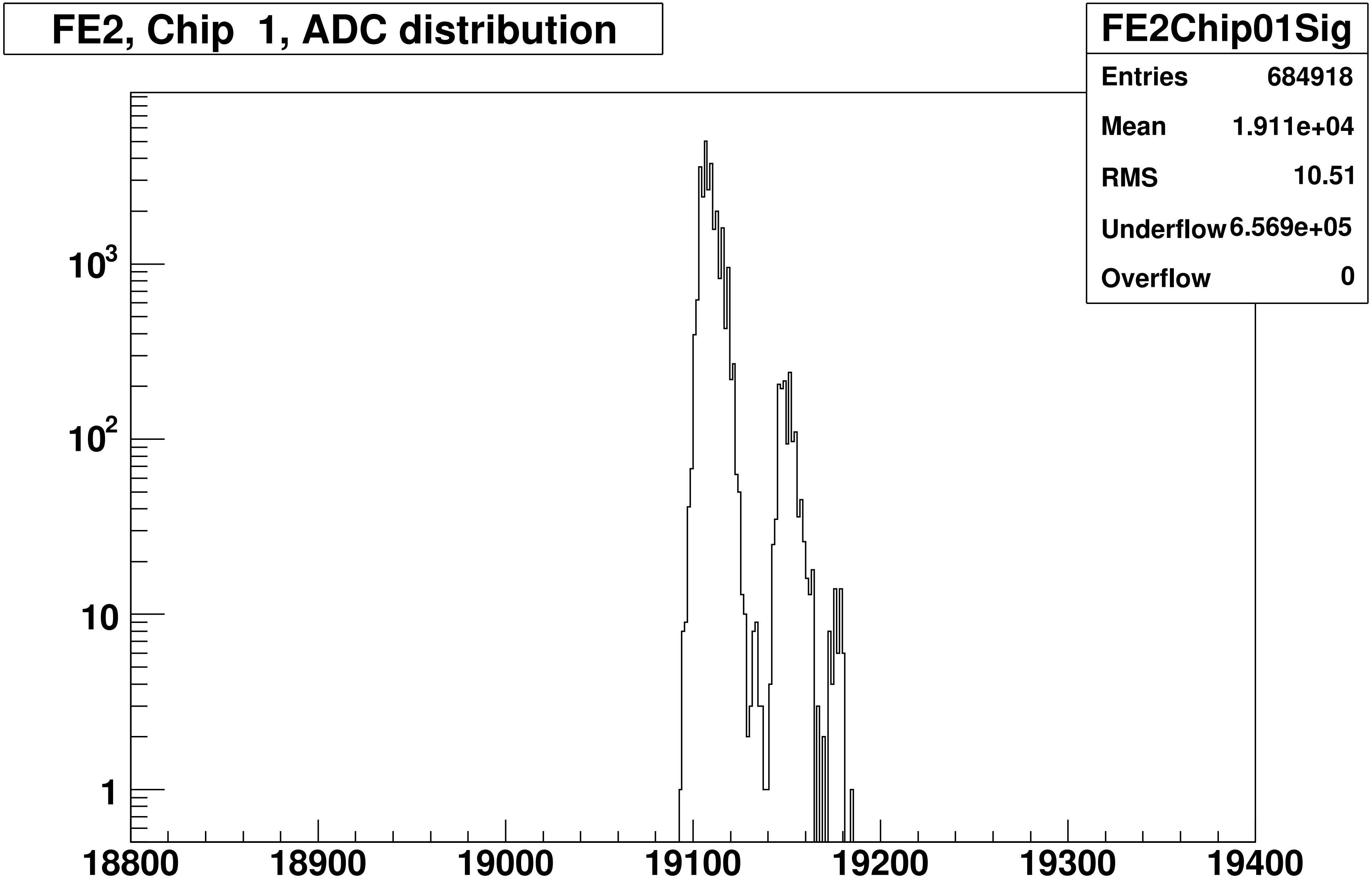

FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 684918
Mean 773.3
RMS 3799
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 684918
Mean -11.94
RMS 1.31
Underflow 0
Overflow 2.806e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

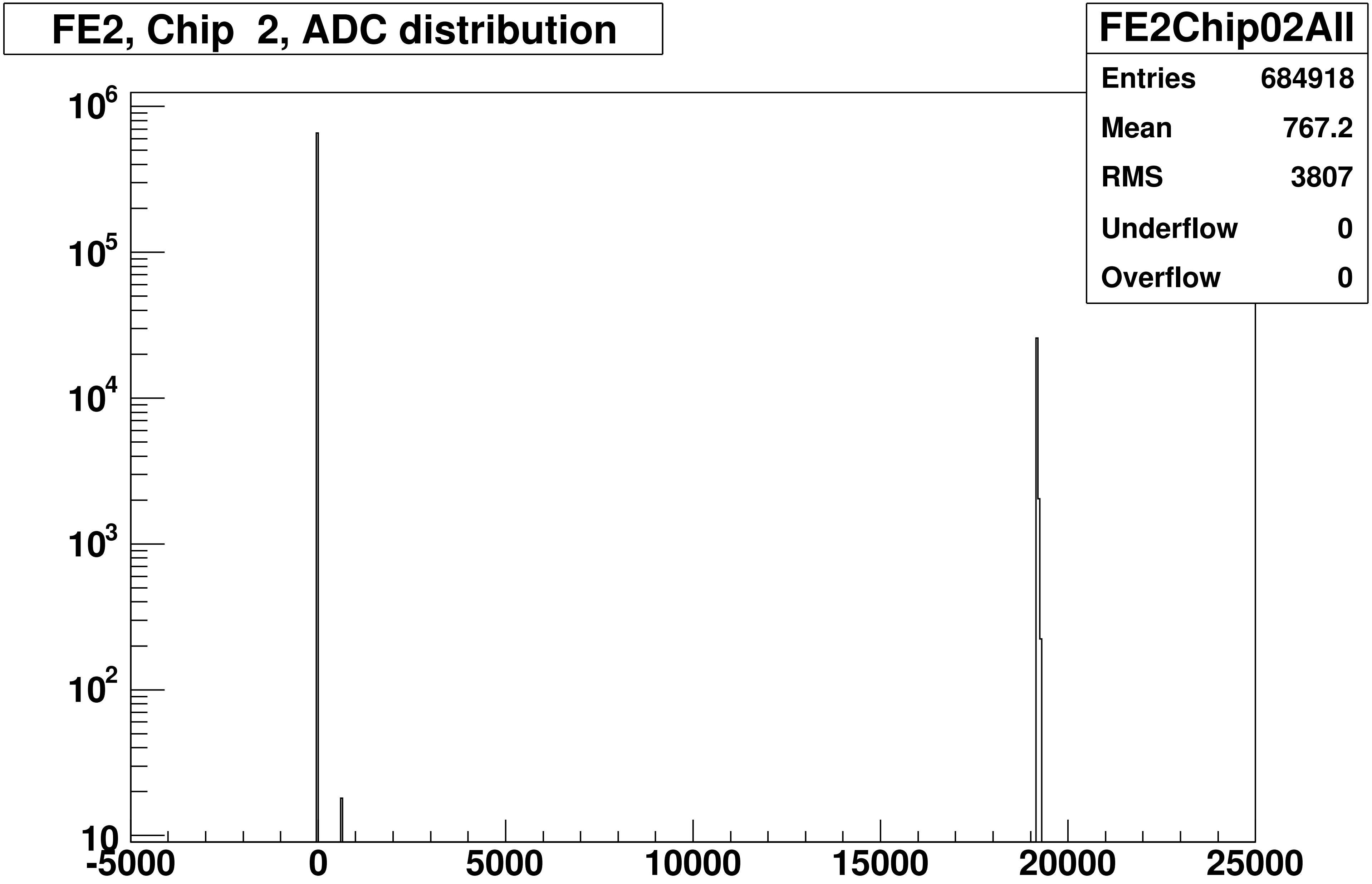

FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 684918
Mean 1.915e+04
RMS 16.98
Underflow 6.569e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 684918
Mean 804.3
RMS 3783
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

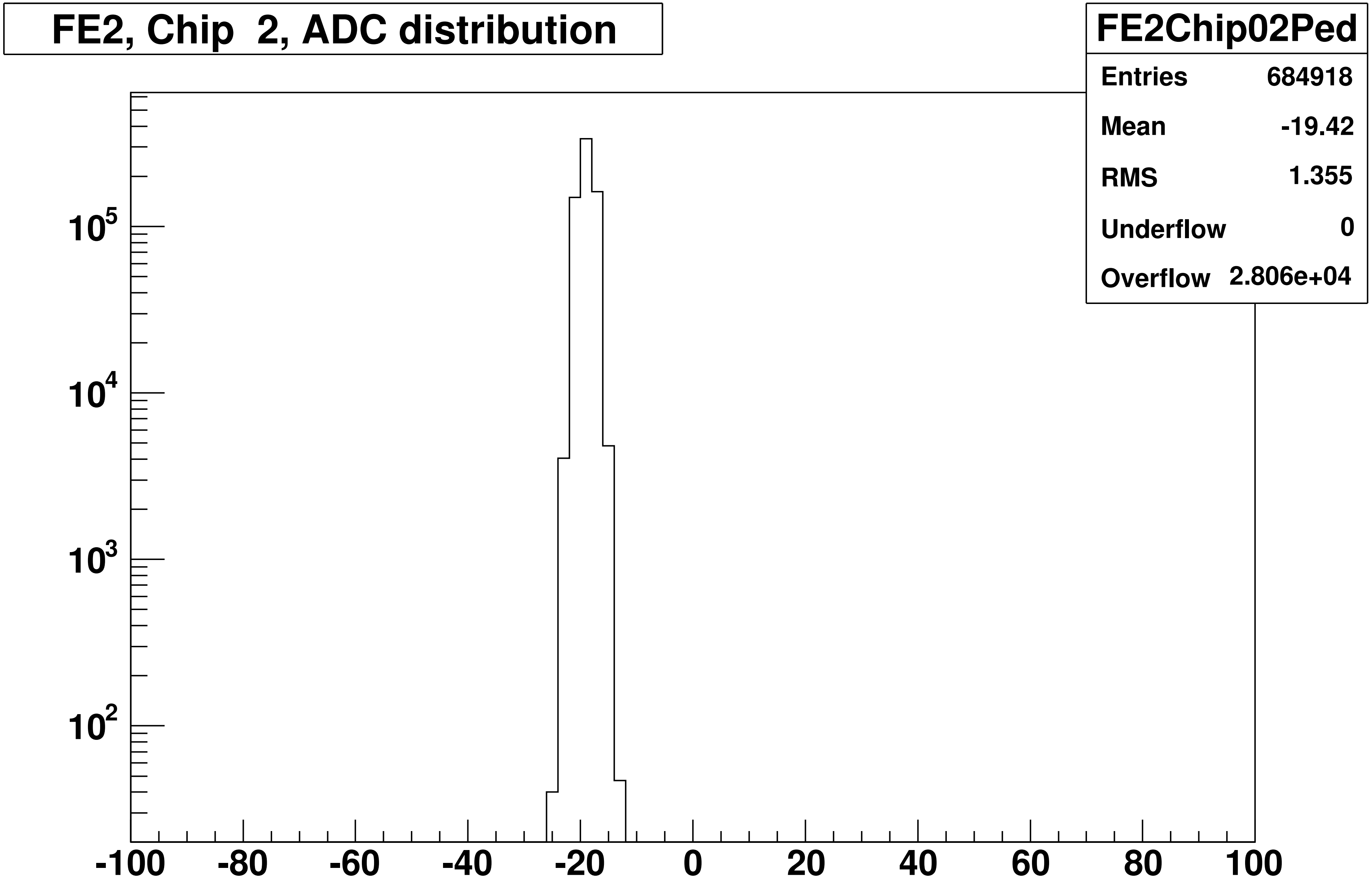

FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 684918
Mean 22.67
RMS 1.264
Underflow 0
Overflow 2.806e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

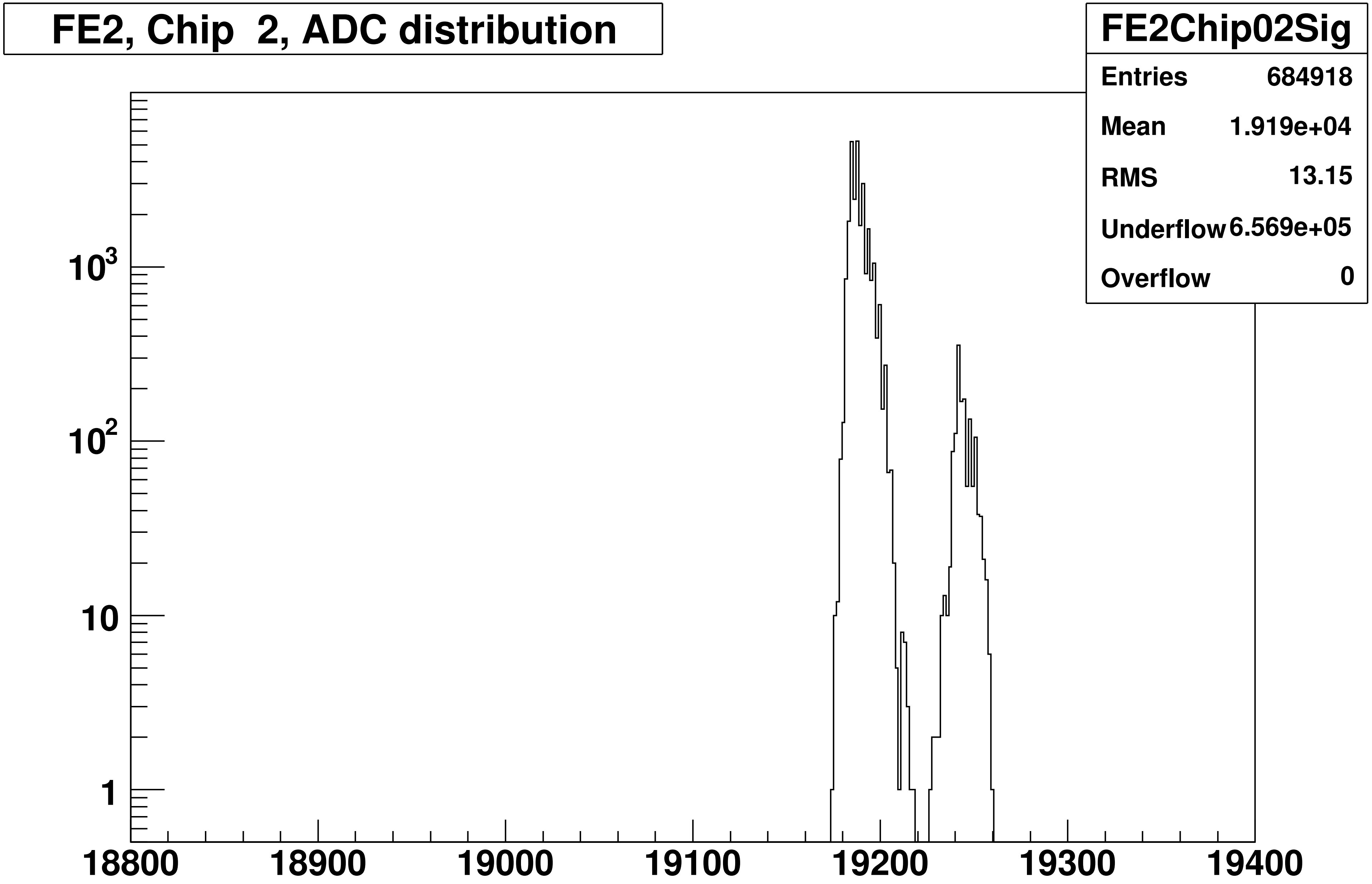


FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 684918
Mean 1.911e+04
RMS 10.51
Underflow 6.569e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

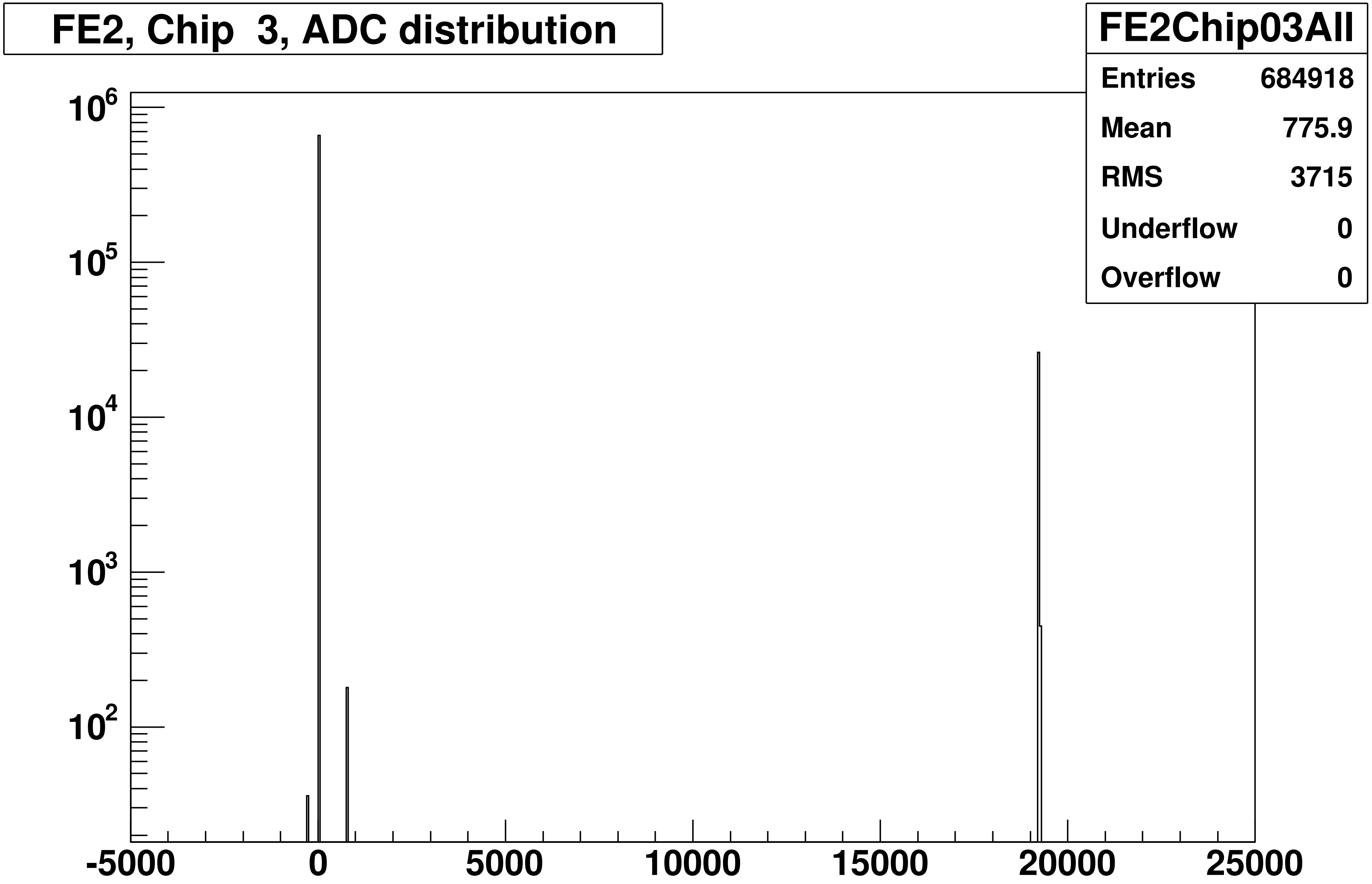


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 684918
Mean 767.2
RMS 3807
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

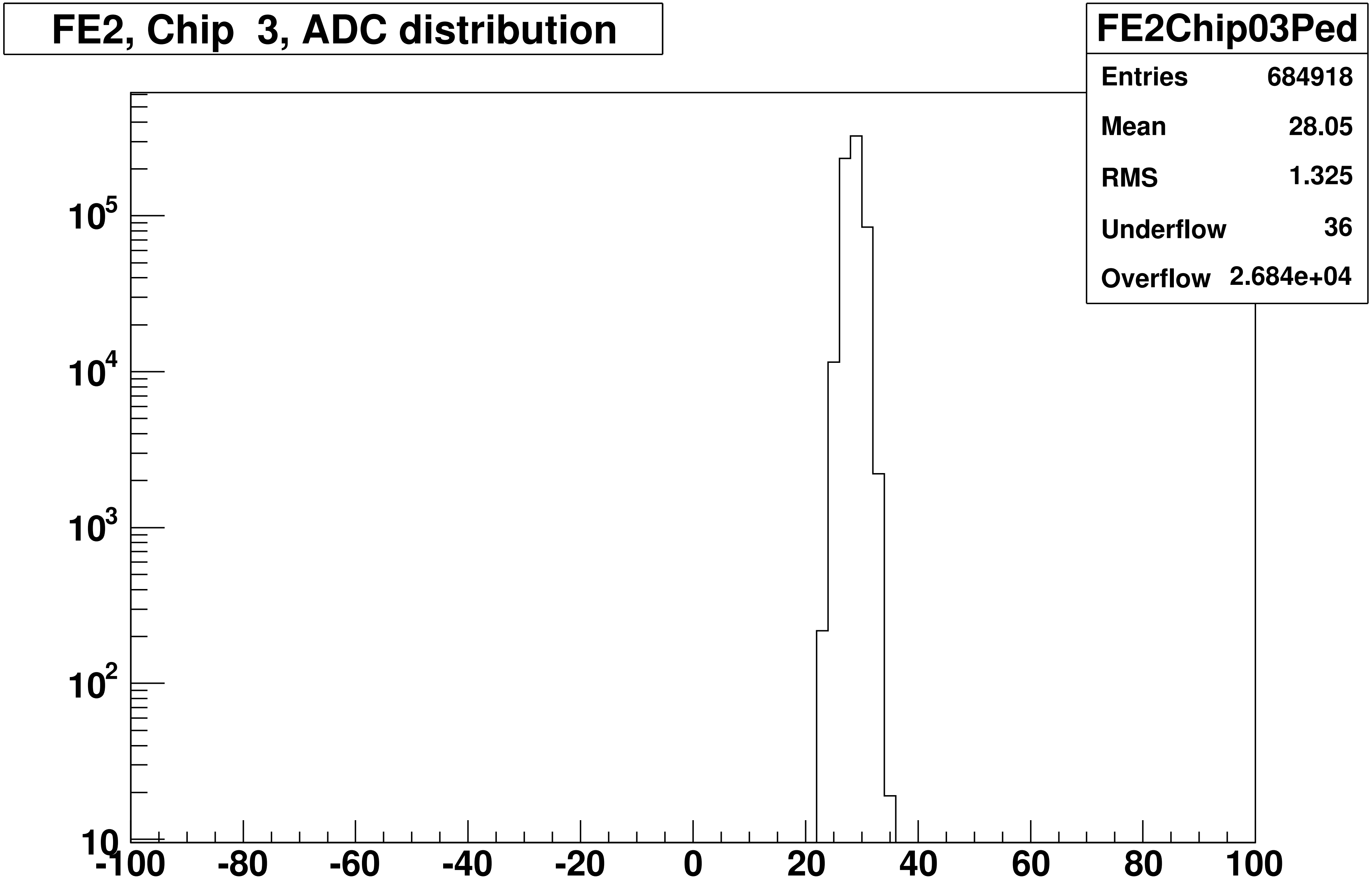

FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 684918
Mean -19.42
RMS 1.355
Underflow 0
Overflow 2.806e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

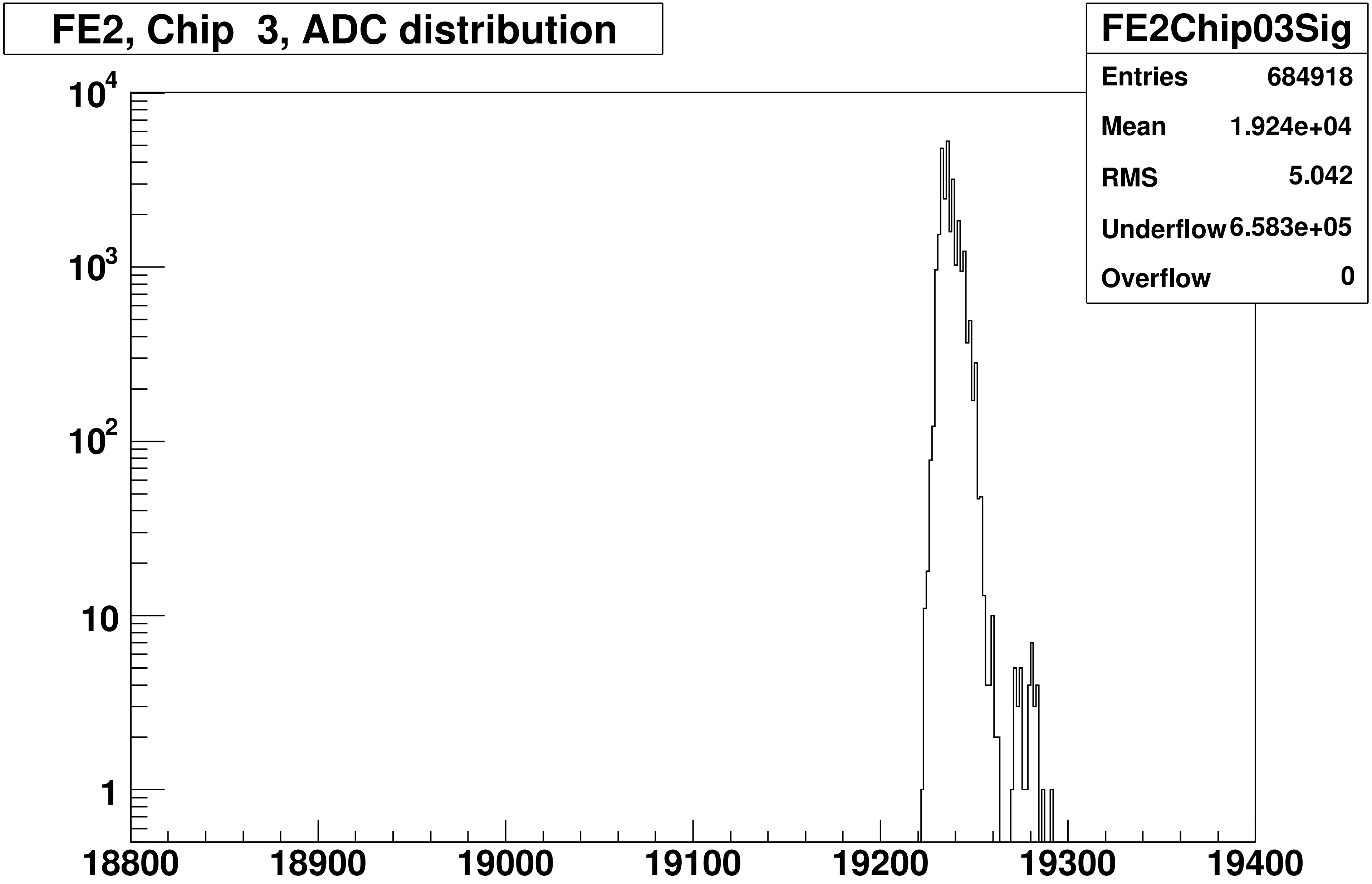


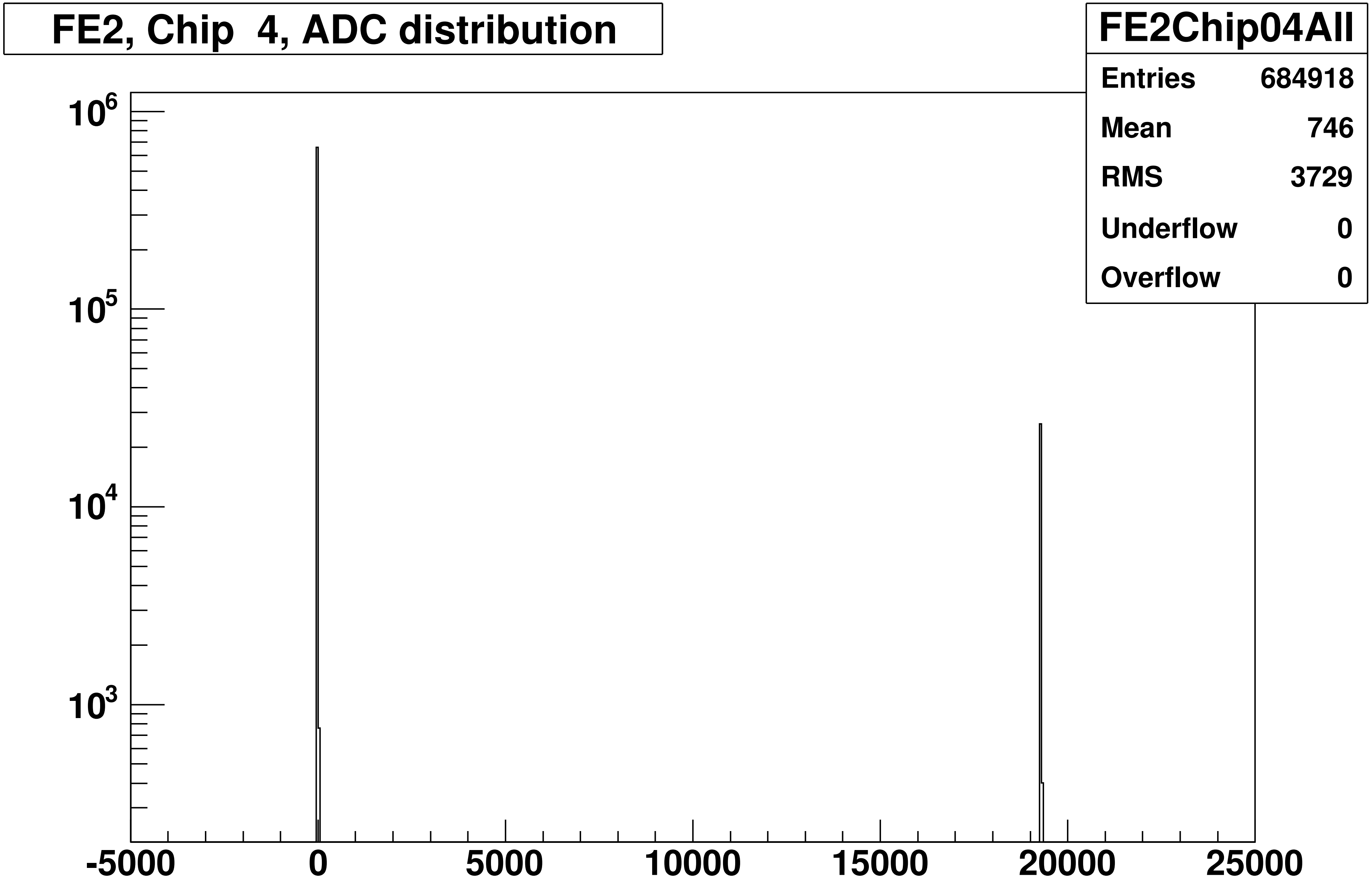
FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 684918
Mean 1.919e+04
RMS 13.15
Underflow 6.569e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

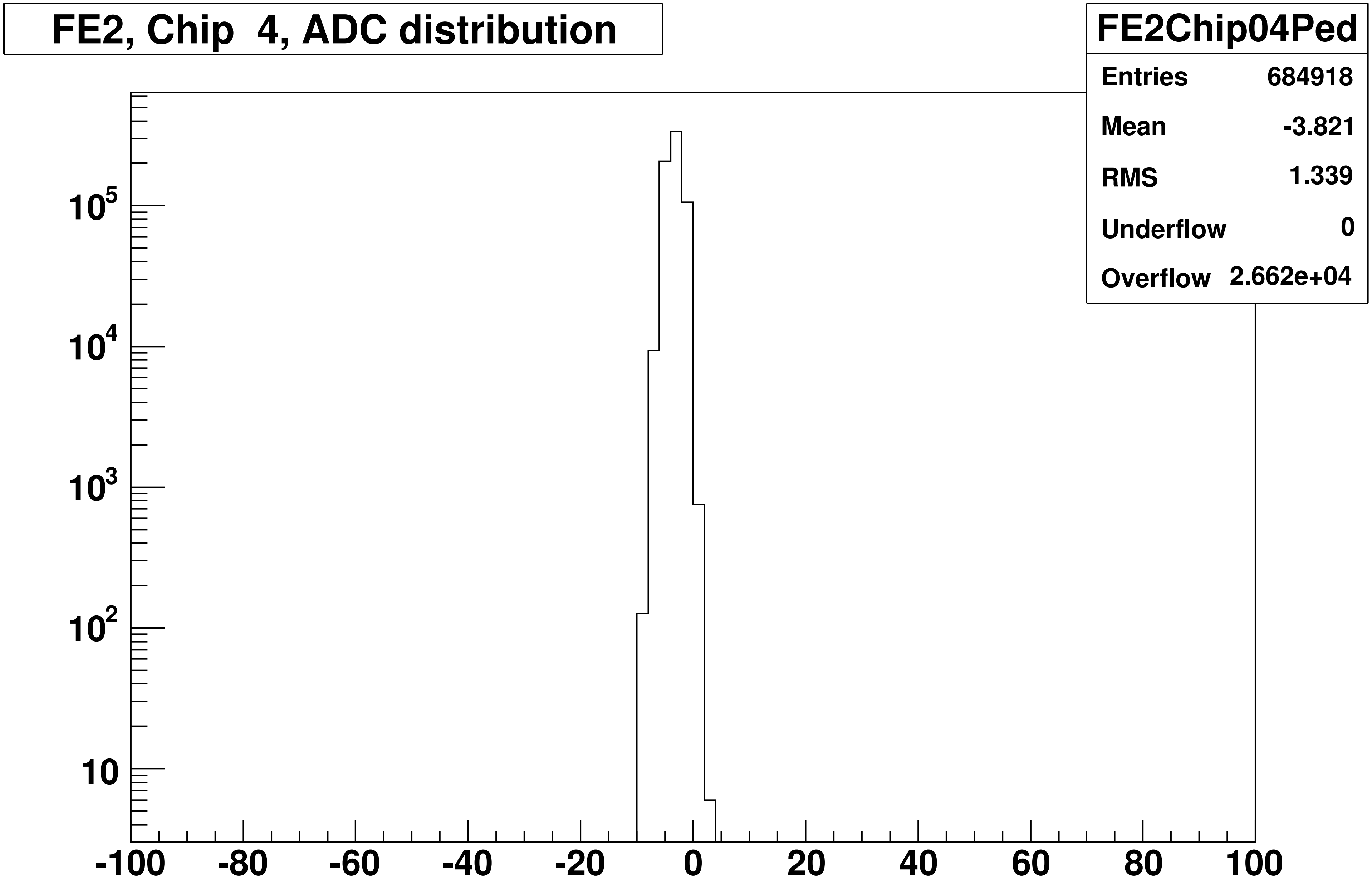


FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 684918
Mean 775.9
RMS 3715
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

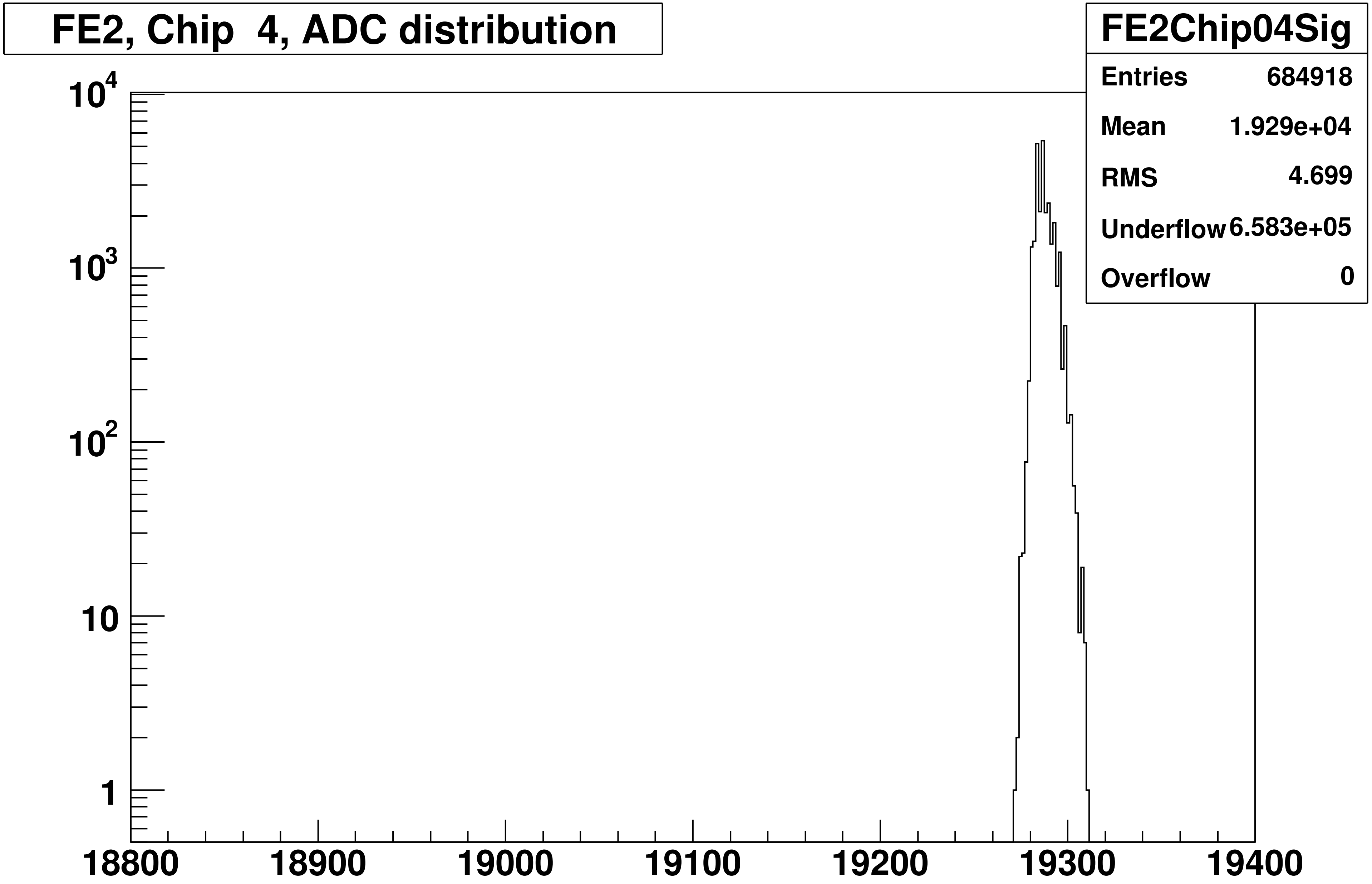

FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 684918
Mean 28.05
RMS 1.325
Underflow 36
Overflow 2.684e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

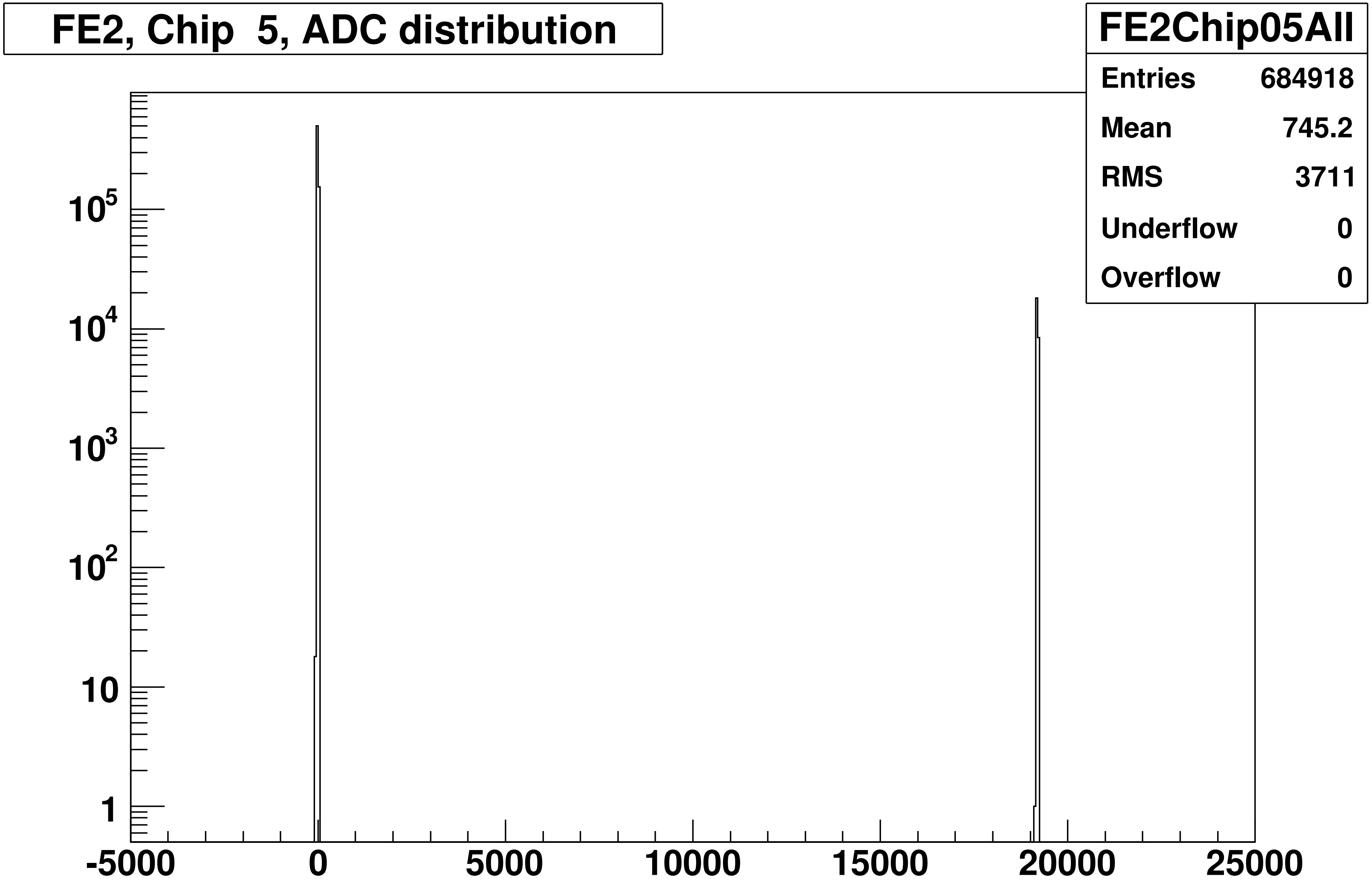

FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 684918
Mean 1.924e+04
RMS 5.042
Underflow 6.583e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

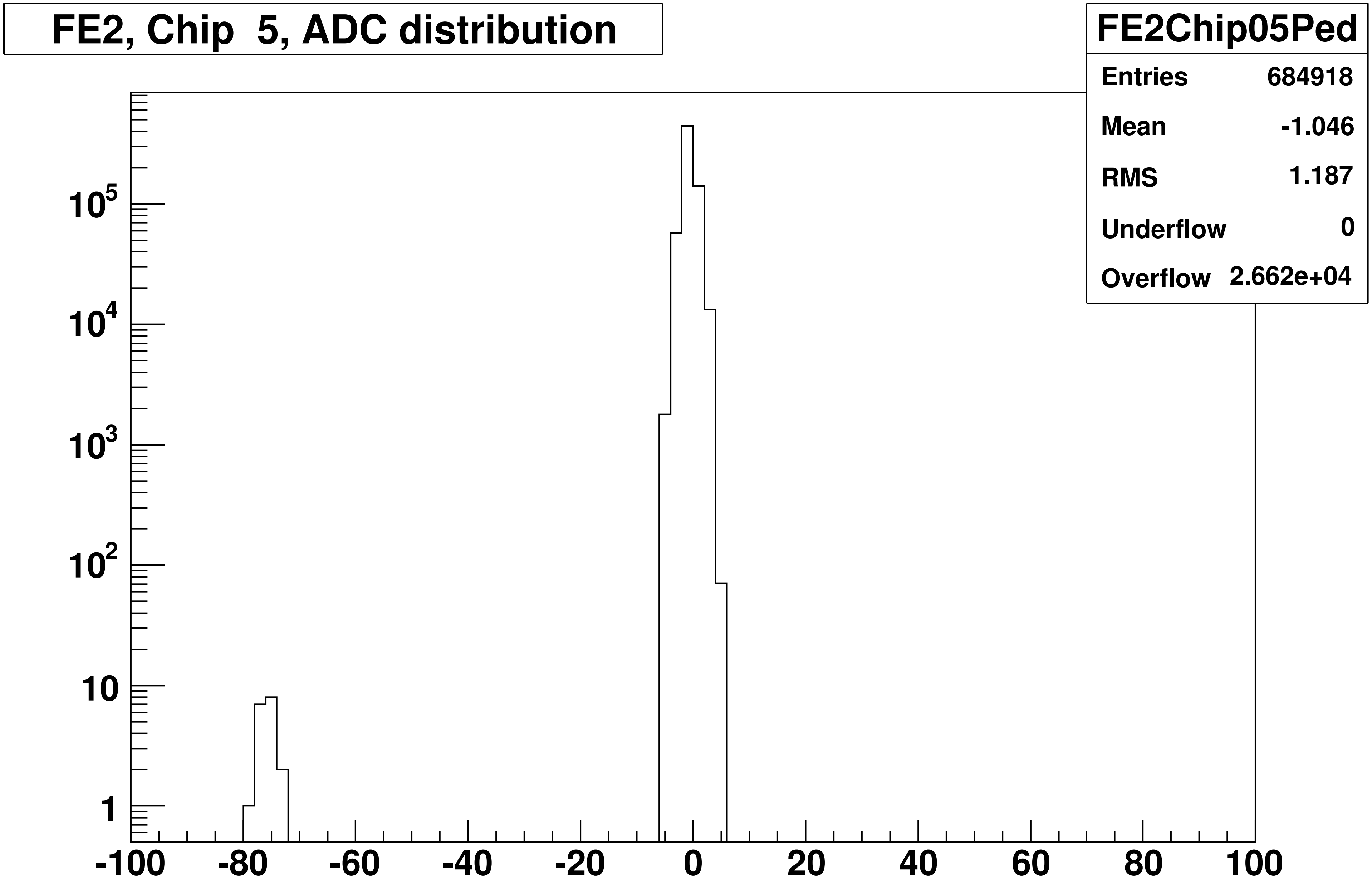

FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 684918
Mean 746
RMS 3729
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

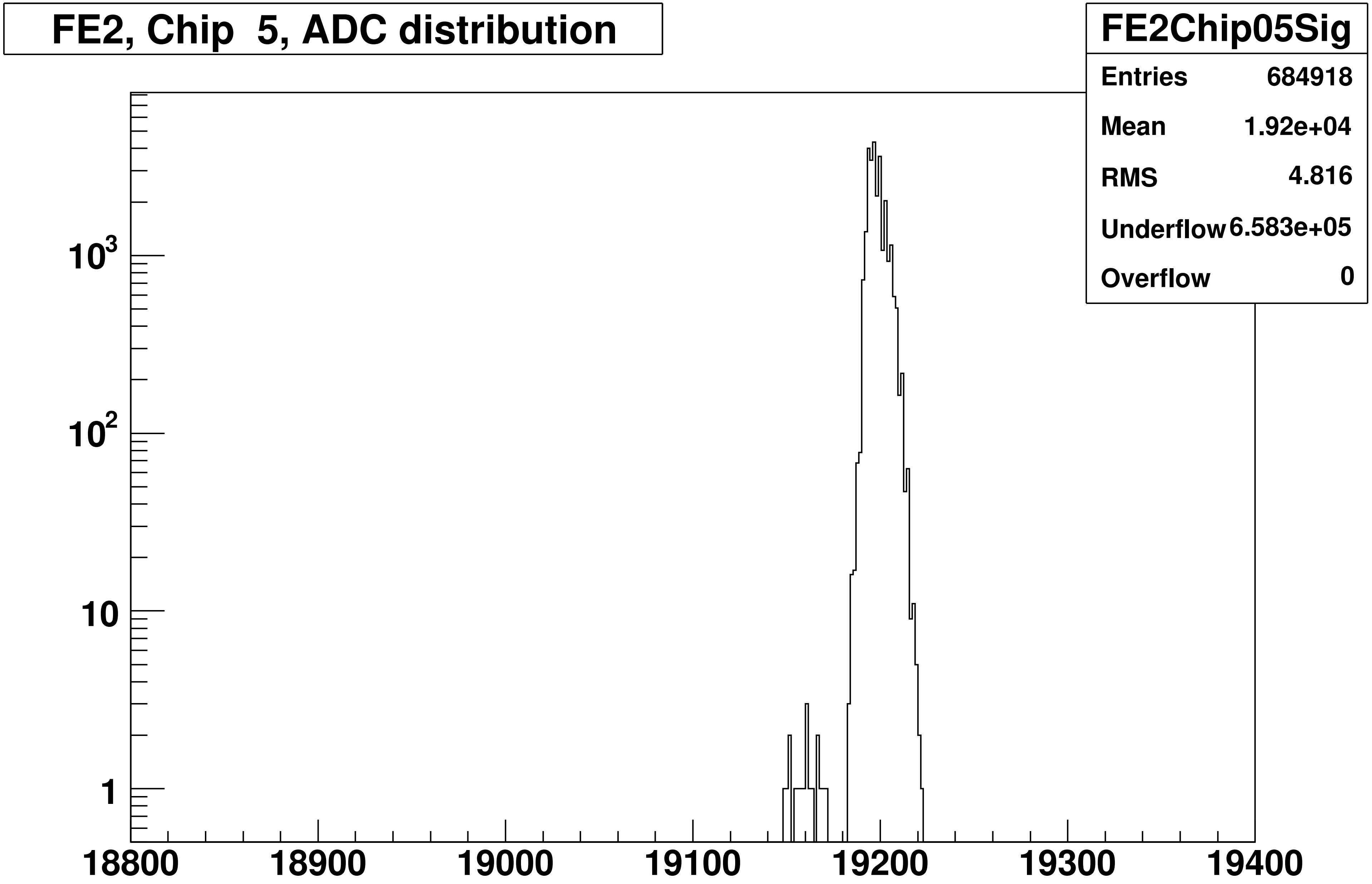


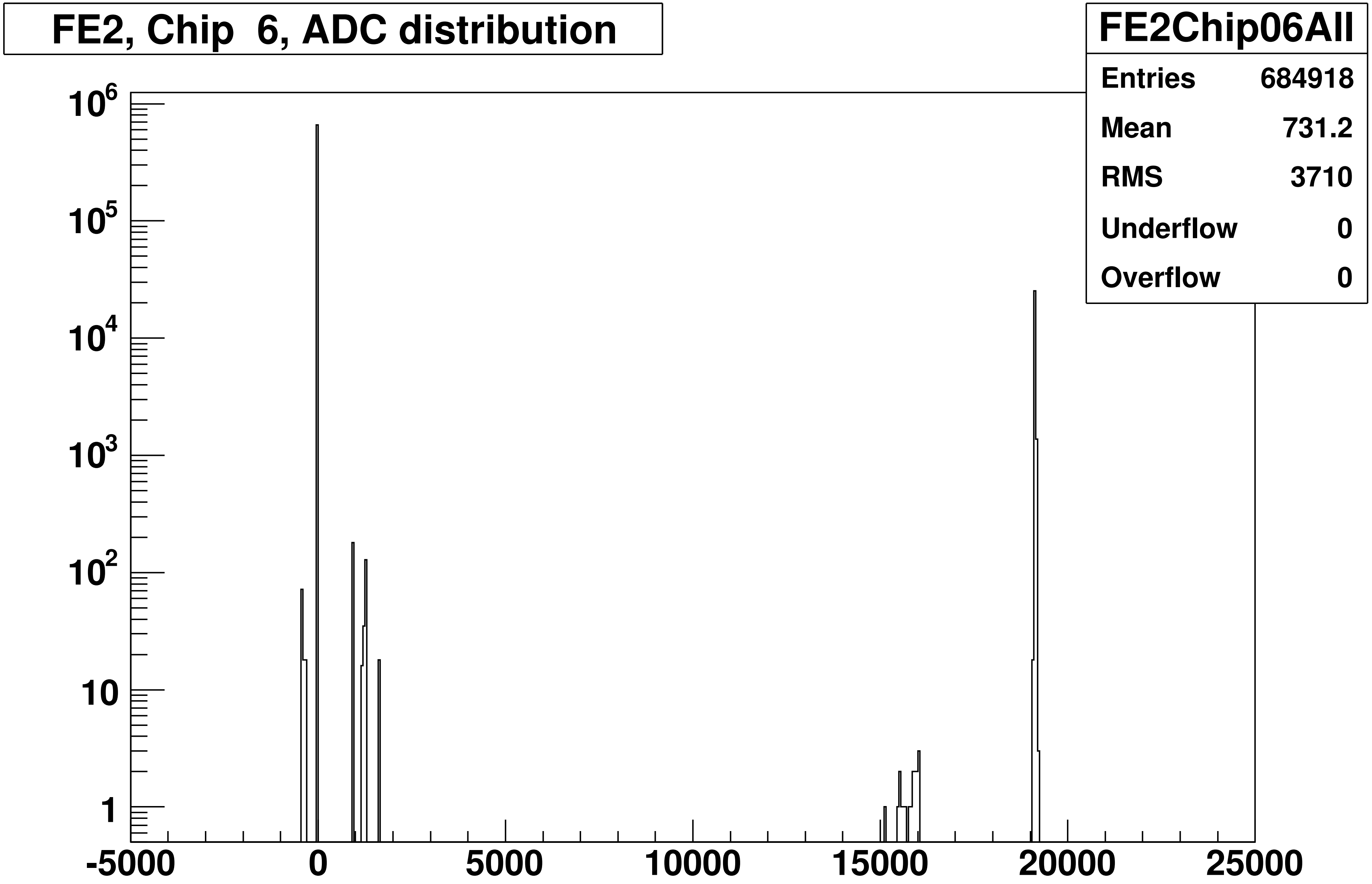
FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 684918
Mean -3.821
RMS 1.339
Underflow 0
Overflow 2.662e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

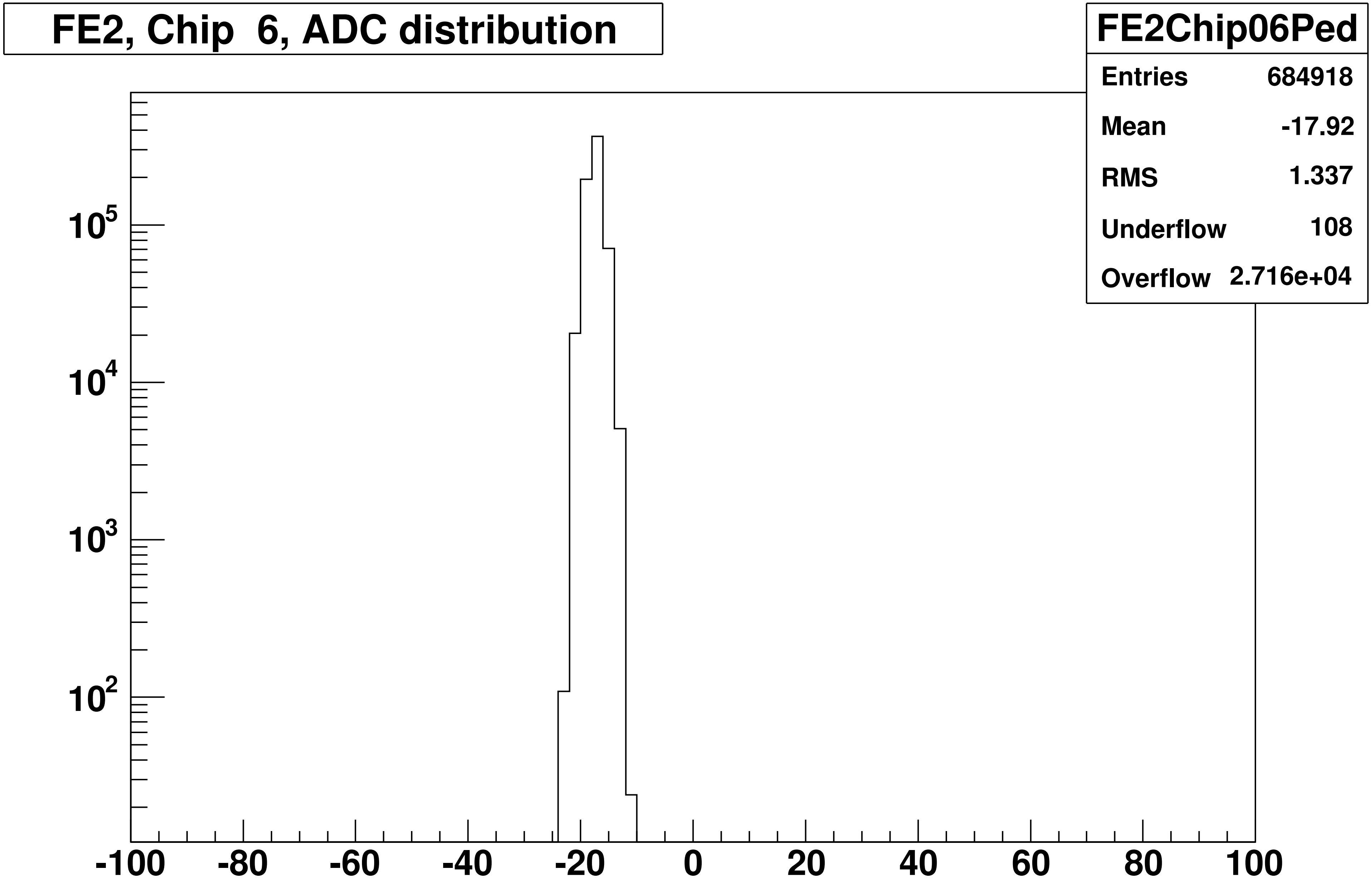

FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 684918
Mean 1.929e+04
RMS 4.699
Underflow 6.583e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

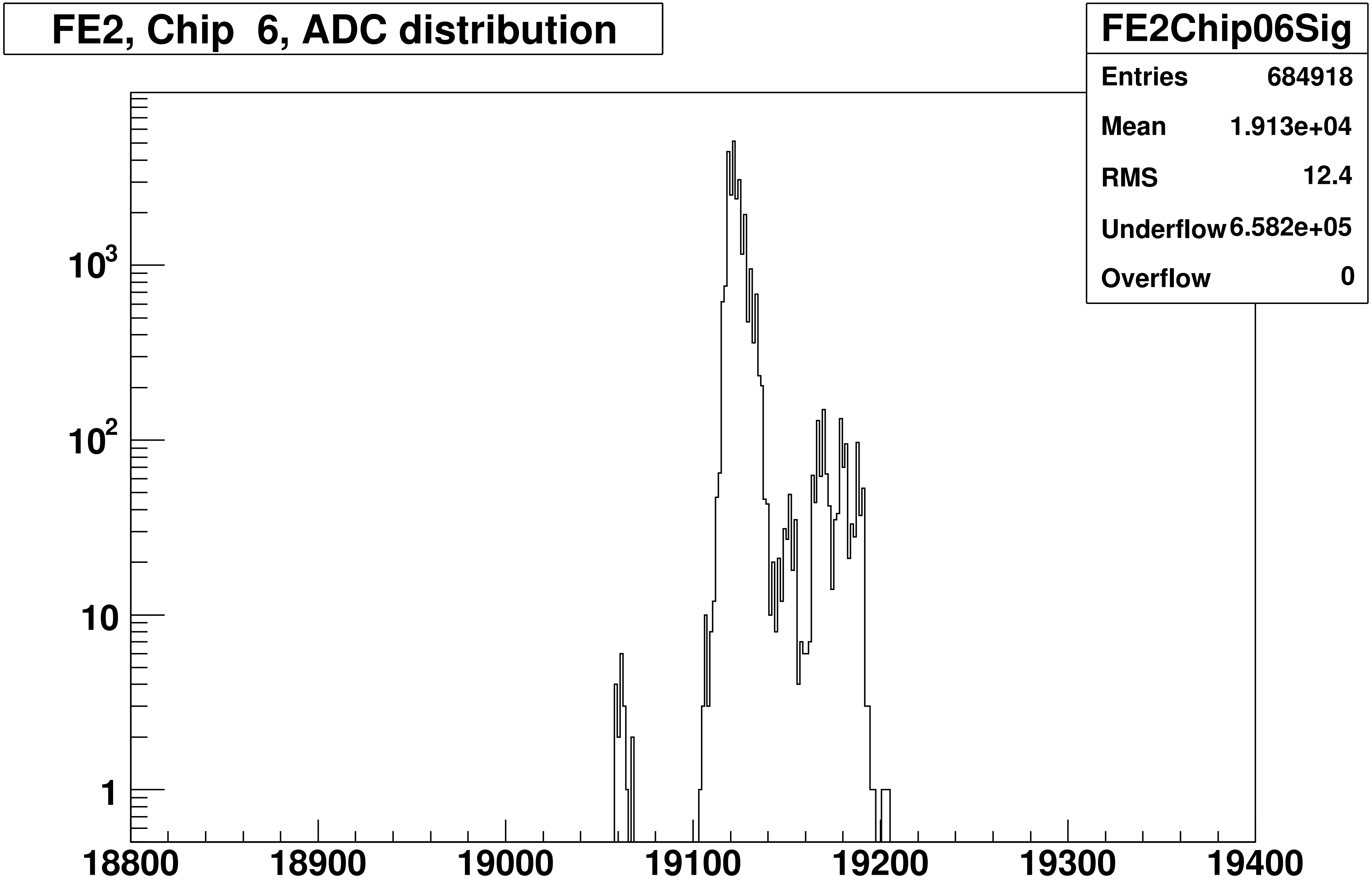

FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 684918
Mean 745.2
RMS 3711
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

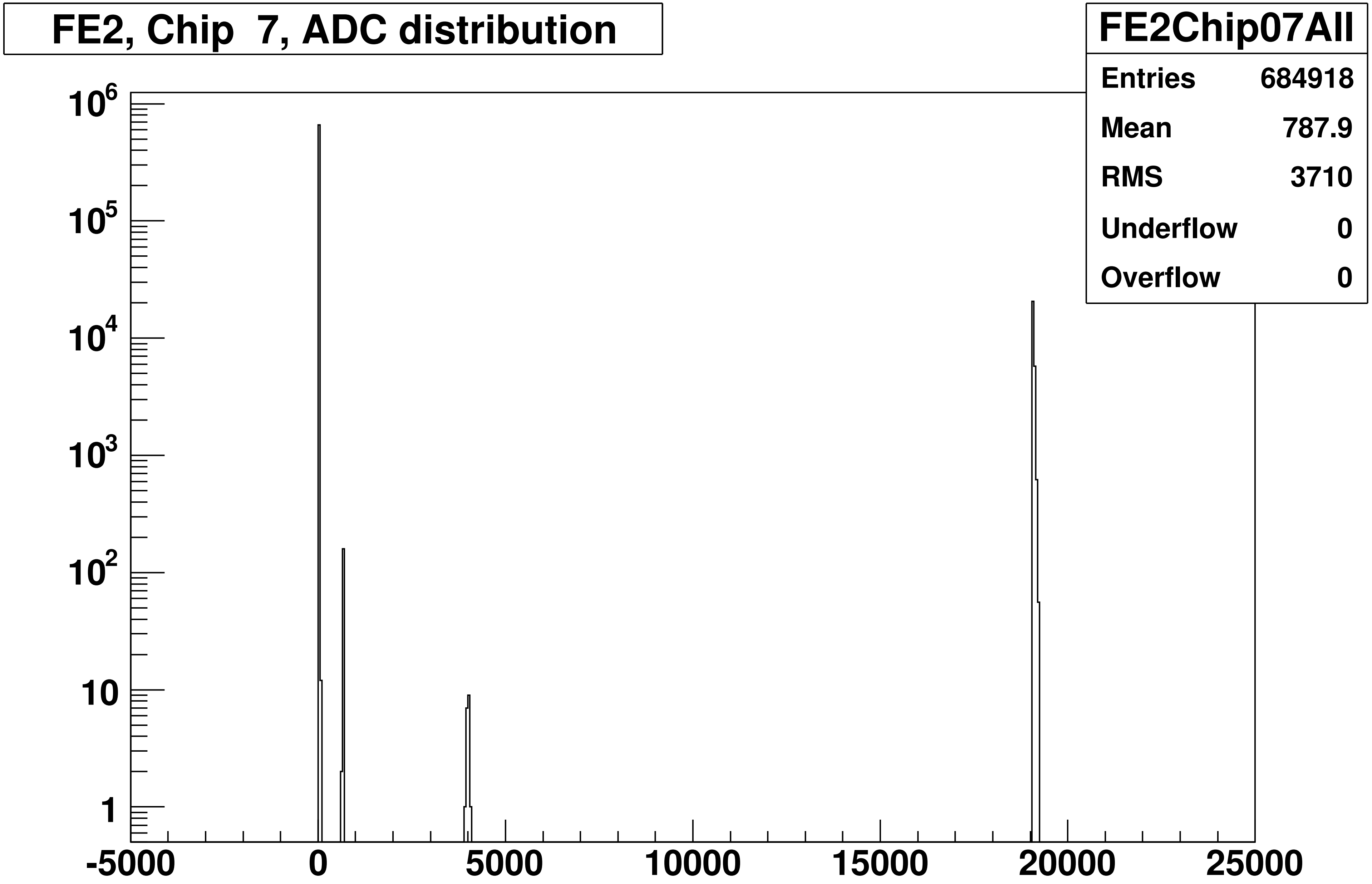

FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 684918
Mean -1.046
RMS 1.187
Underflow 0
Overflow 2.662e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

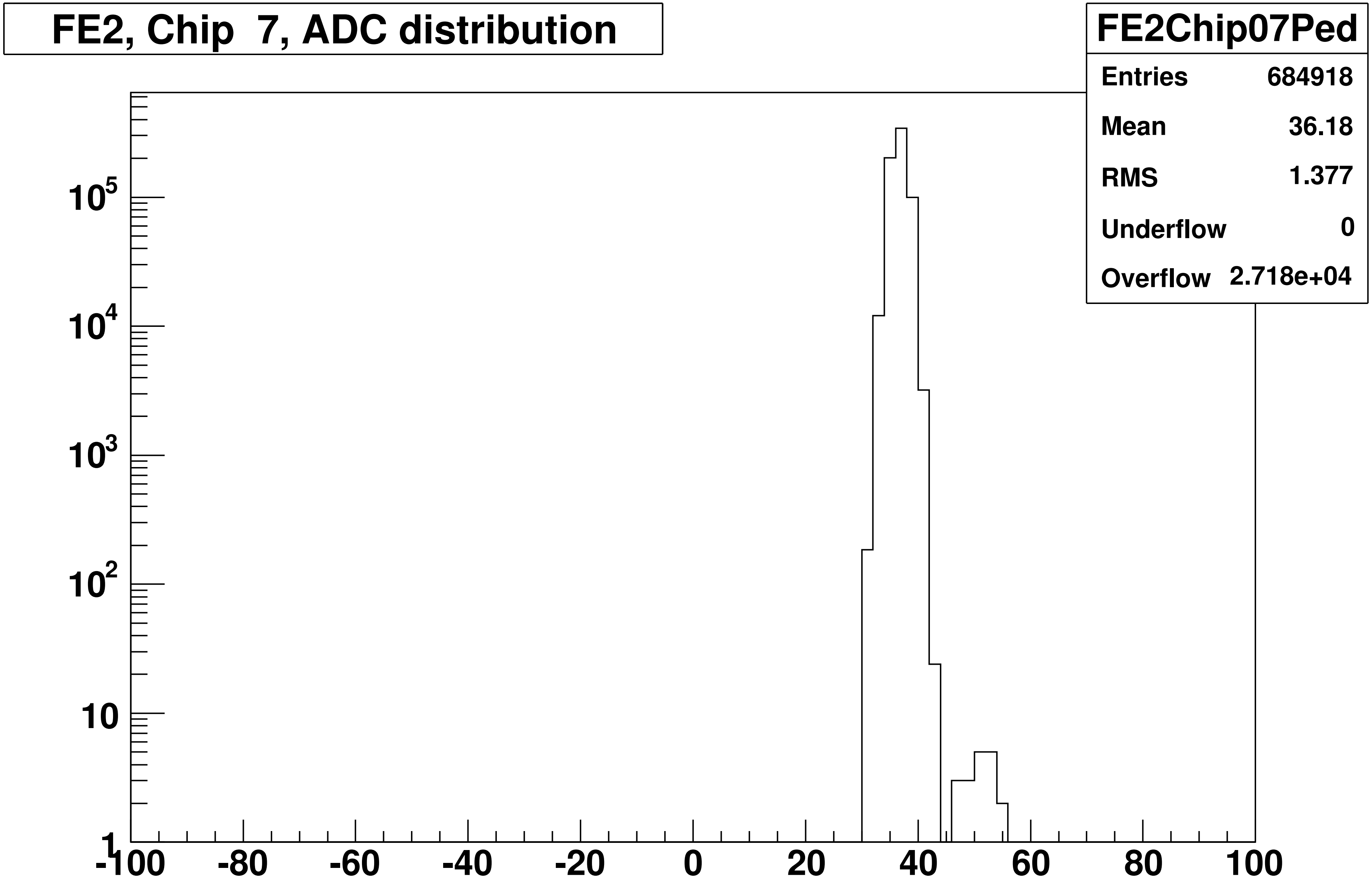

FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 684918
Mean 1.92e+04
RMS 4.816
Underflow 6.583e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

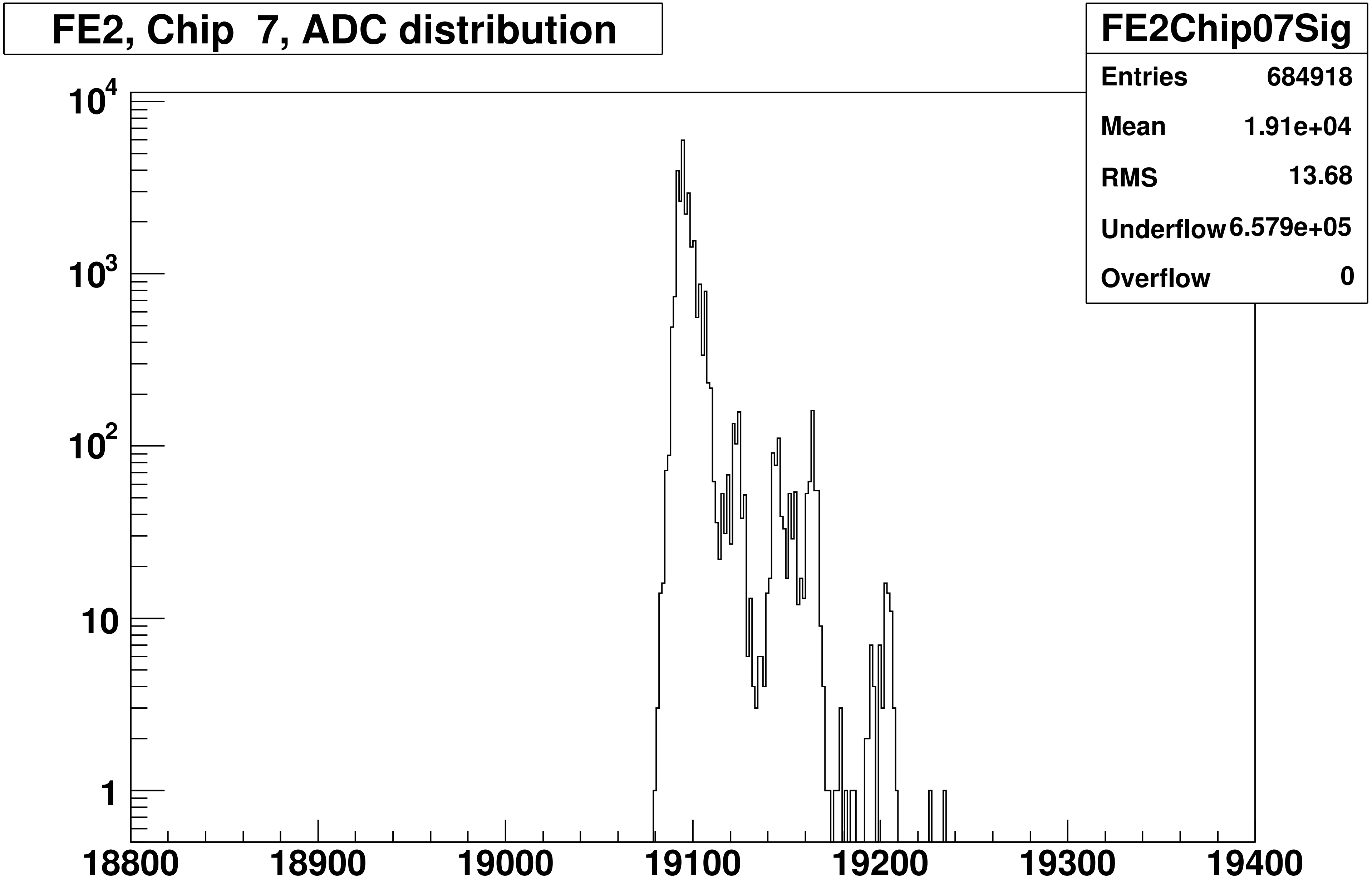

FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 684918
Mean 731.2
RMS 3710
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

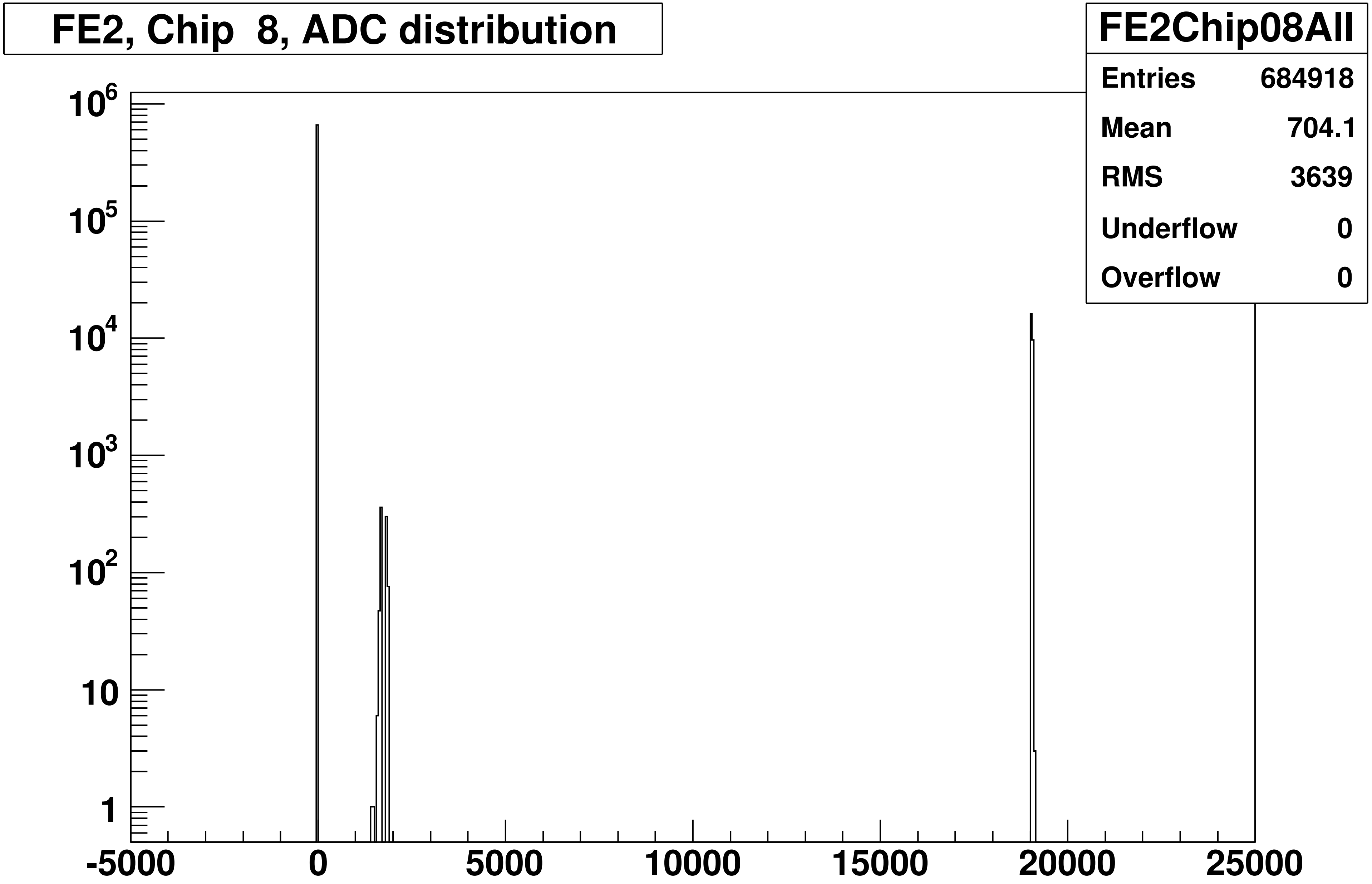

FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 684918
Mean -17.92
RMS 1.337
Underflow 108
Overflow 2.716e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

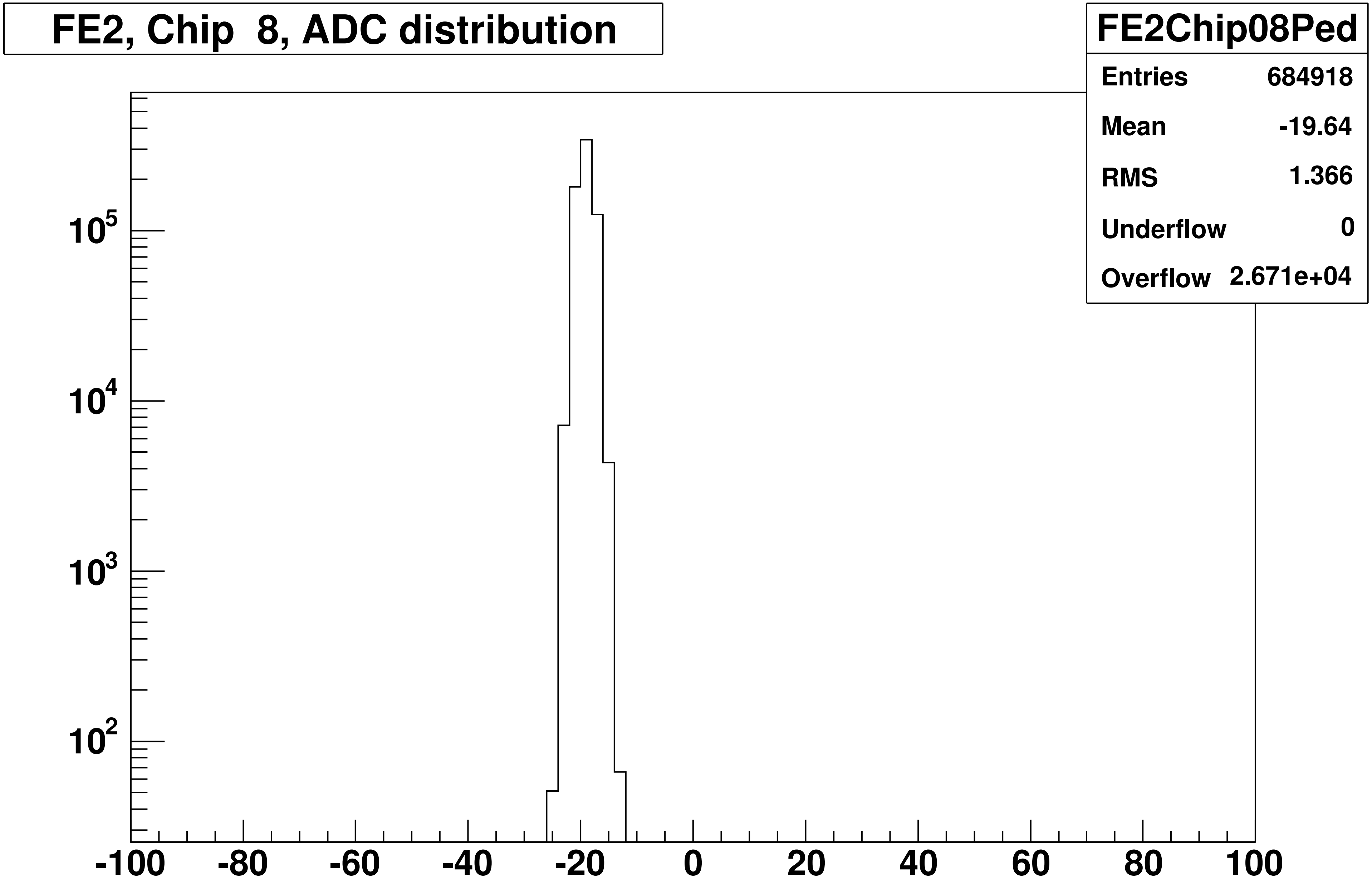

FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 684918
Mean 1.913e+04
RMS 12.4
Underflow 6.582e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

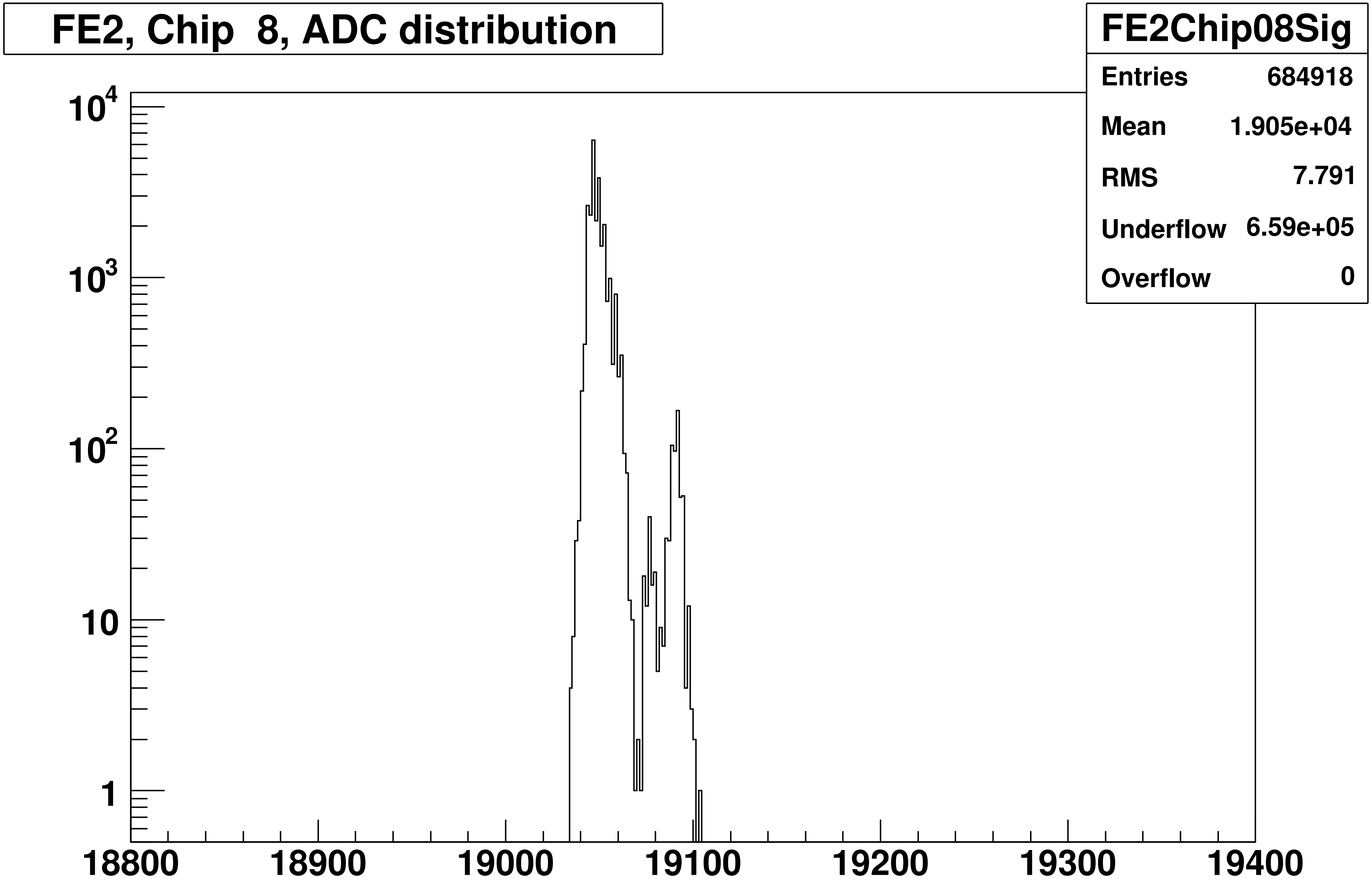

FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 684918
Mean 787.9
RMS 3710
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

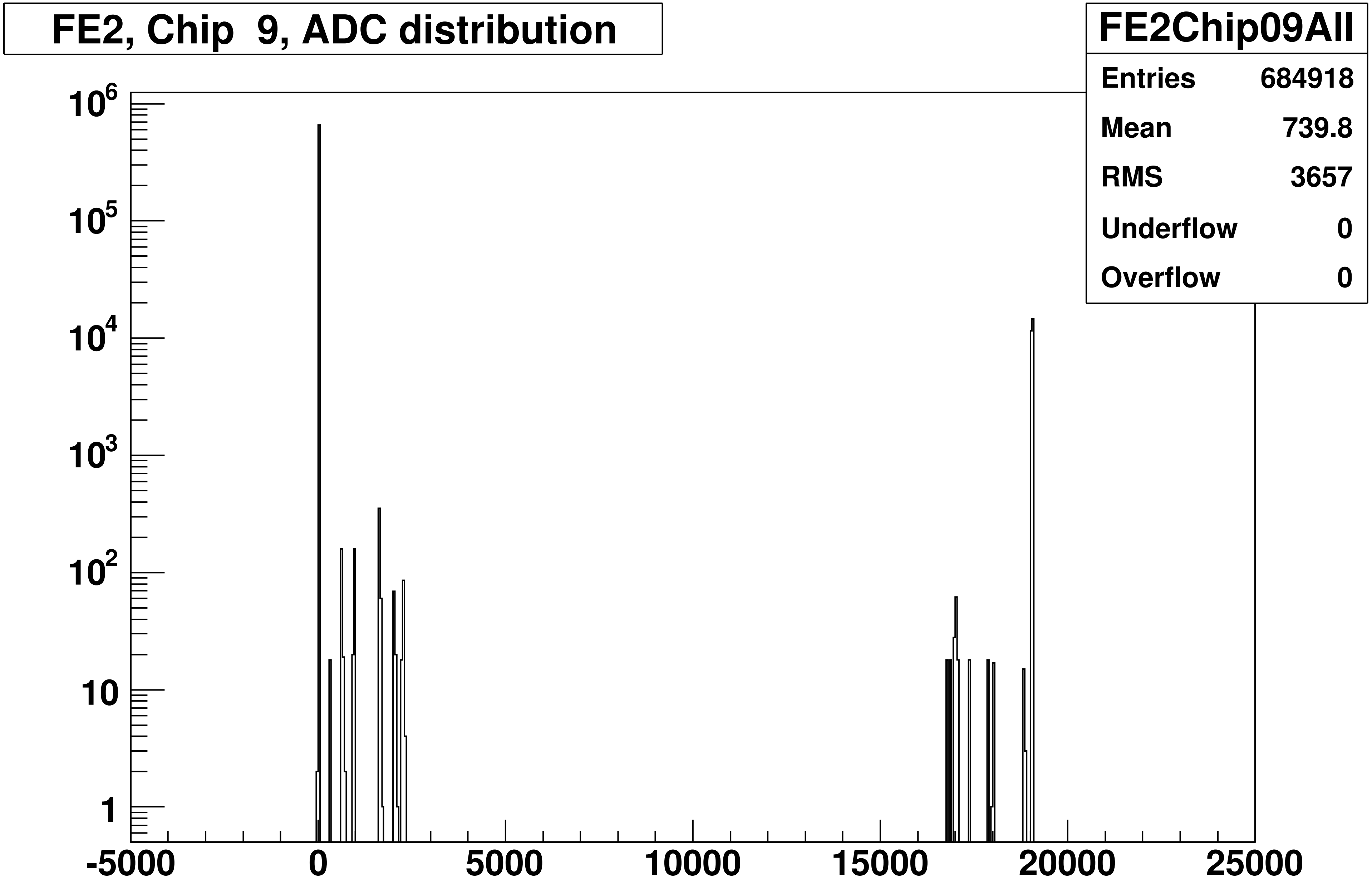

FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 684918
Mean 36.18
RMS 1.377
Underflow 0
Overflow 2.718e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

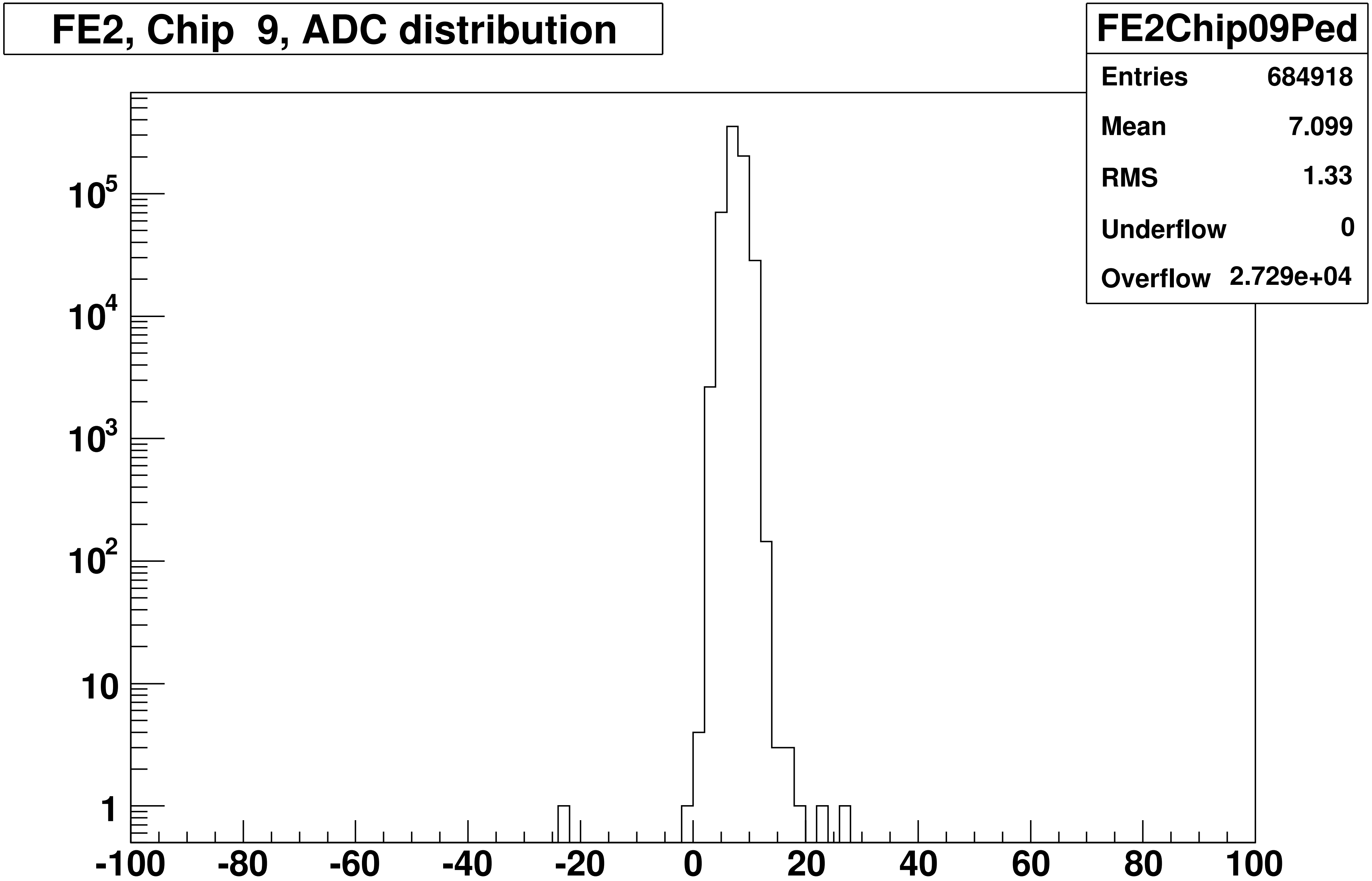

FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 684918
Mean 1.91e+04
RMS 13.68
Underflow 6.579e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 684918
Mean 704.1
RMS 3639
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

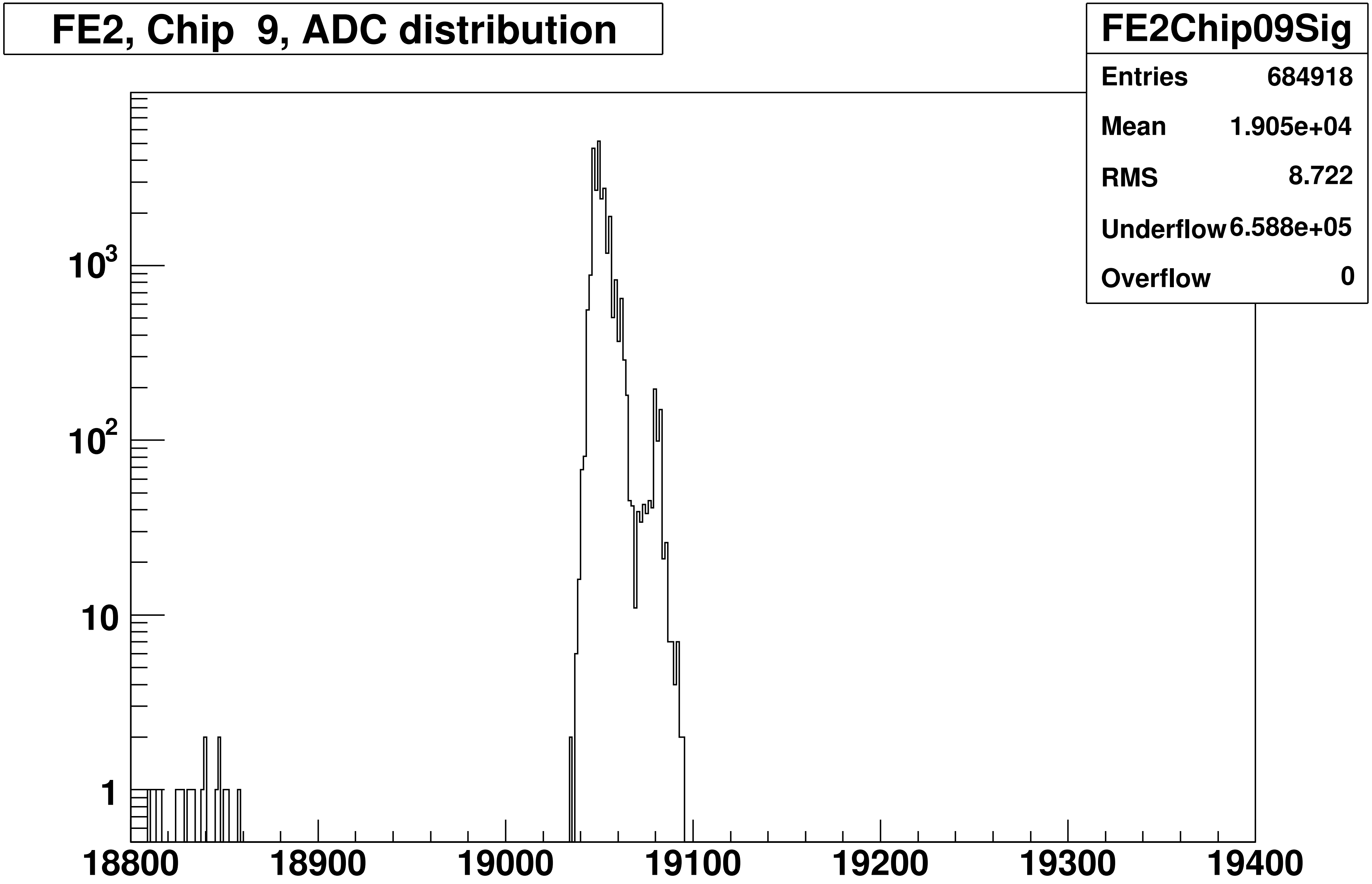

FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 684918
Mean -19.64
RMS 1.366
Underflow 0
Overflow 2.671e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

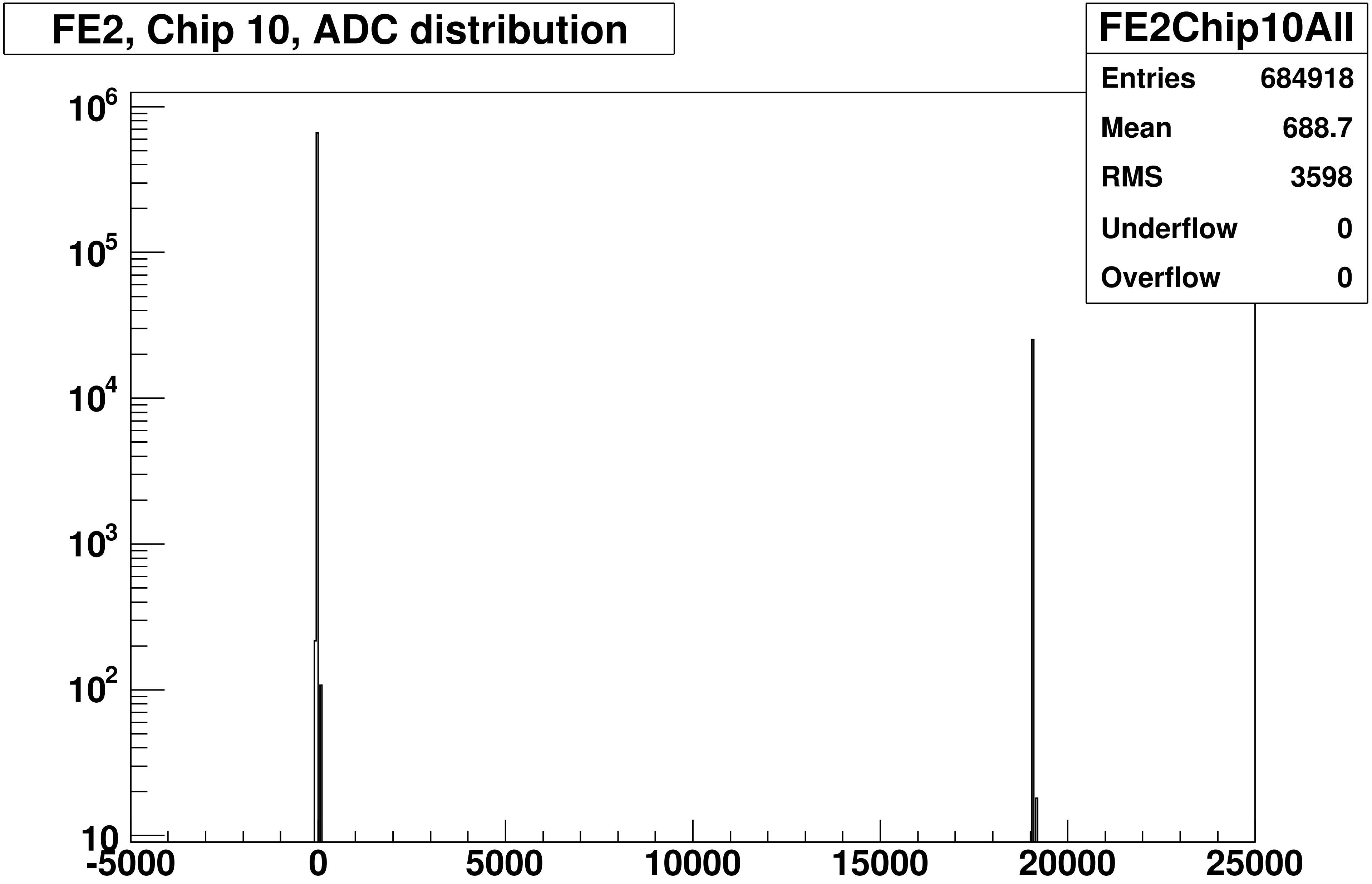

FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 684918
Mean 1.905e+04
RMS 7.791
Underflow 6.59e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 684918
Mean 739.8
RMS 3657
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

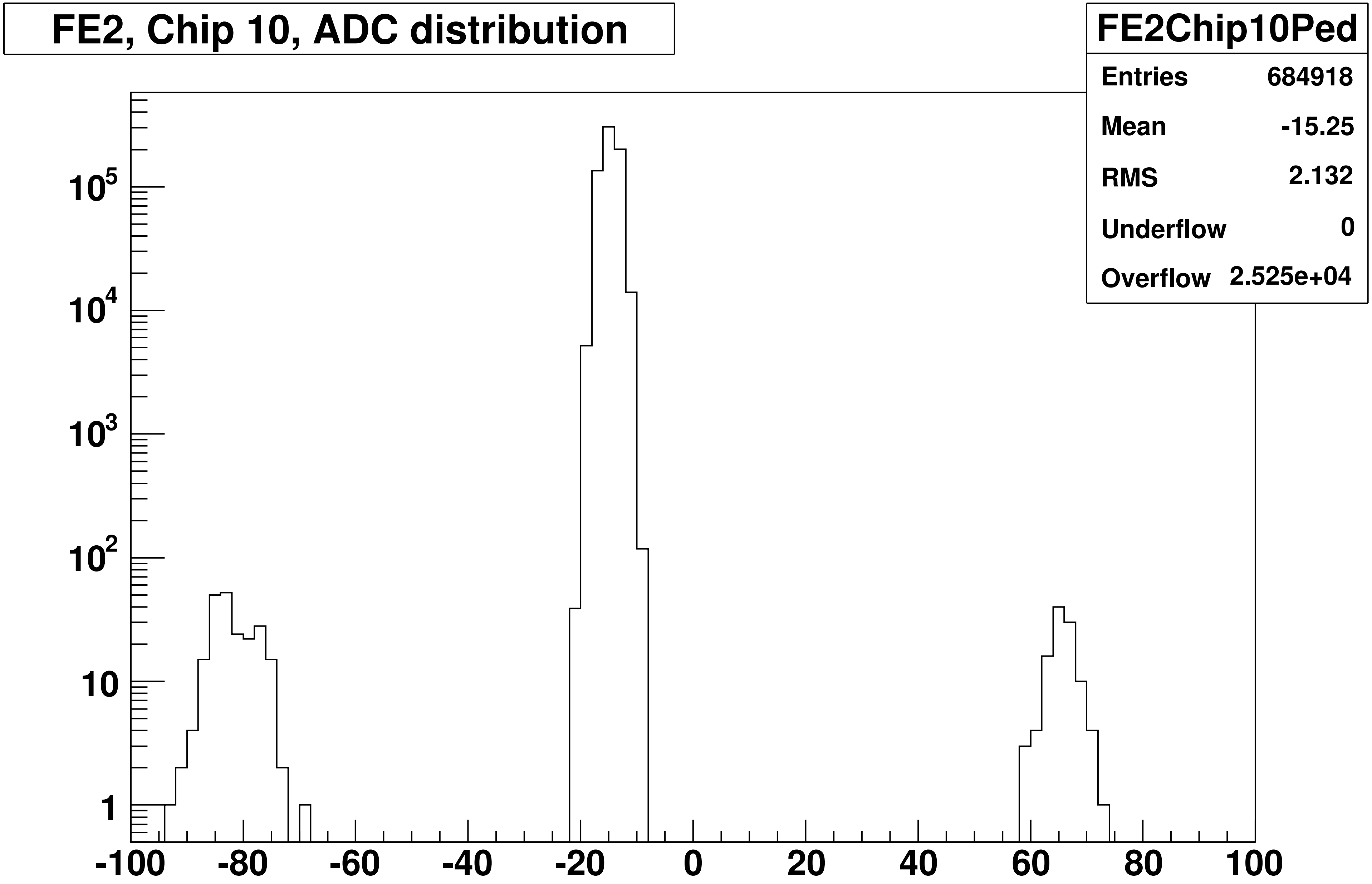


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 684918
Mean 7.099
RMS 1.33
Underflow 0
Overflow 2.729e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

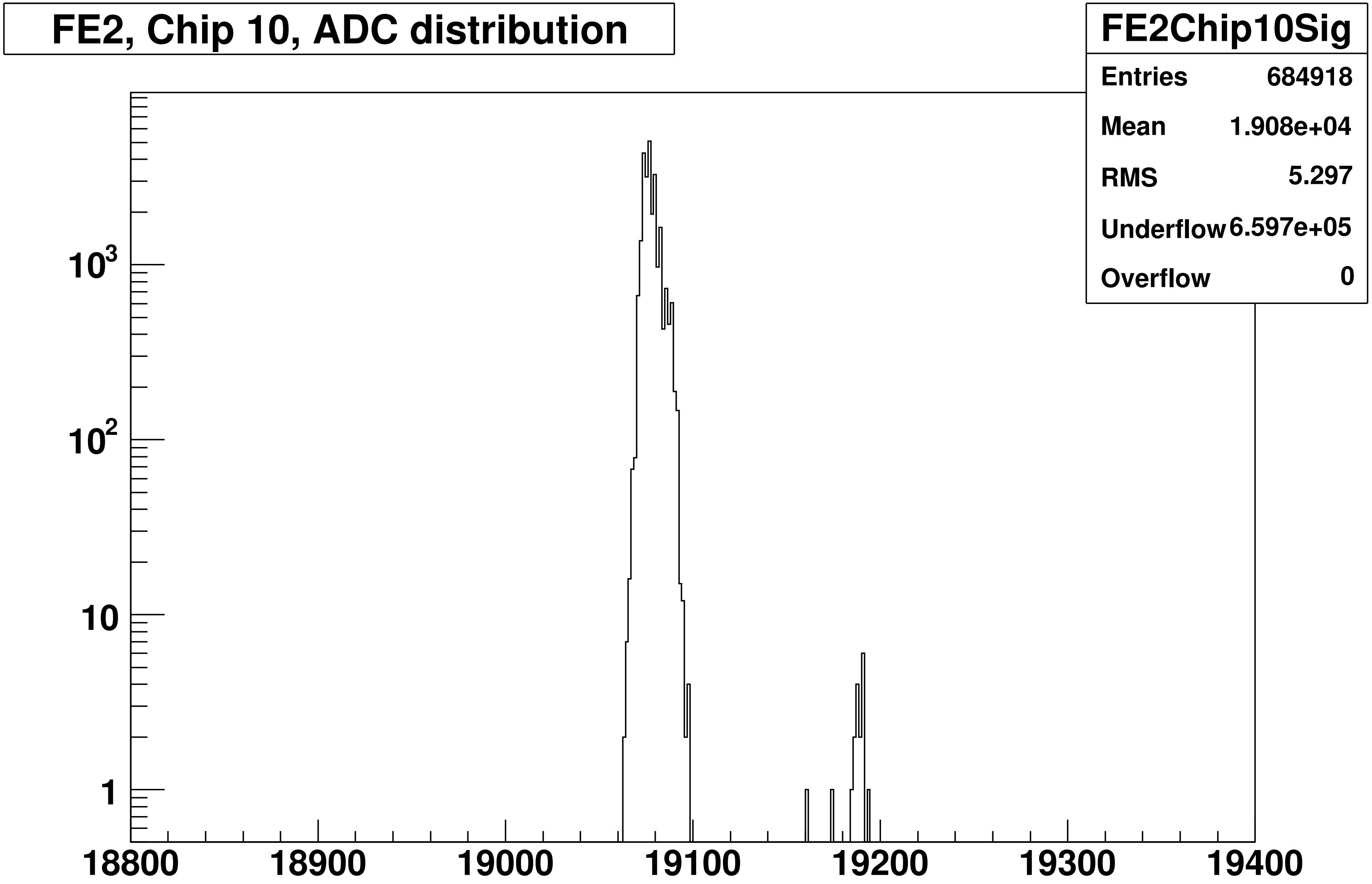

FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 684918
Mean 1.905e+04
RMS 8.722
Underflow 6.588e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



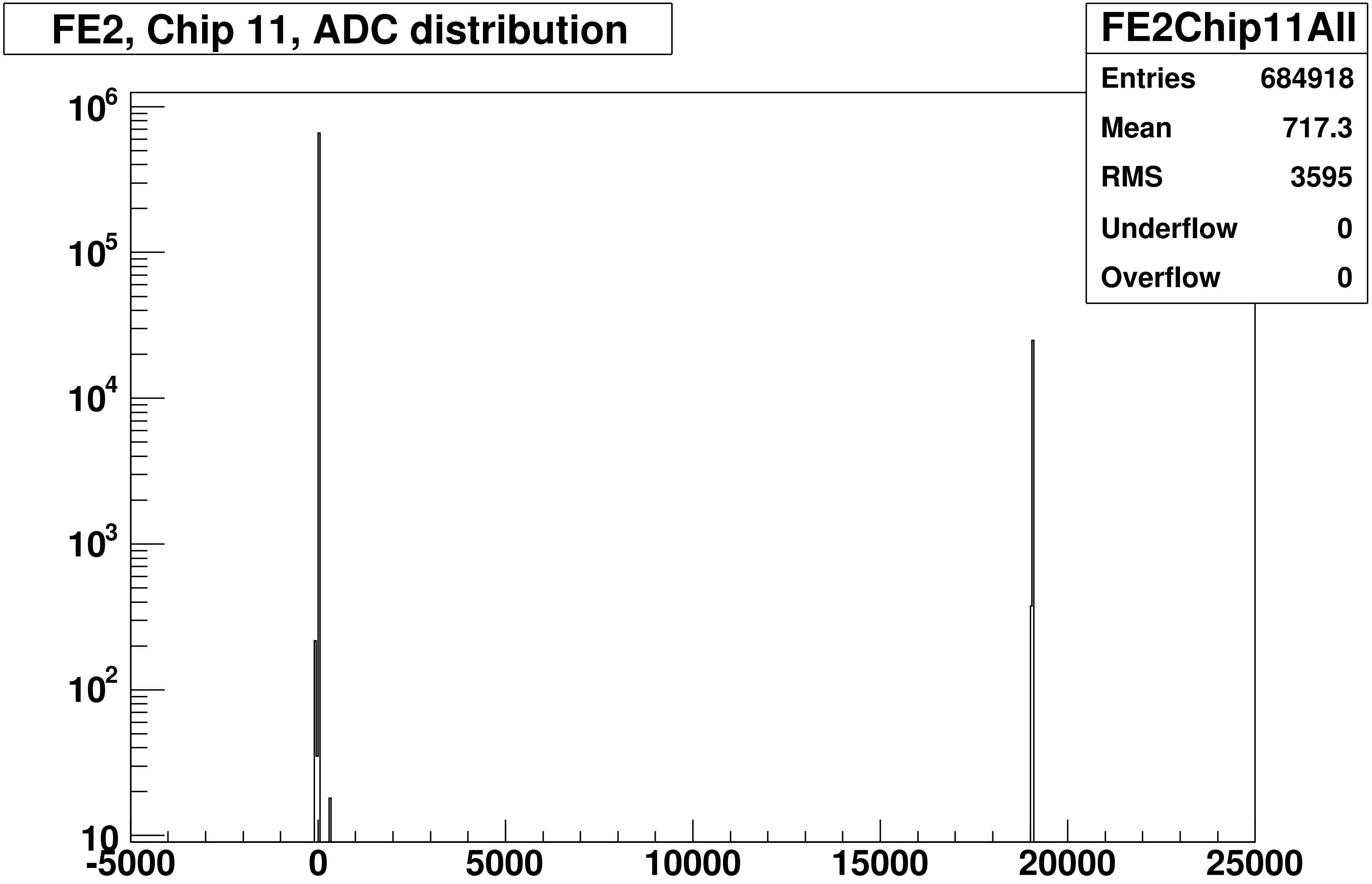
FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 684918
Mean 688.7
RMS 3598
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000



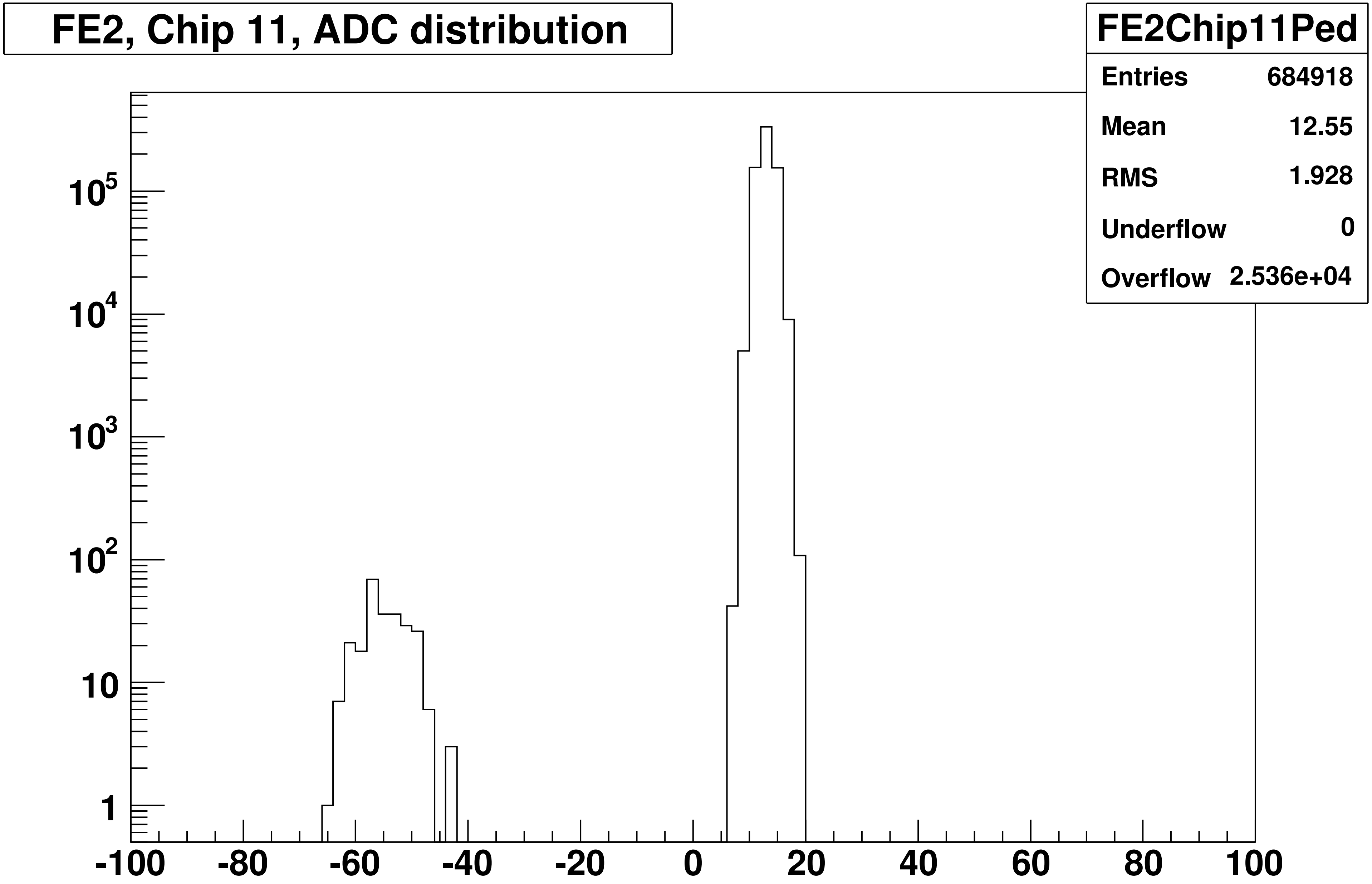
FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 684918
Mean -15.25
RMS 2.132
Underflow 0
Overflow 2.525e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

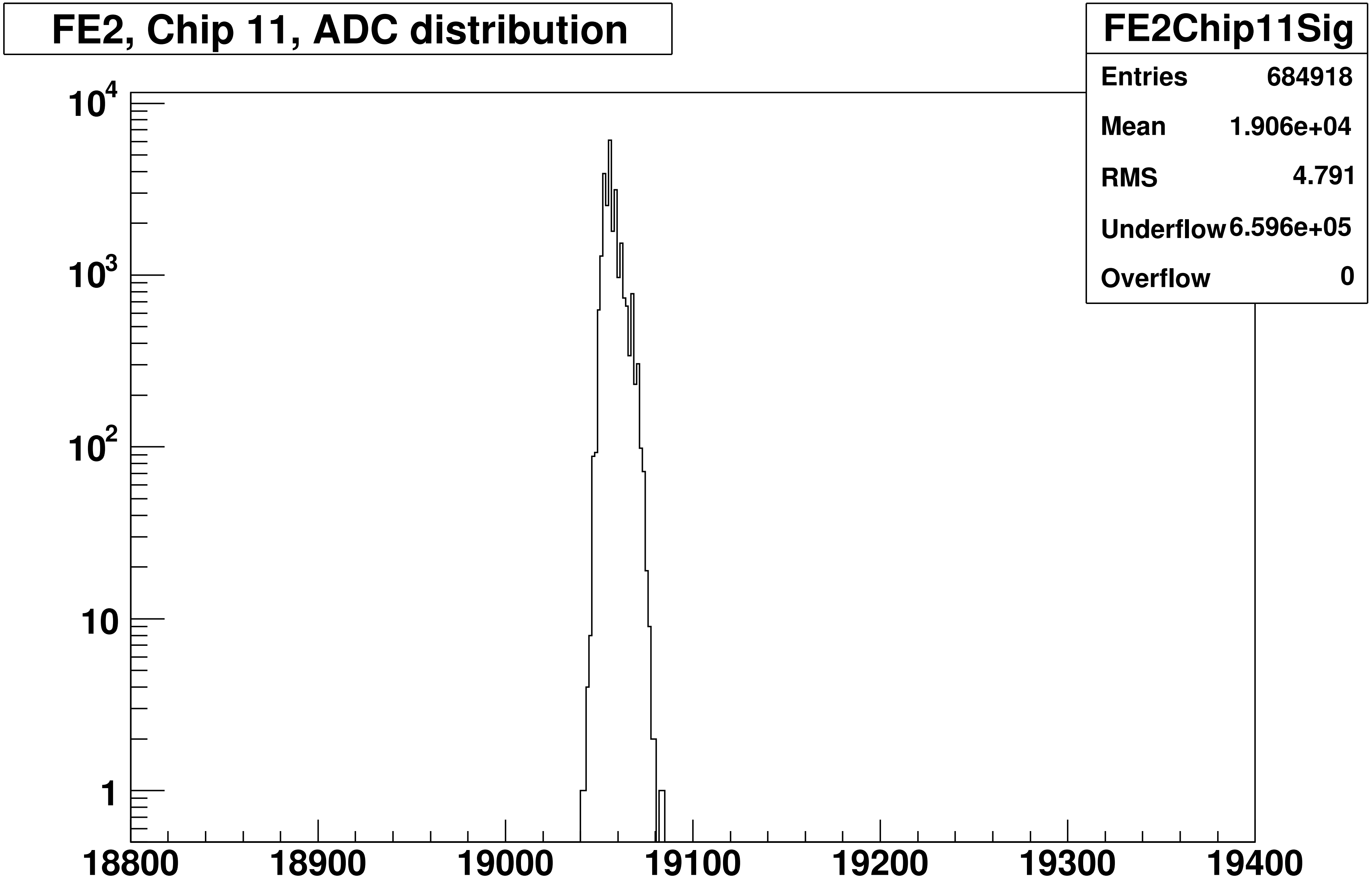


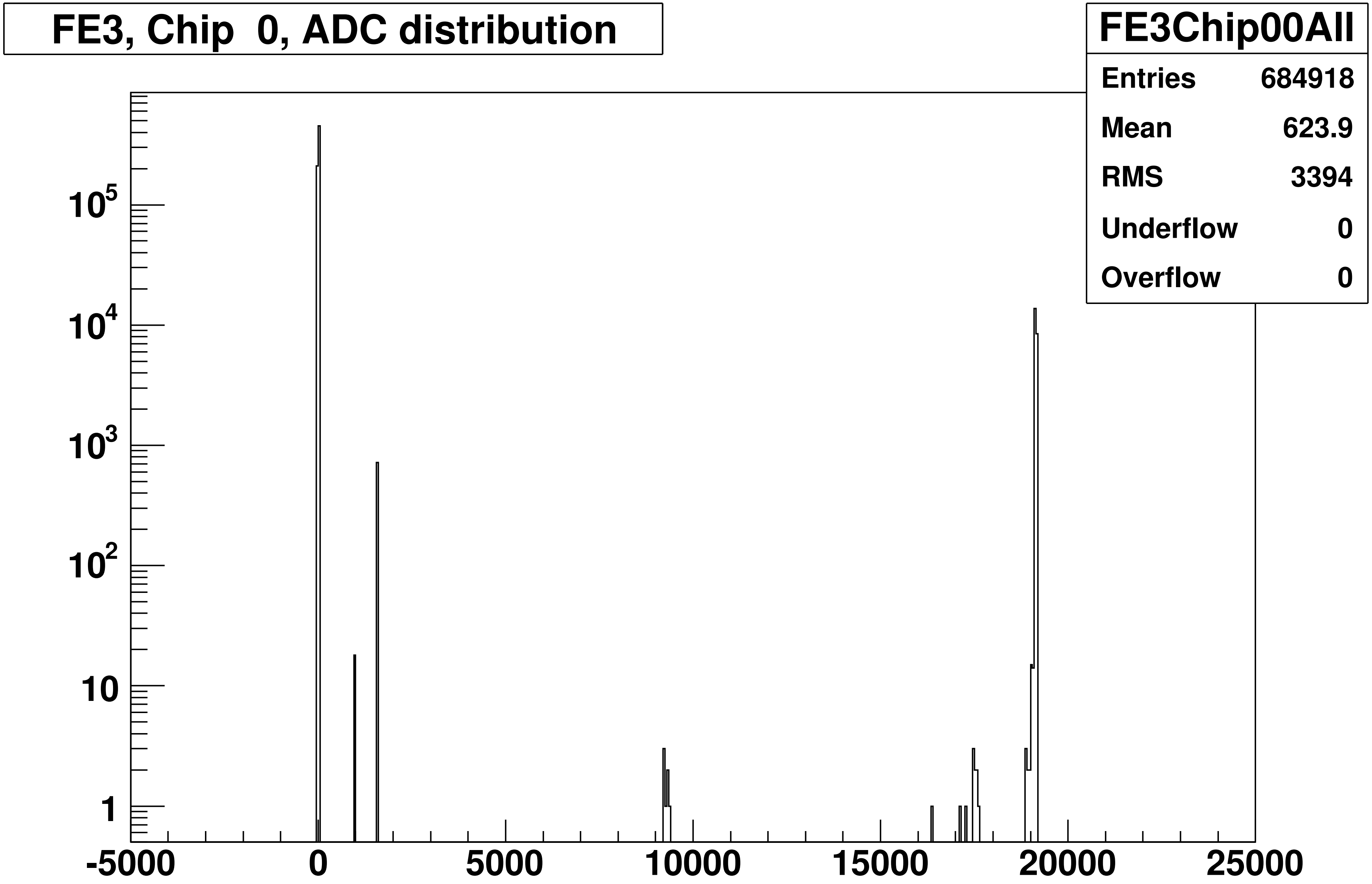
FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 684918
Mean 1.908e+04
RMS 5.297
Underflow 6.597e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

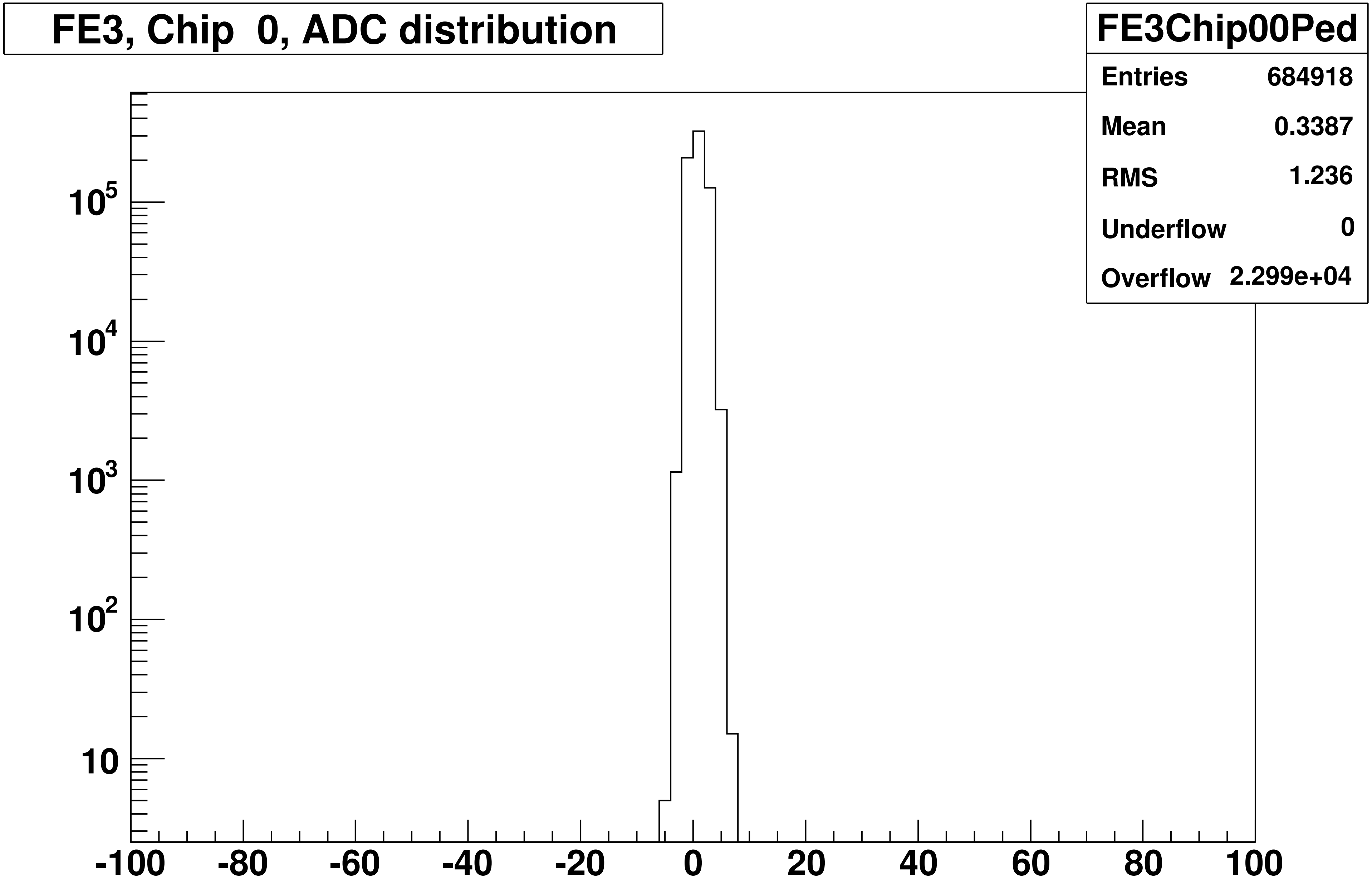


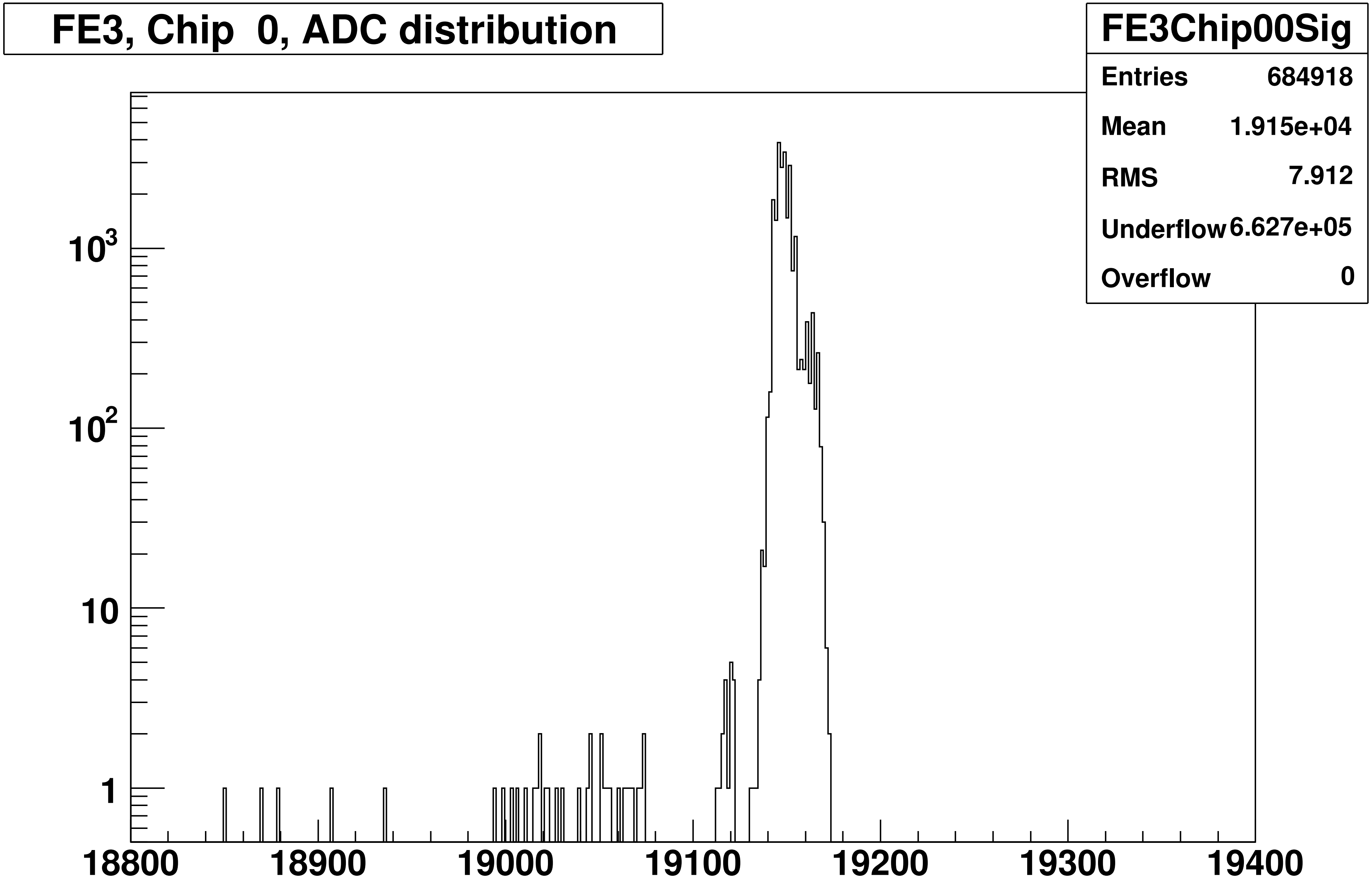
FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 684918
Mean 717.3
RMS 3595
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 684918
Mean 12.55
RMS 1.928
Underflow 0
Overflow 2.536e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

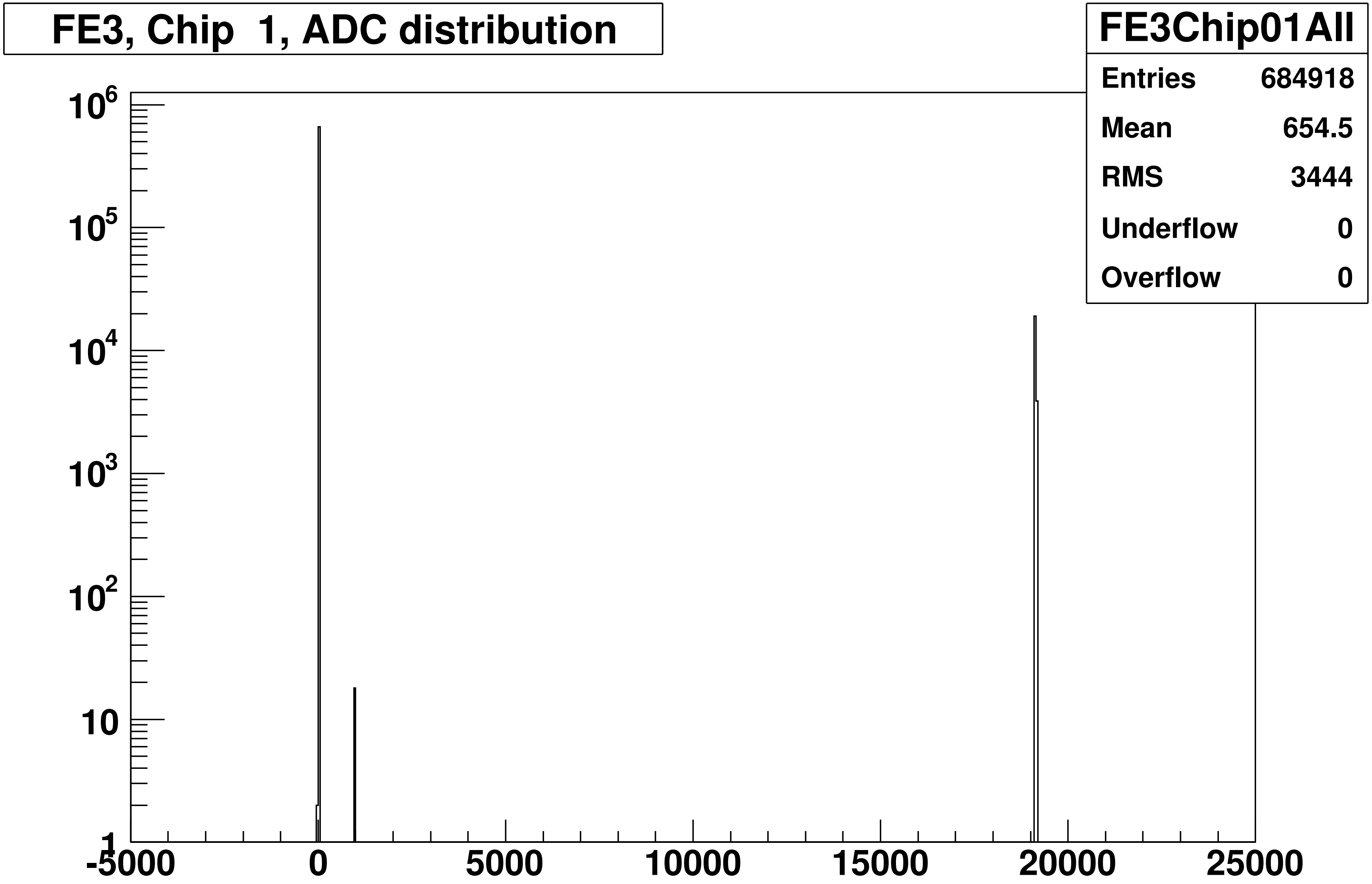

FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 684918
Mean 1.906e+04
RMS 4.791
Underflow 6.596e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

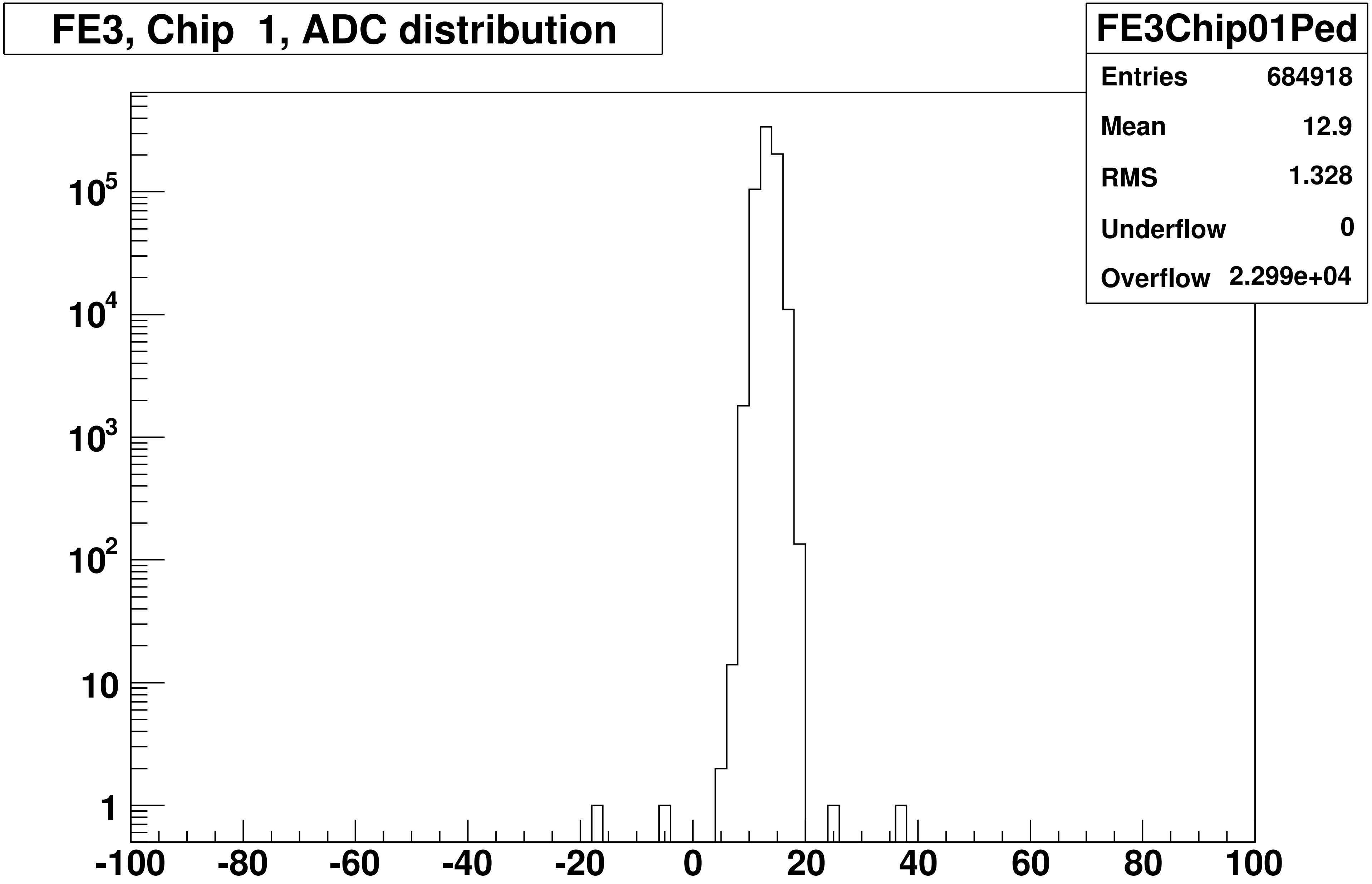

FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 684918
Mean 623.9
RMS 3394
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

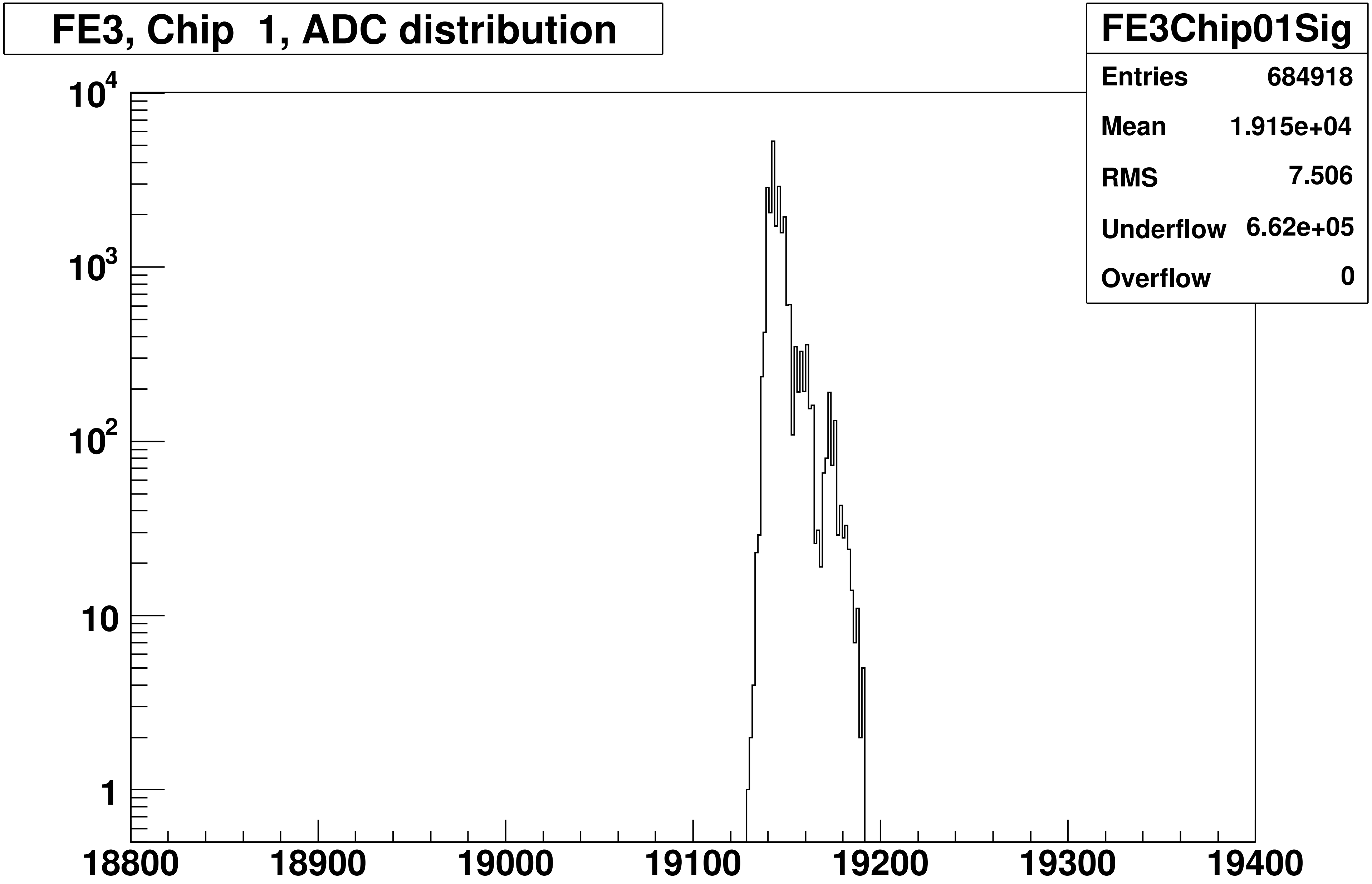

FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 684918
Mean 0.3387
RMS 1.236
Underflow 0
Overflow 2.299e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

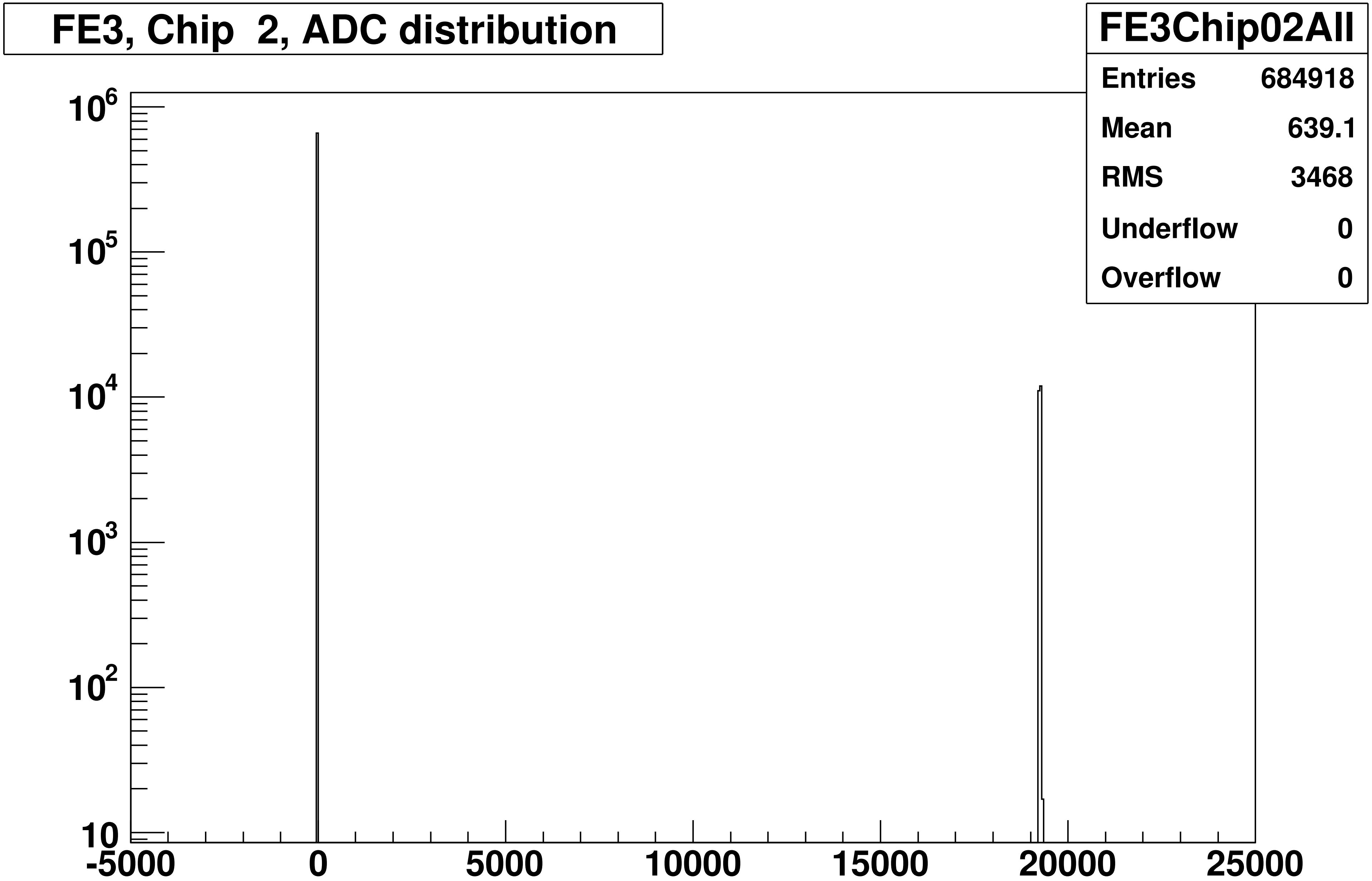


FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 684918
Mean 1.915e+04
RMS 7.912
Underflow 6.627e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

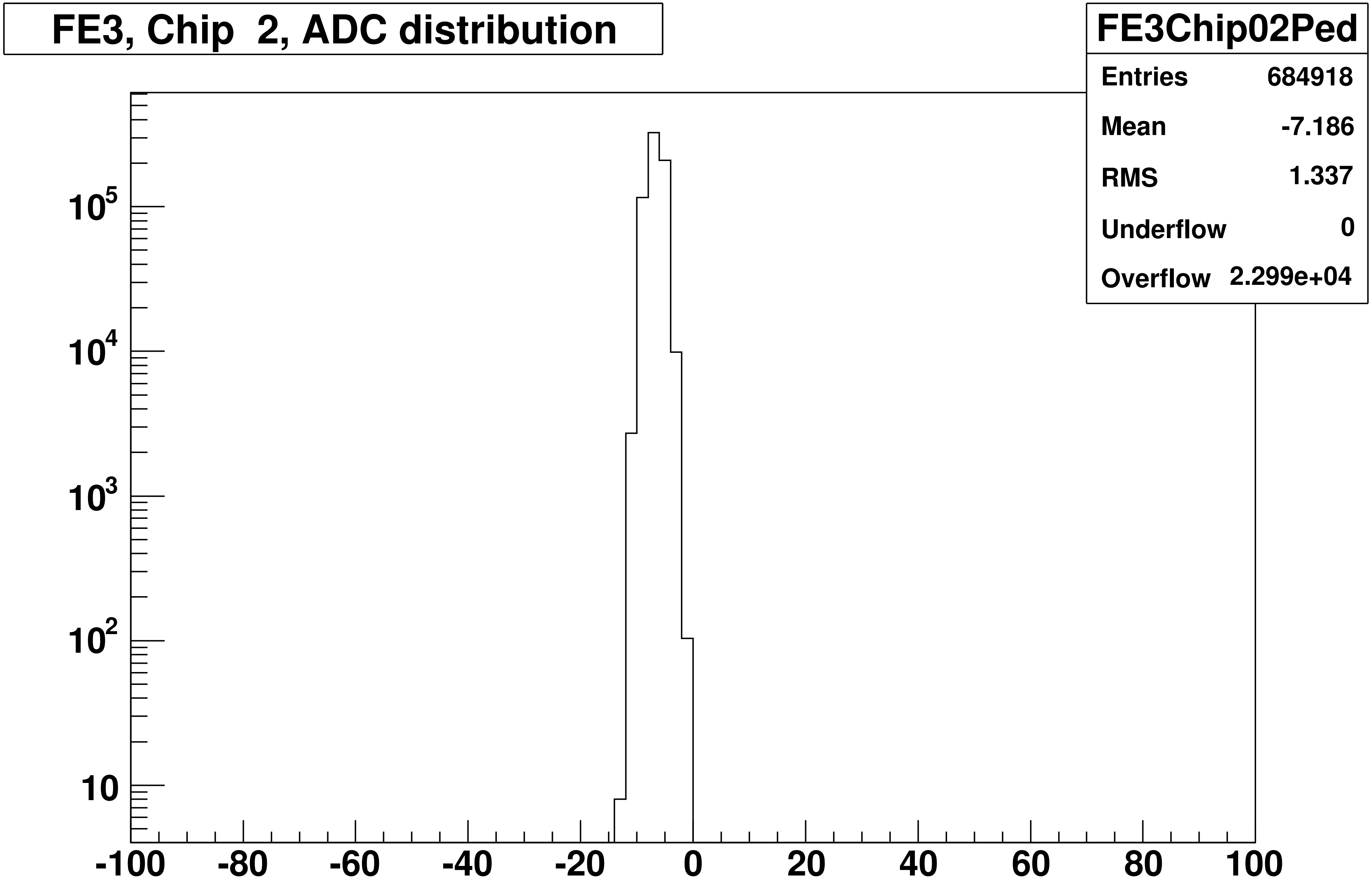

FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 684918
Mean 654.5
RMS 3444
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

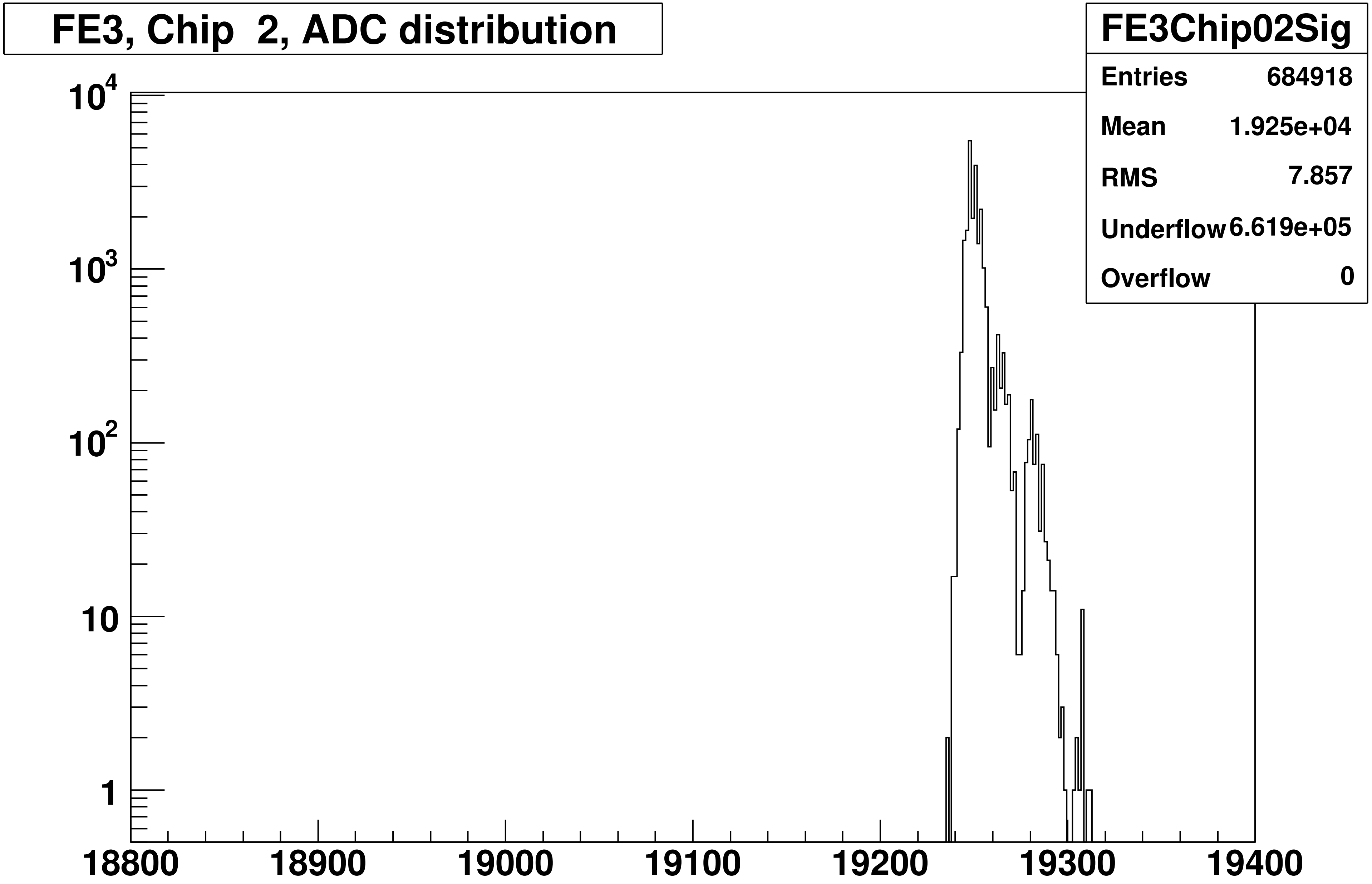

FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 684918
Mean 12.9
RMS 1.328
Underflow 0
Overflow 2.299e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

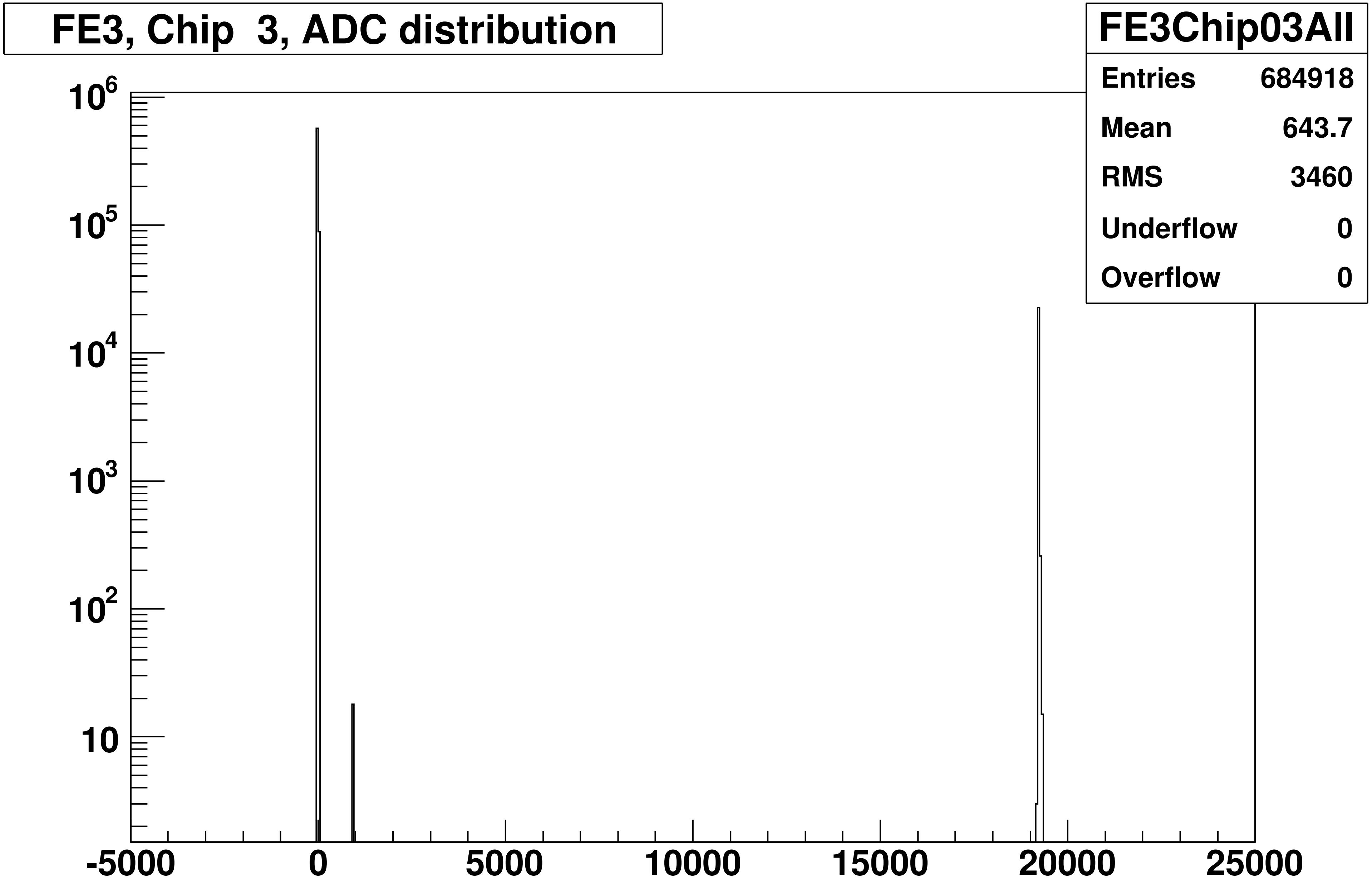

FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 684918
Mean 1.915e+04
RMS 7.506
Underflow 6.62e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



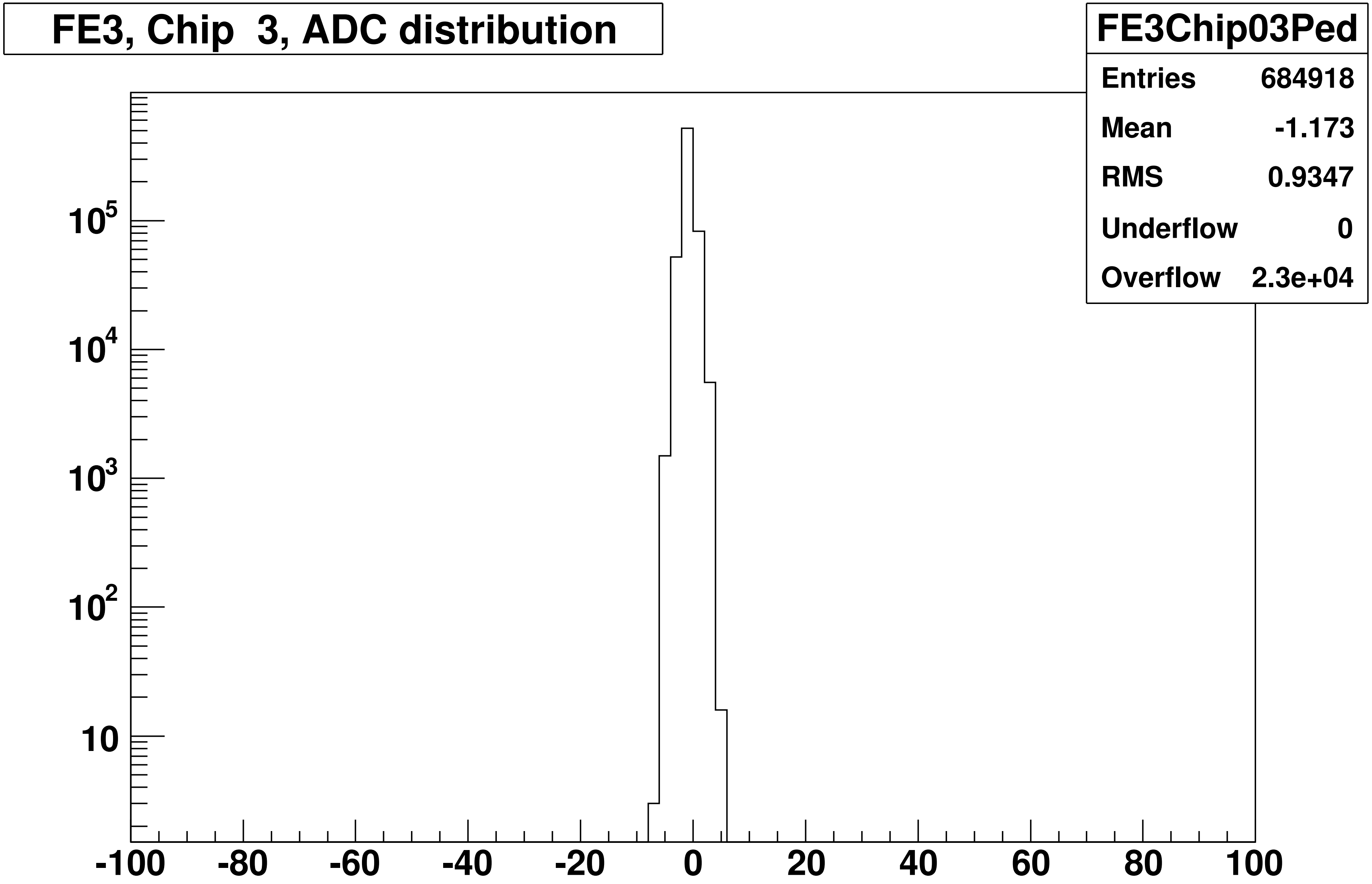
FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 684918
Mean 639.1
RMS 3468
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

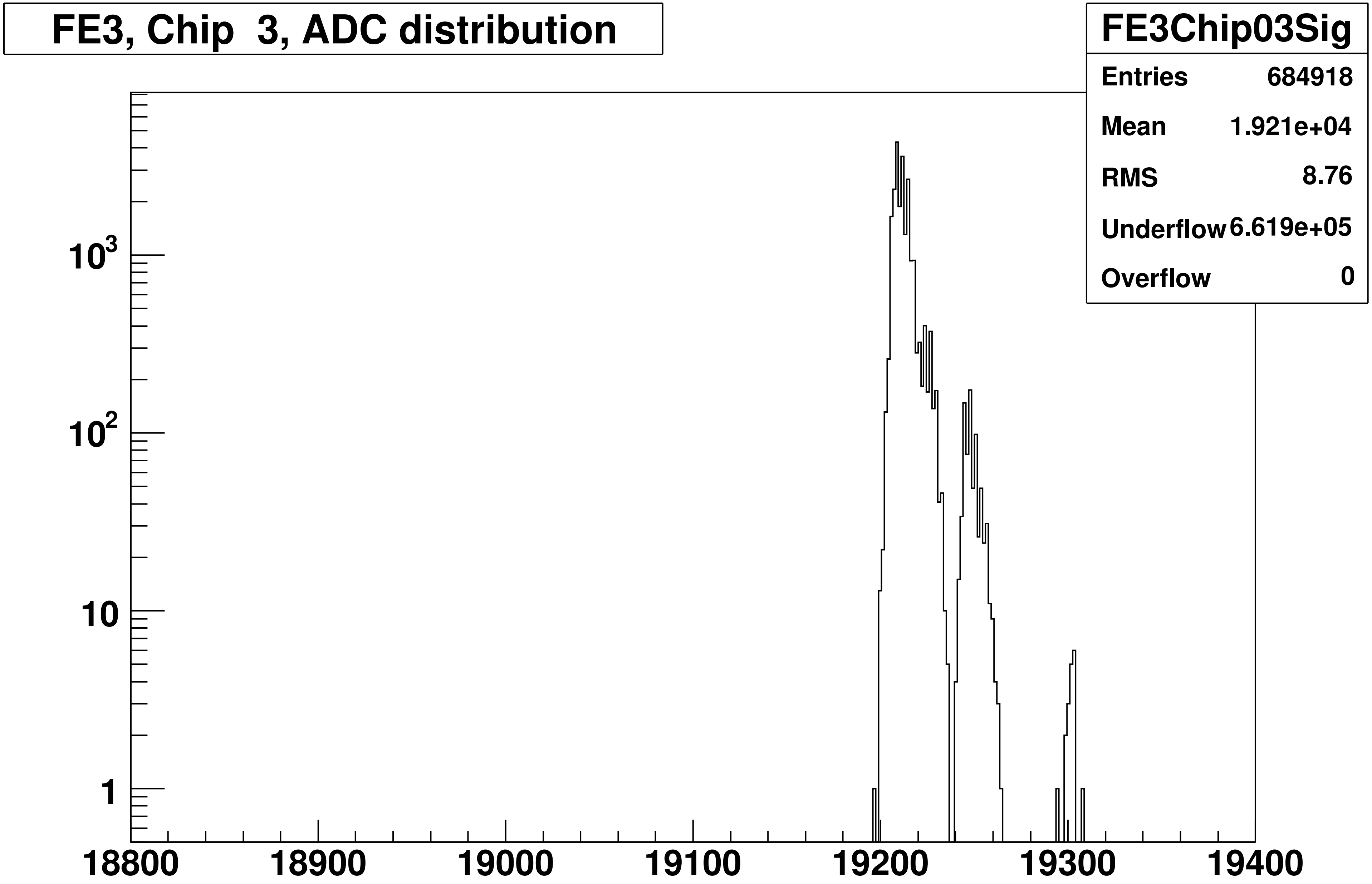

FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 684918
Mean -7.186
RMS 1.337
Underflow 0
Overflow 2.299e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

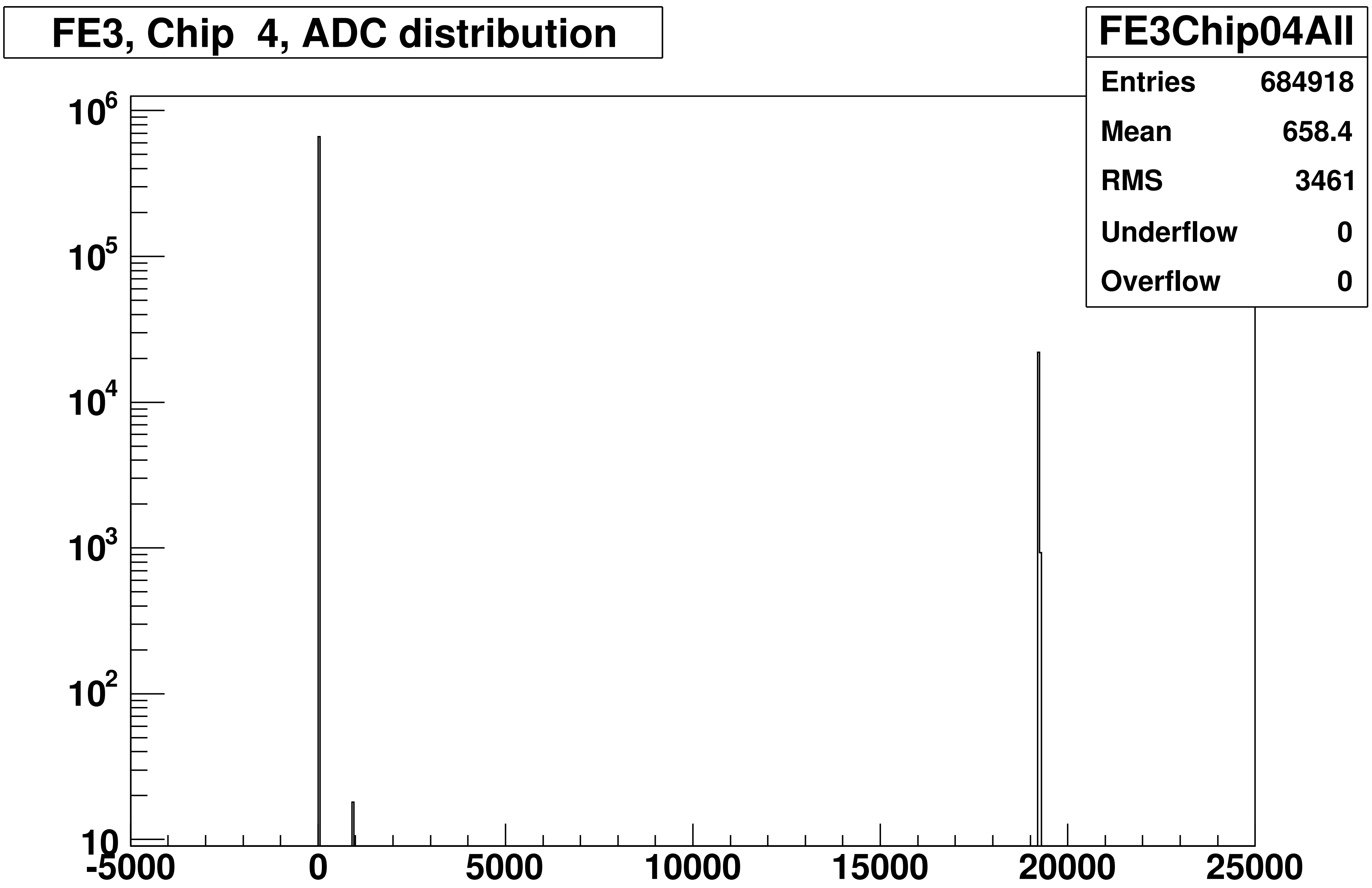

FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 684918
Mean 1.925e+04
RMS 7.857
Underflow 6.619e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

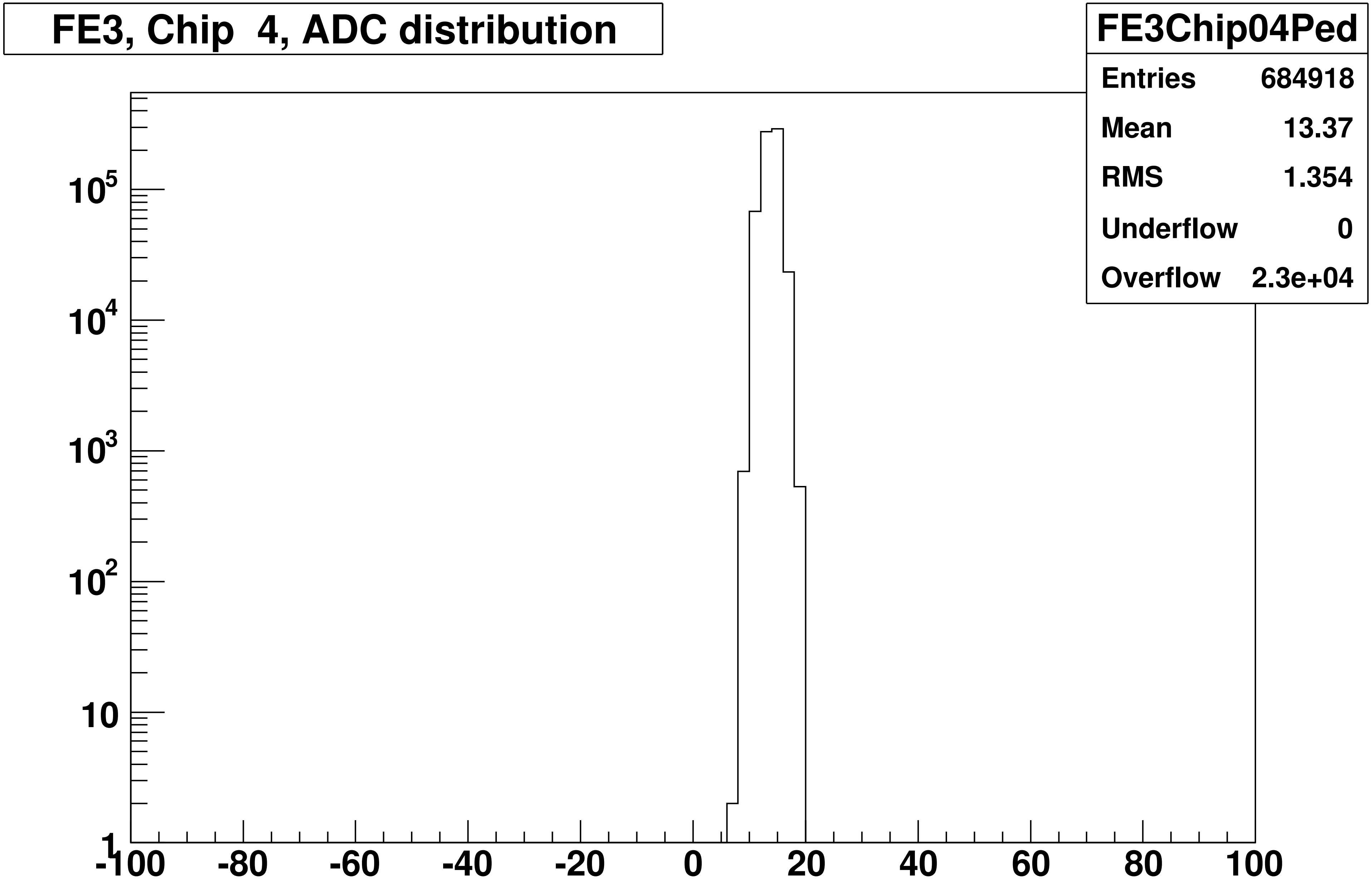


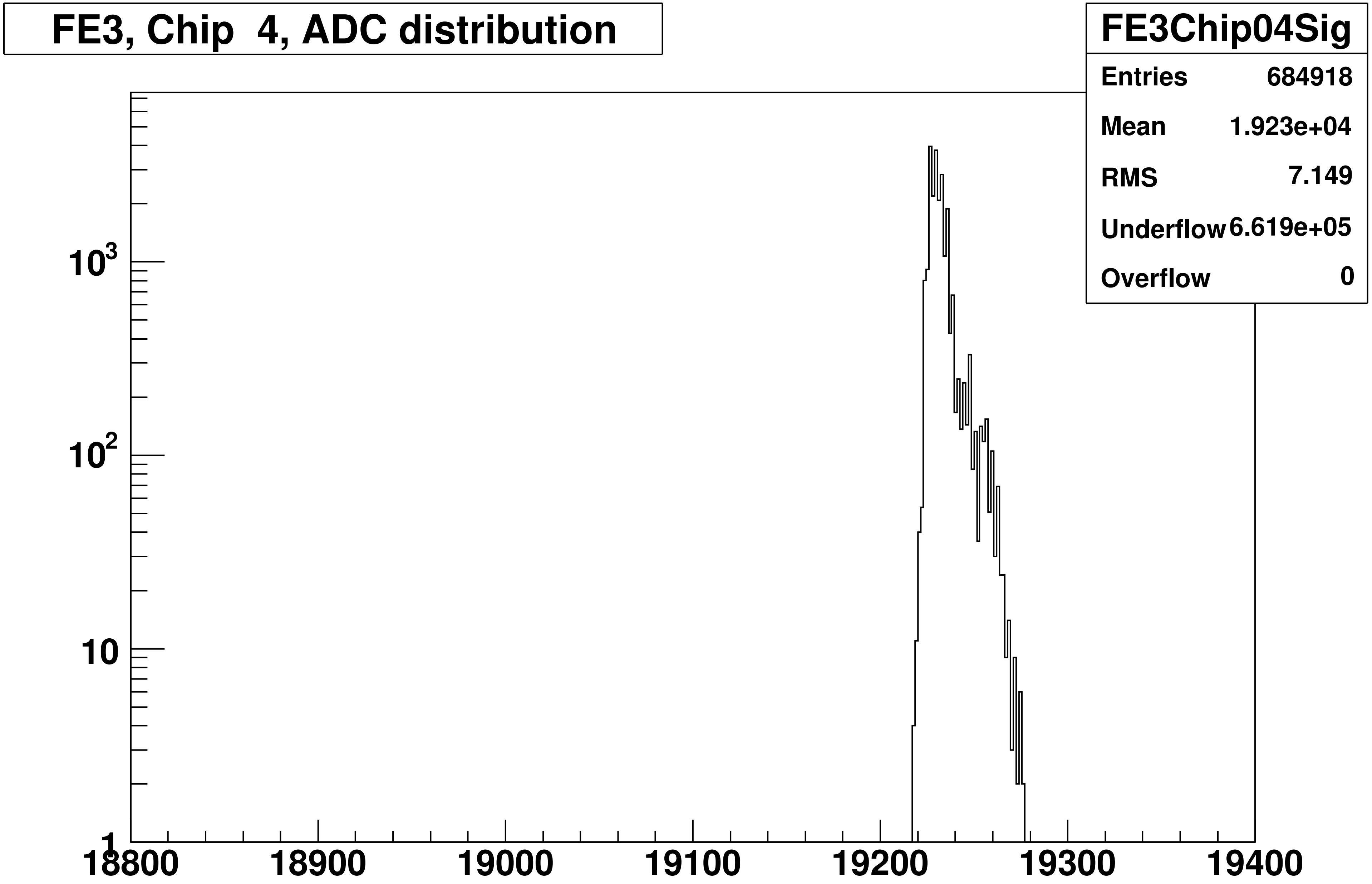
FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 684918
Mean 643.7
RMS 3460
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

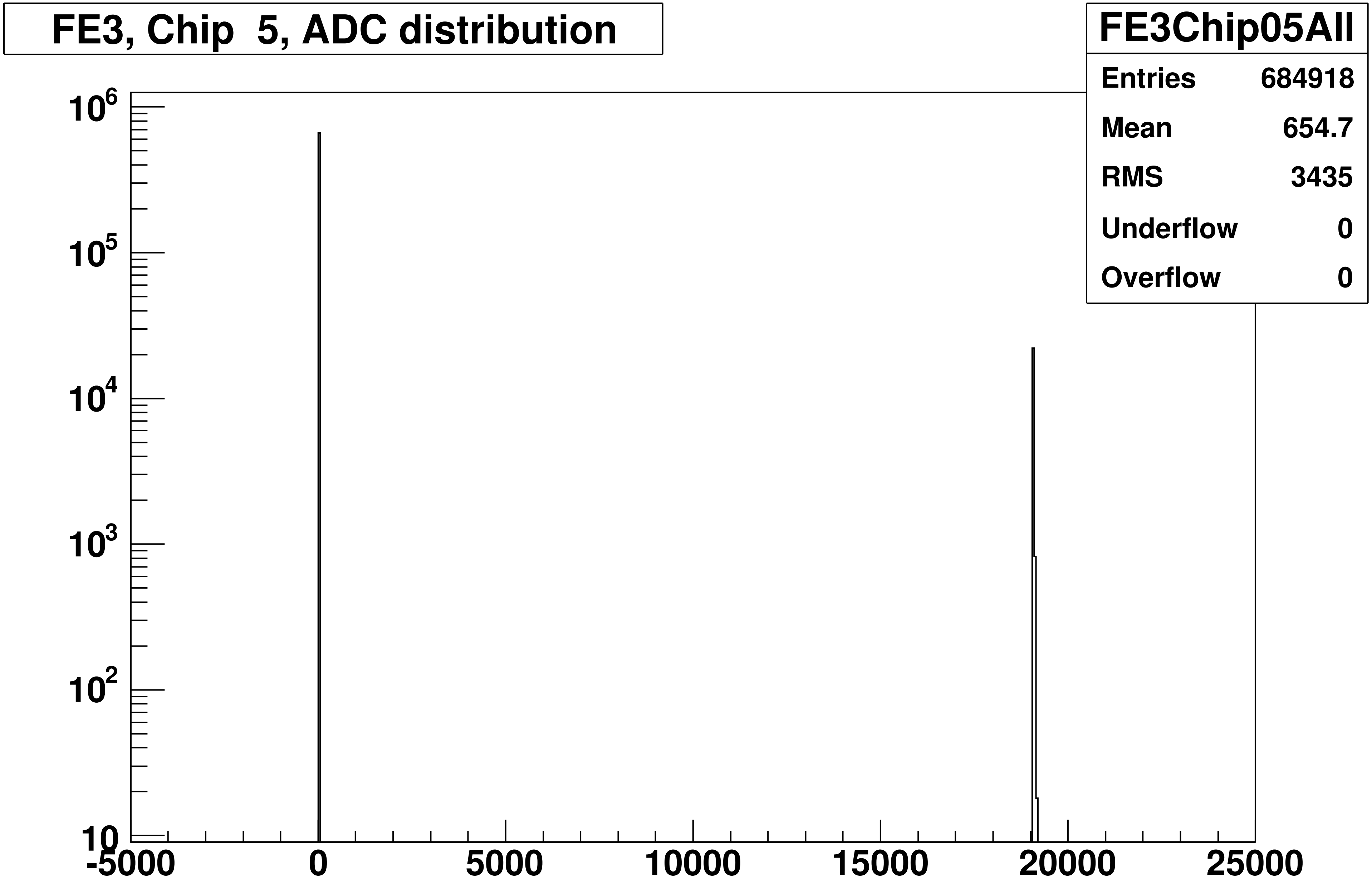

FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 684918
Mean -1.173
RMS 0.9347
Underflow 0
Overflow 2.3e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 684918
Mean 1.921e+04
RMS 8.76
Underflow 6.619e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

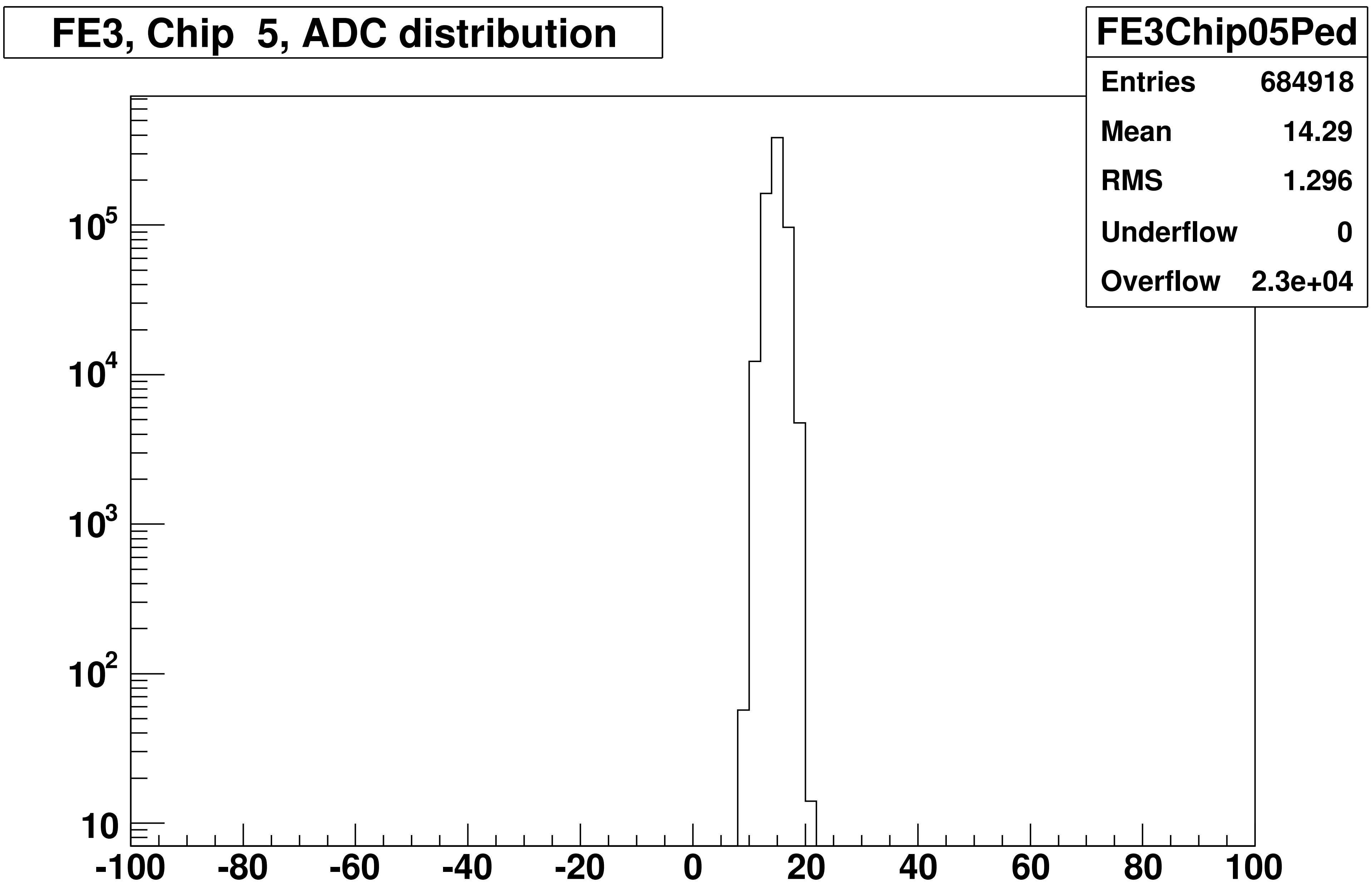

FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 684918
Mean 658.4
RMS 3461
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 684918
Mean 13.37
RMS 1.354
Underflow 0
Overflow 2.3e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

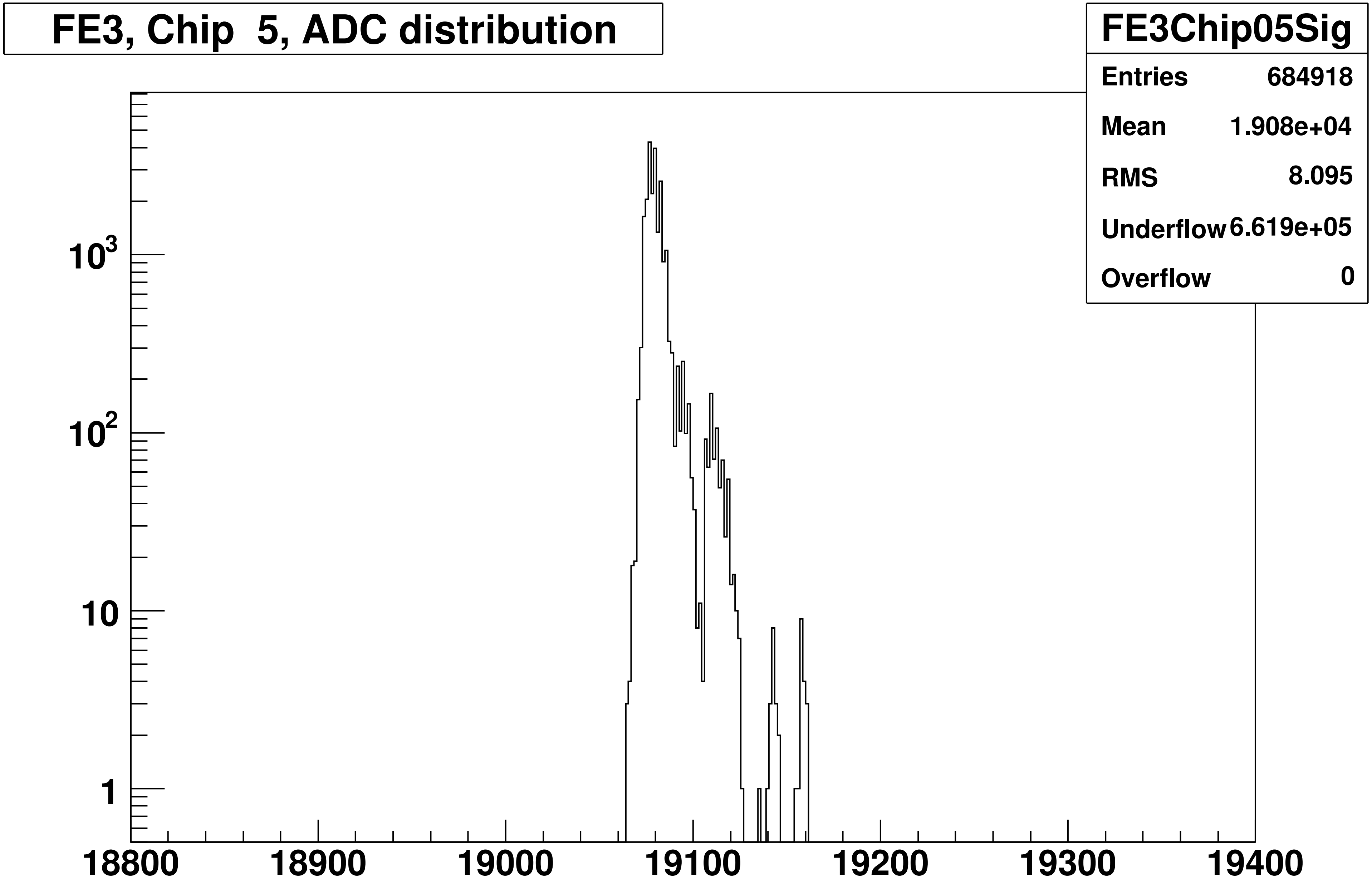

FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 684918
Mean 1.923e+04
RMS 7.149
Underflow 6.619e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

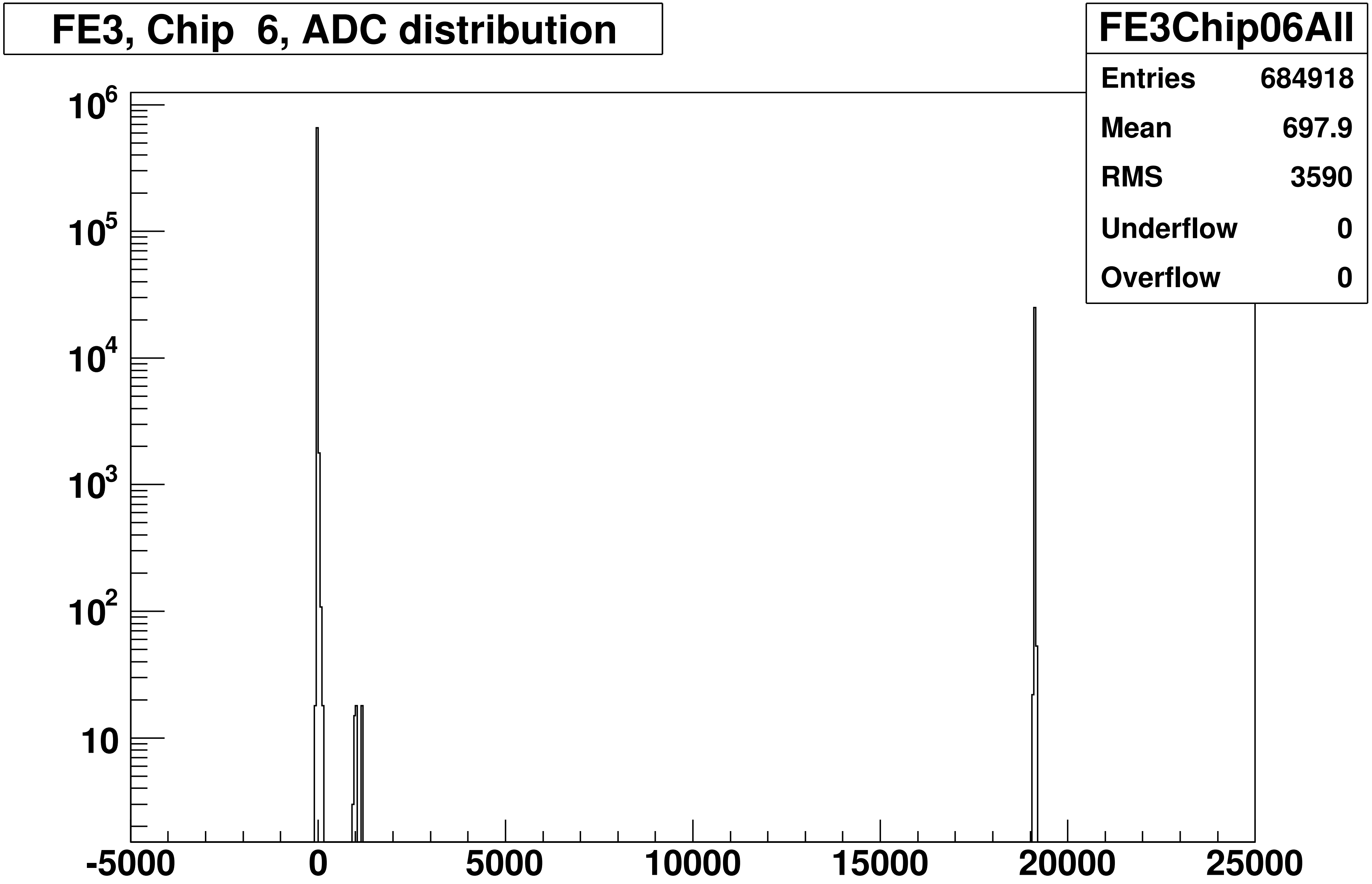


FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 684918
Mean 654.7
RMS 3435
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

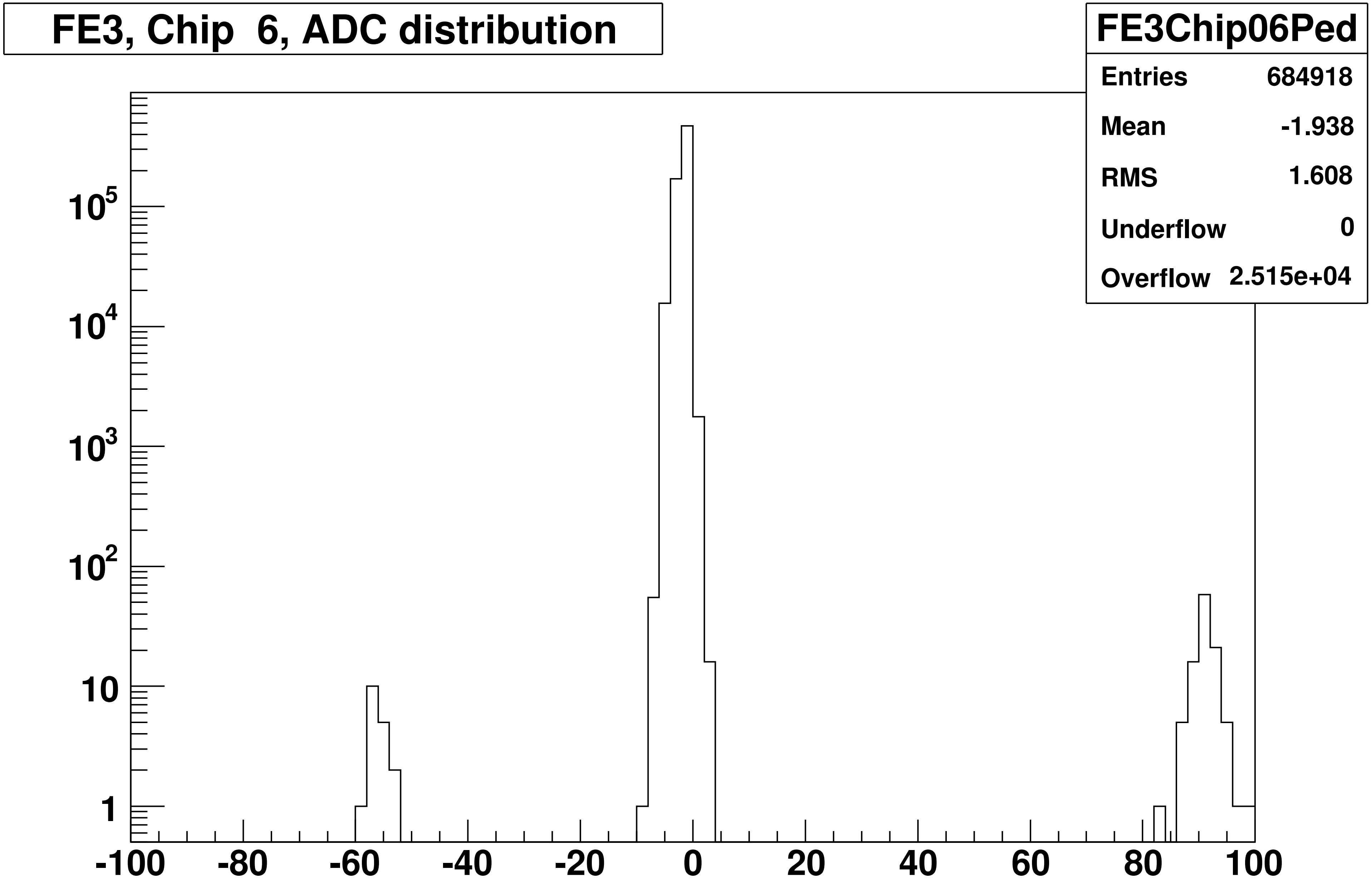


FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 684918
Mean 14.29
RMS 1.296
Underflow 0
Overflow 2.3e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

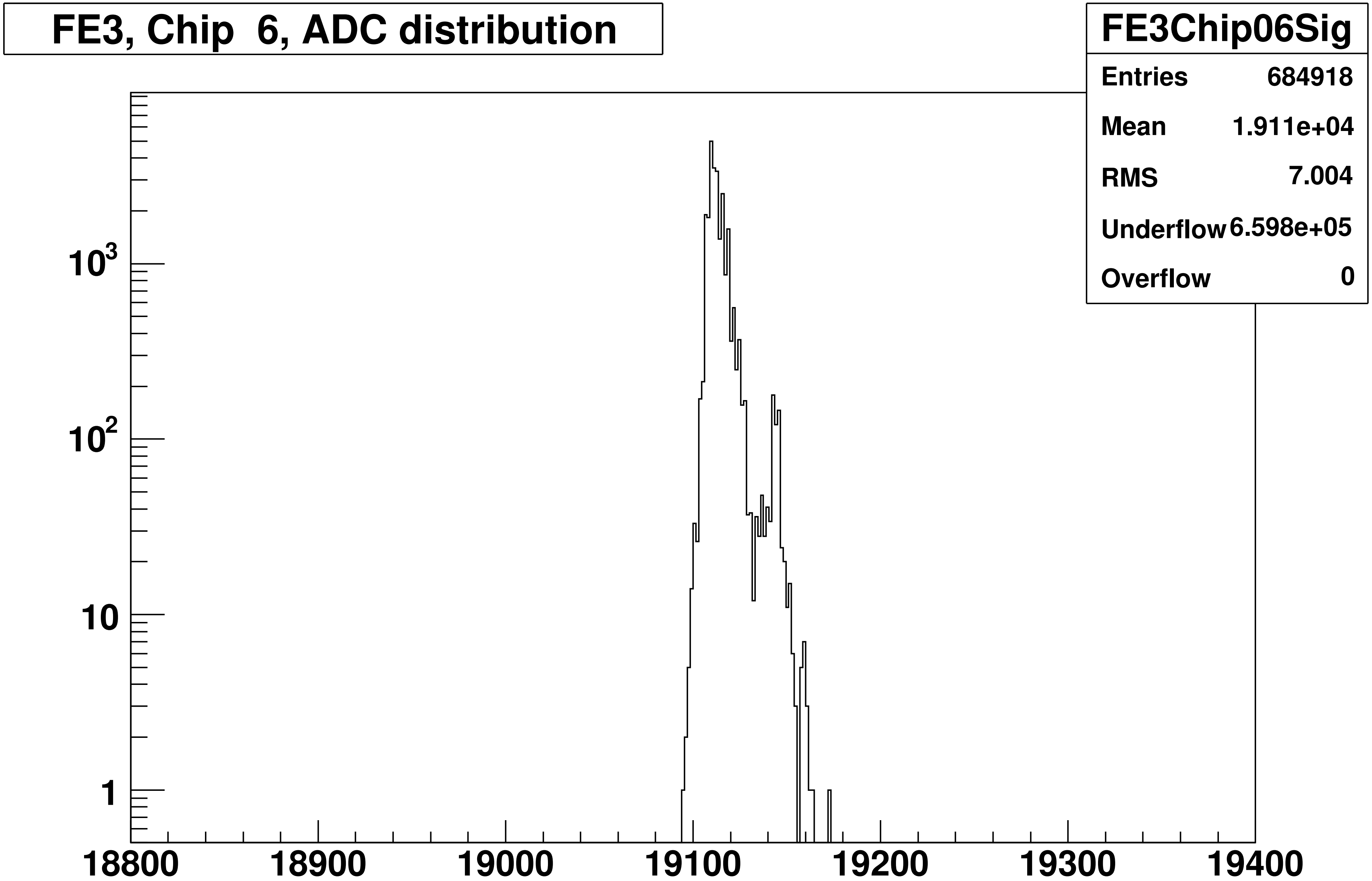

FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 684918
Mean 1.908e+04
RMS 8.095
Underflow 6.619e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

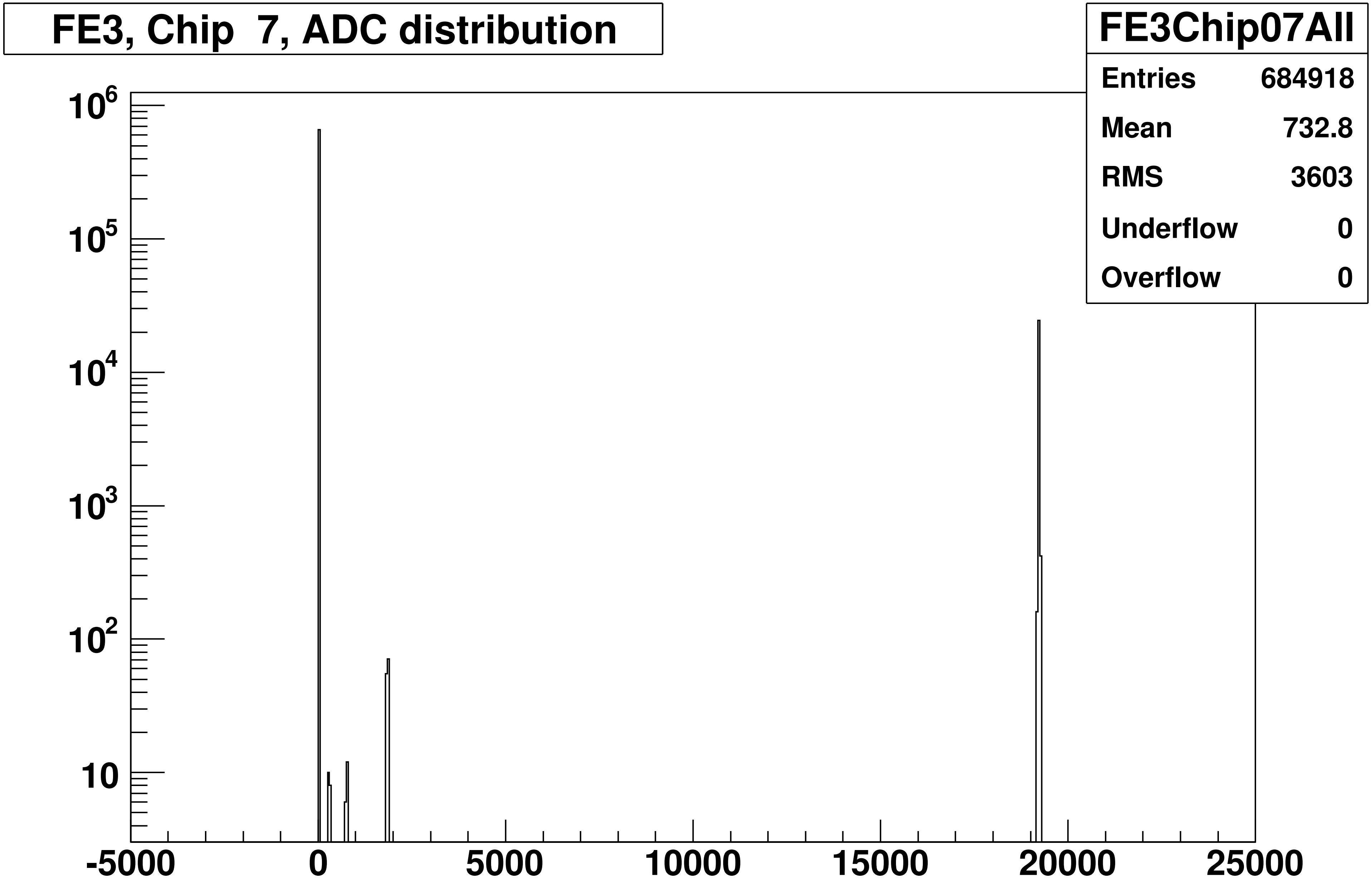


FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 684918
Mean 697.9
RMS 3590
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

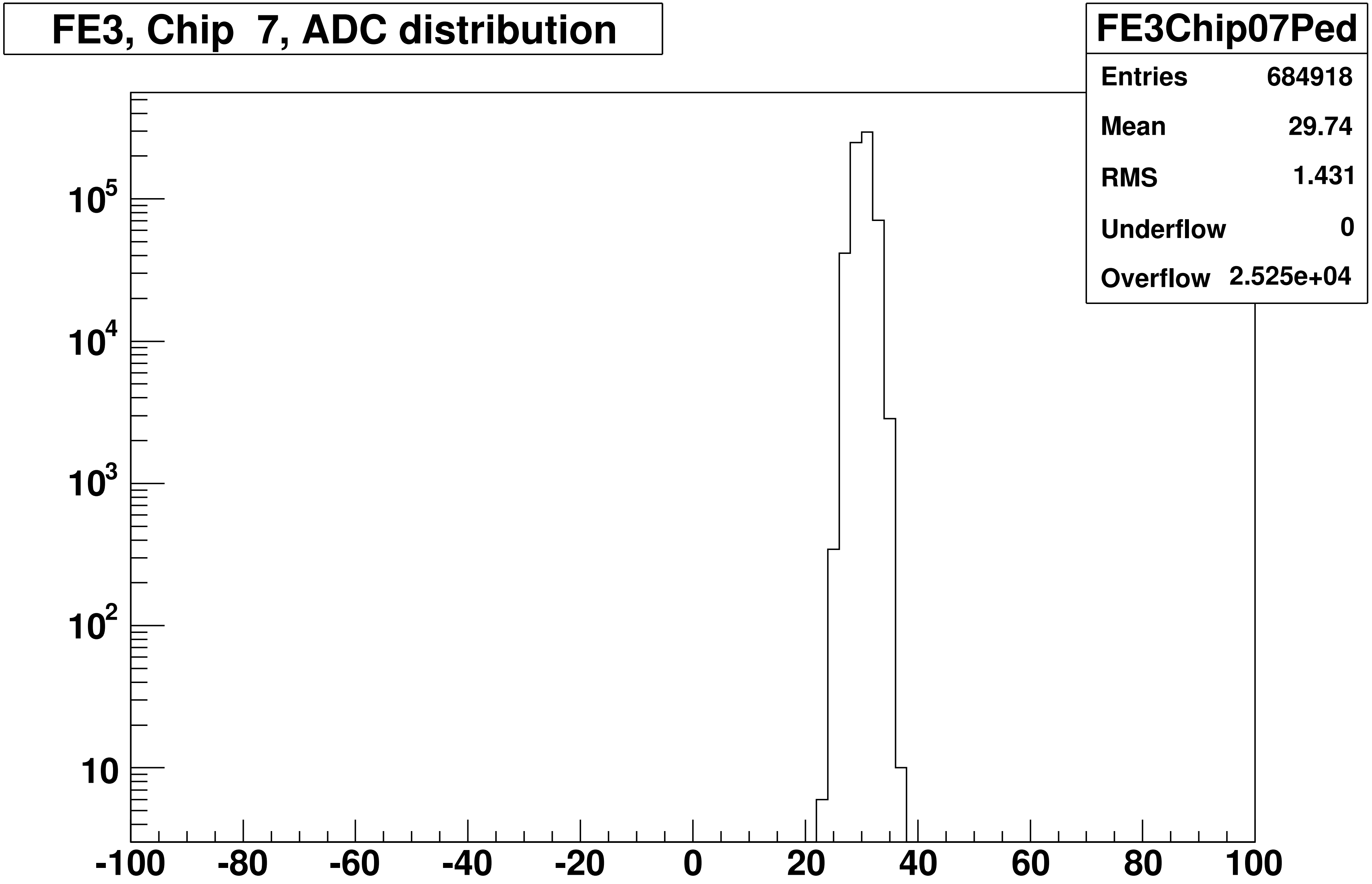


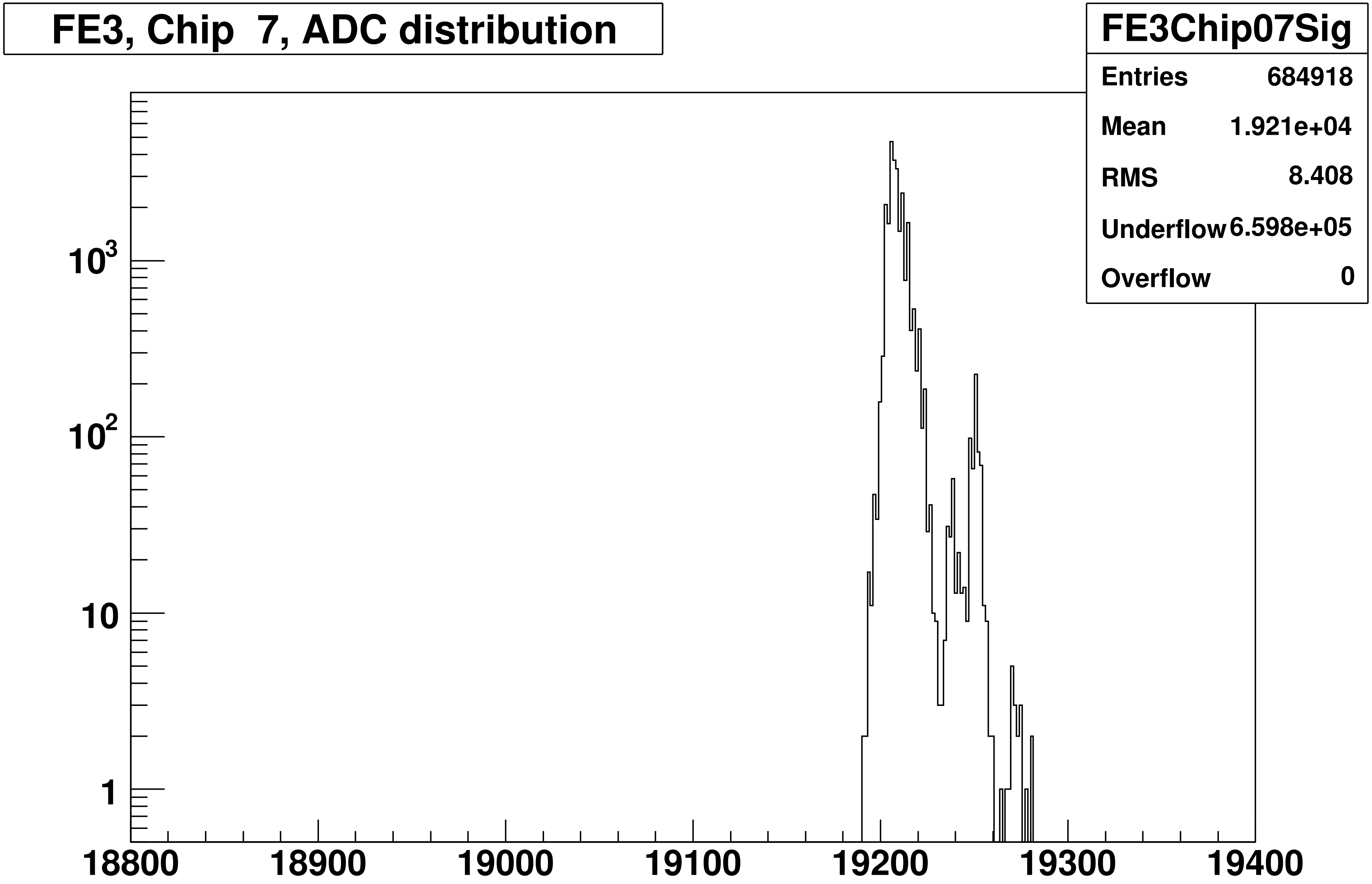
FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 684918
Mean -1.938
RMS 1.608
Underflow 0
Overflow 2.515e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 684918
Mean 1.911e+04
RMS 7.004
Underflow 6.598e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

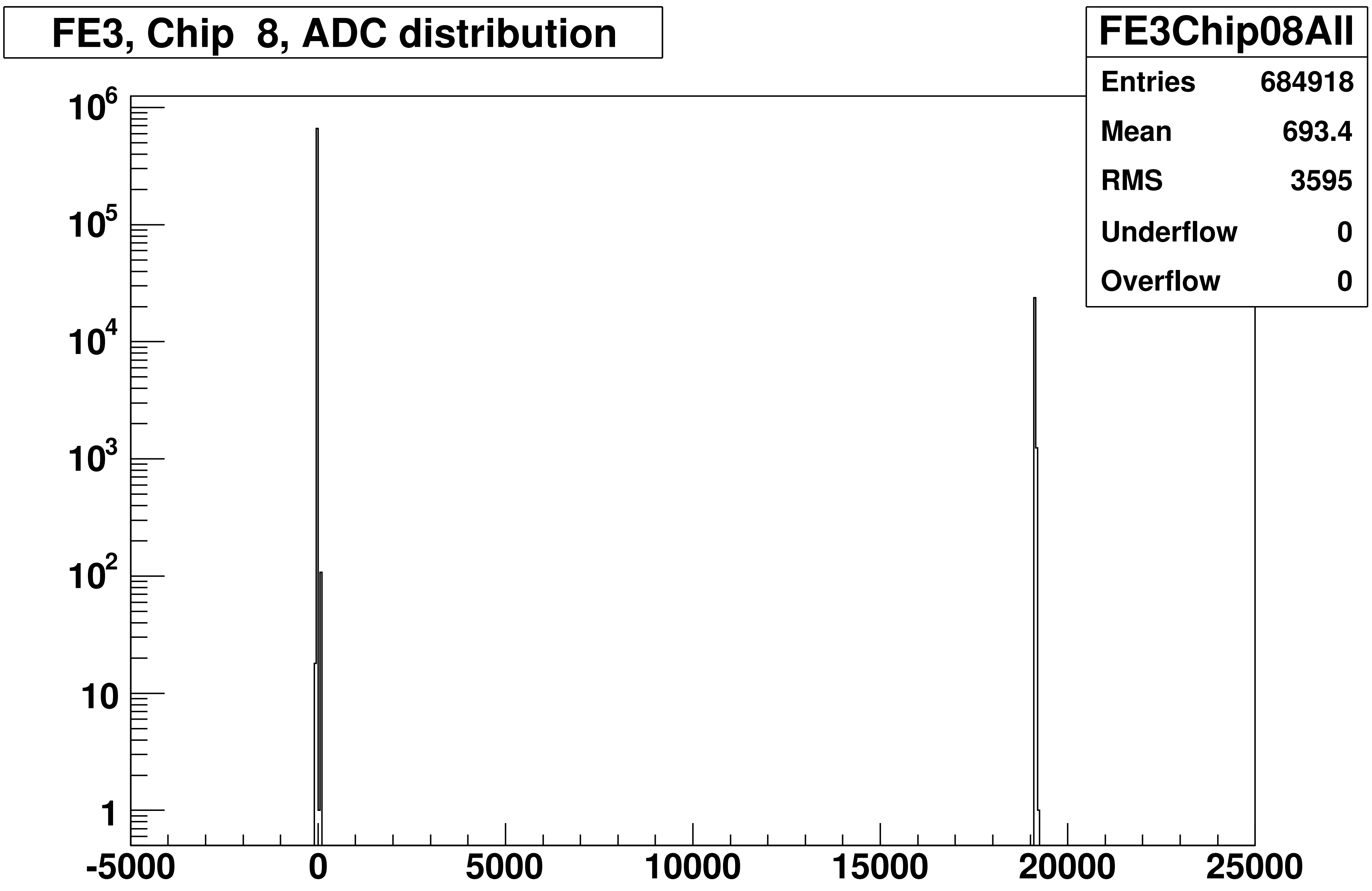


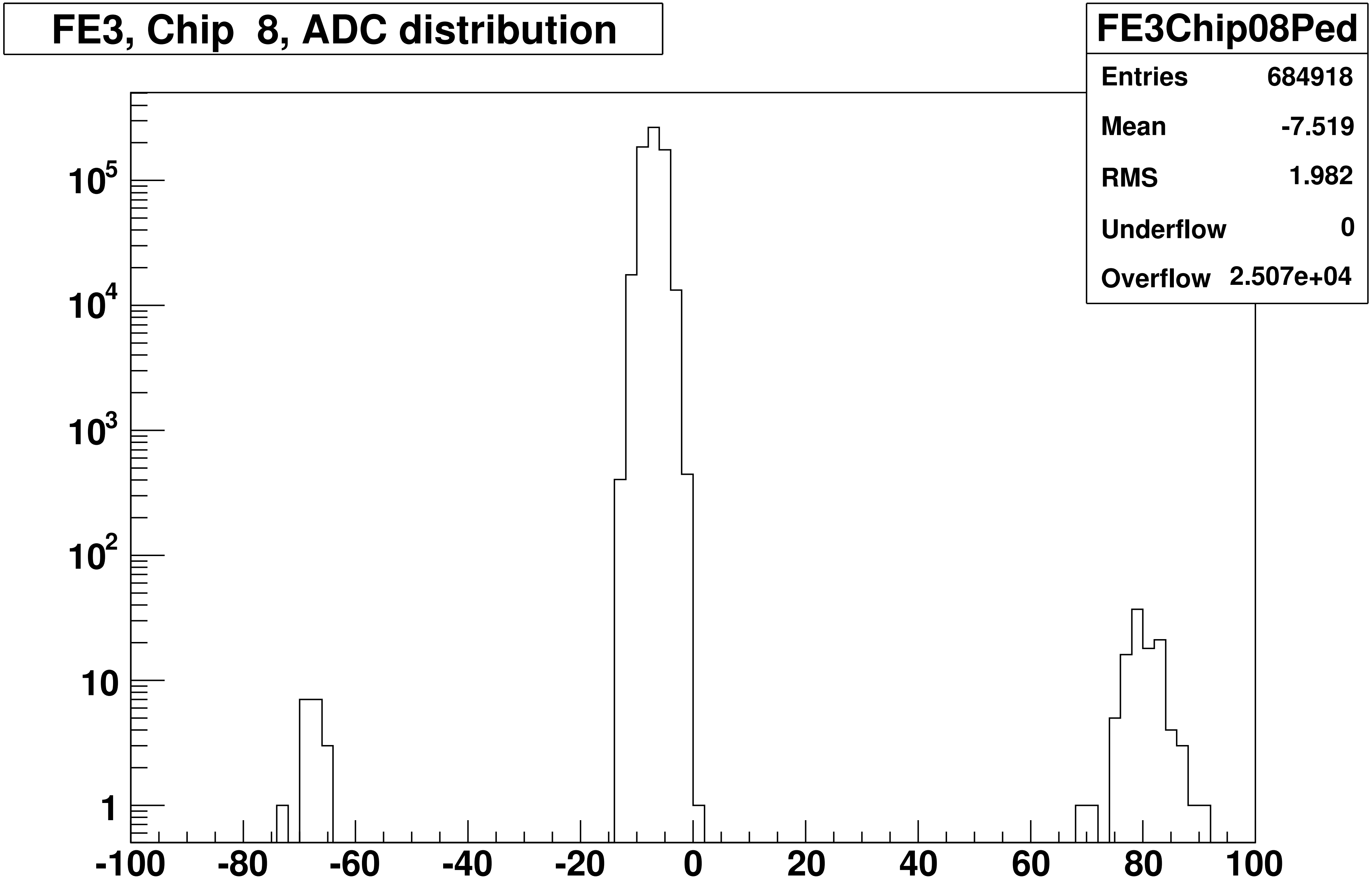
FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 684918
Mean 732.8
RMS 3603
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 684918
Mean 29.74
RMS 1.431
Underflow 0
Overflow 2.525e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

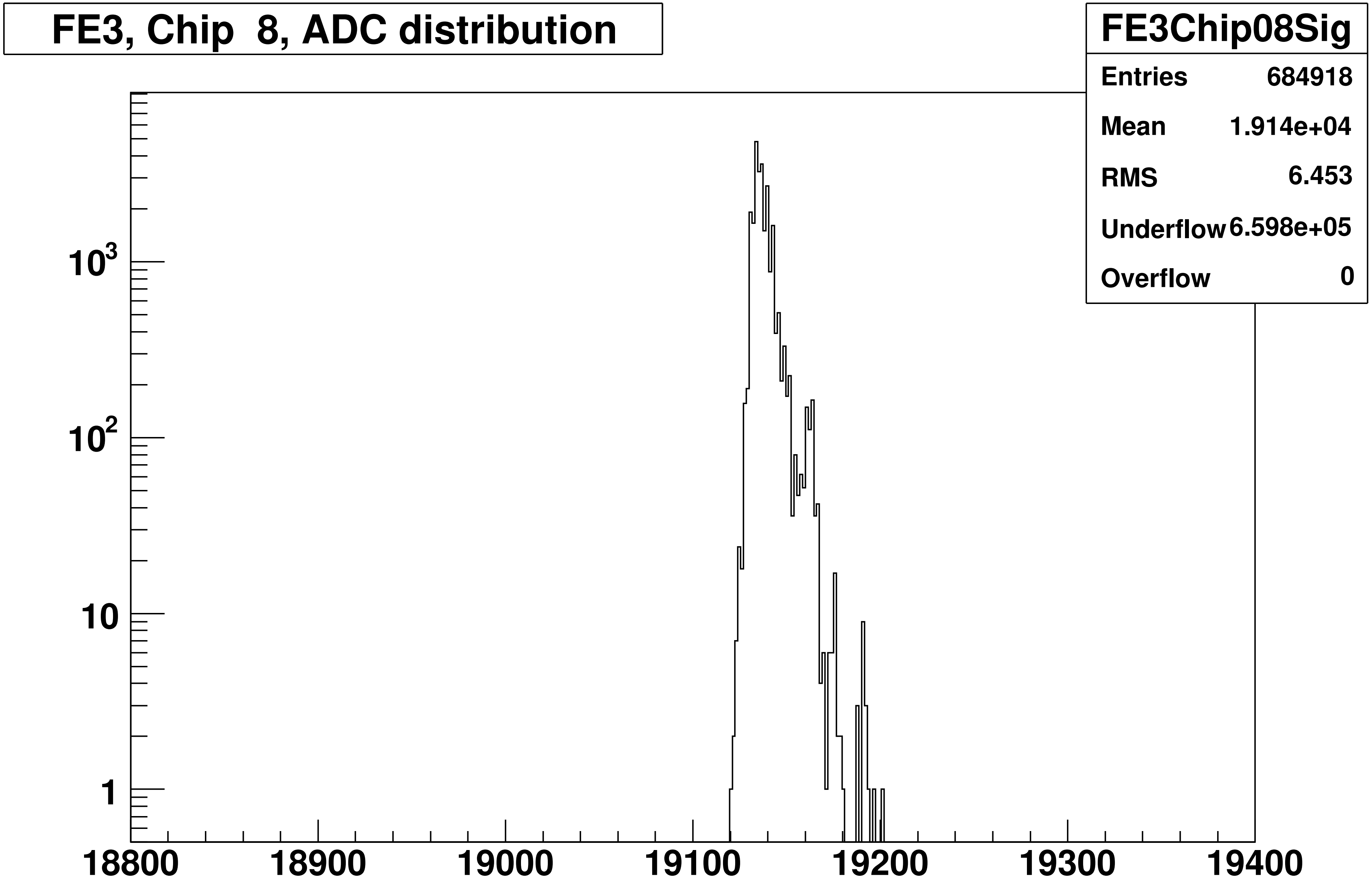


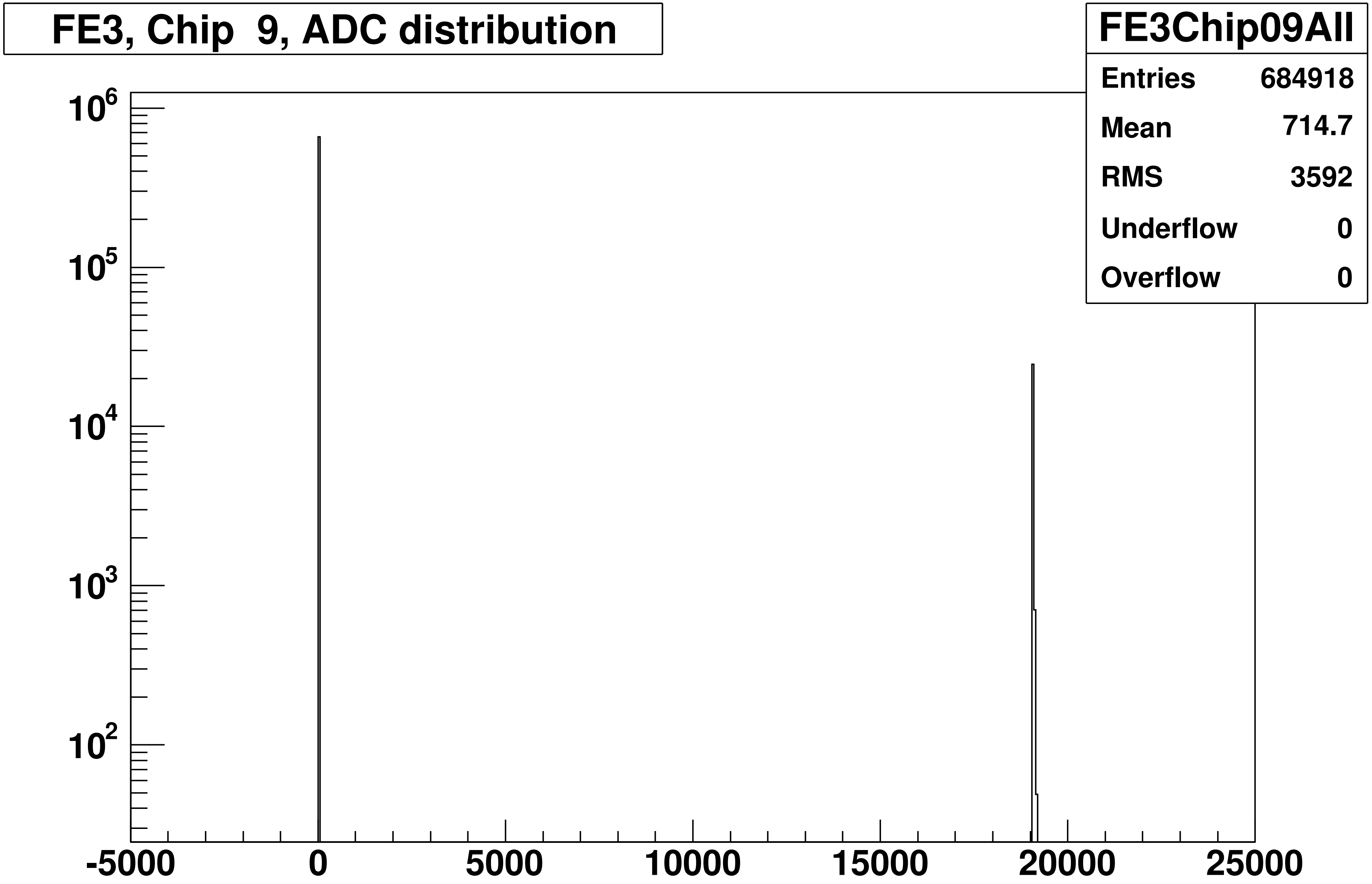
FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 684918
Mean 1.921e+04
RMS 8.408
Underflow 6.598e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

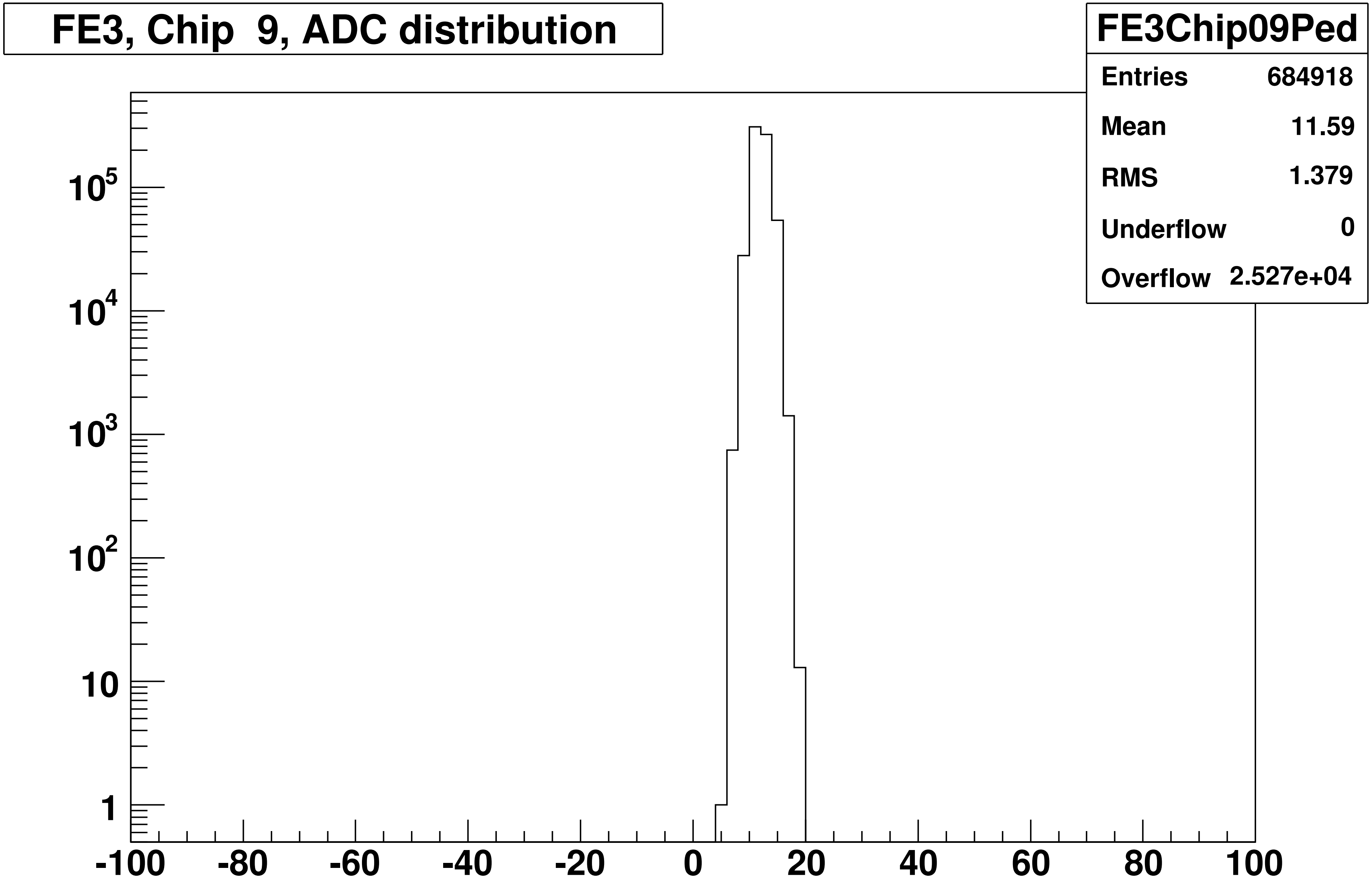

FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 684918
Mean 693.4
RMS 3595
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

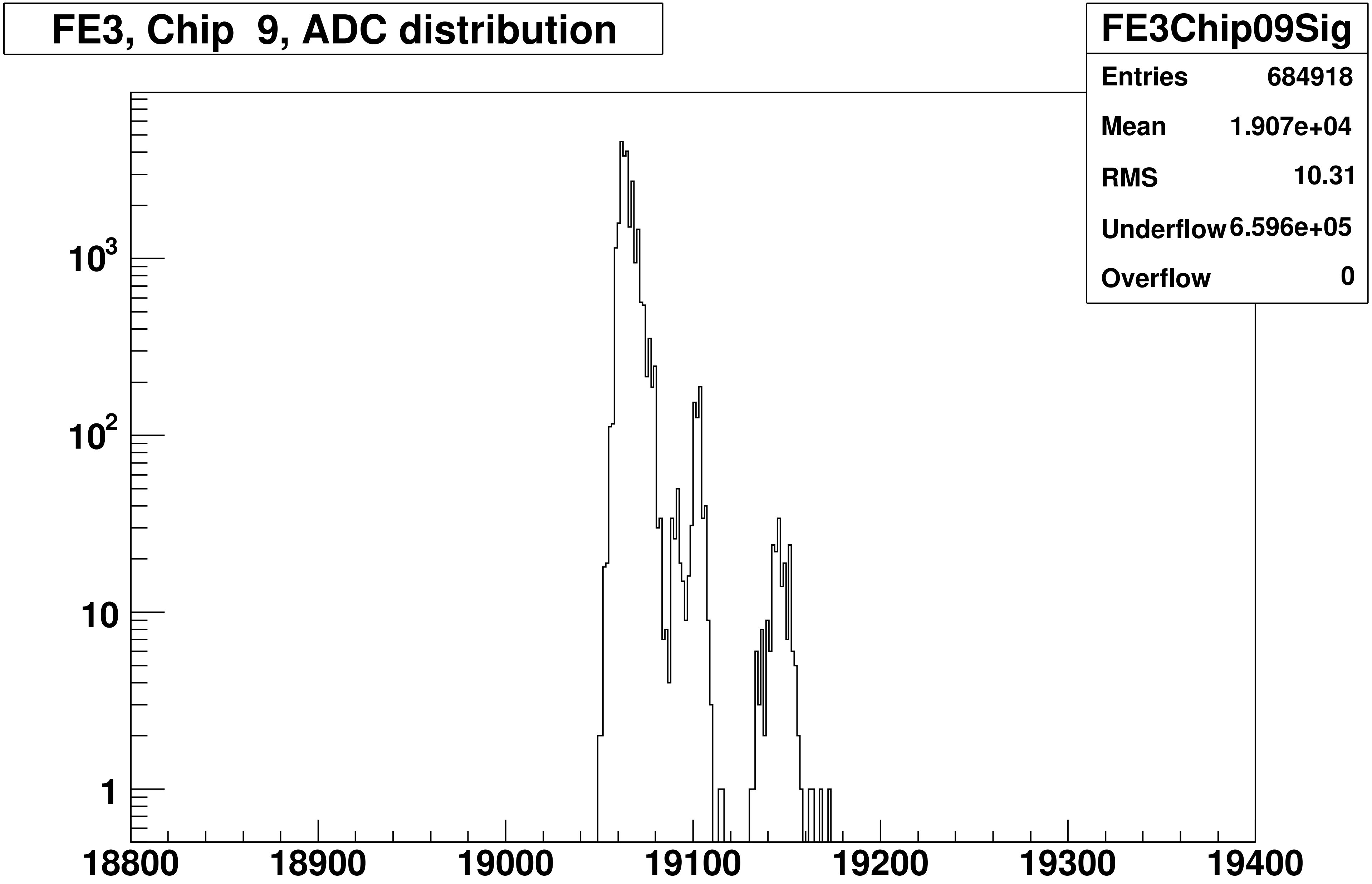


FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 684918
Mean -7.519
RMS 1.982
Underflow 0
Overflow 2.507e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

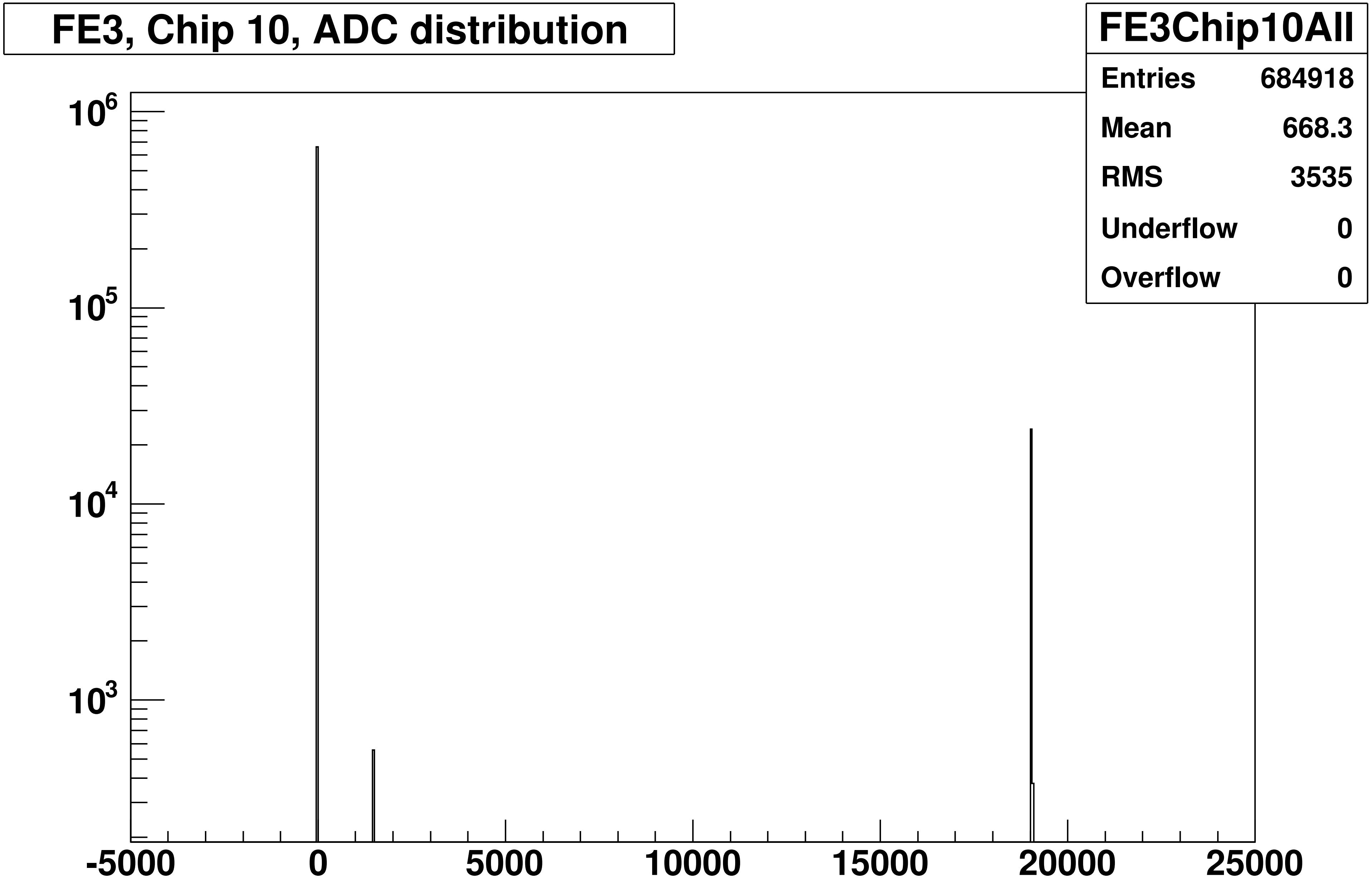

FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 684918
Mean 1.914e+04
RMS 6.453
Underflow 6.598e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 684918
Mean 714.7
RMS 3592
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

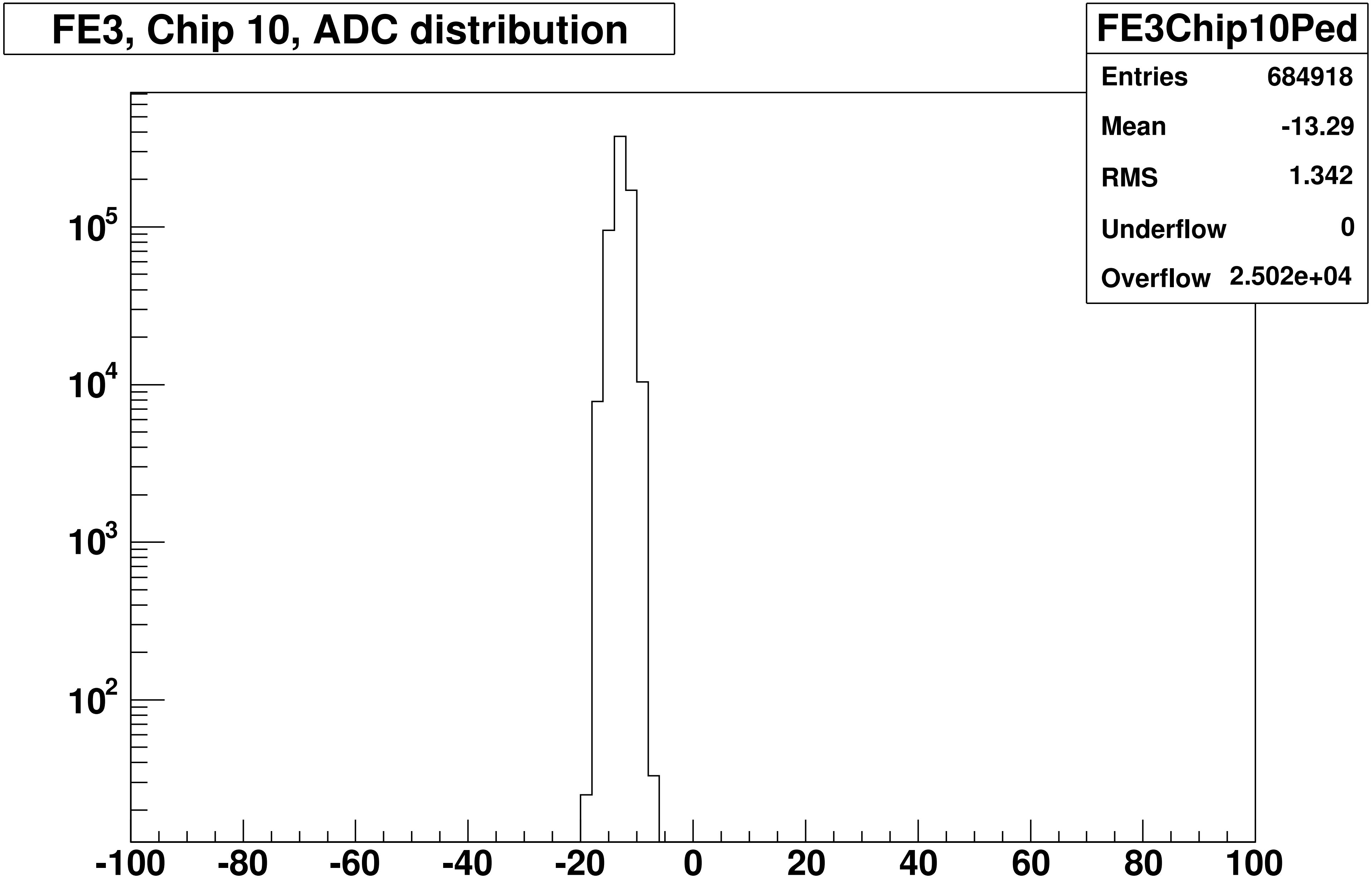

FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 684918
Mean 11.59
RMS 1.379
Underflow 0
Overflow 2.527e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



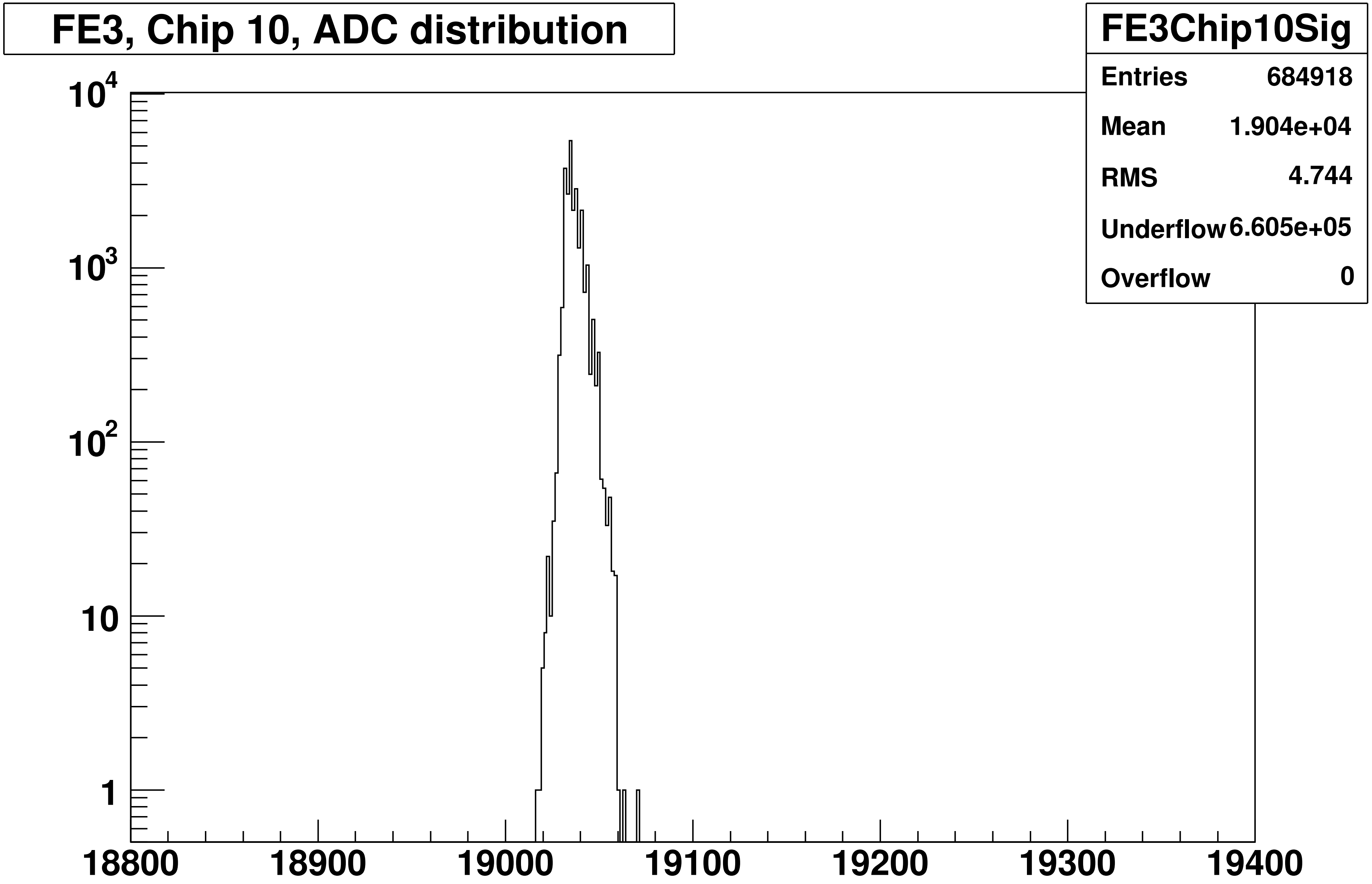
FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 684918
Mean 1.907e+04
RMS 10.31
Underflow 6.596e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

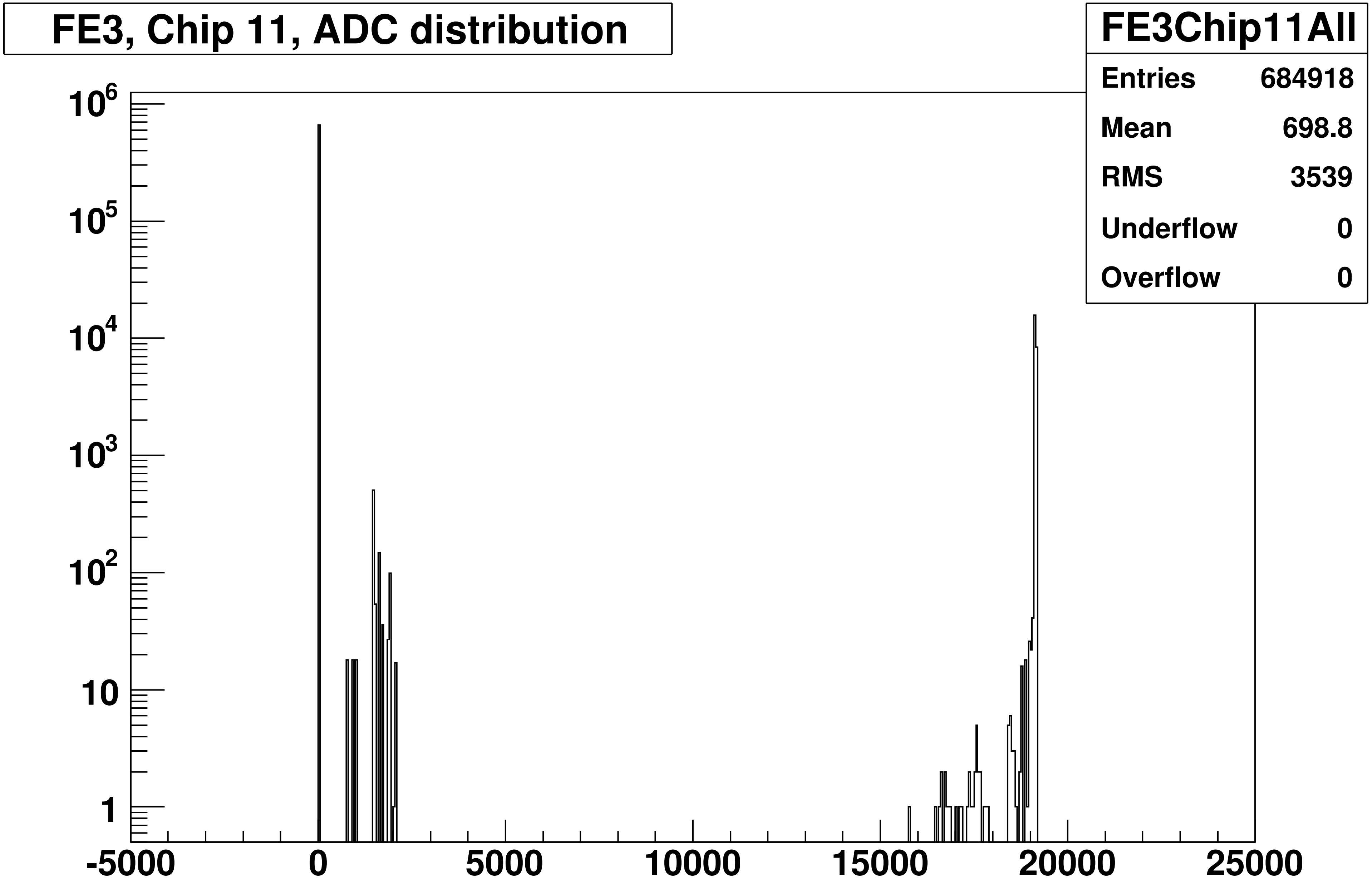


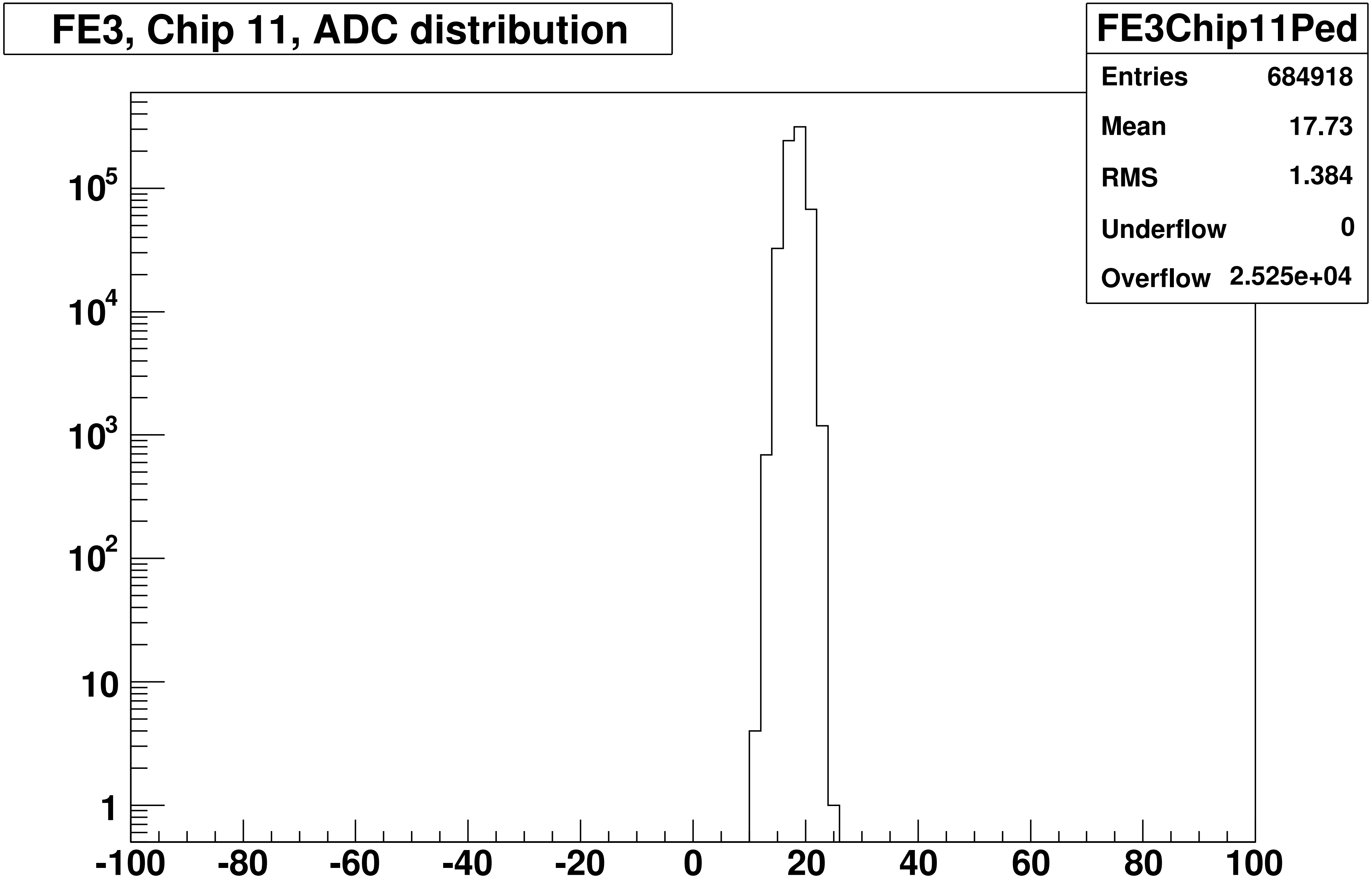
FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 684918
Mean 668.3
RMS 3535
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

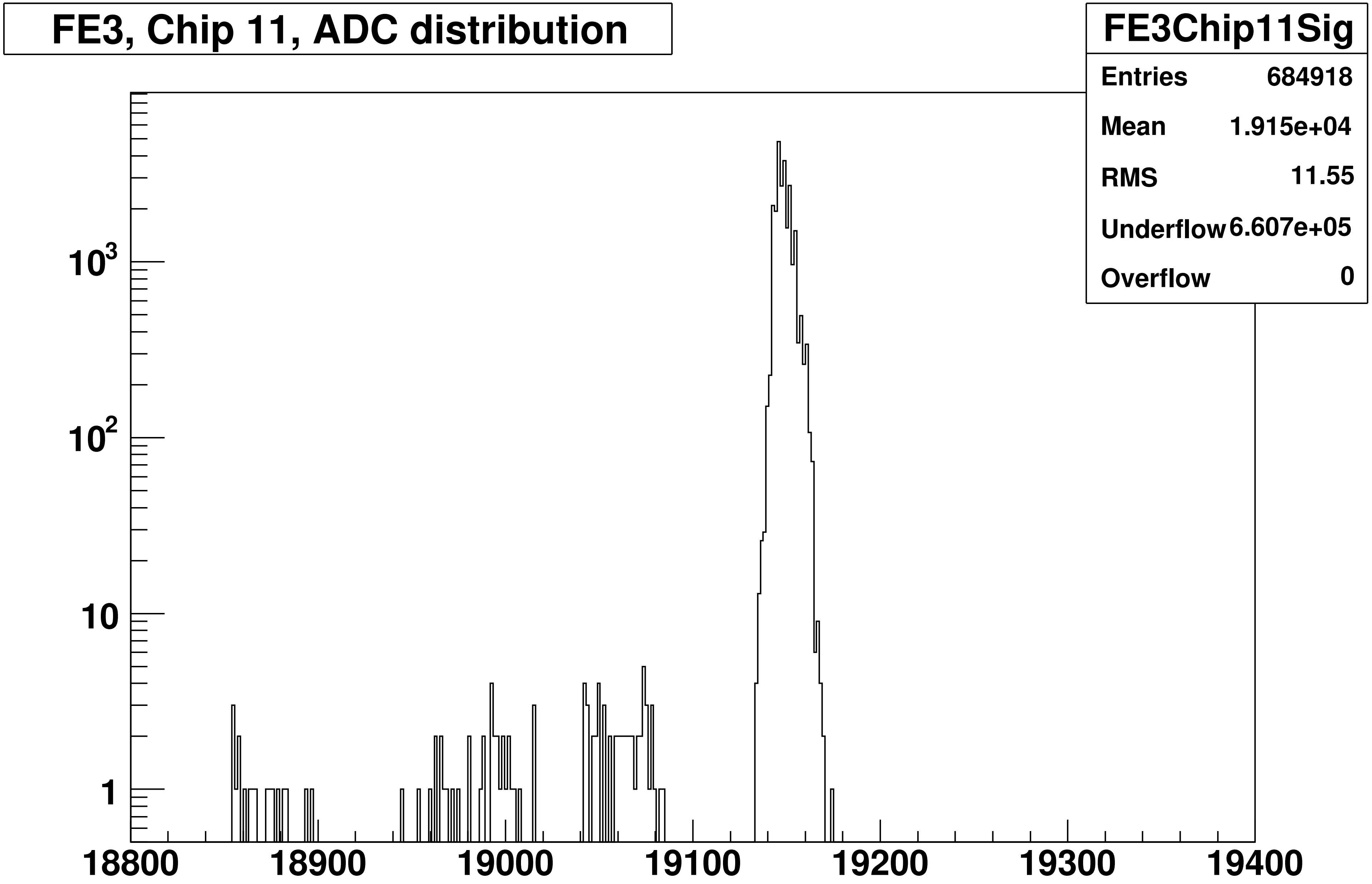


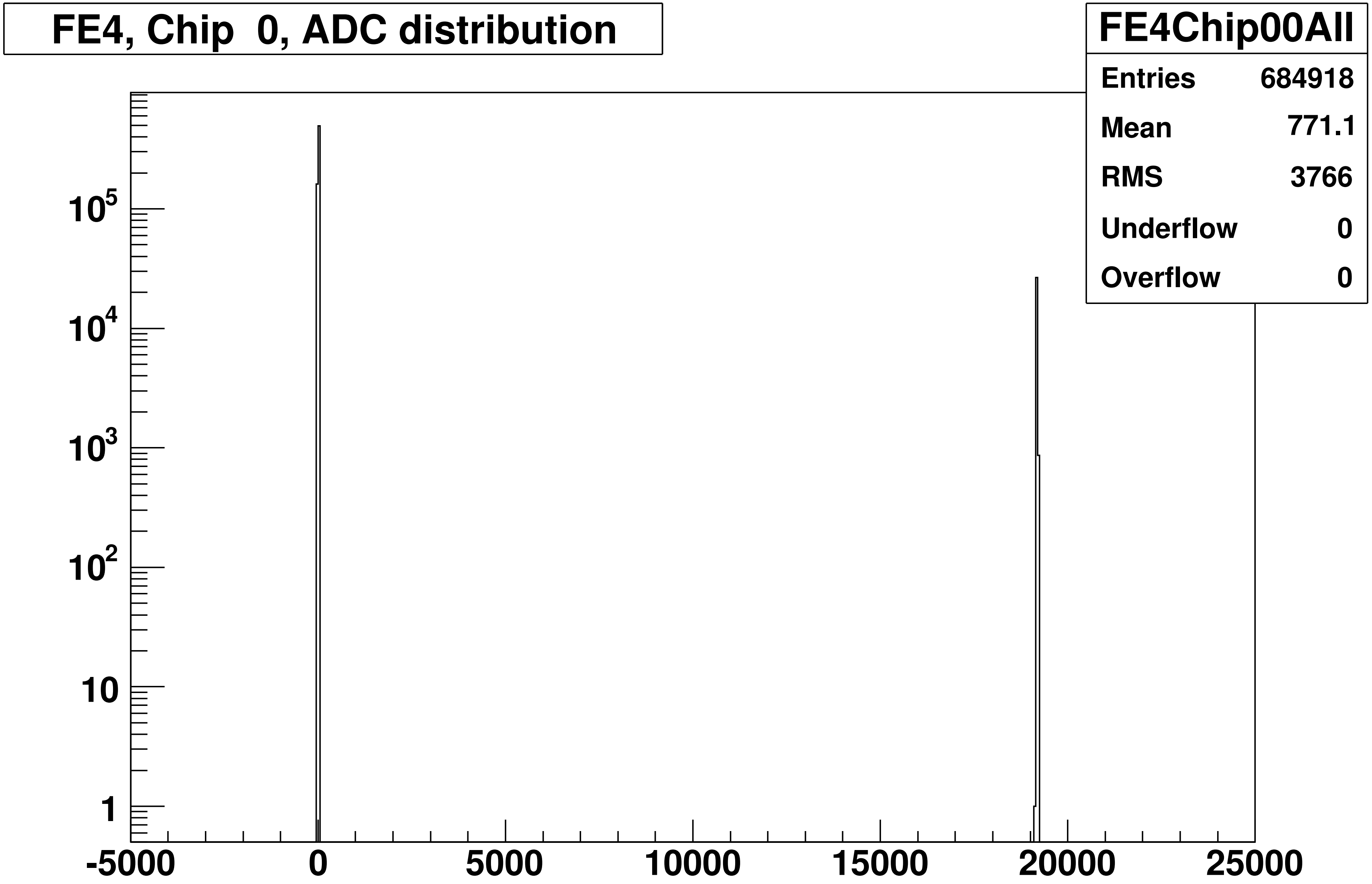
FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 684918
Mean -13.29
RMS 1.342
Underflow 0
Overflow 2.502e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 684918
Mean 1.904e+04
RMS 4.744
Underflow 6.605e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

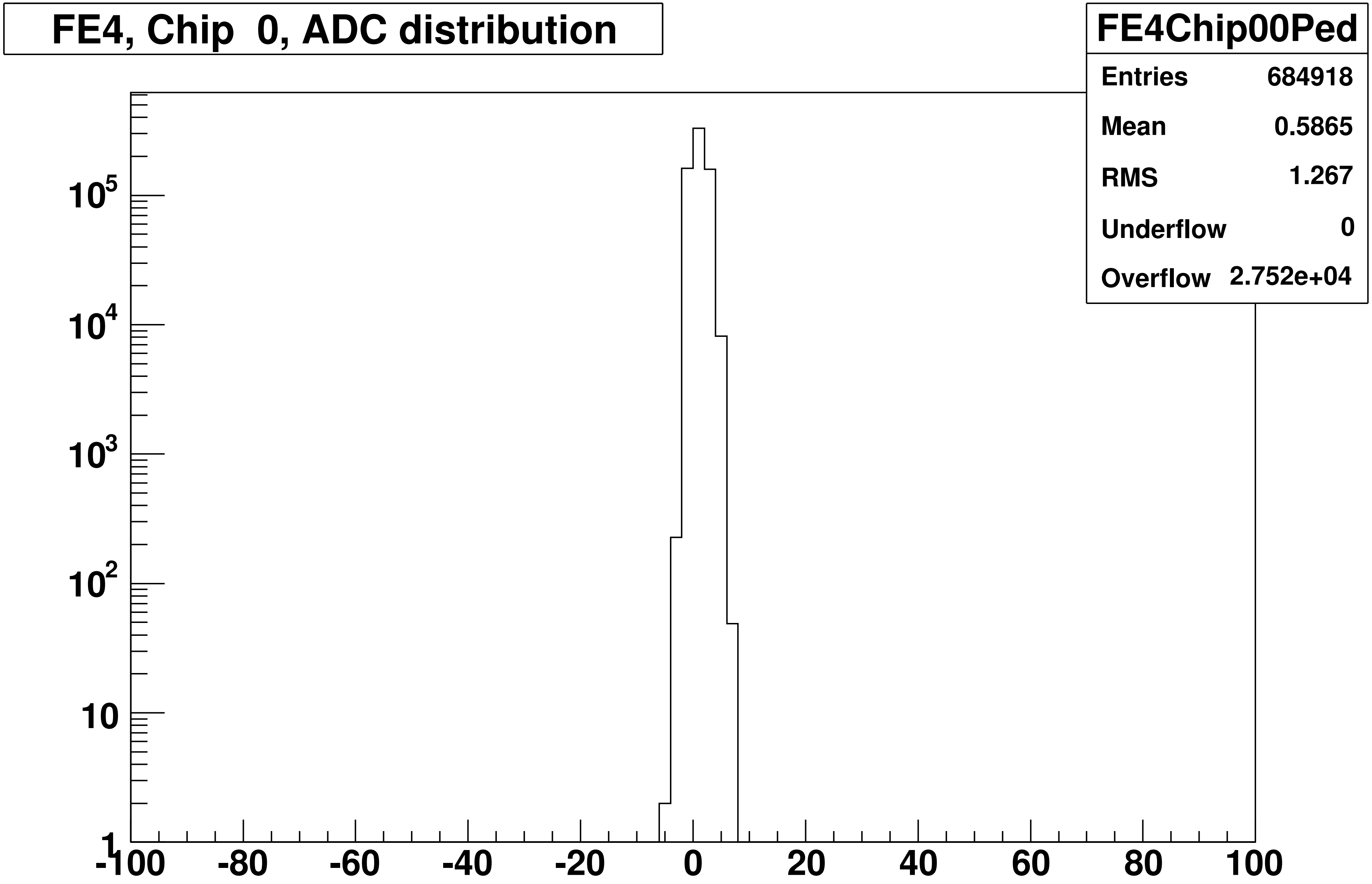

FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 684918
Mean 698.8
RMS 3539
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 684918
Mean 17.73
RMS 1.384
Underflow 0
Overflow 2.525e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

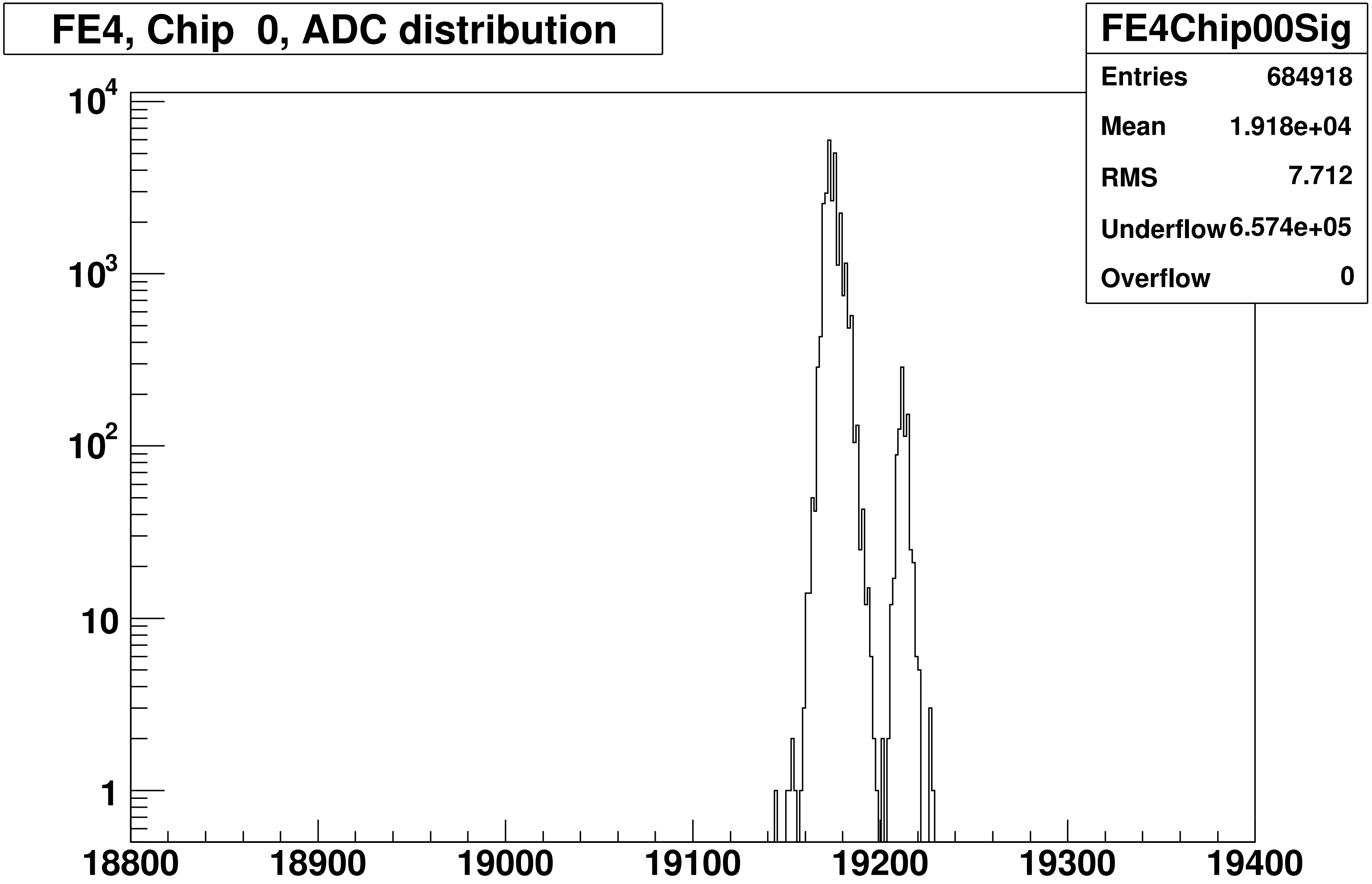

FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 684918
Mean 1.915e+04
RMS 11.55
Underflow 6.607e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



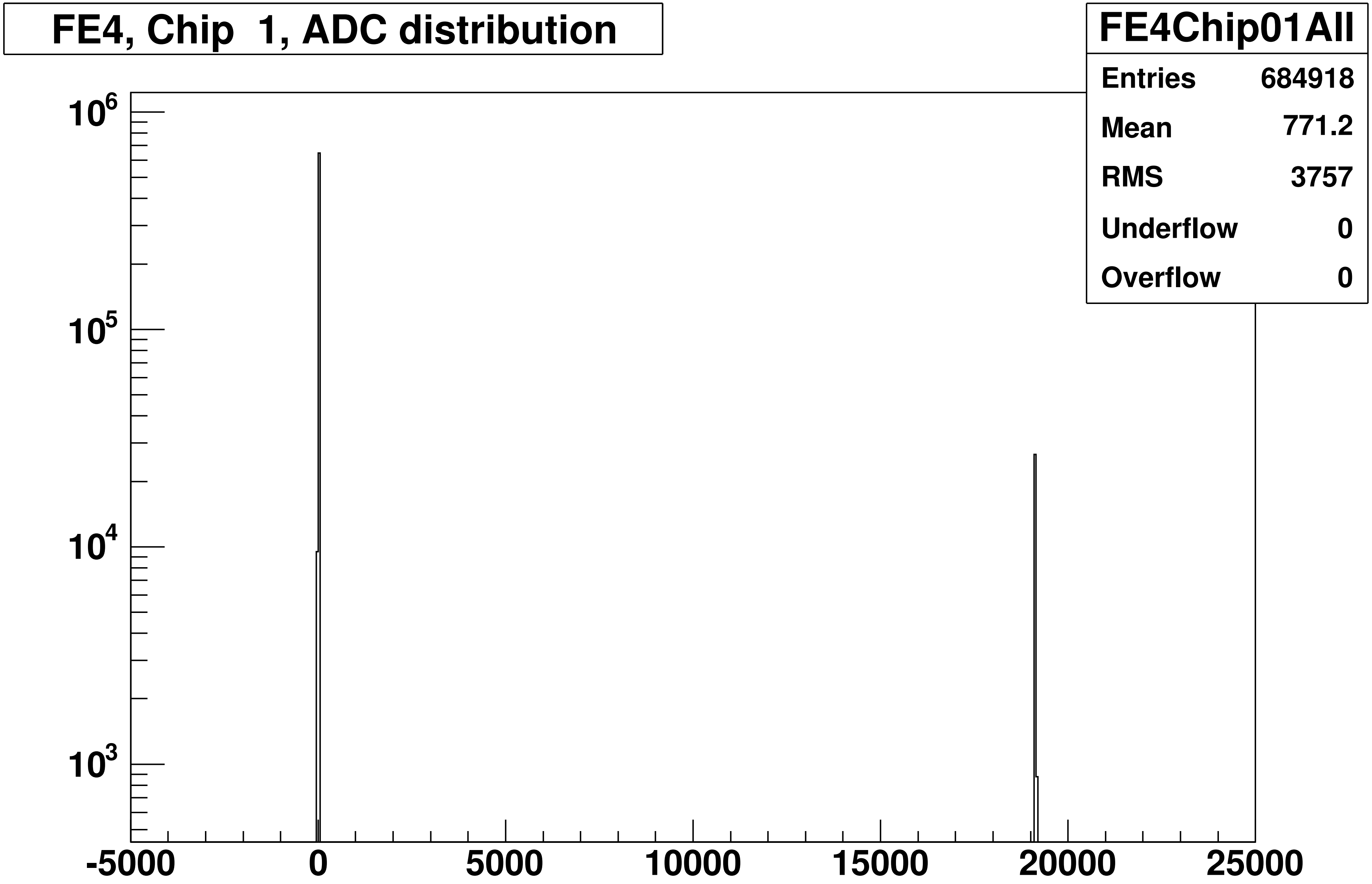
FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 684918
Mean 771.1
RMS 3766
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

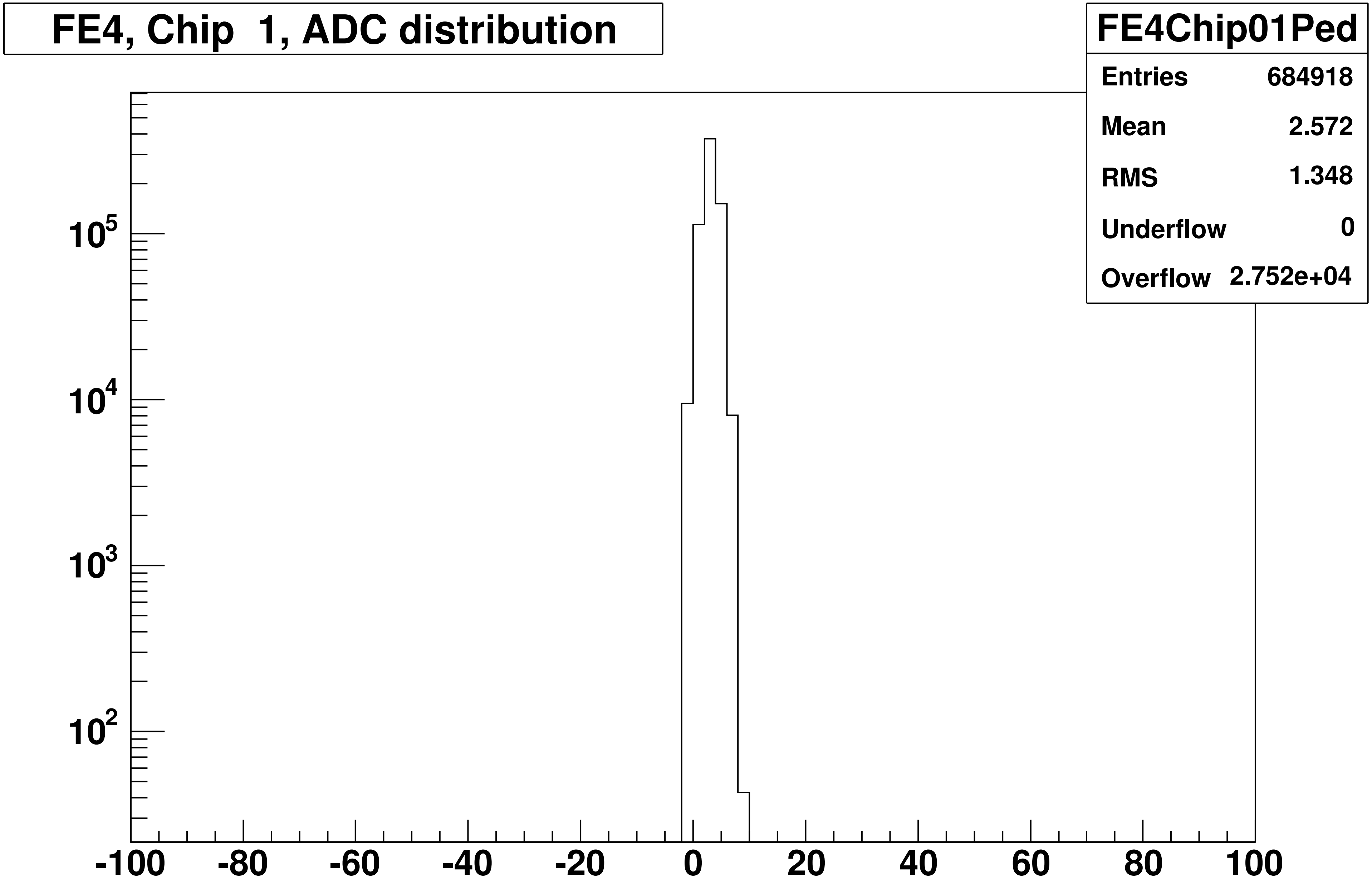


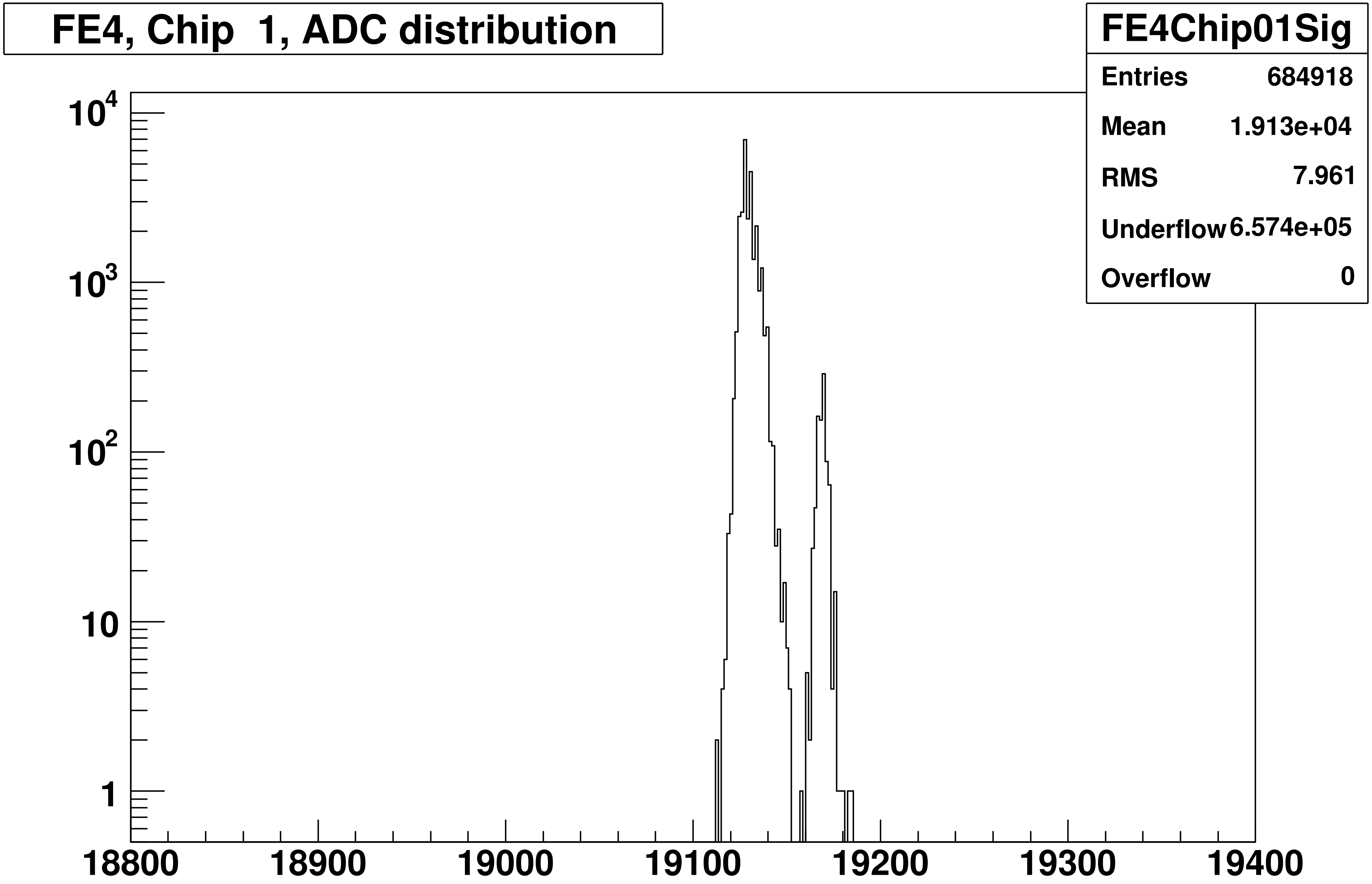
FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 684918
Mean 0.5865
RMS 1.267
Underflow 0
Overflow 2.752e+04
1
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

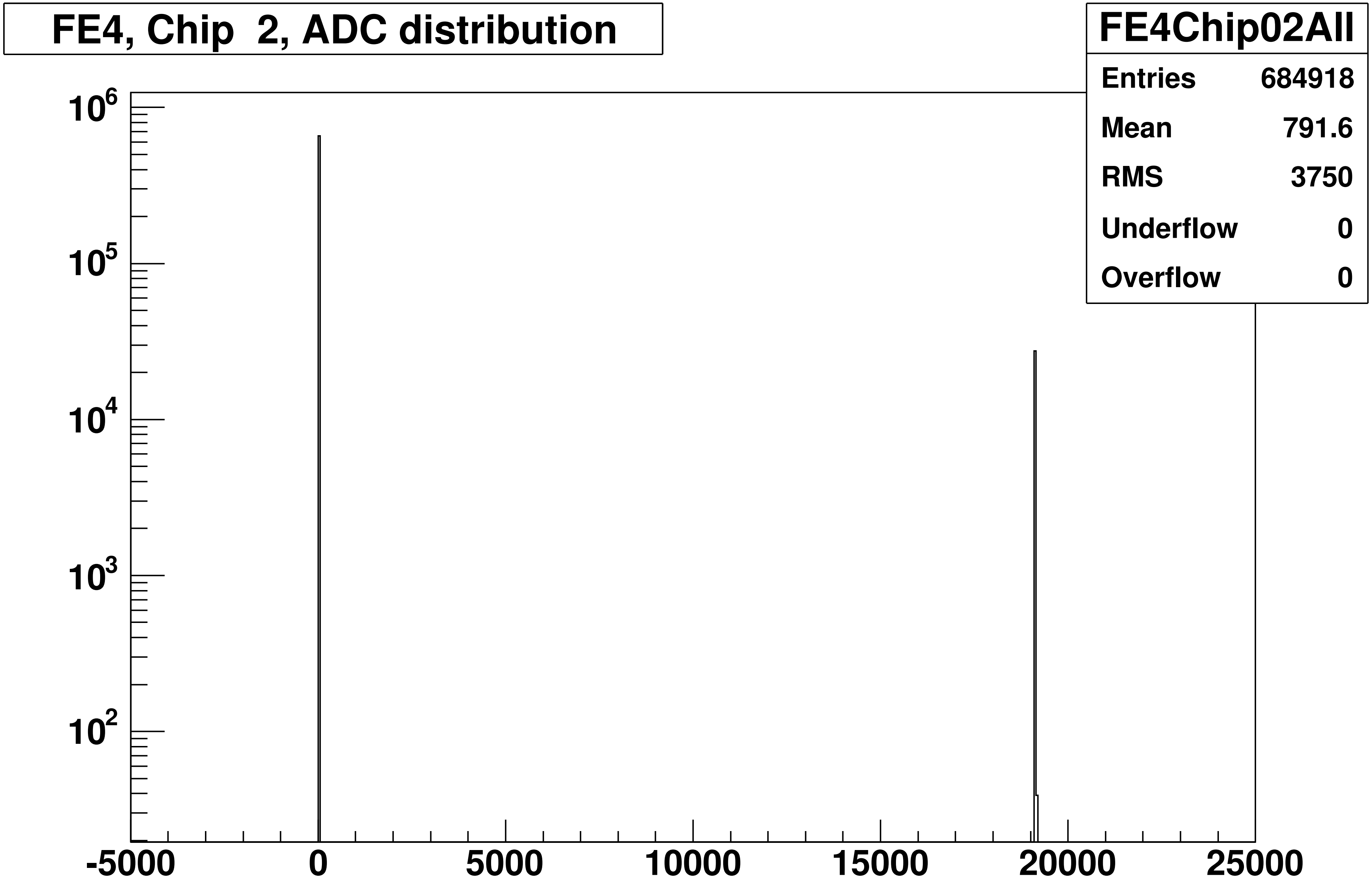


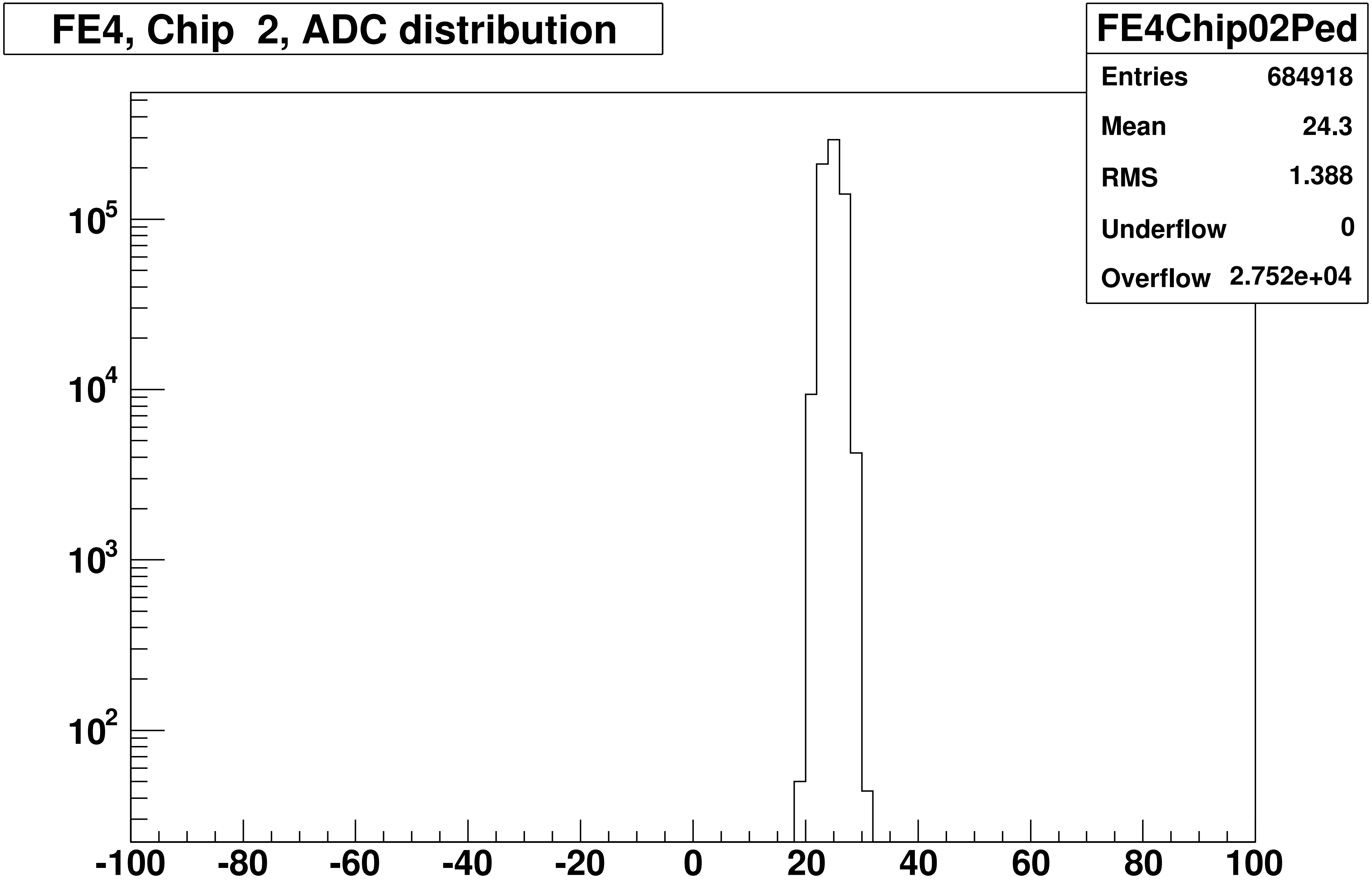
FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 684918
Mean 1.918e+04
RMS 7.712
Underflow 6.574e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

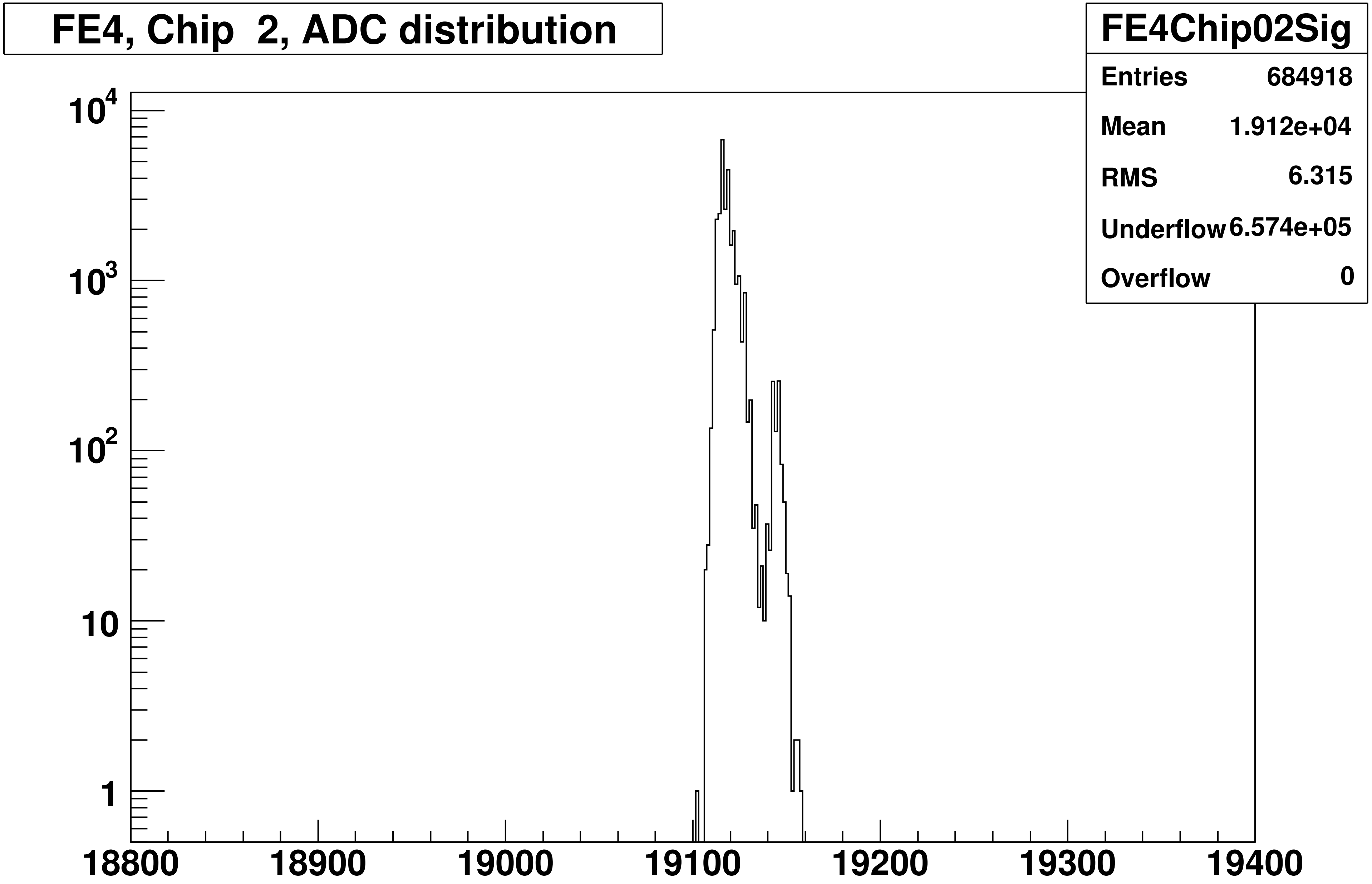

FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 684918
Mean 771.2
RMS 3757
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 684918
Mean 2.572
RMS 1.348
Underflow 0
Overflow 2.752e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

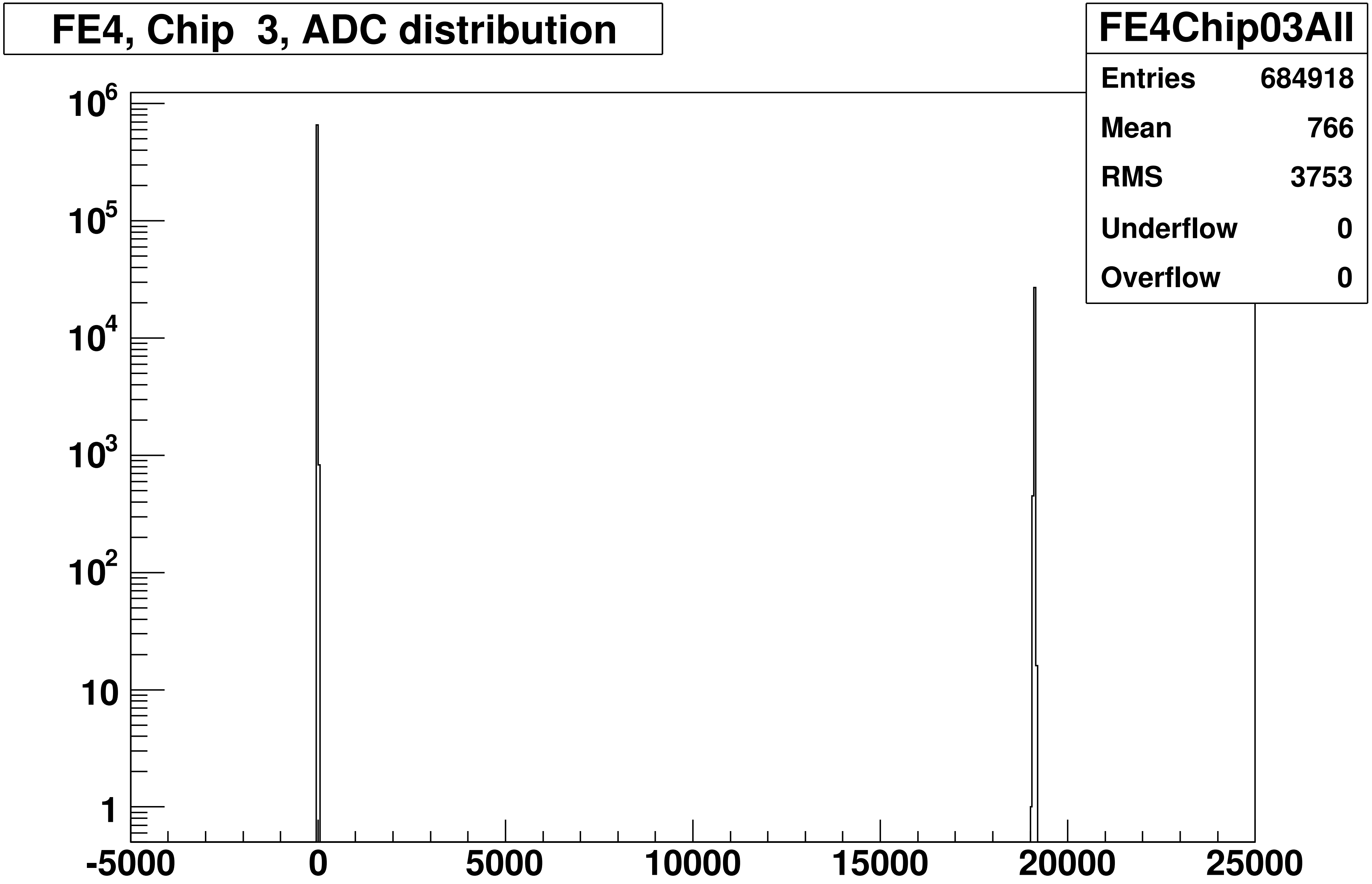

FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 684918
Mean 1.913e+04
RMS 7.961
Underflow 6.574e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

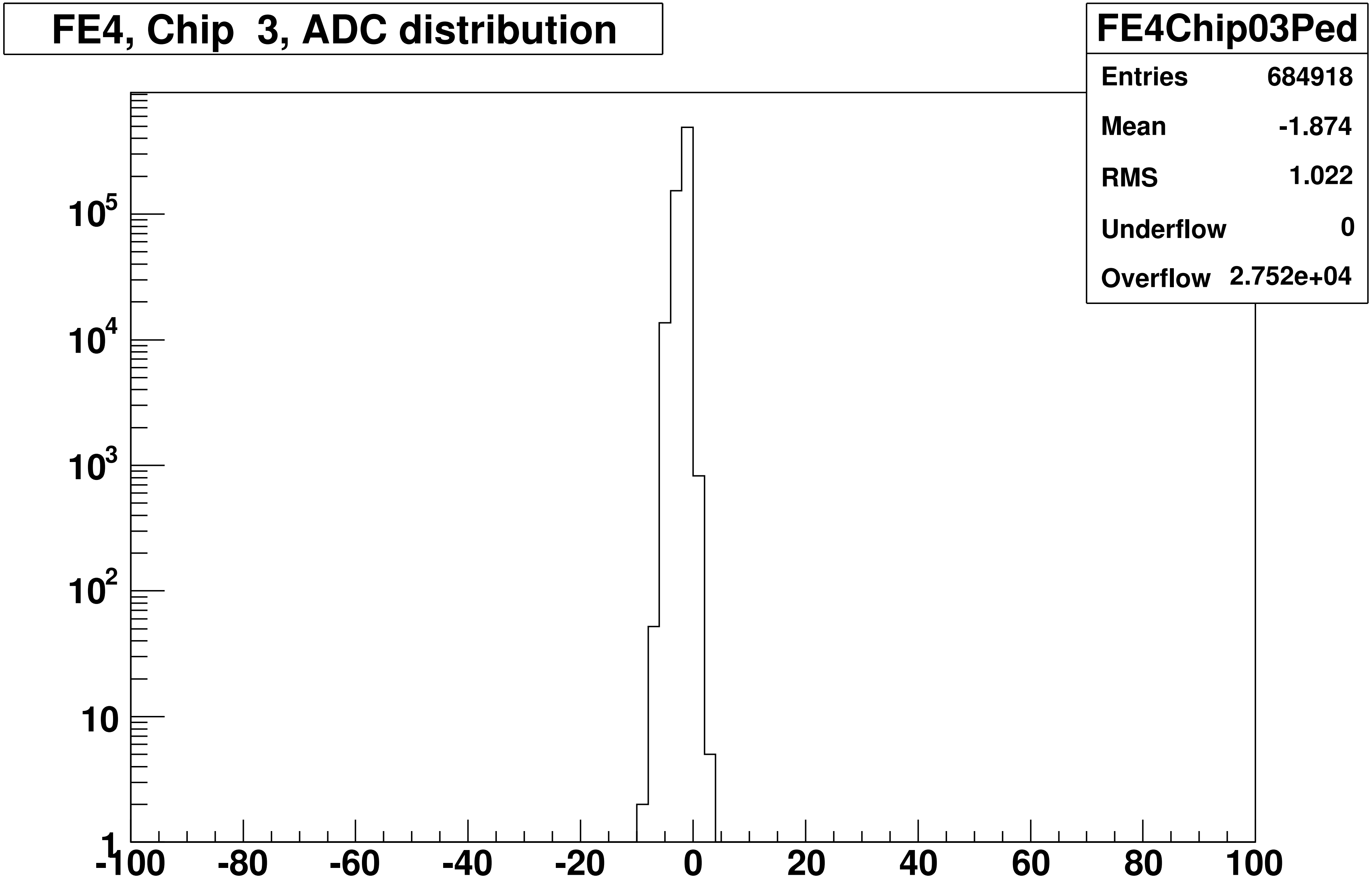

FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 684918
Mean 791.6
RMS 3750
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 684918
Mean 24.3
RMS 1.388
Underflow 0
Overflow 2.752e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

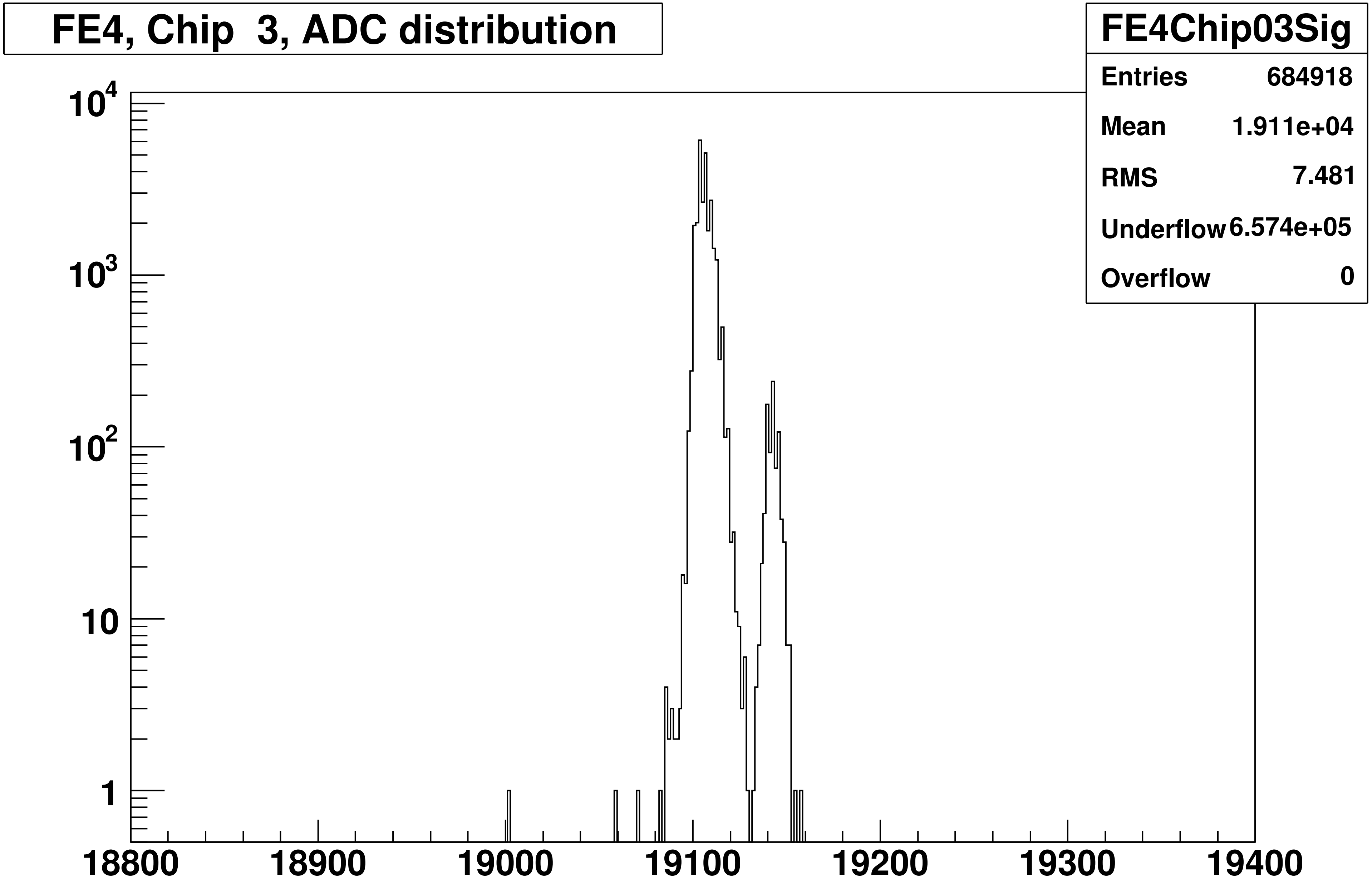


FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 684918
Mean 1.912e+04
RMS 6.315
Underflow 6.574e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

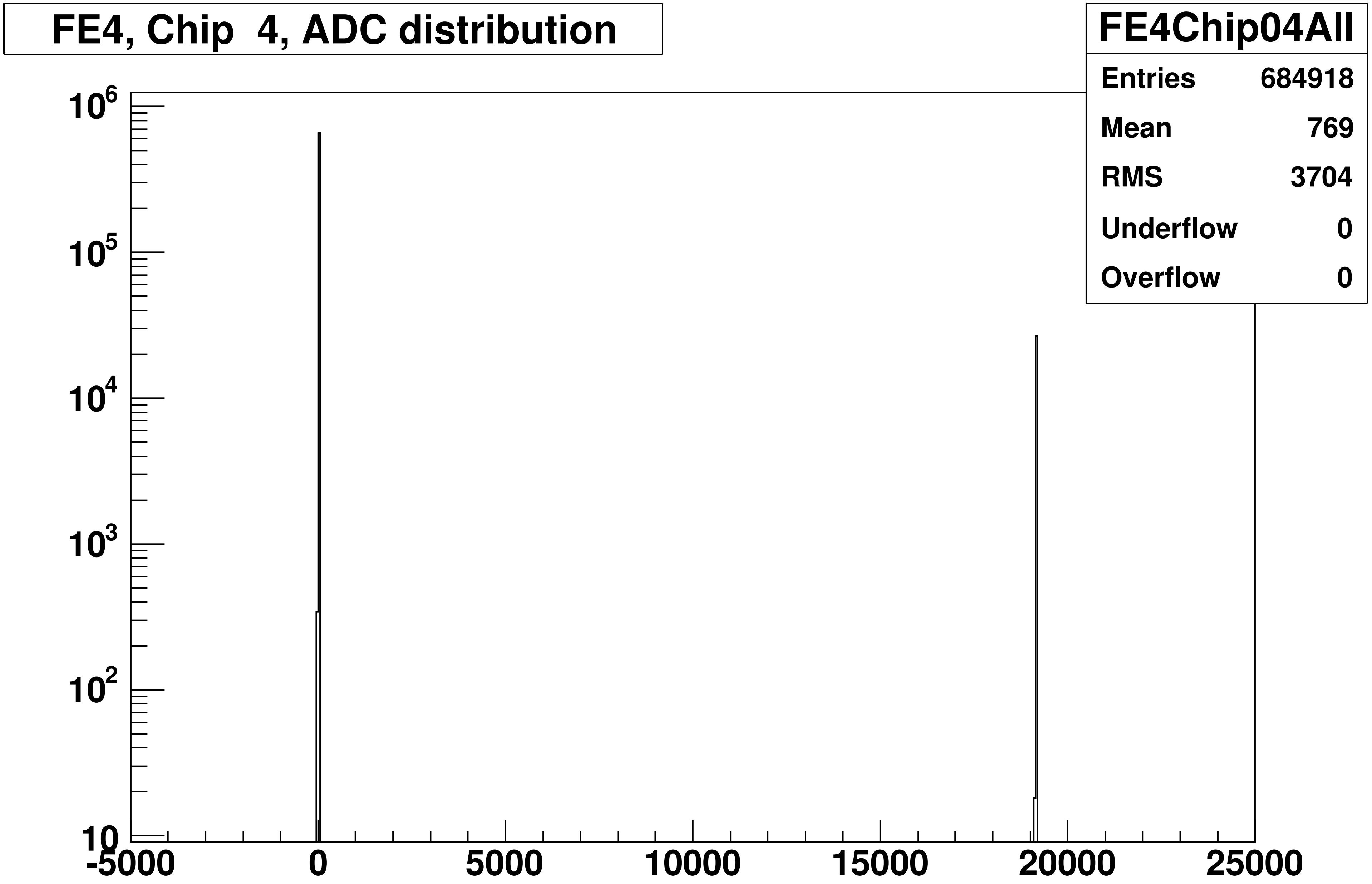

FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 684918
Mean 766
RMS 3753
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



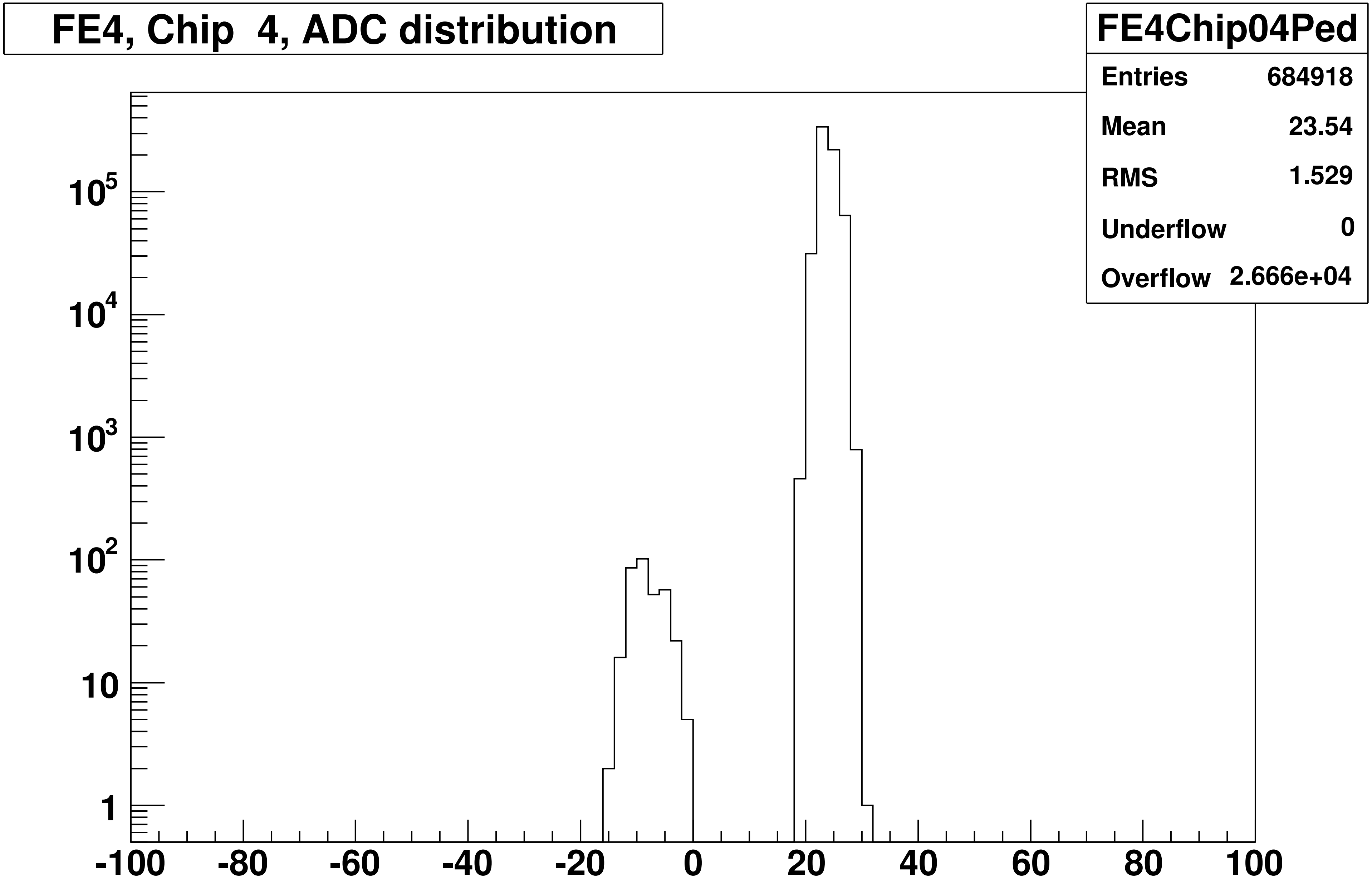
FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 684918
Mean -1.874
RMS 1.022
Underflow 0
Overflow 2.752e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

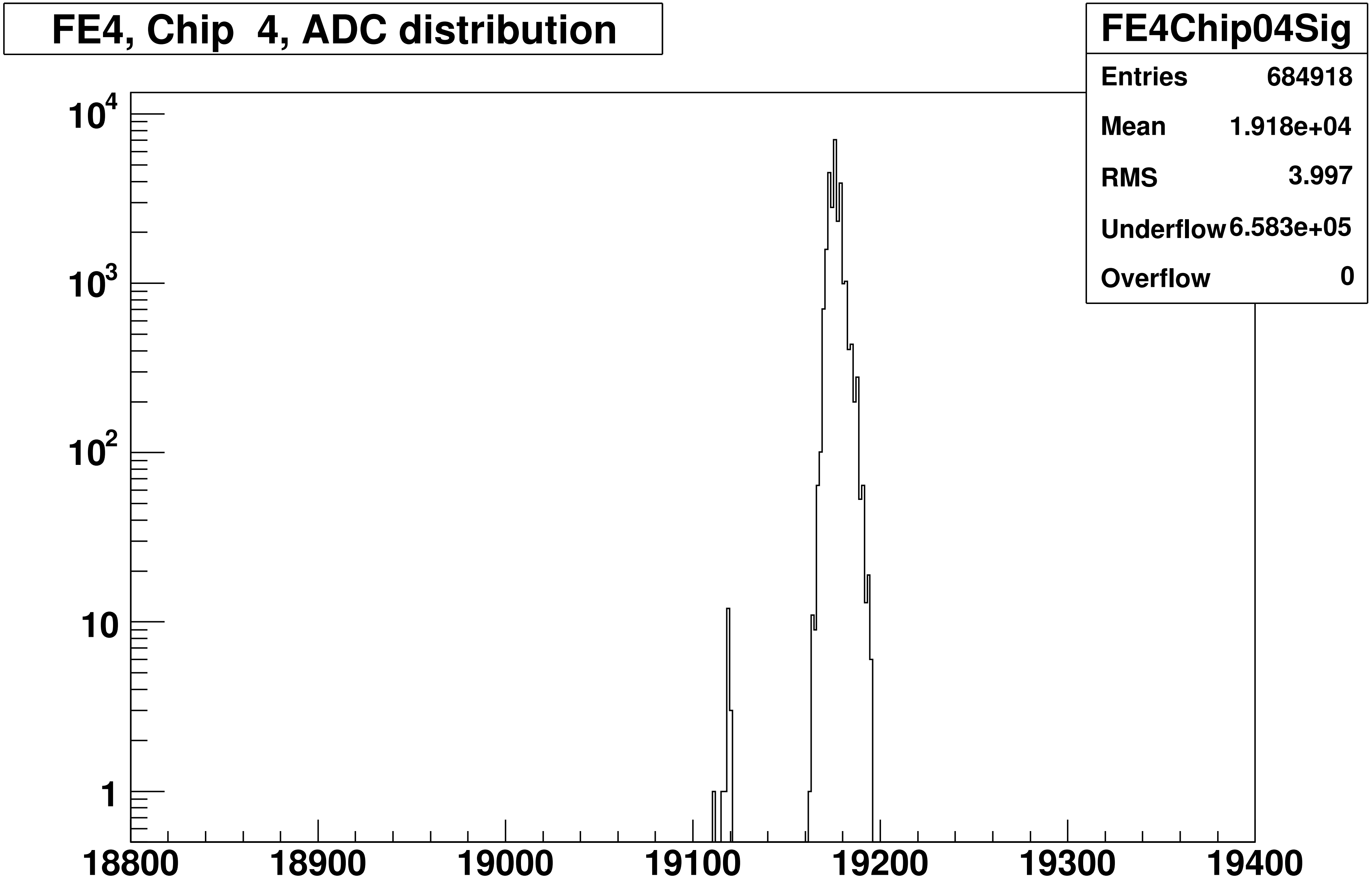


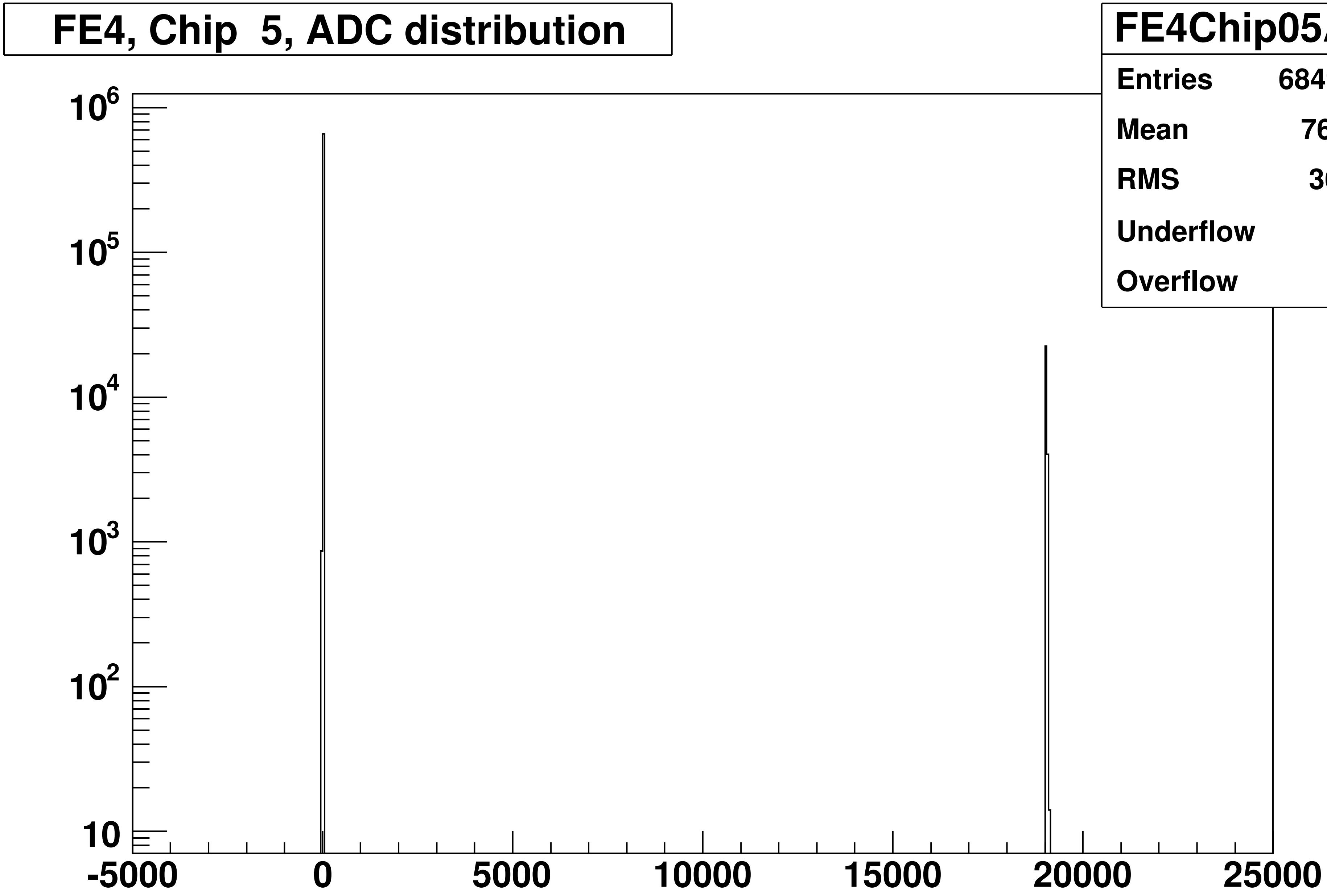
FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 684918
Mean 1.911e+04
RMS 7.481
Underflow 6.574e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

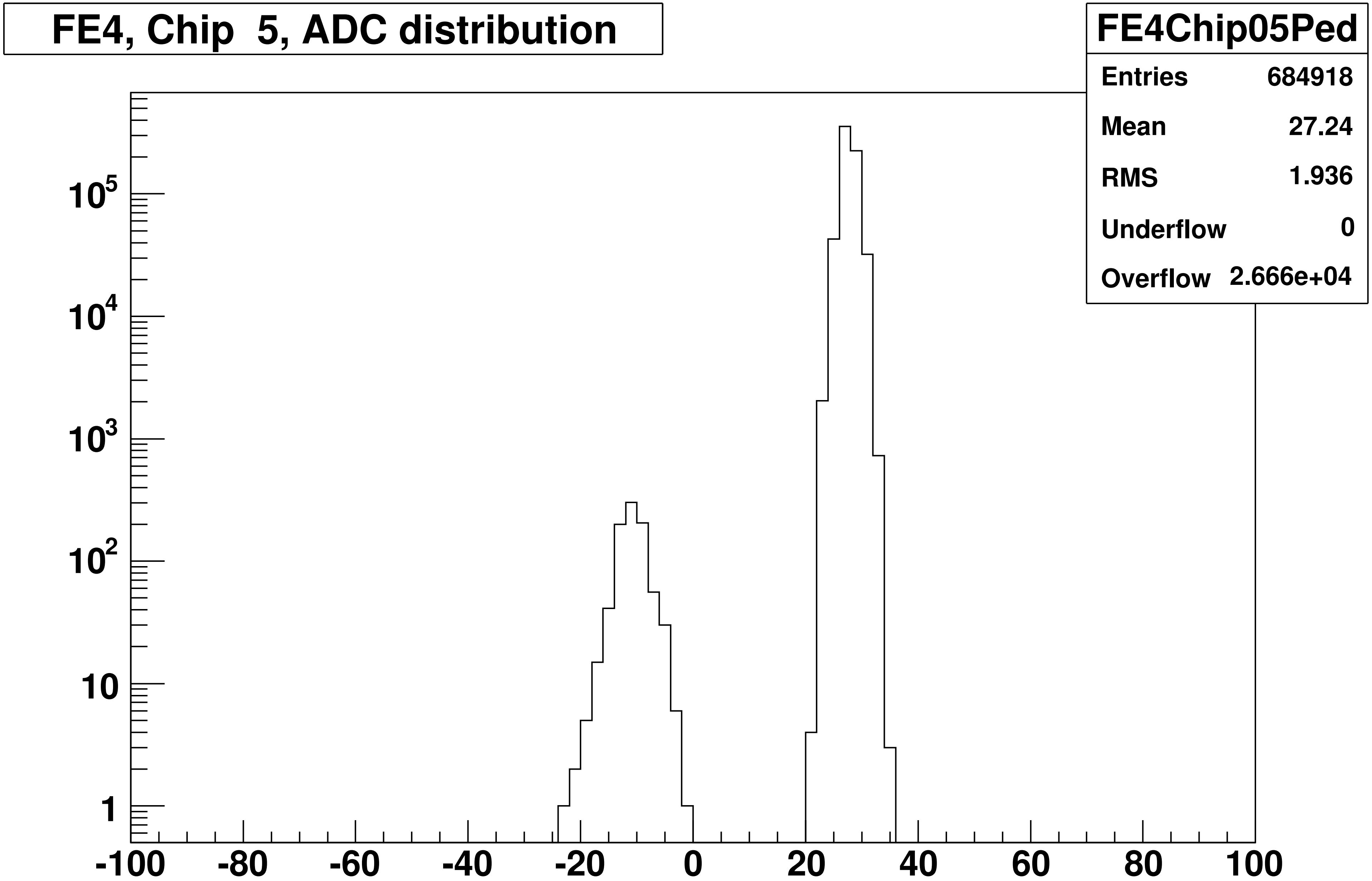


FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 684918
Mean 769
RMS 3704
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

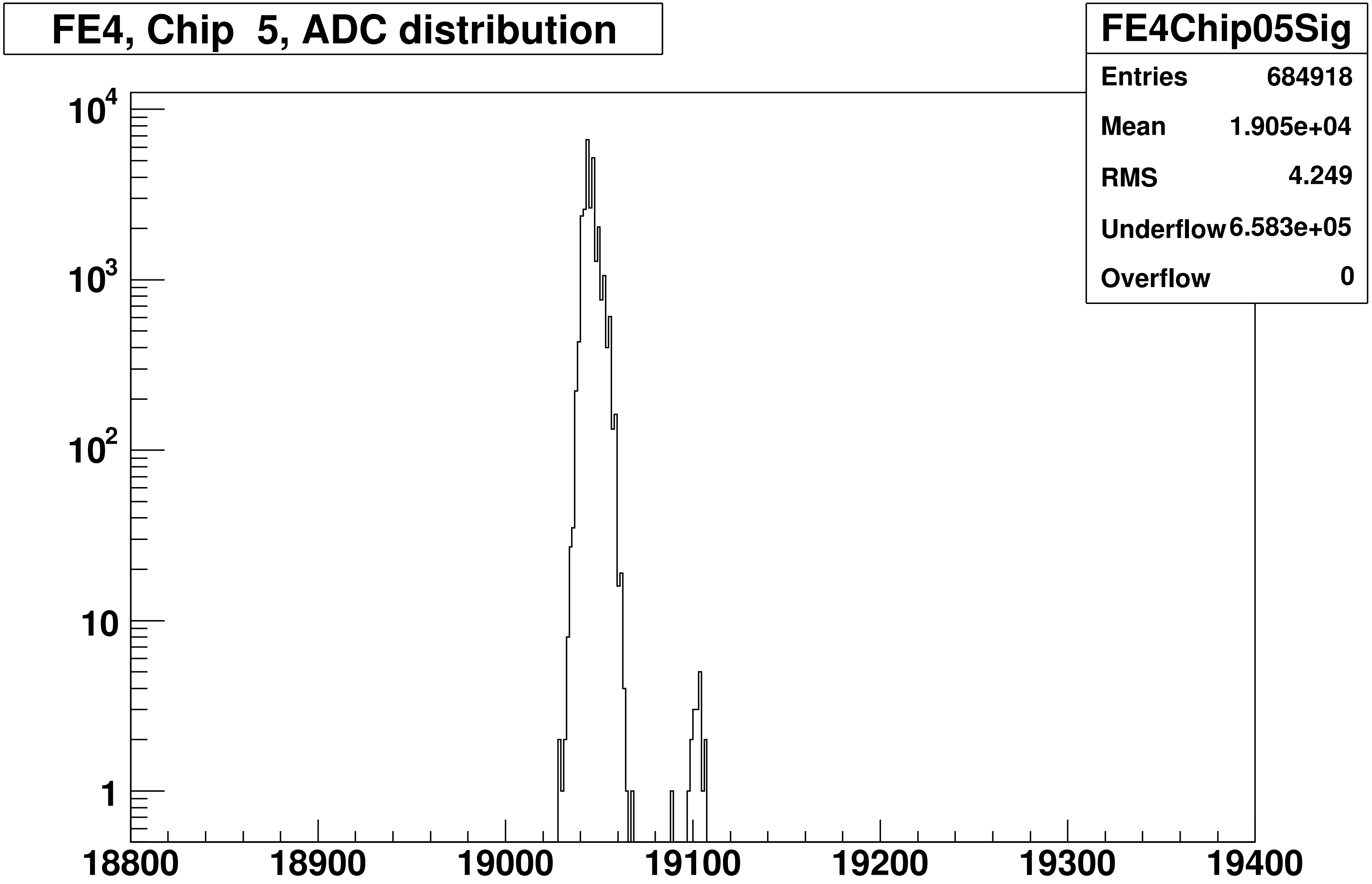

FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 684918
Mean 23.54
RMS 1.529
Underflow 0
Overflow 2.666e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 684918
Mean 1.918e+04
RMS 3.997
Underflow 6.583e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

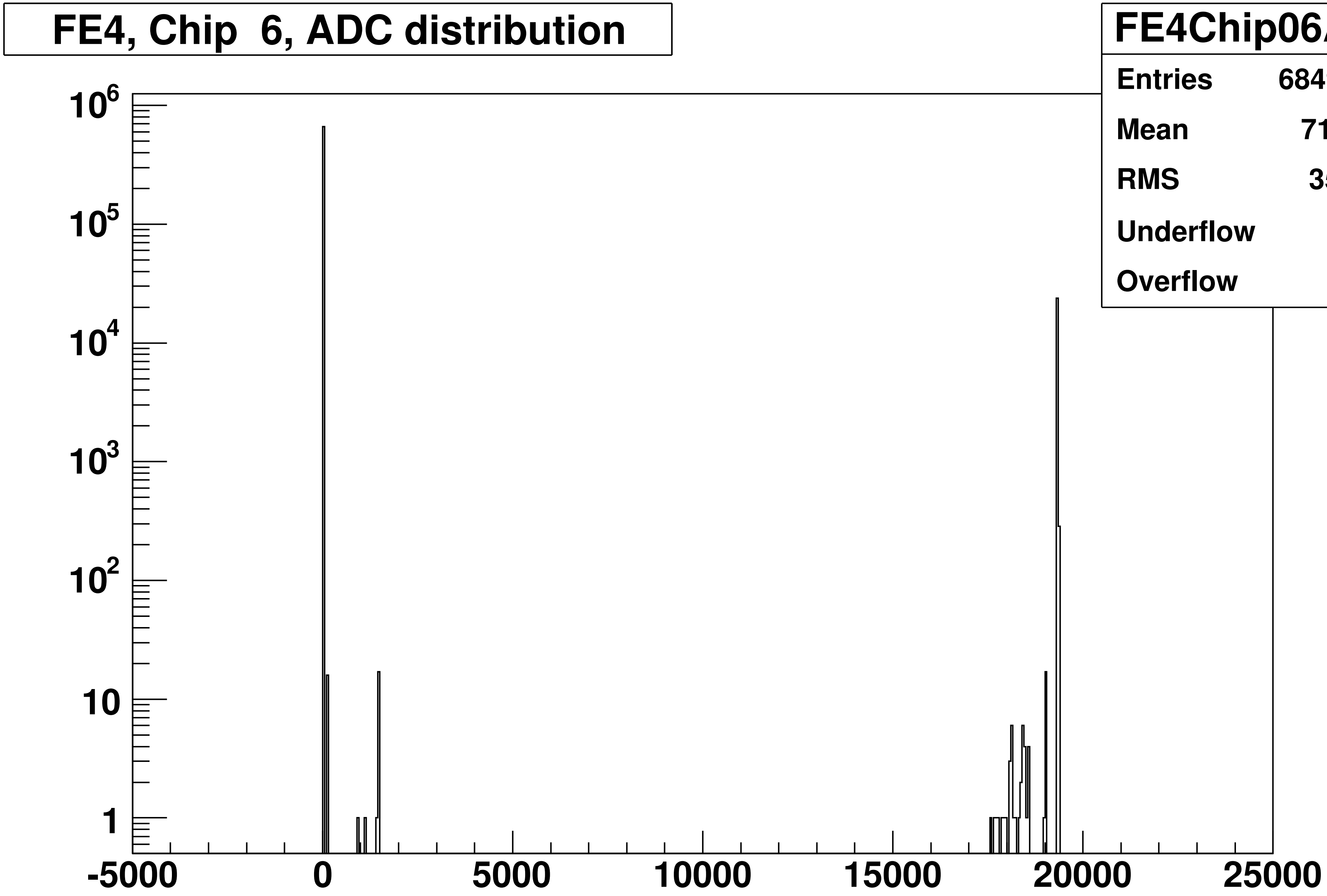

FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 684918
Mean 767.5
RMS 3678
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

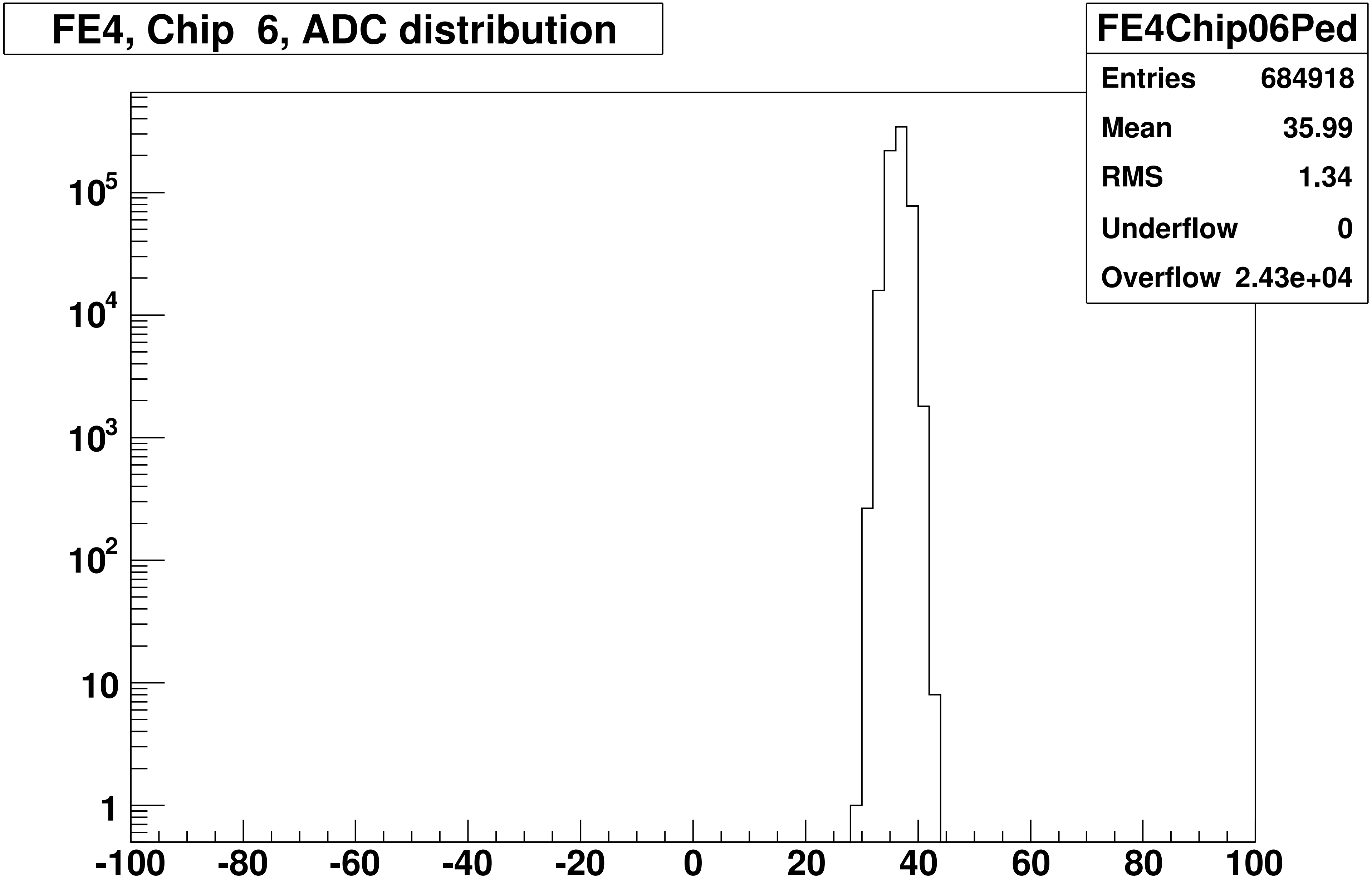


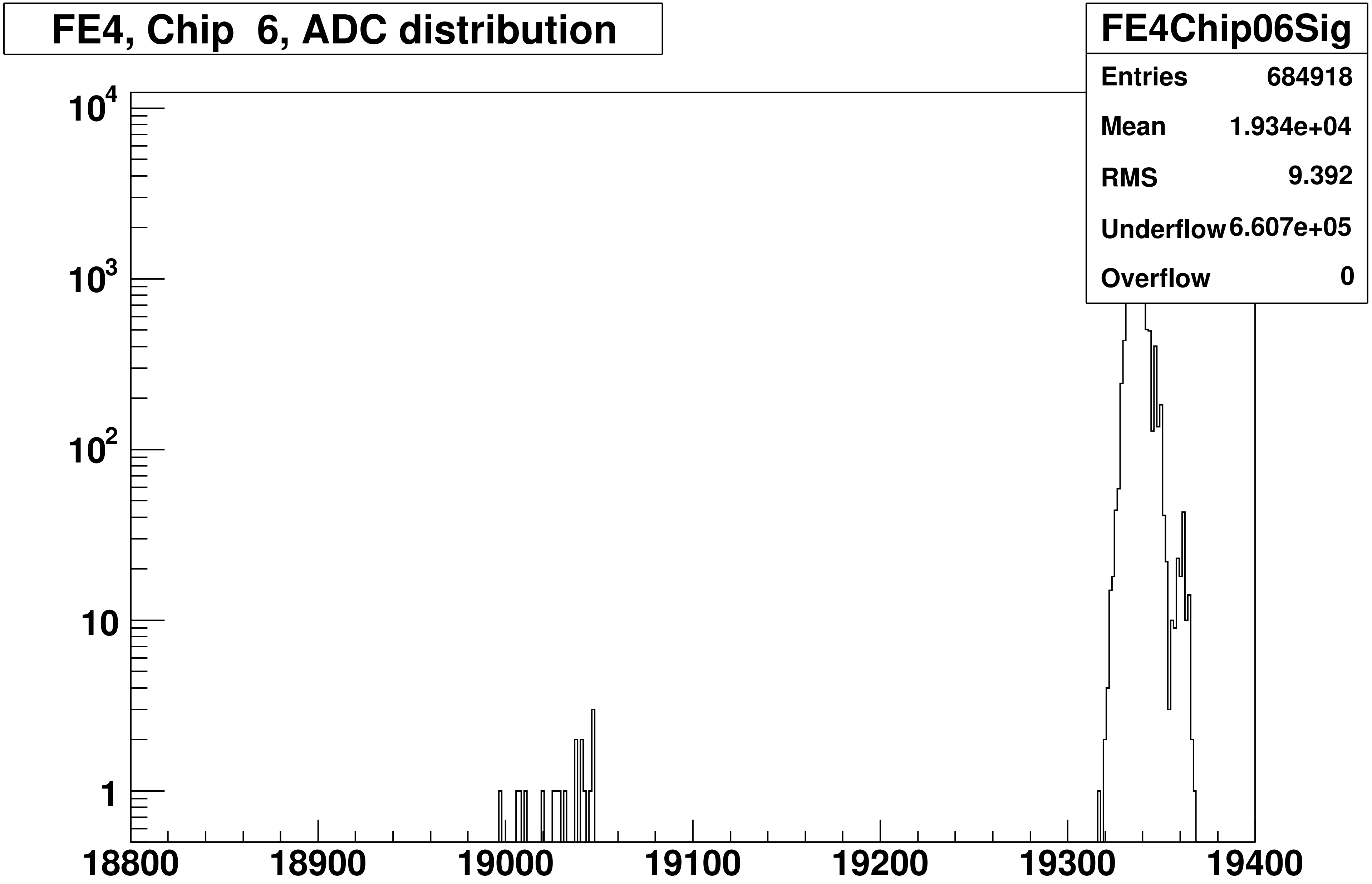
FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 684918
Mean 27.24
RMS 1.936
Underflow 0
Overflow 2.666e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

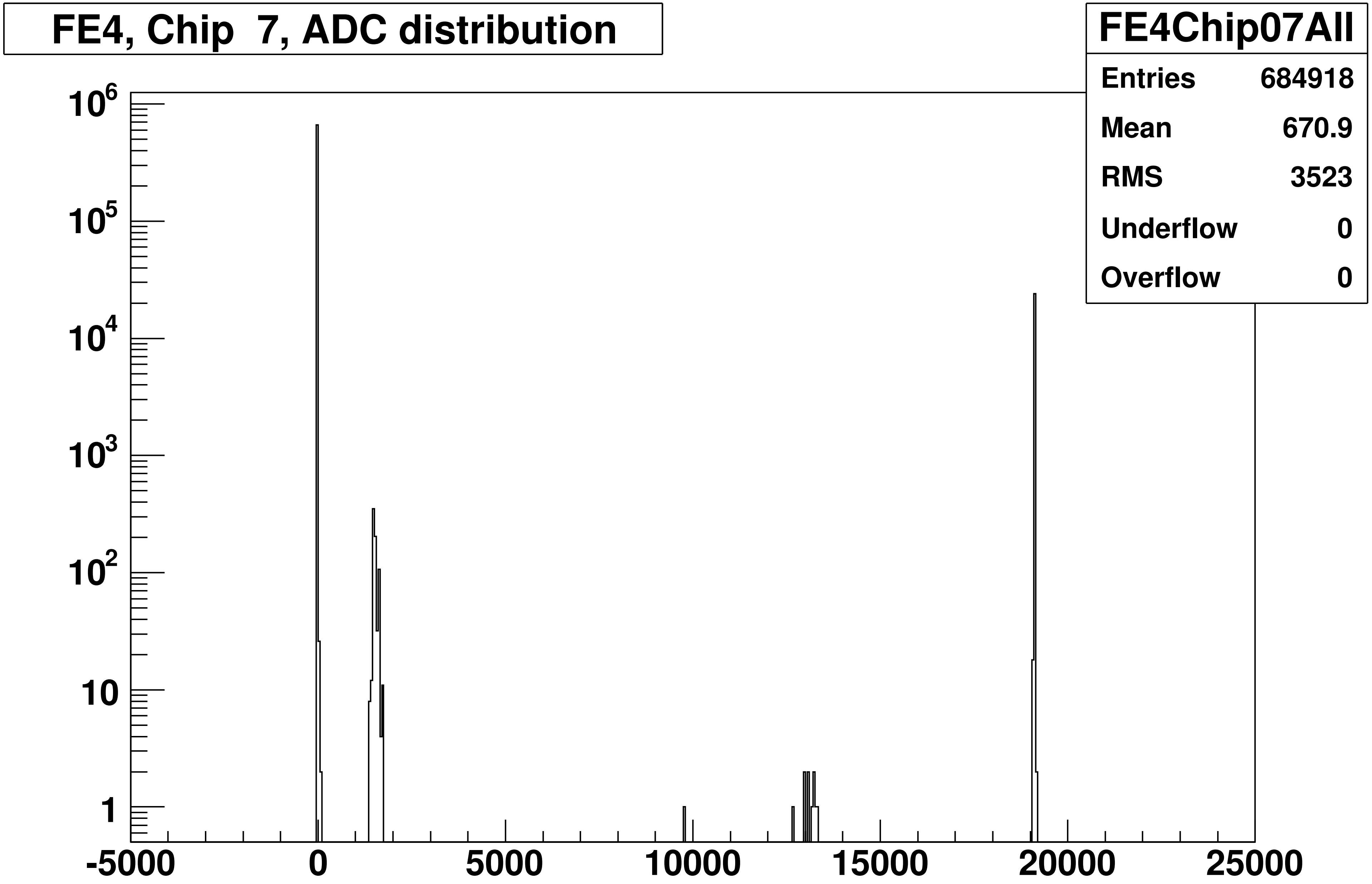


FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 684918
Mean 1.905e+04
RMS 4.249
Underflow 6.583e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

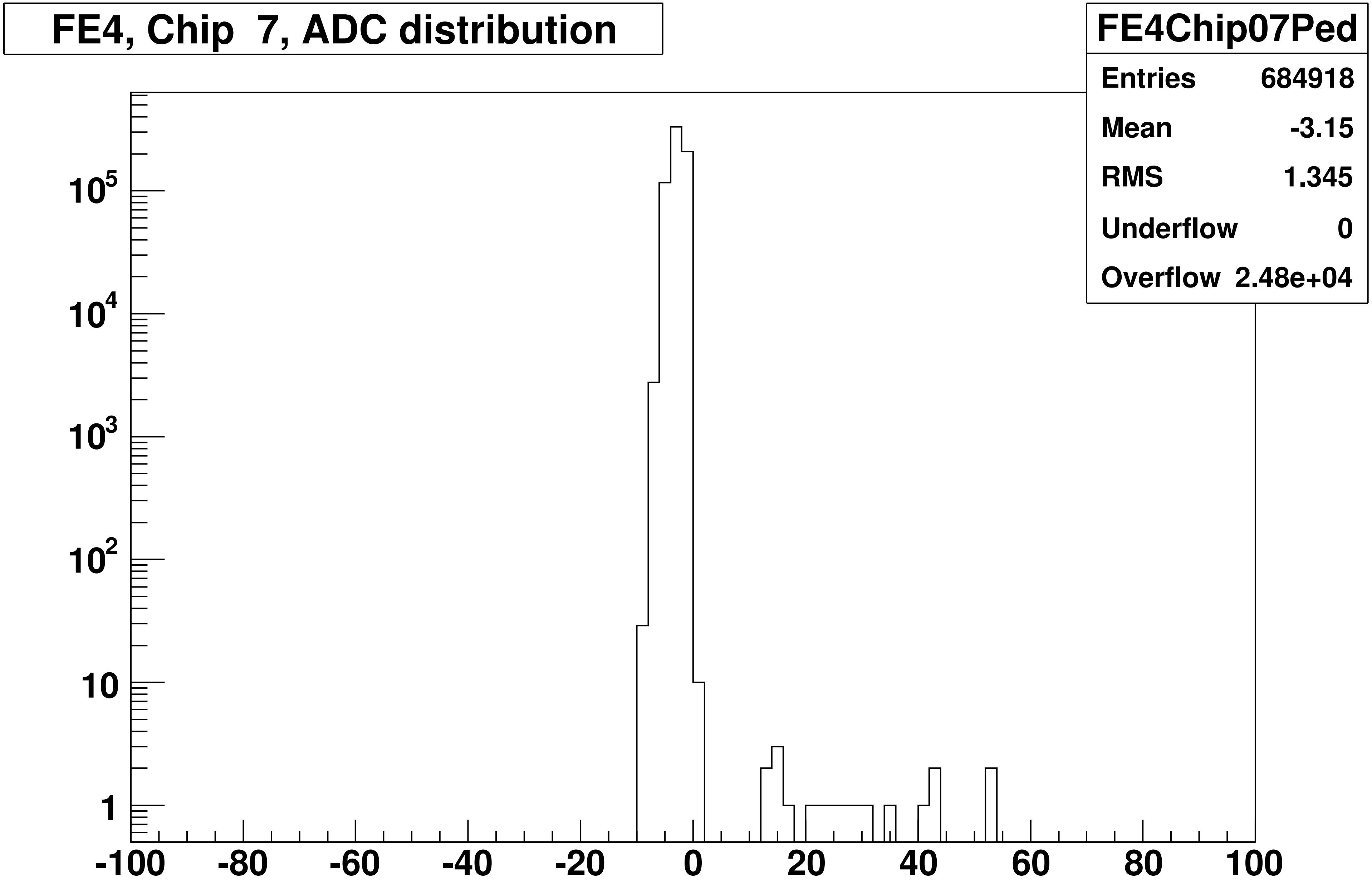

FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 684918
Mean 719.7
RMS 3567
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 684918
Mean 35.99
RMS 1.34
Underflow 0
Overflow 2.43e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

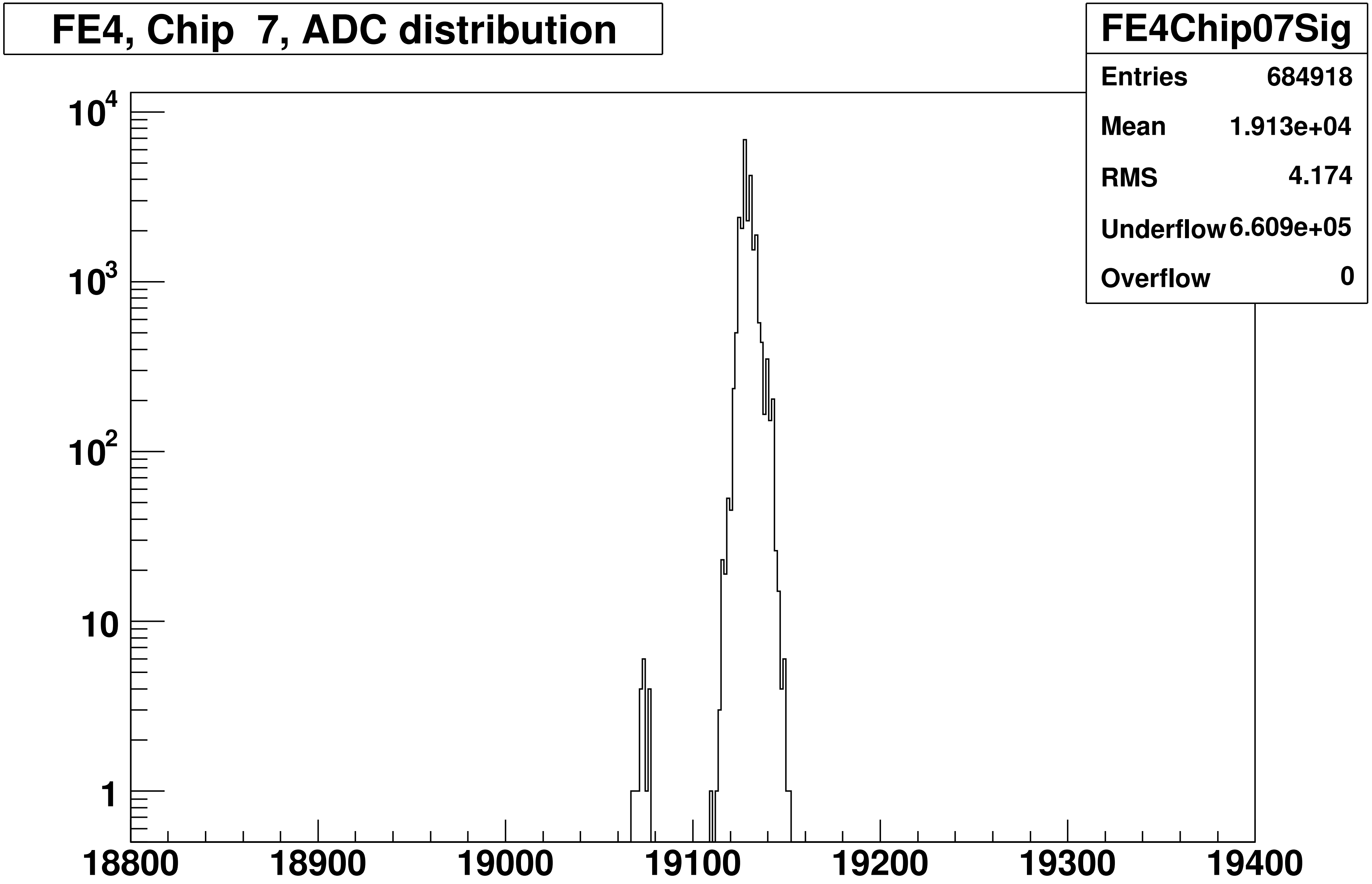

FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 684918
Mean 1.934e+04
RMS 9.392
Underflow 6.607e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

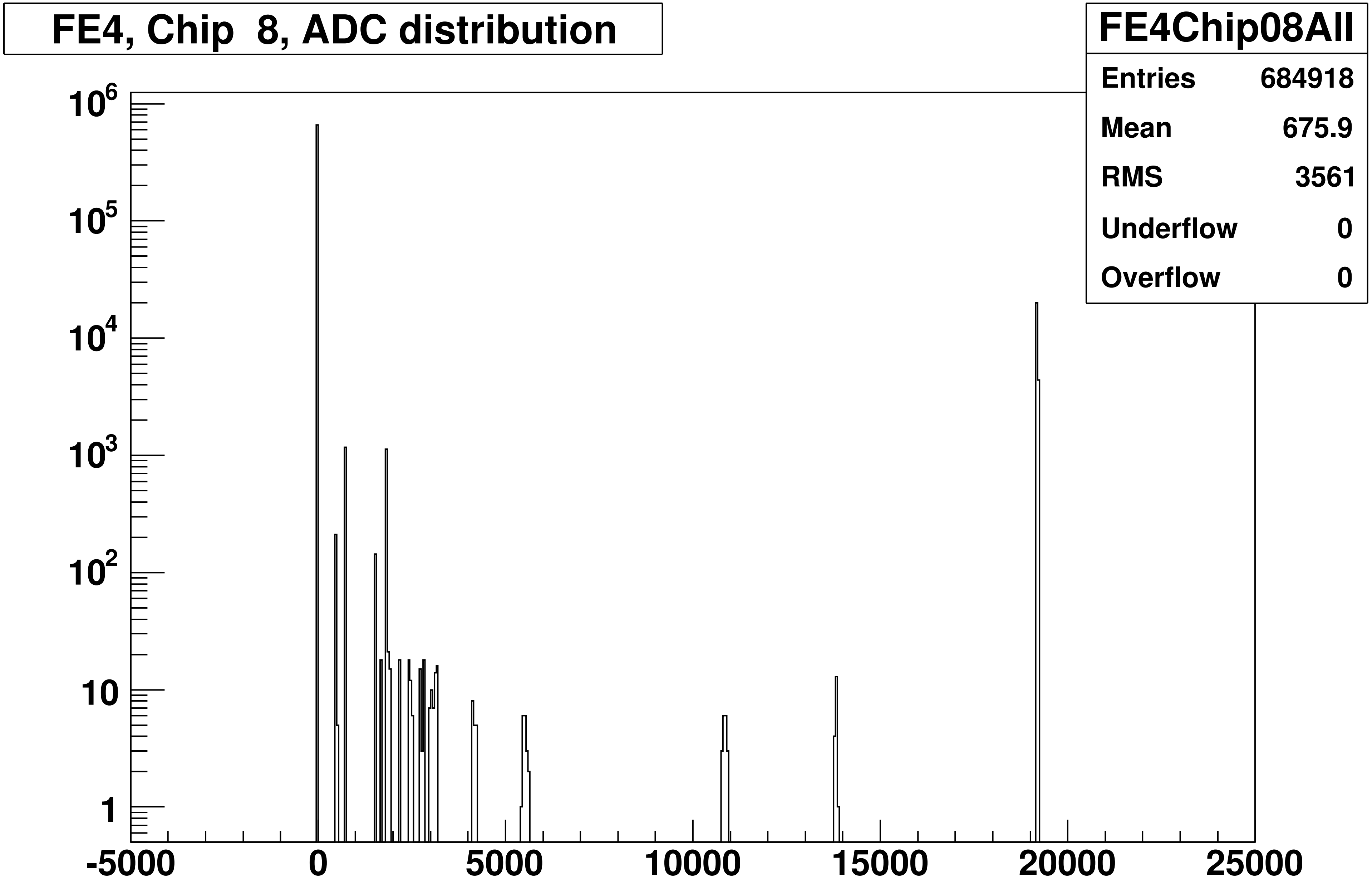


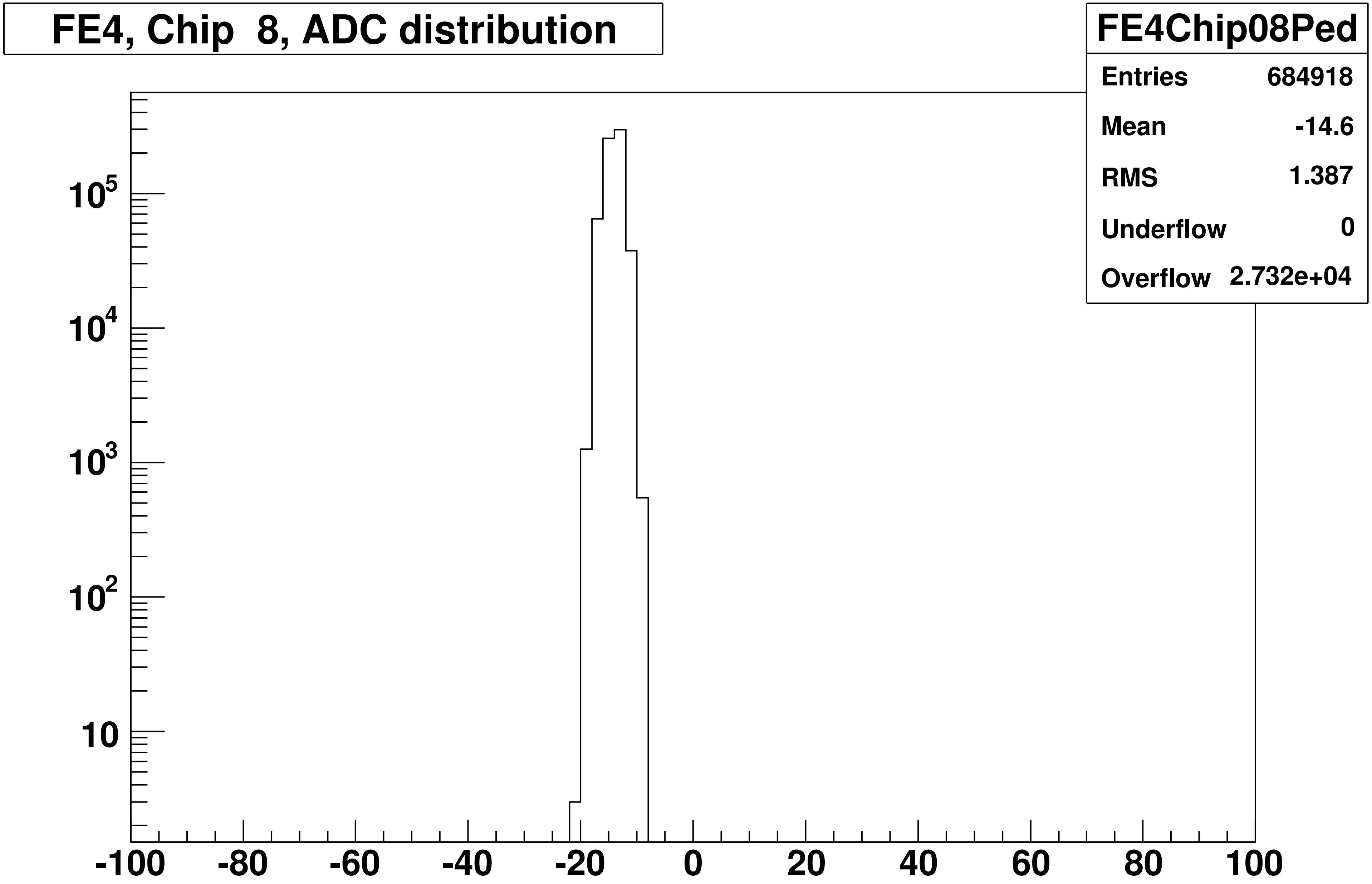
FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 684918
Mean 670.9
RMS 3523
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

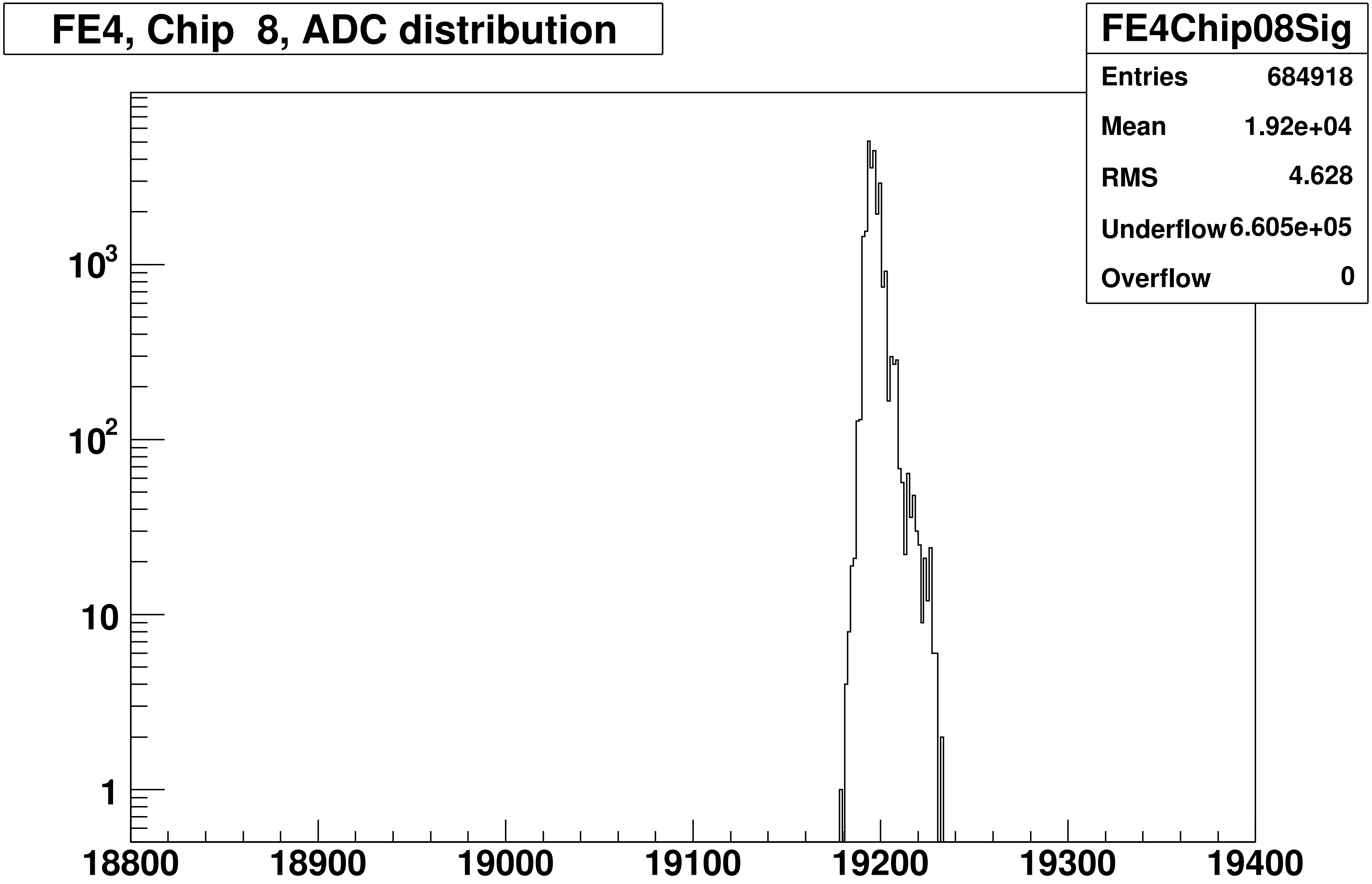


FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 684918
Mean -3.15
RMS 1.345
Underflow 0
Overflow 2.48e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

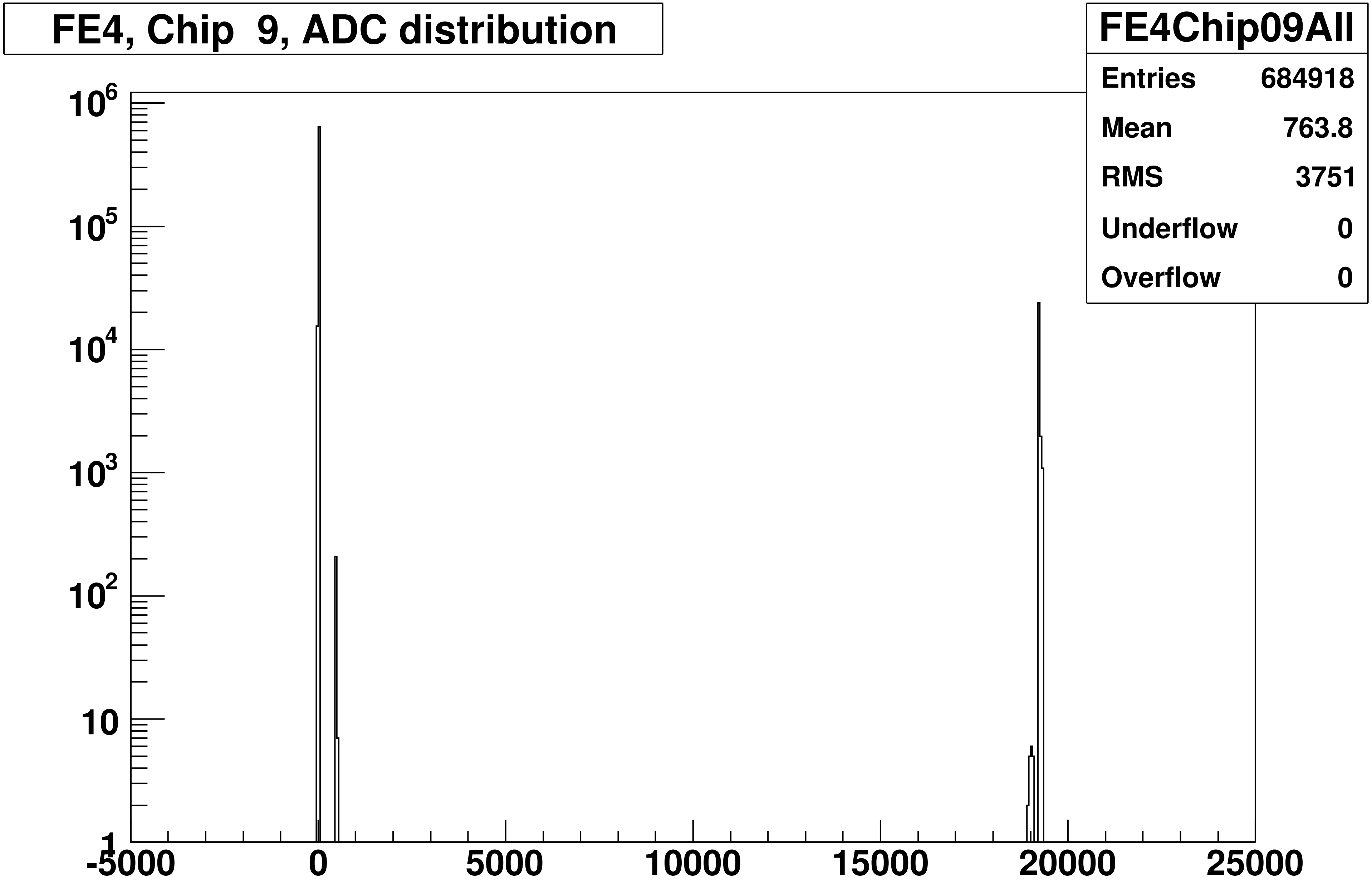

FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 684918
Mean 1.913e+04
RMS 4.174
Underflow 6.609e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 684918
Mean 675.9
RMS 3561
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

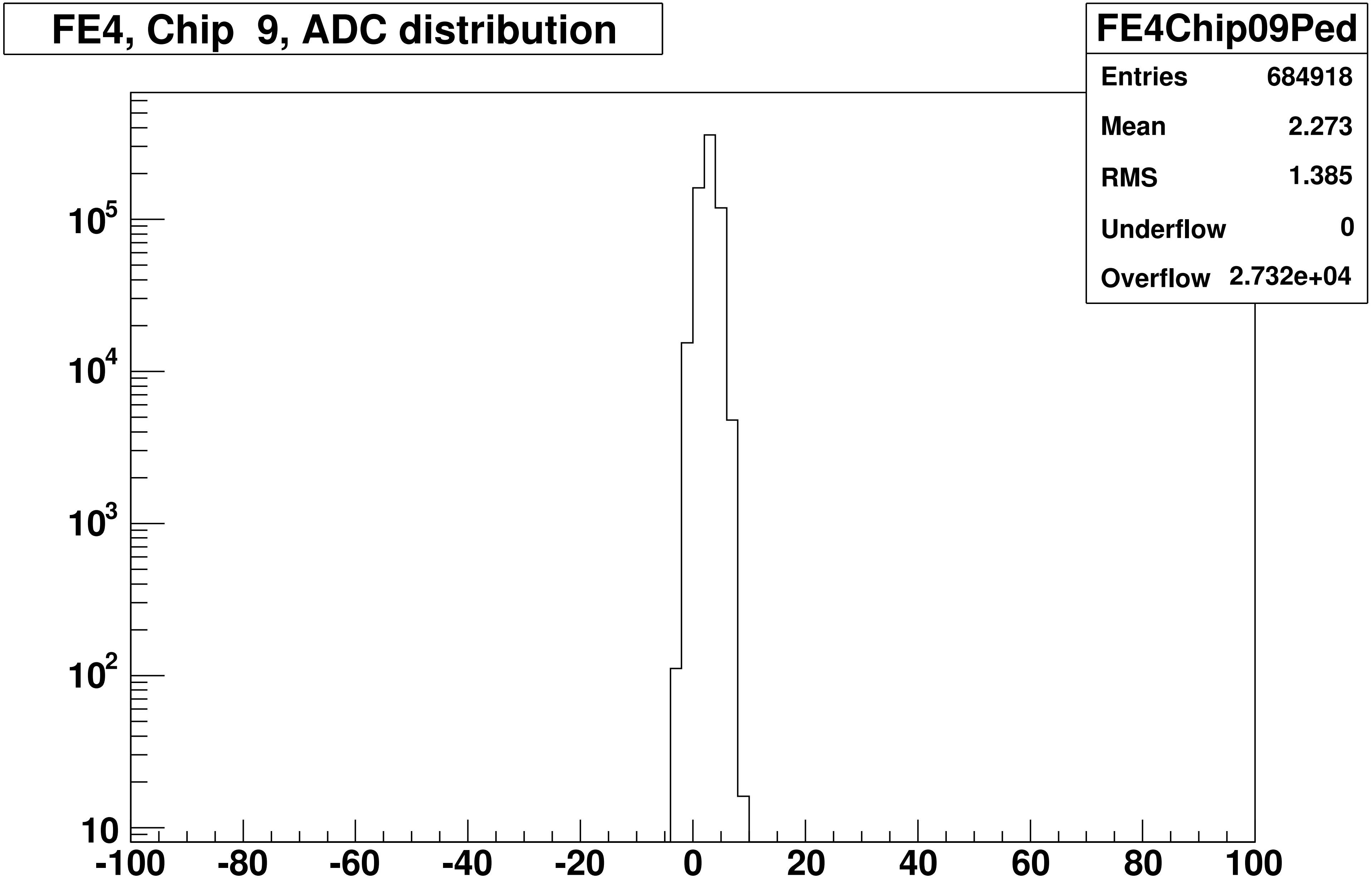

FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 684918
Mean -14.6
RMS 1.387
Underflow 0
Overflow 2.732e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

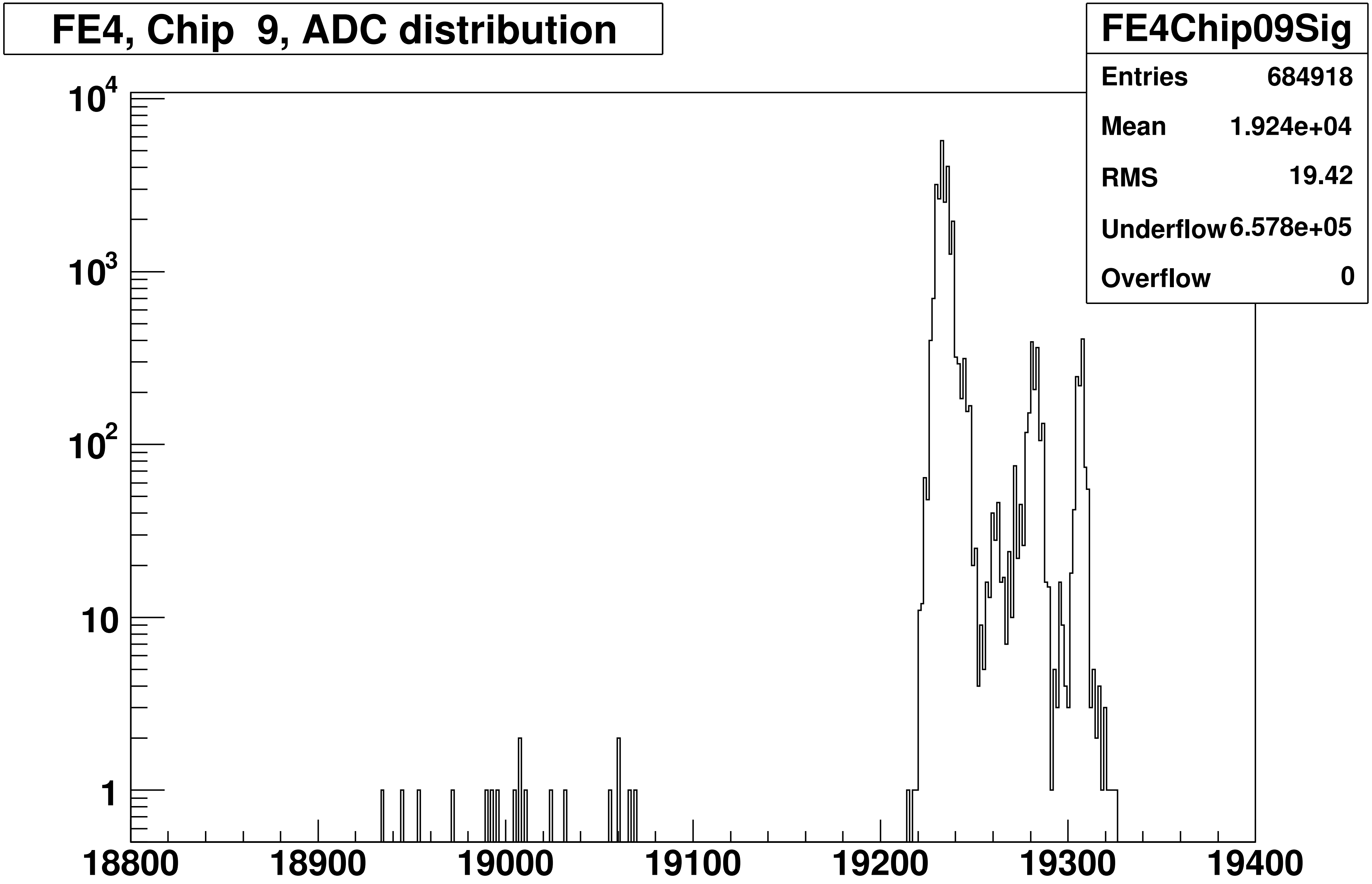


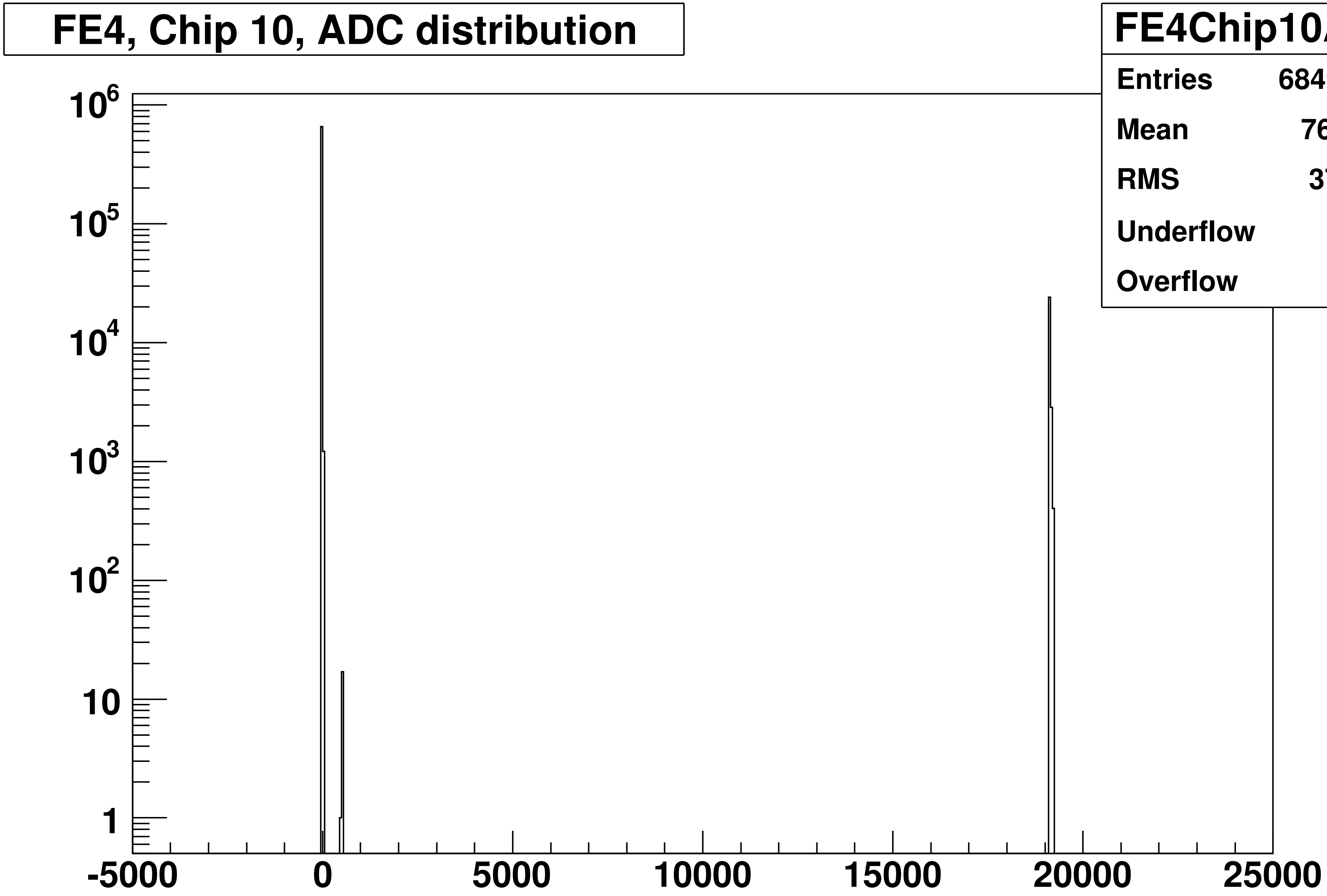
FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 684918
Mean 1.92e+04
RMS 4.628
Underflow 6.605e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



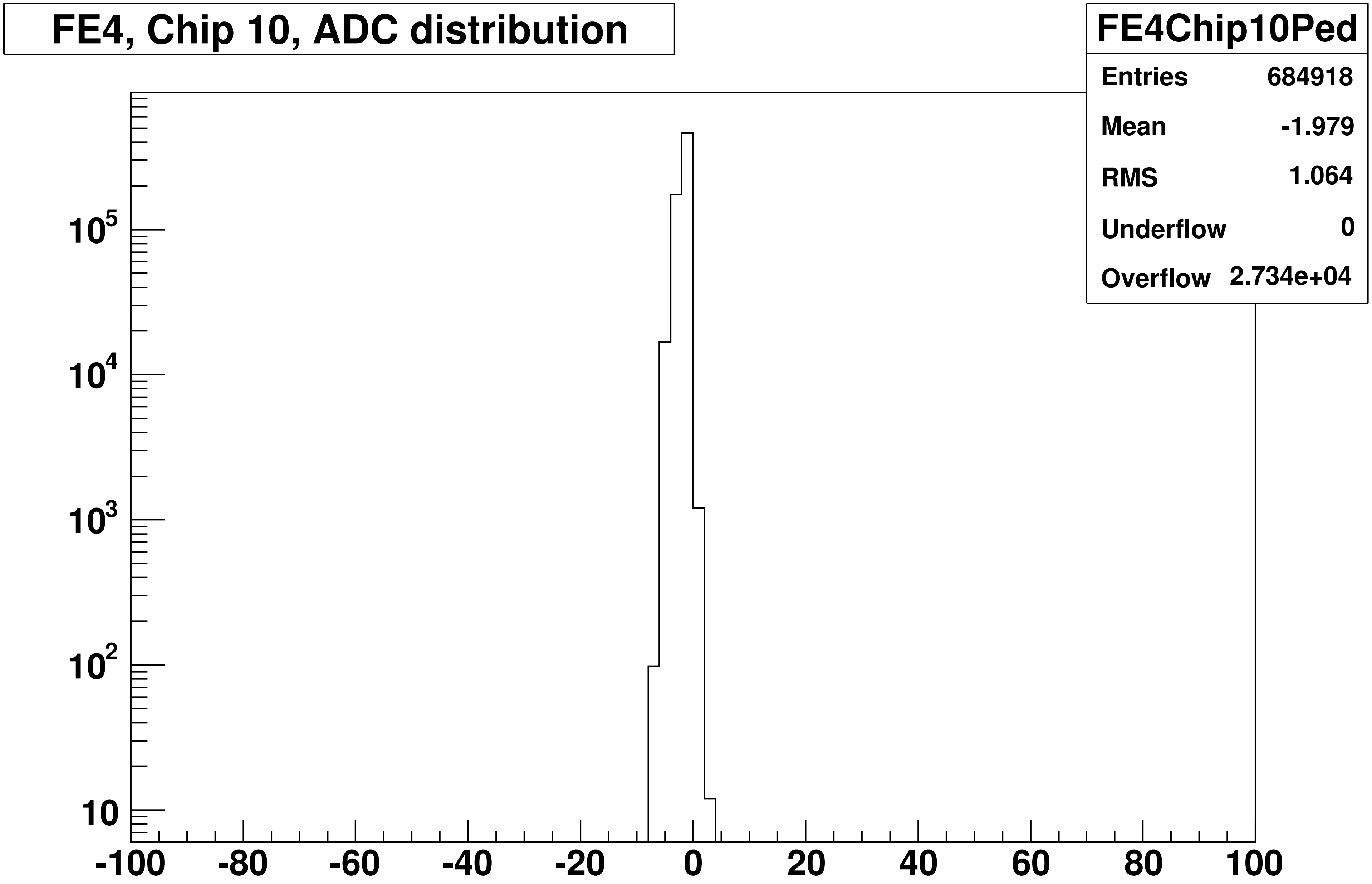
FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 684918
Mean 763.8
RMS 3751
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

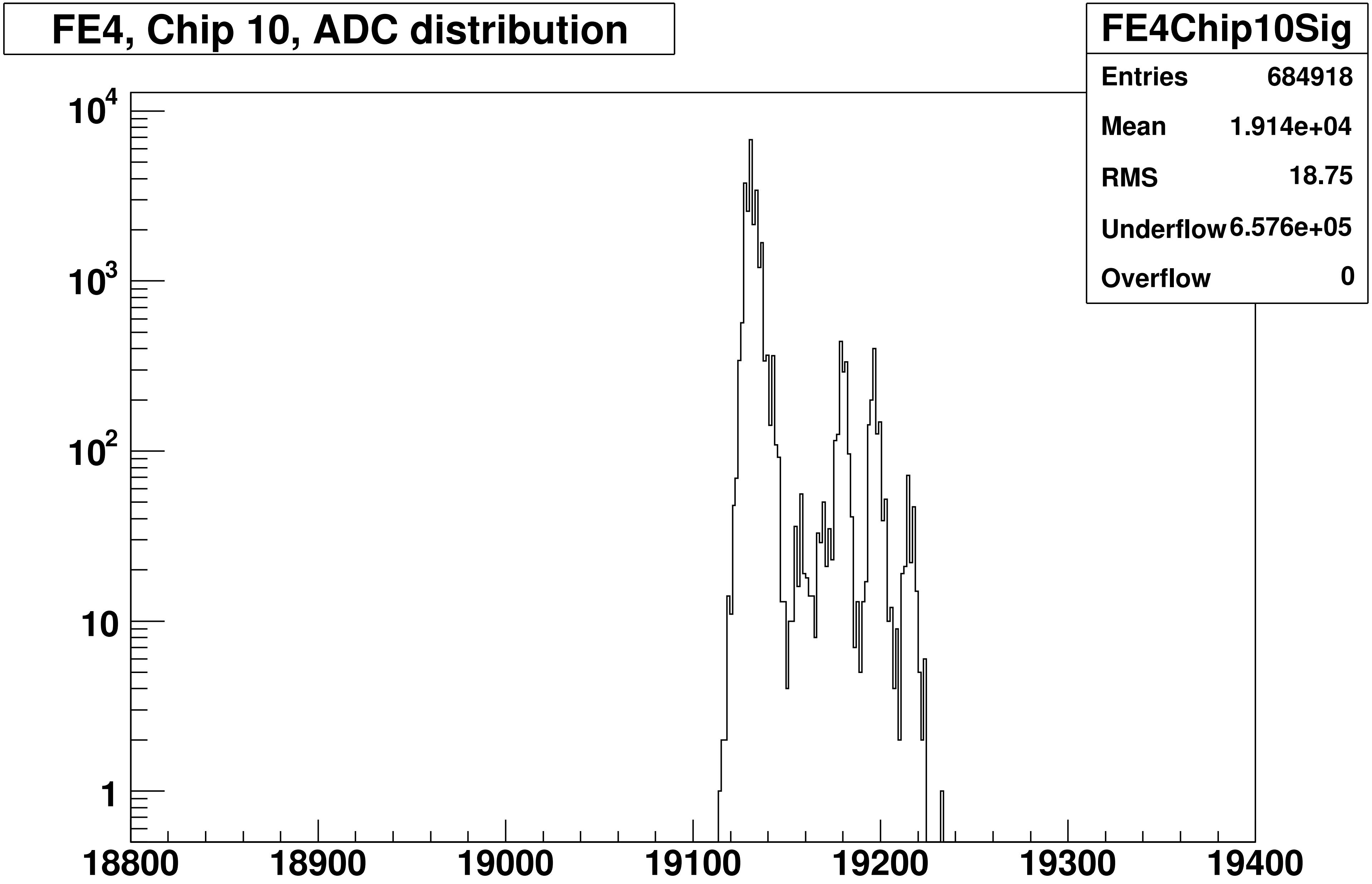

FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 684918
Mean 2.273
RMS 1.385
Underflow 0
Overflow 2.732e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

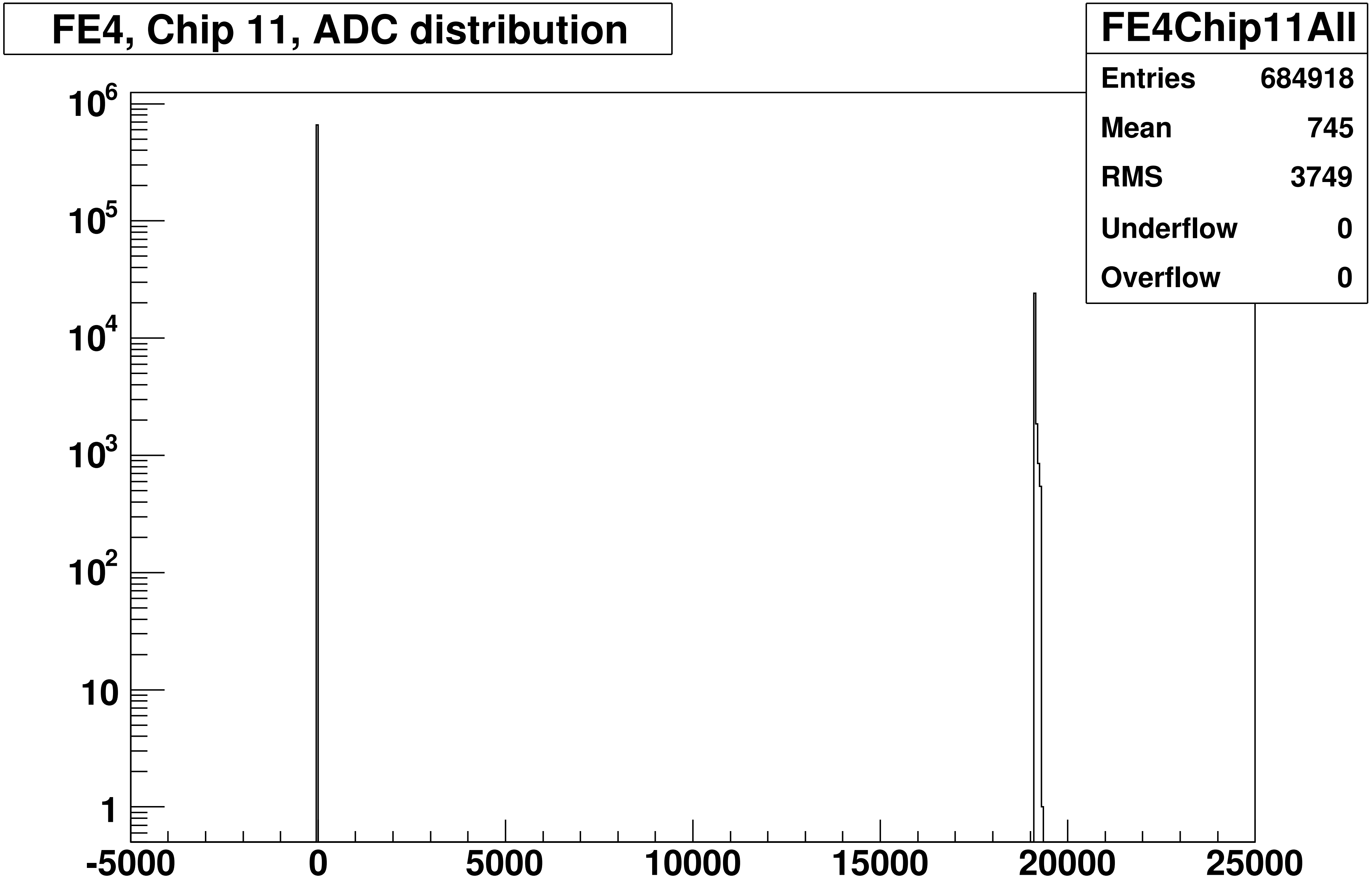

FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 684918
Mean 1.924e+04
RMS 19.42
Underflow 6.578e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

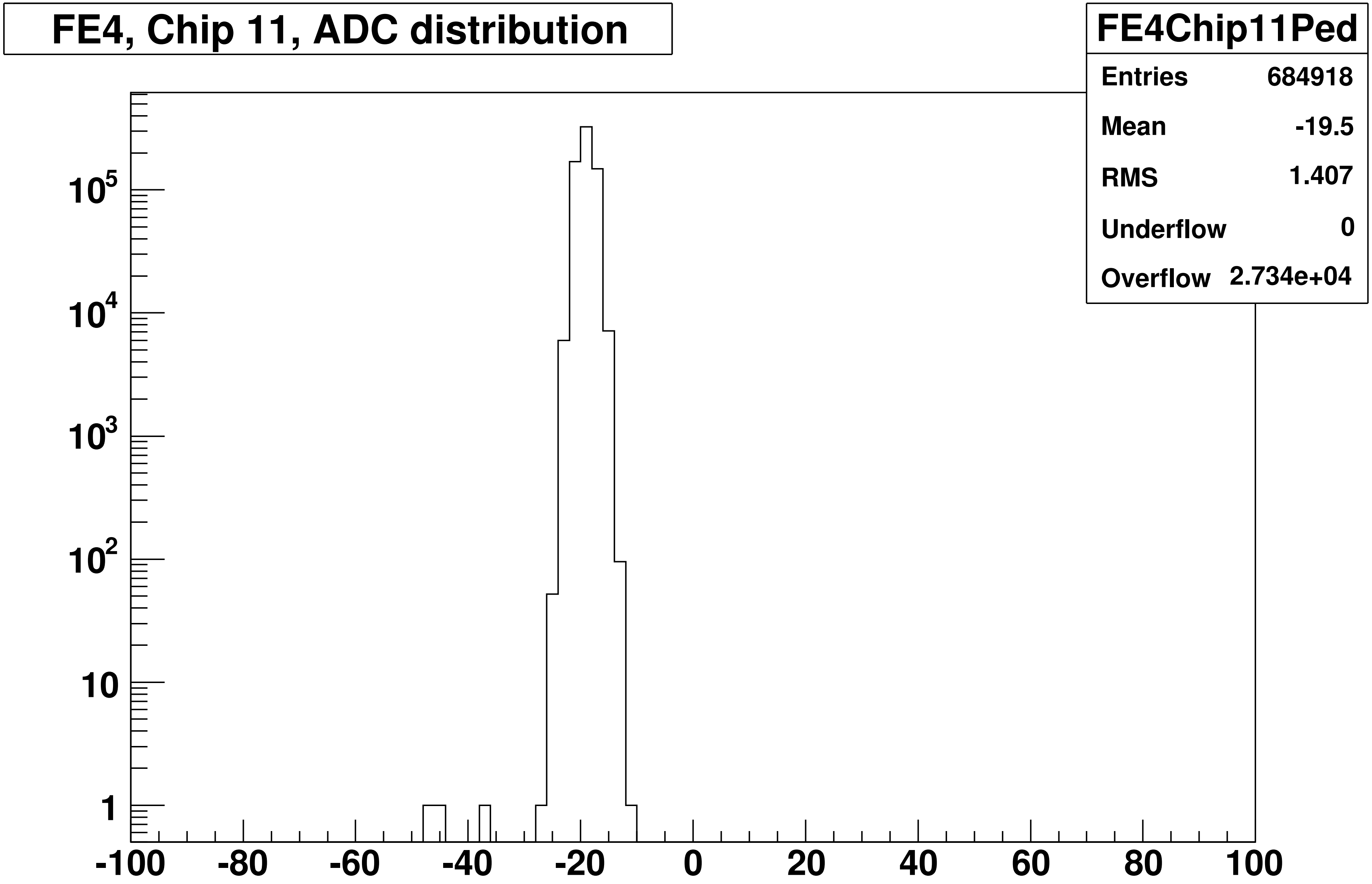


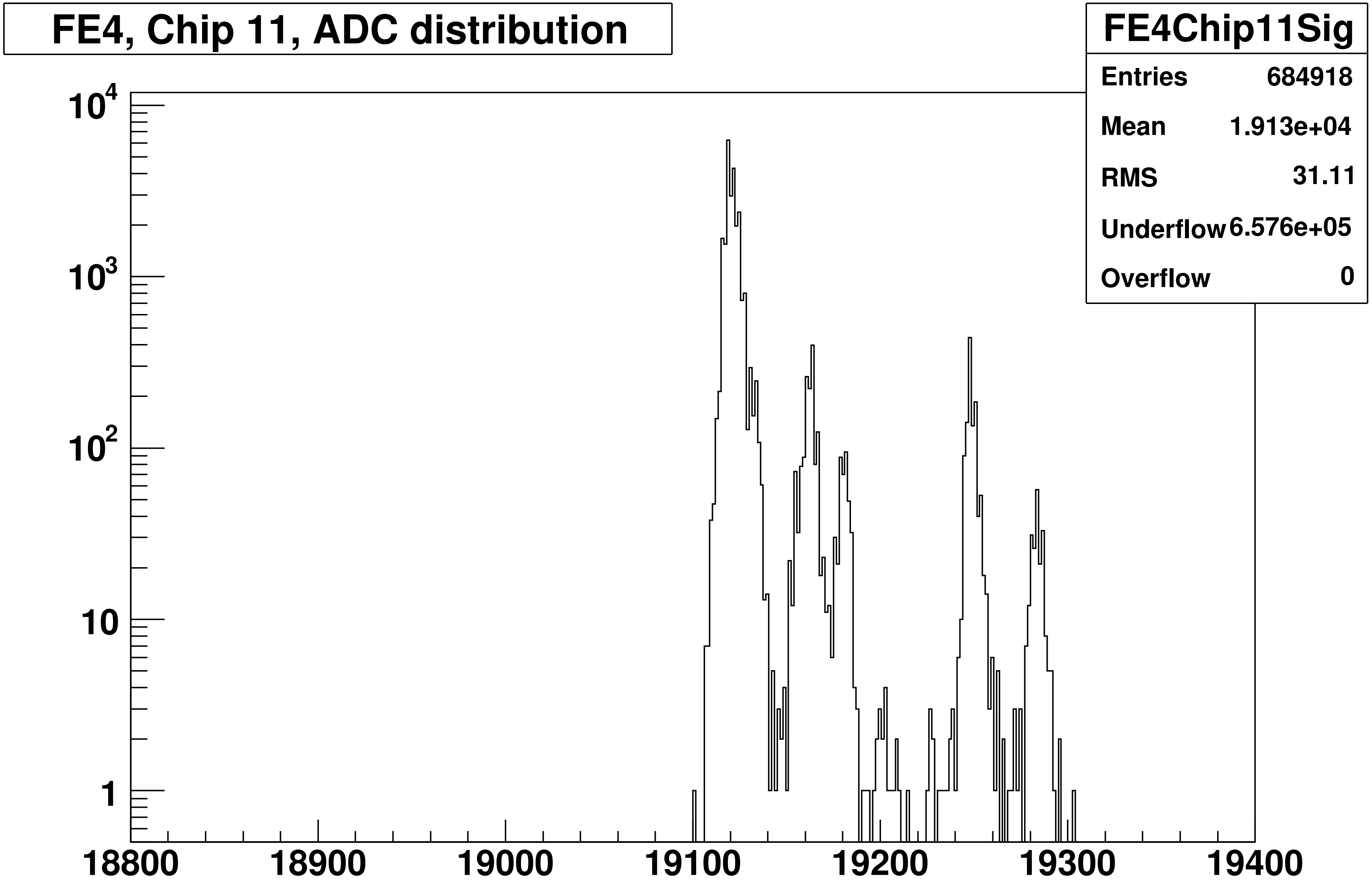
FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 684918
Mean 761.6
RMS 3746
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

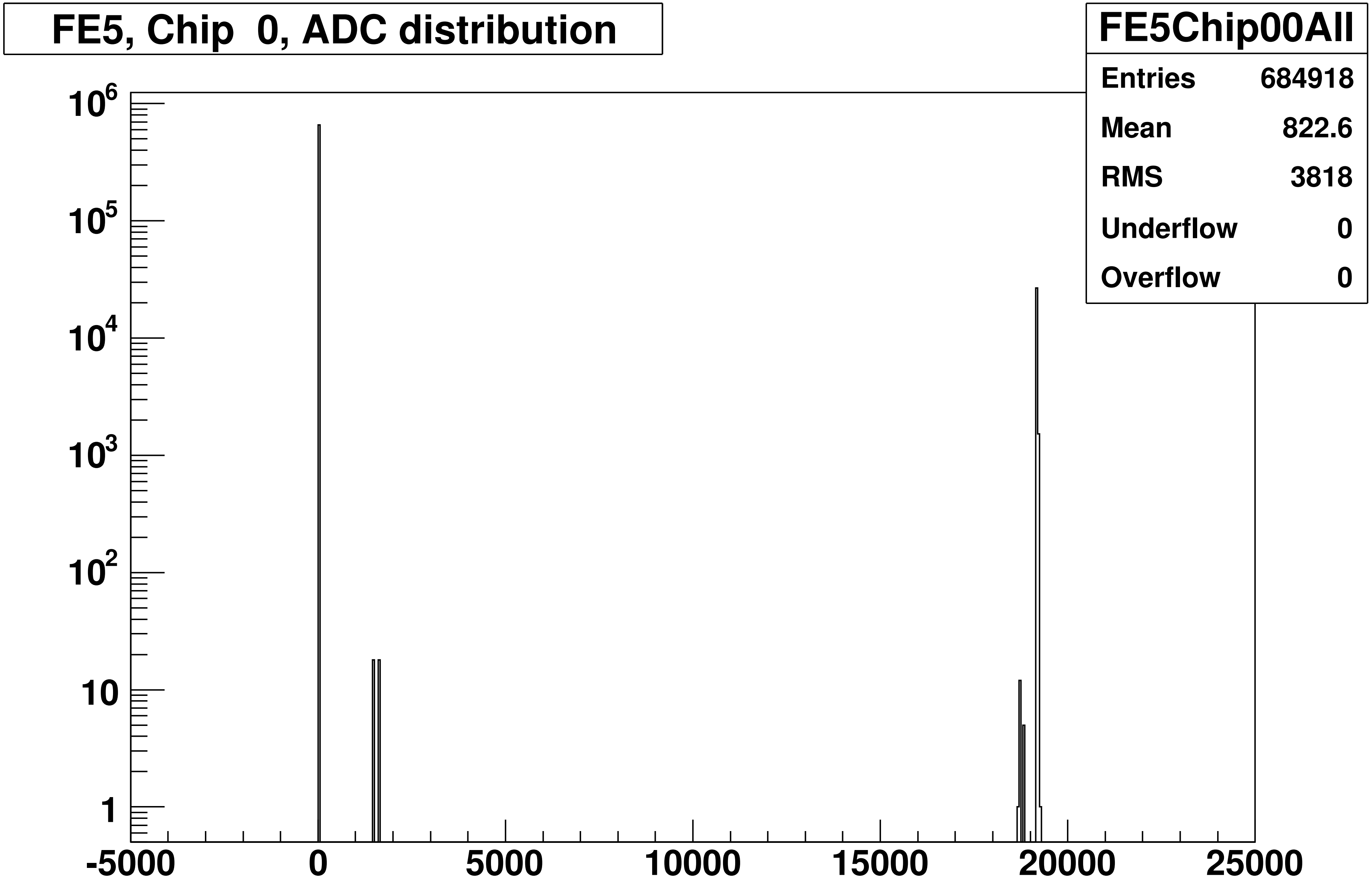

FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 684918
Mean -1.979
RMS 1.064
Underflow 0
Overflow 2.734e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

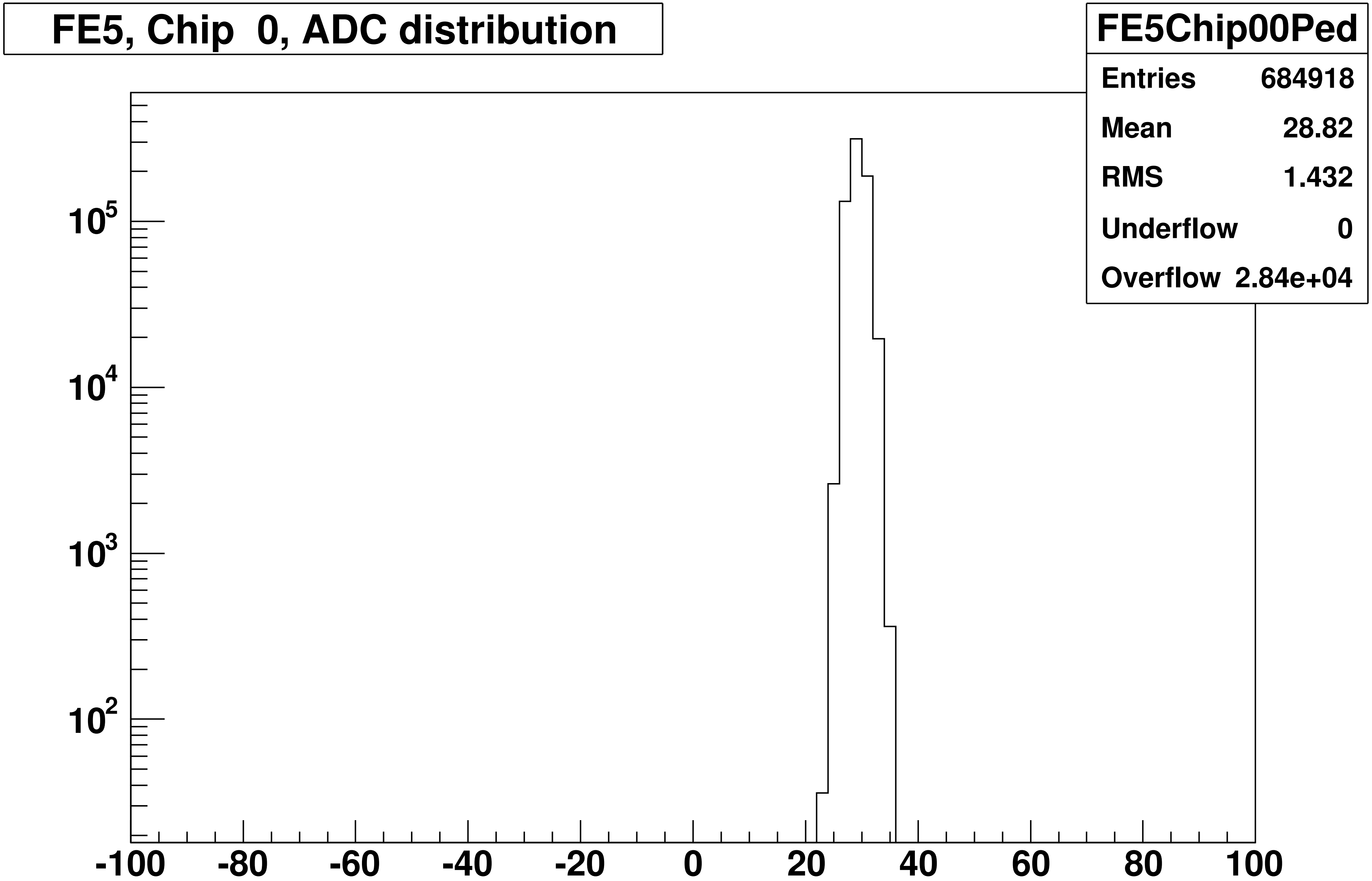

FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 684918
Mean 1.914e+04
RMS 18.75
Underflow 6.576e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 684918
Mean 745
RMS 3749
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

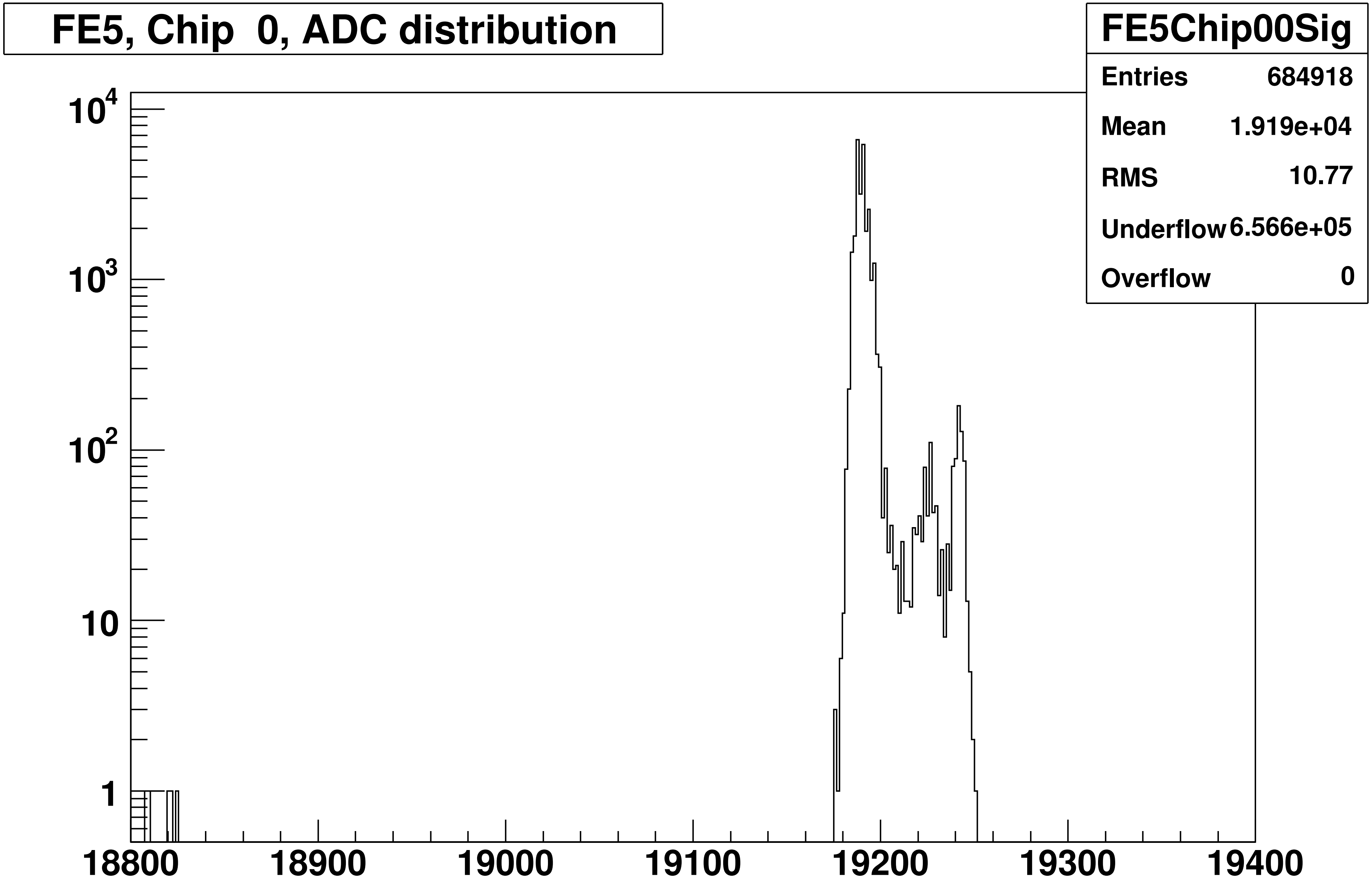

FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 684918
Mean -19.5
RMS 1.407
Underflow 0
Overflow 2.734e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

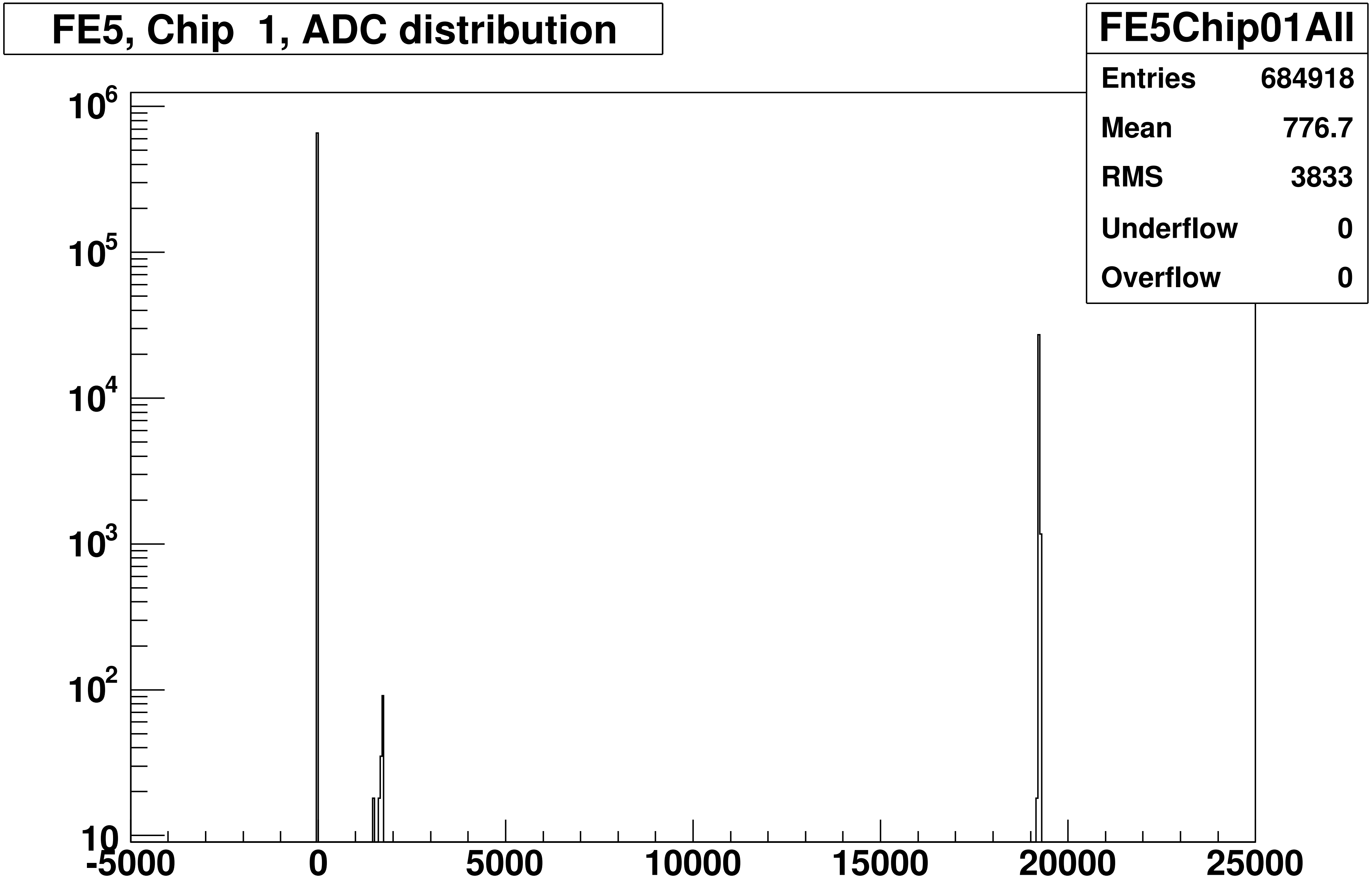

FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 684918
Mean 1.913e+04
RMS 31.11
Underflow 6.576e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

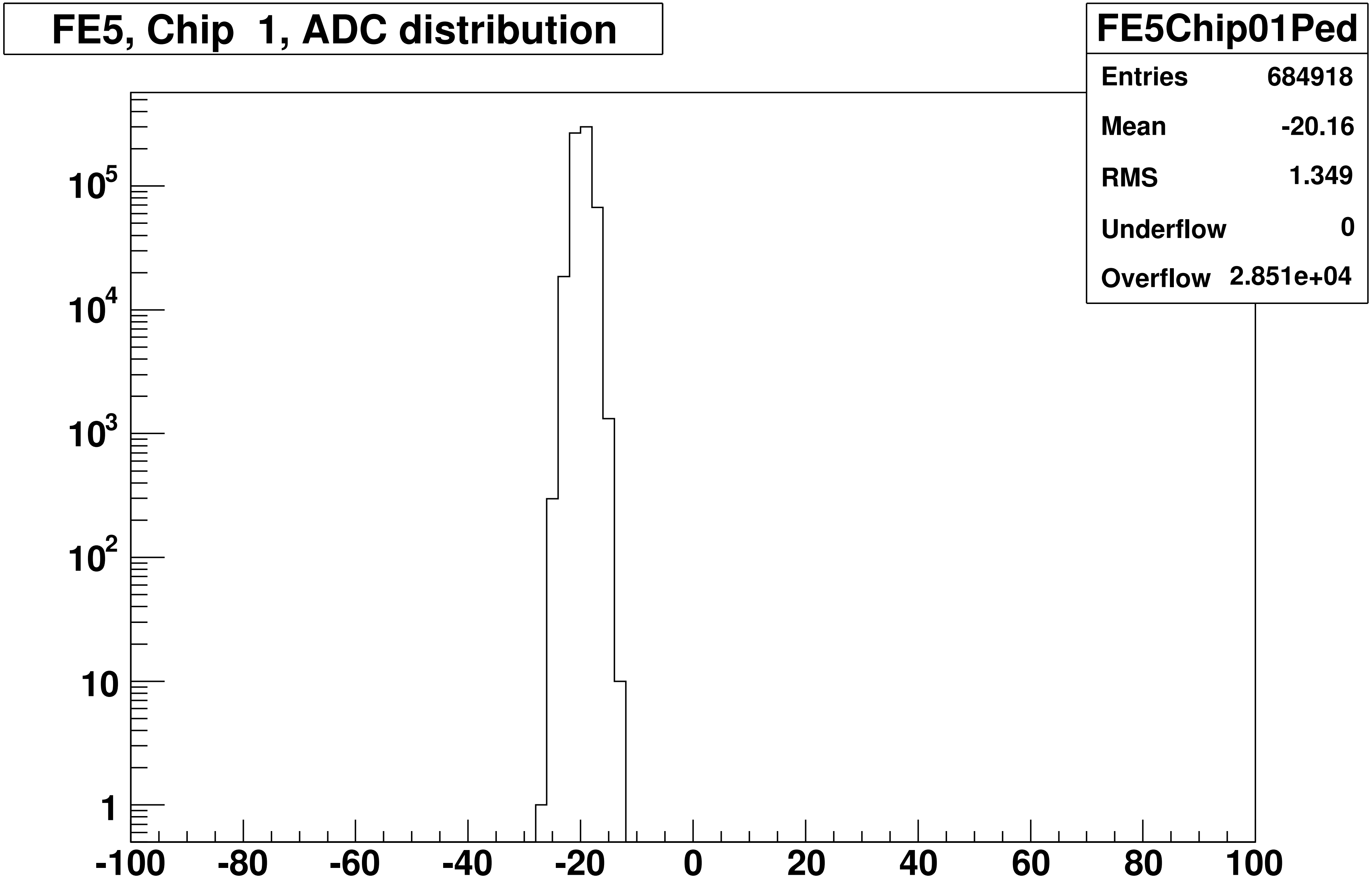

FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 684918
Mean 822.6
RMS 3818
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

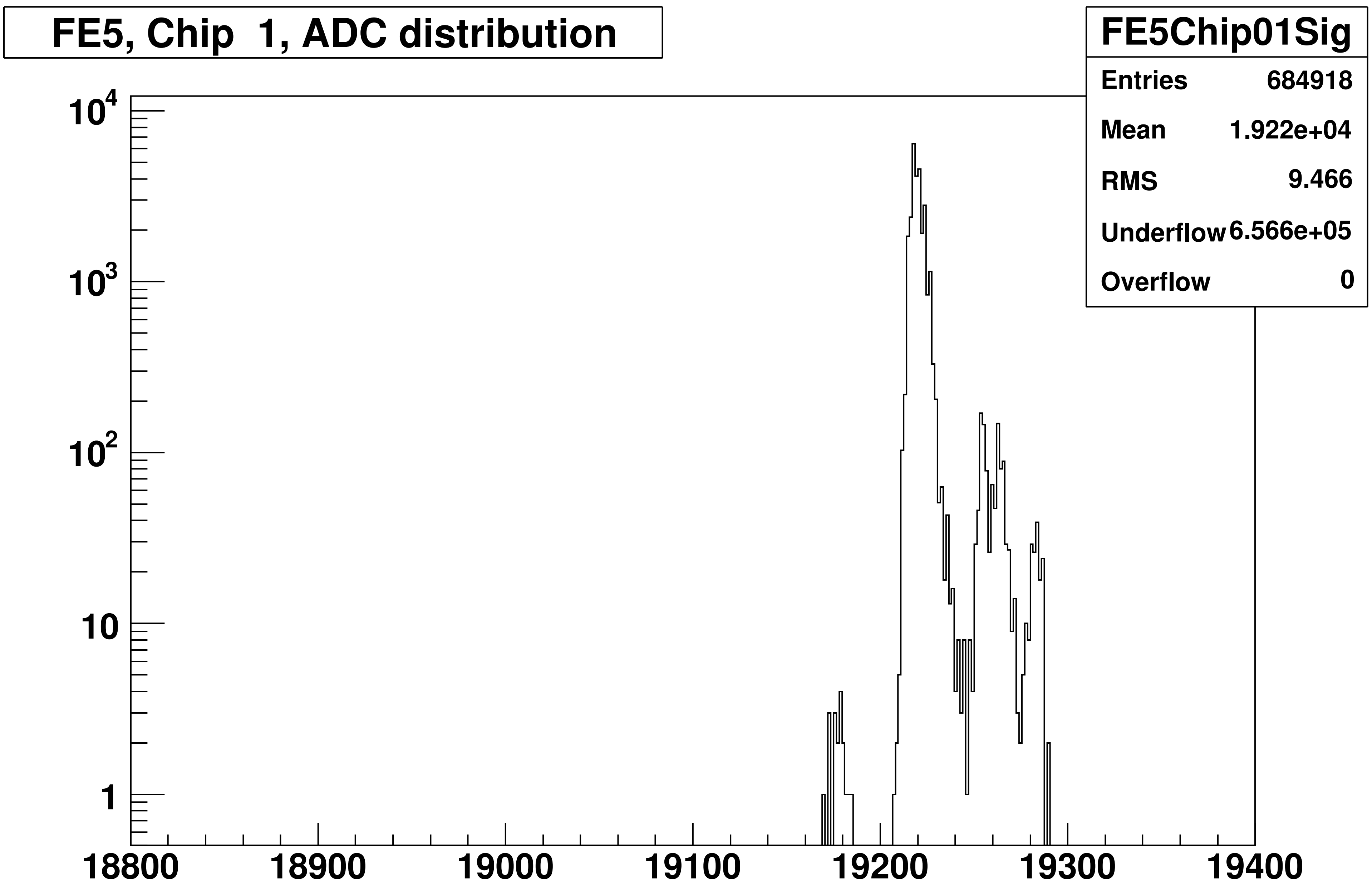


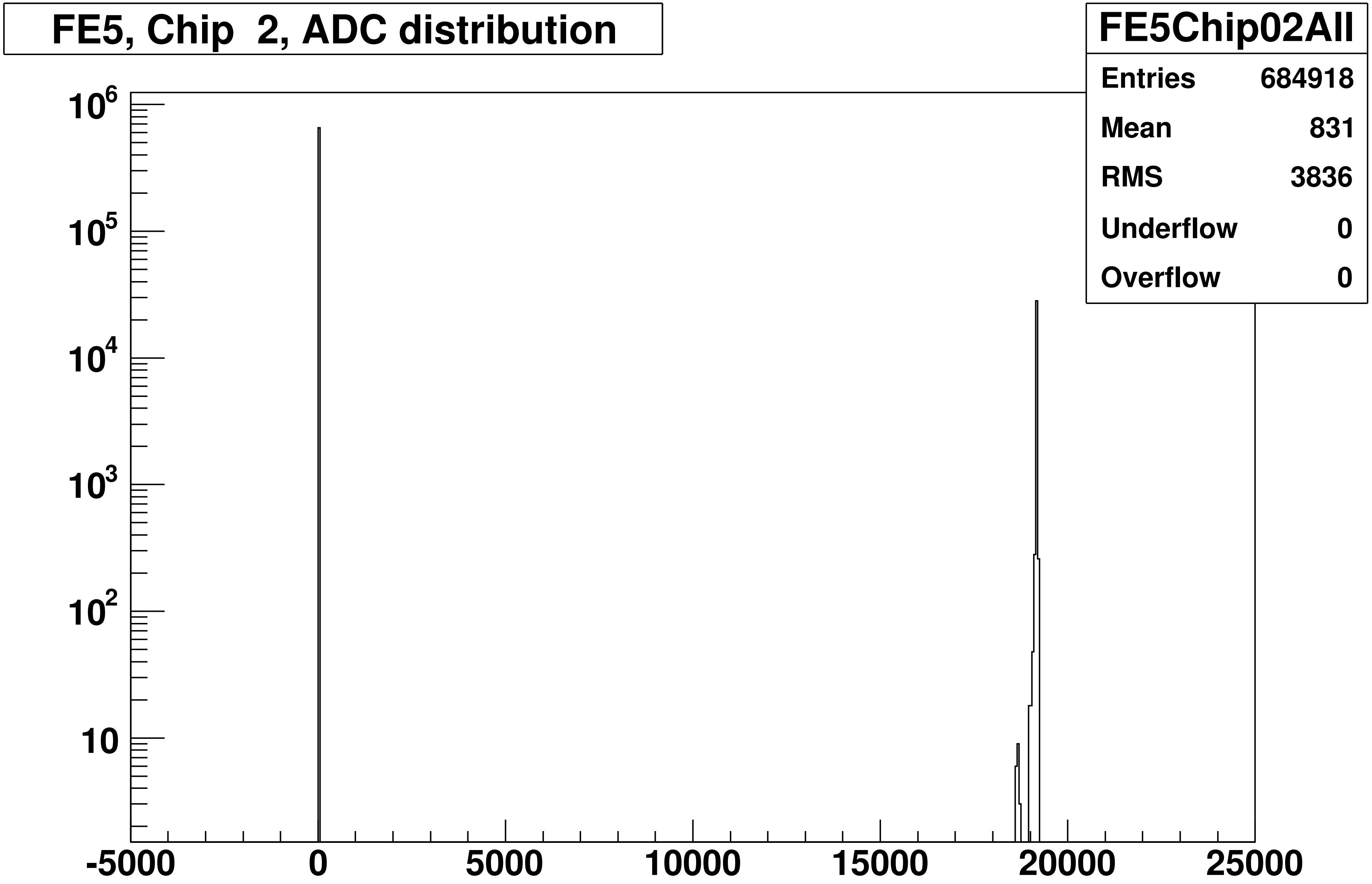
FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 684918
Mean 28.82
RMS 1.432
Underflow 0
Overflow 2.84e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 684918
Mean 1.919e+04
RMS 10.77
Underflow 6.566e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

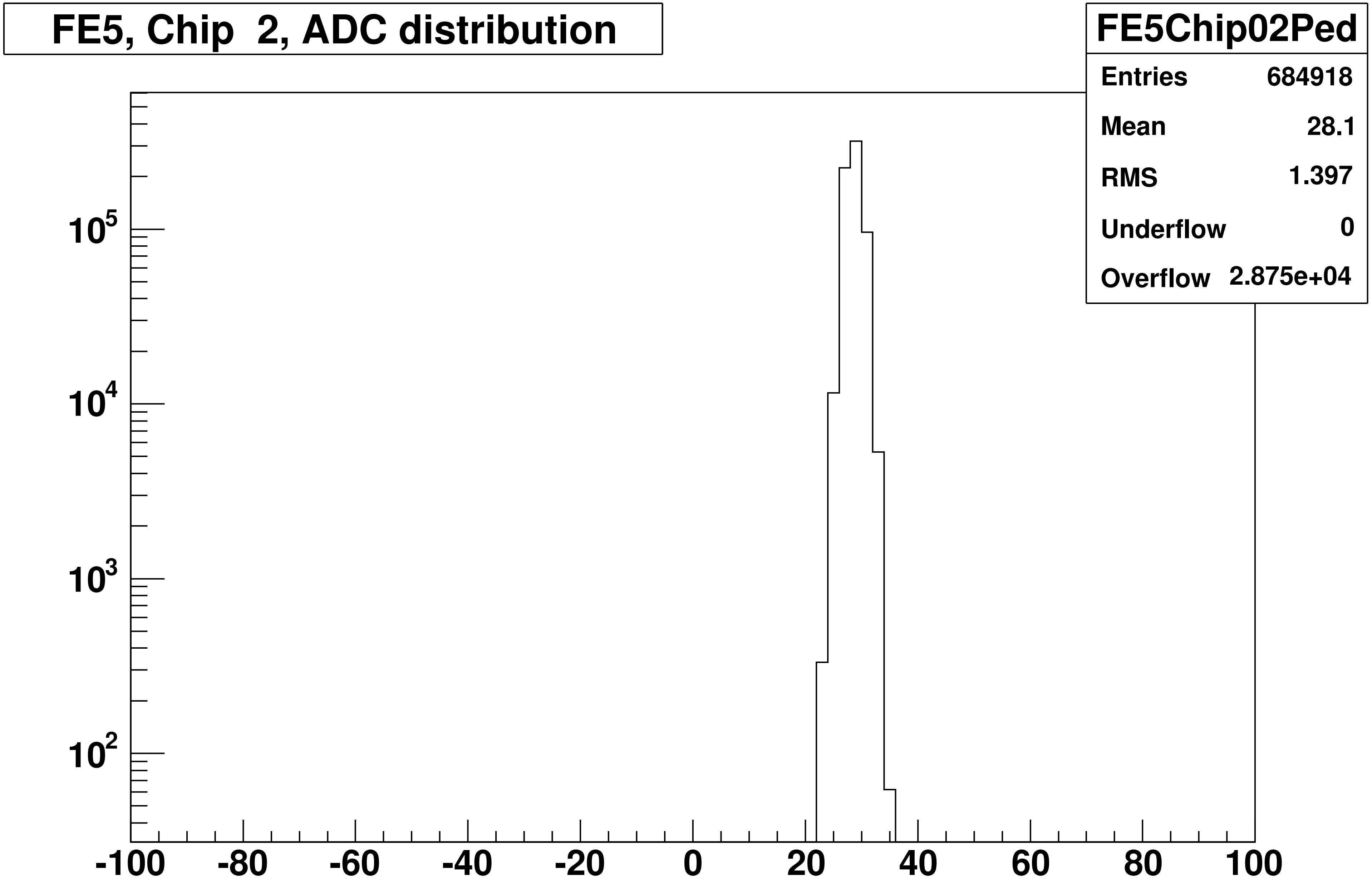

FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 684918
Mean 776.7
RMS 3833
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 684918
Mean -20.16
RMS 1.349
Underflow 0
Overflow 2.851e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

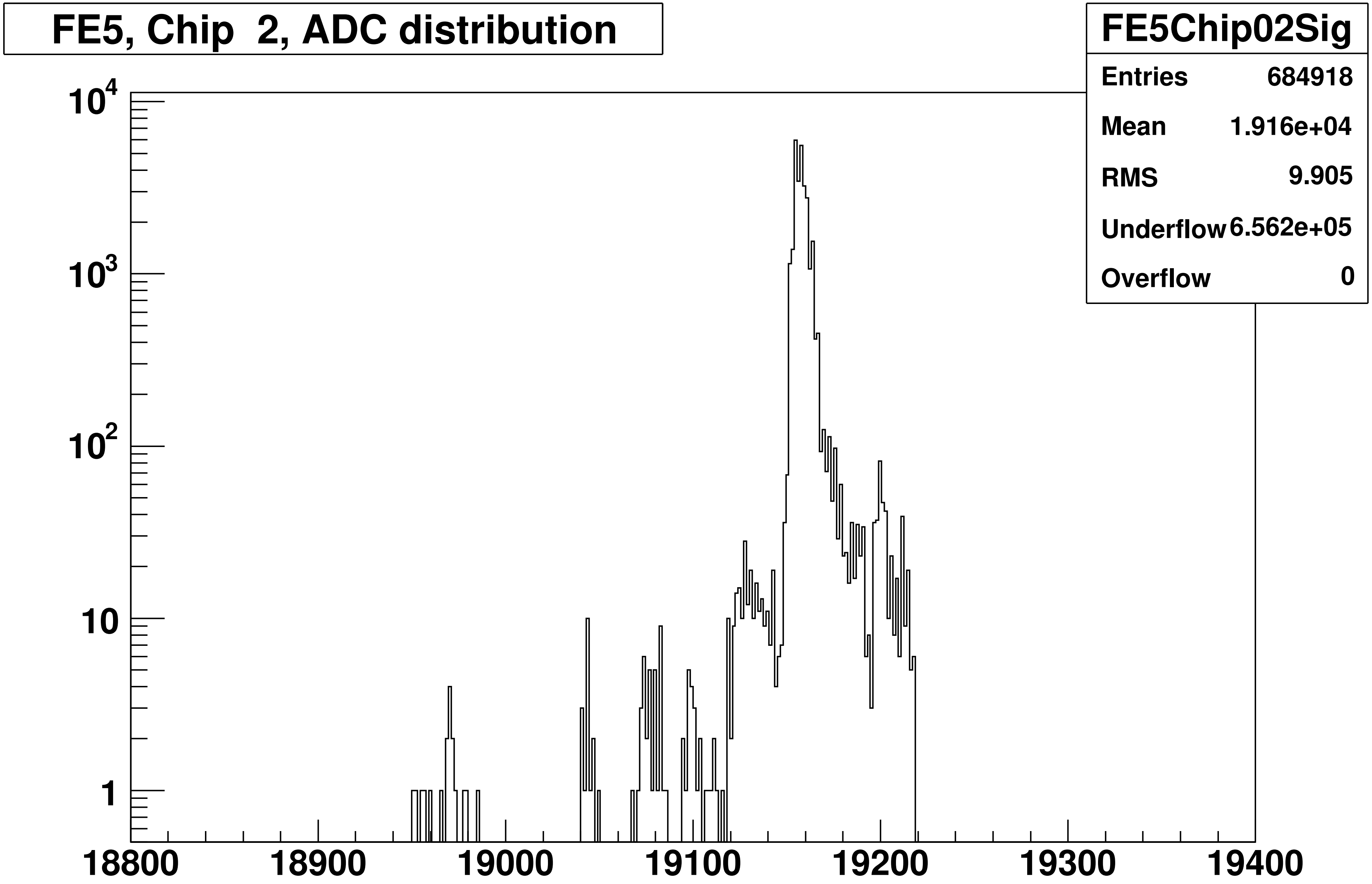

FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 684918
Mean 1.922e+04
RMS 9.466
Underflow 6.566e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

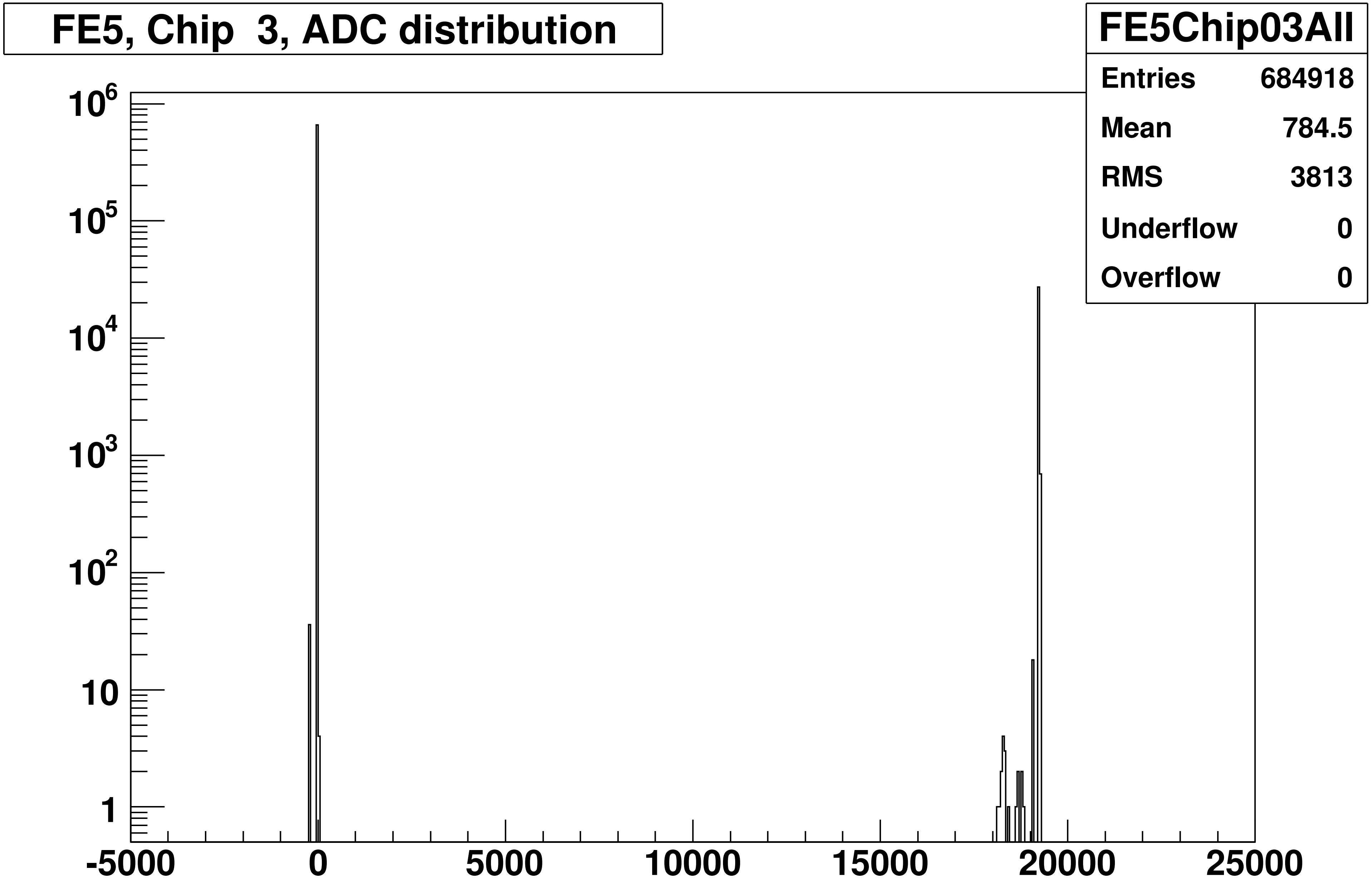


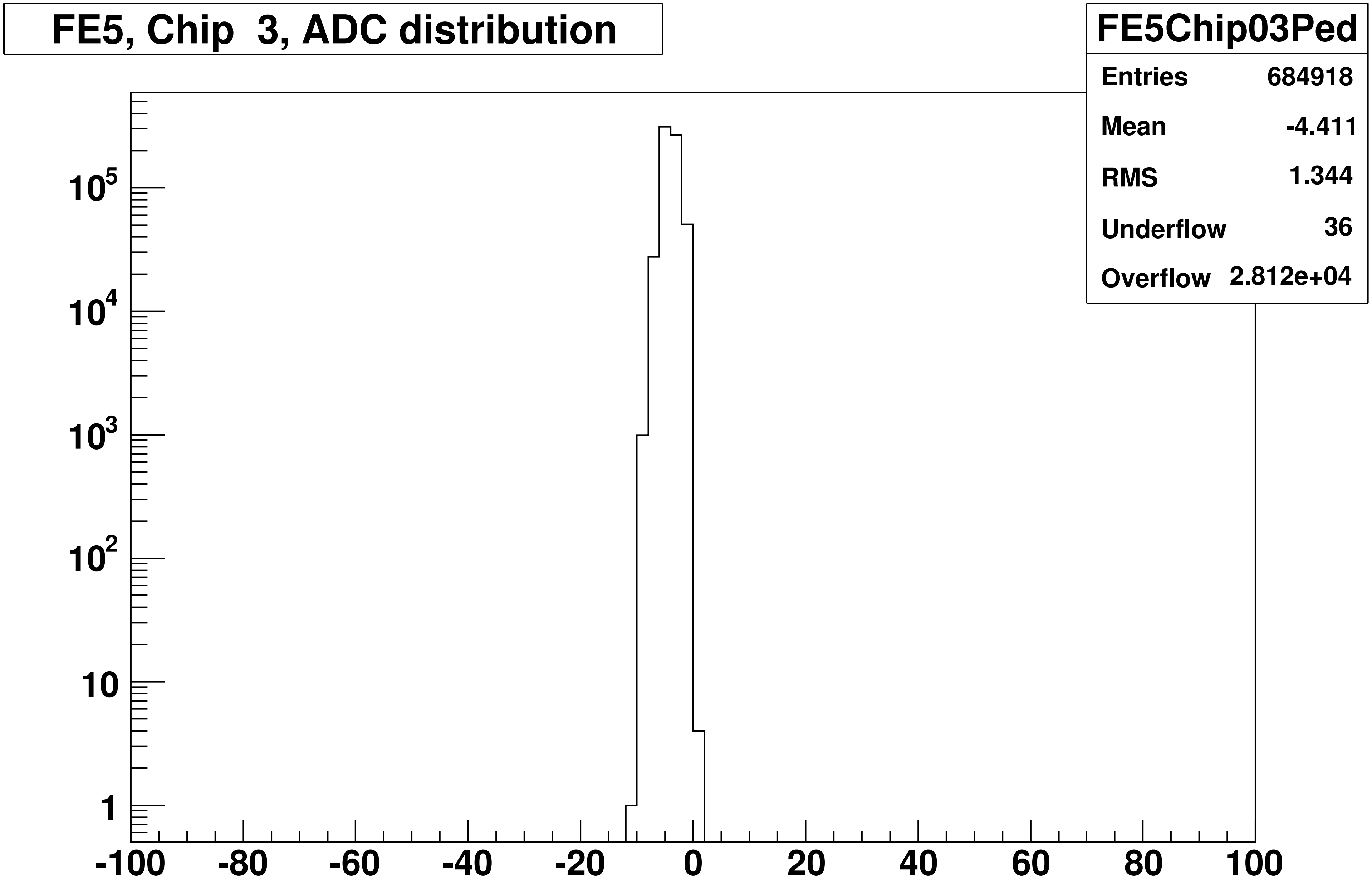
FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 684918
Mean 831
RMS 3836
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

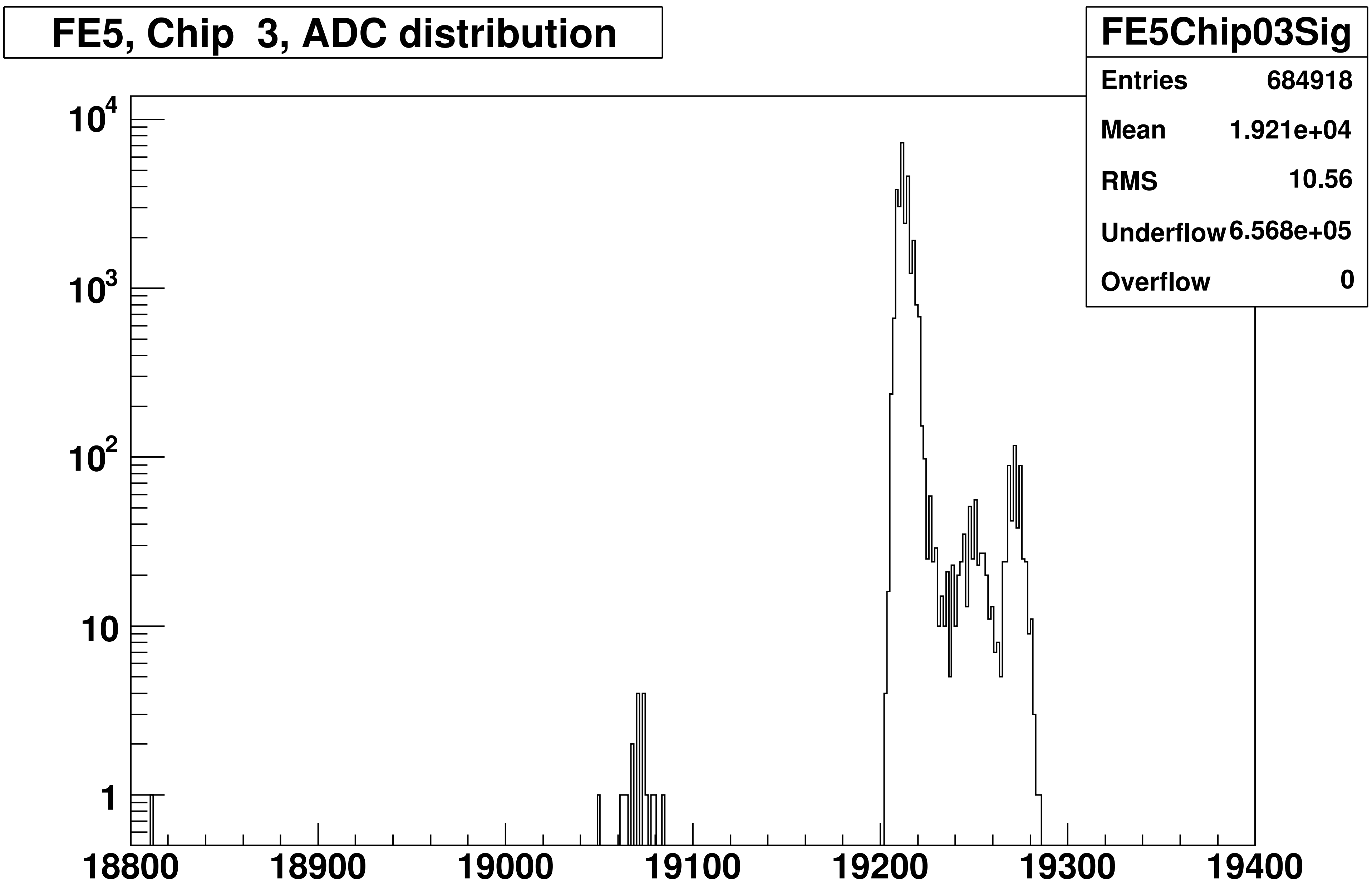


FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 684918
Mean 28.1
RMS 1.397
Underflow 0
Overflow 2.875e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

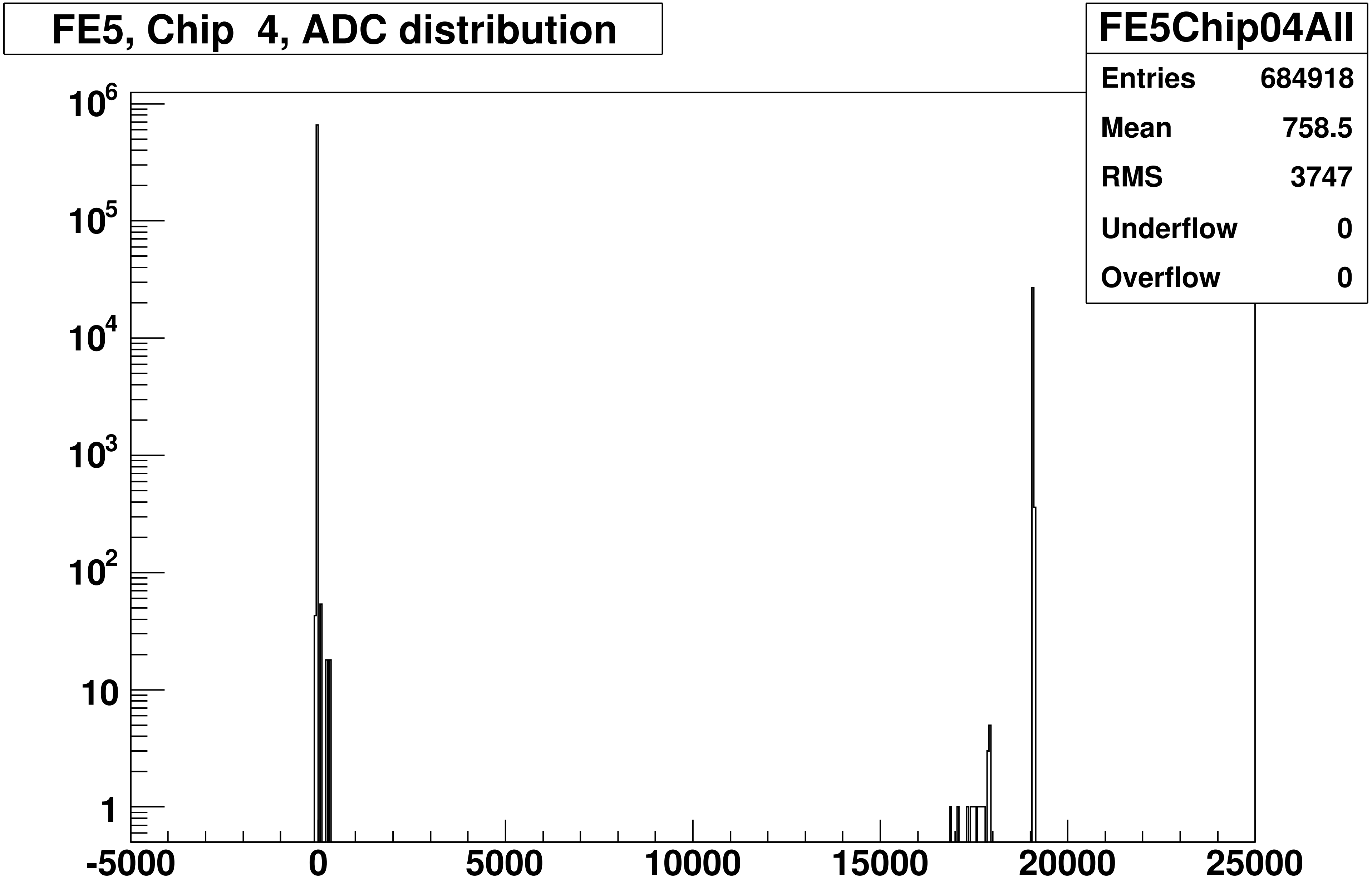

FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 684918
Mean 1.916e+04
RMS 9.905
Underflow 6.562e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

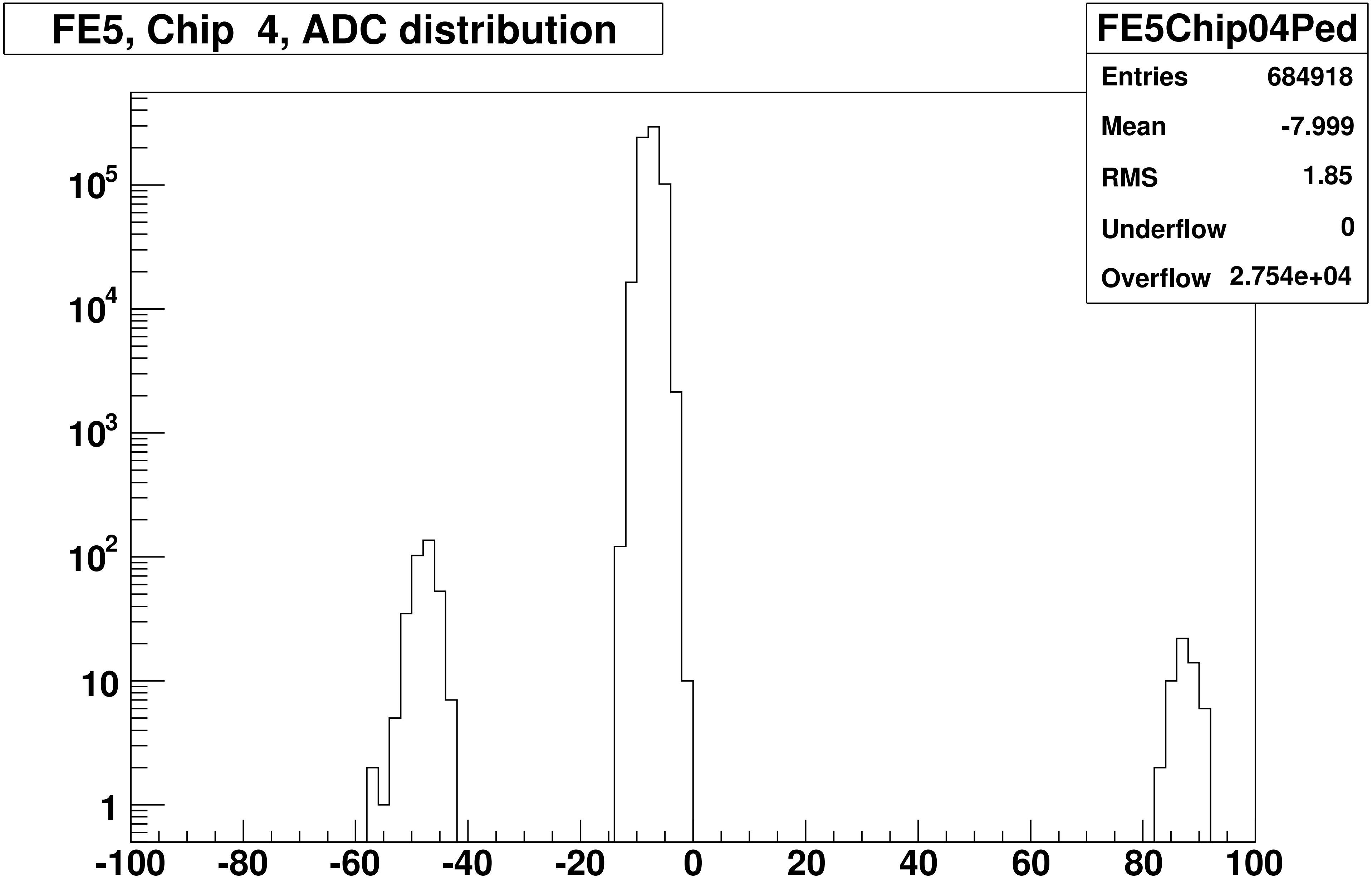

FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 684918
Mean 784.5
RMS 3813
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 684918
Mean -4.411
RMS 1.344
Underflow 36
Overflow 2.812e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

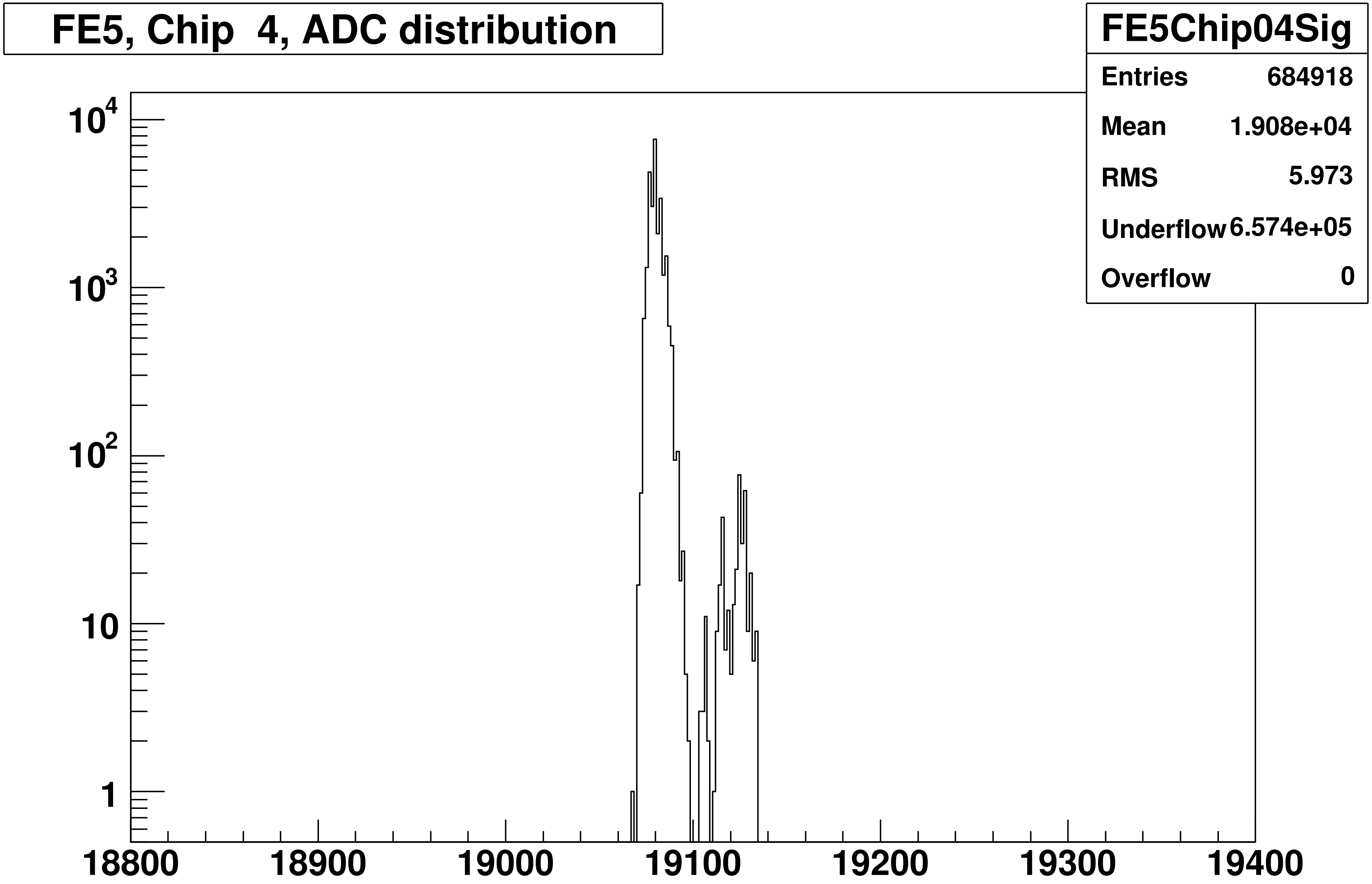


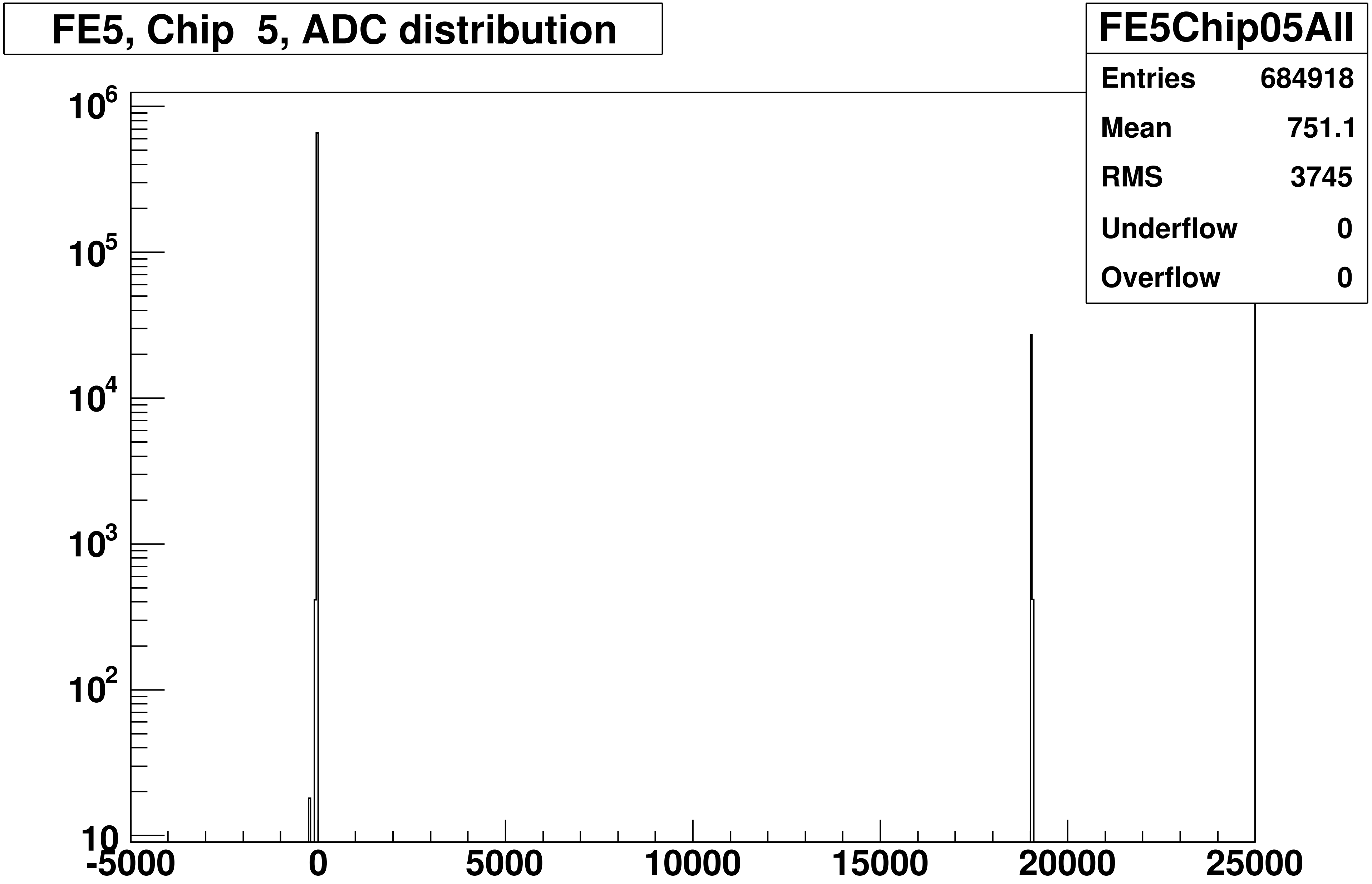
FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 684918
Mean 1.921e+04
RMS 10.56
Underflow 6.568e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

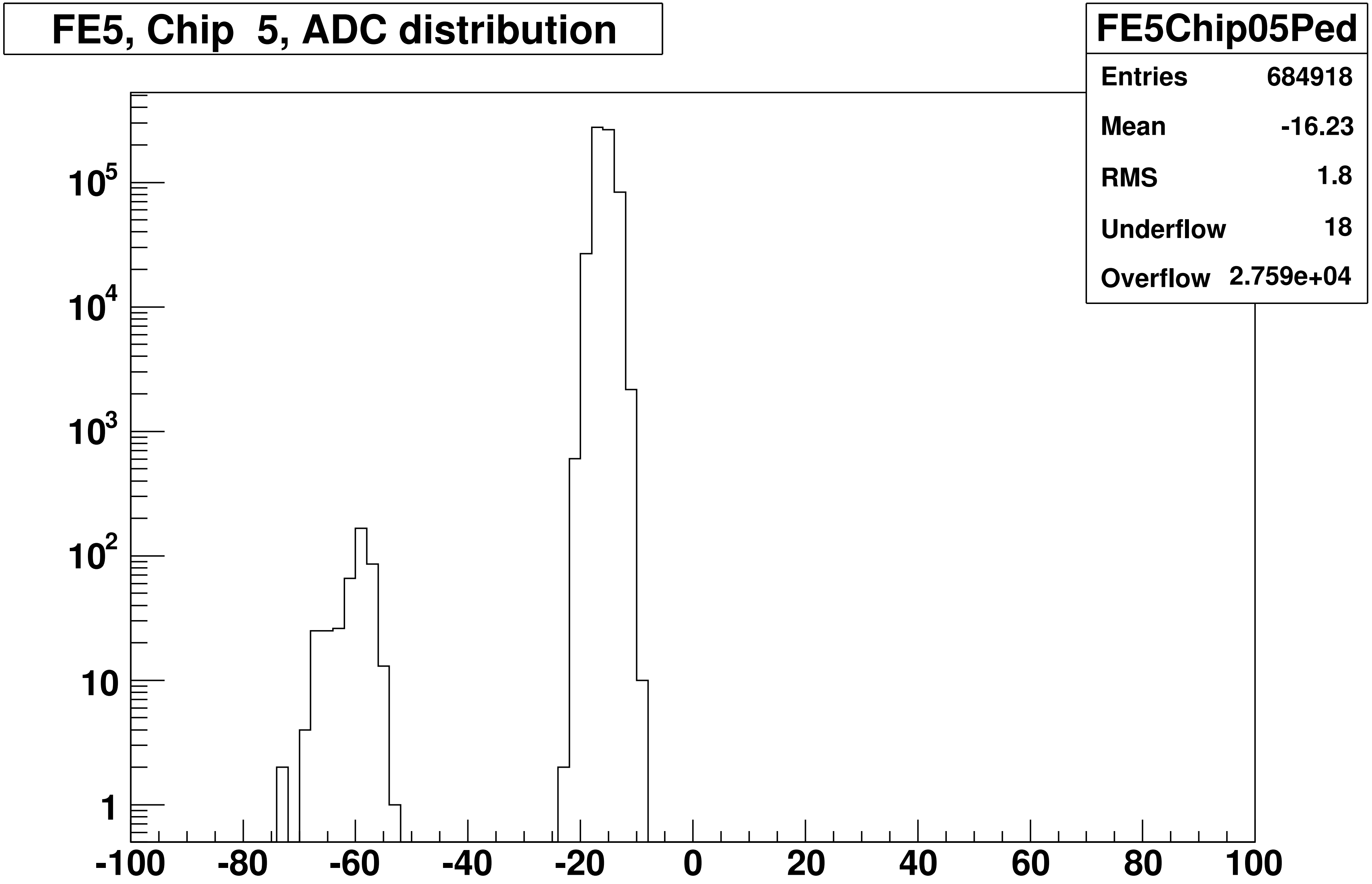

FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 684918
Mean 758.5
RMS 3747
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

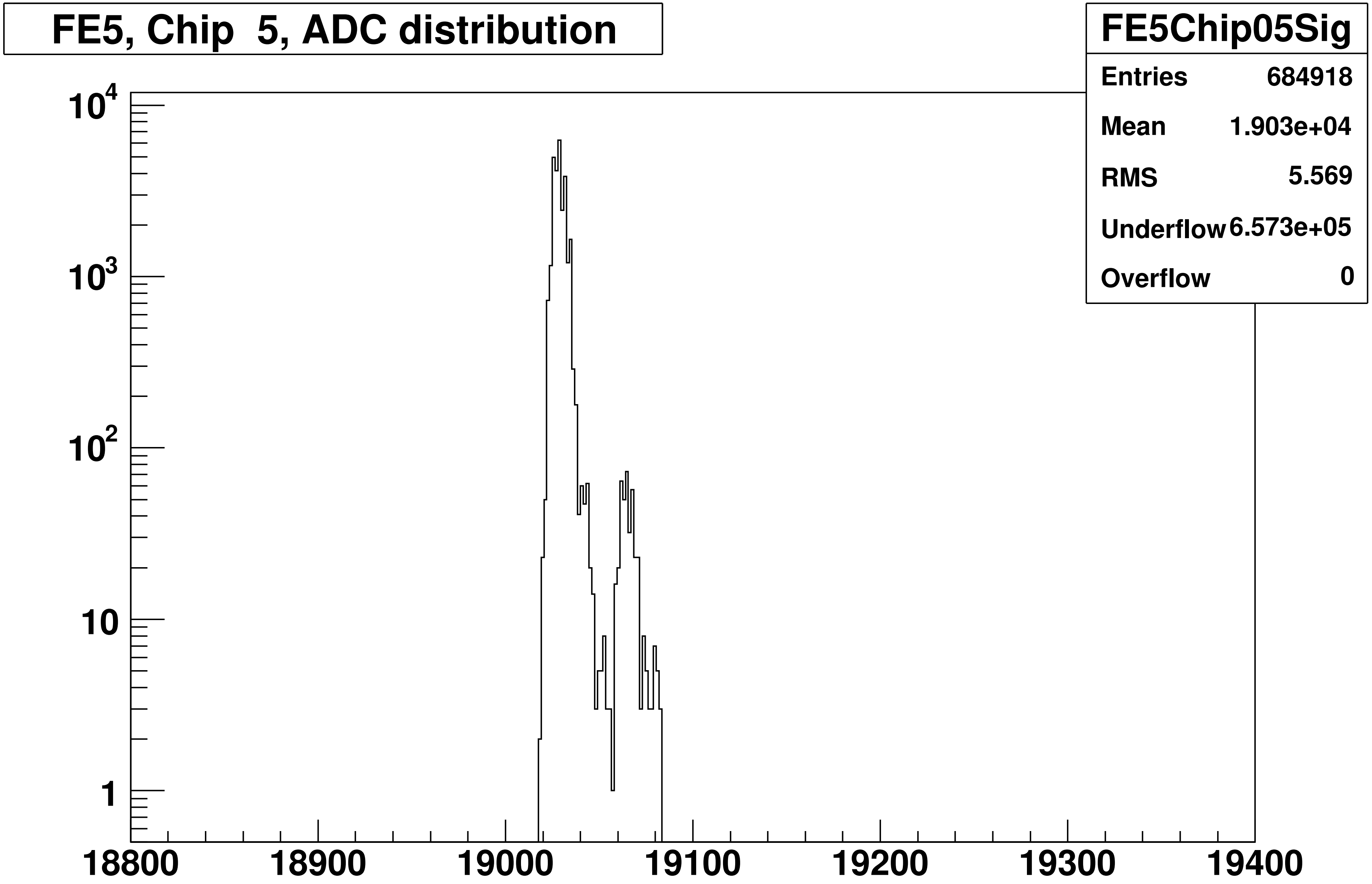


FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 684918
Mean -7.999
RMS 1.85
Underflow 0
Overflow 2.754e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

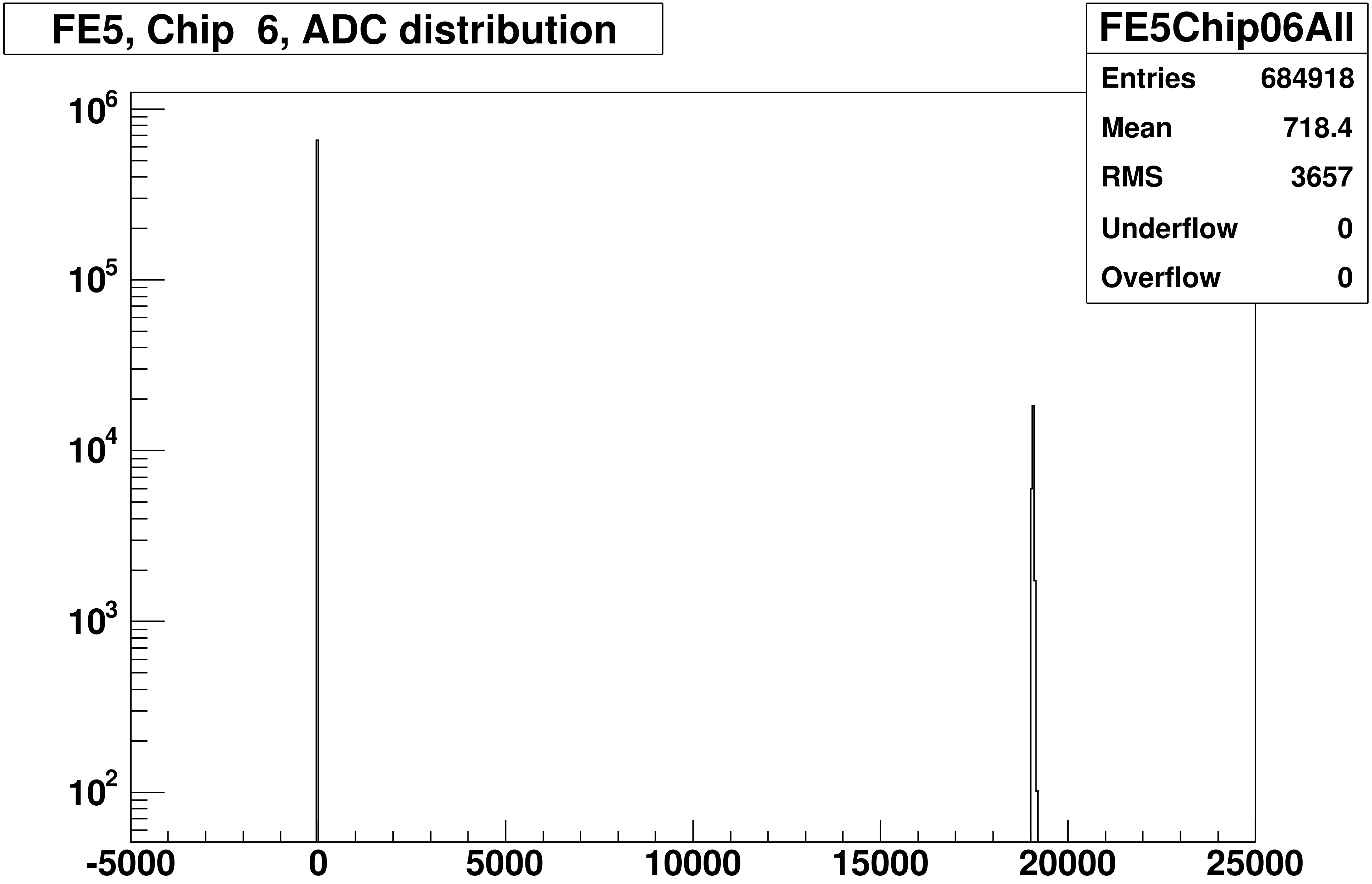

FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 684918
Mean 1.908e+04
RMS 5.973
Underflow 6.574e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

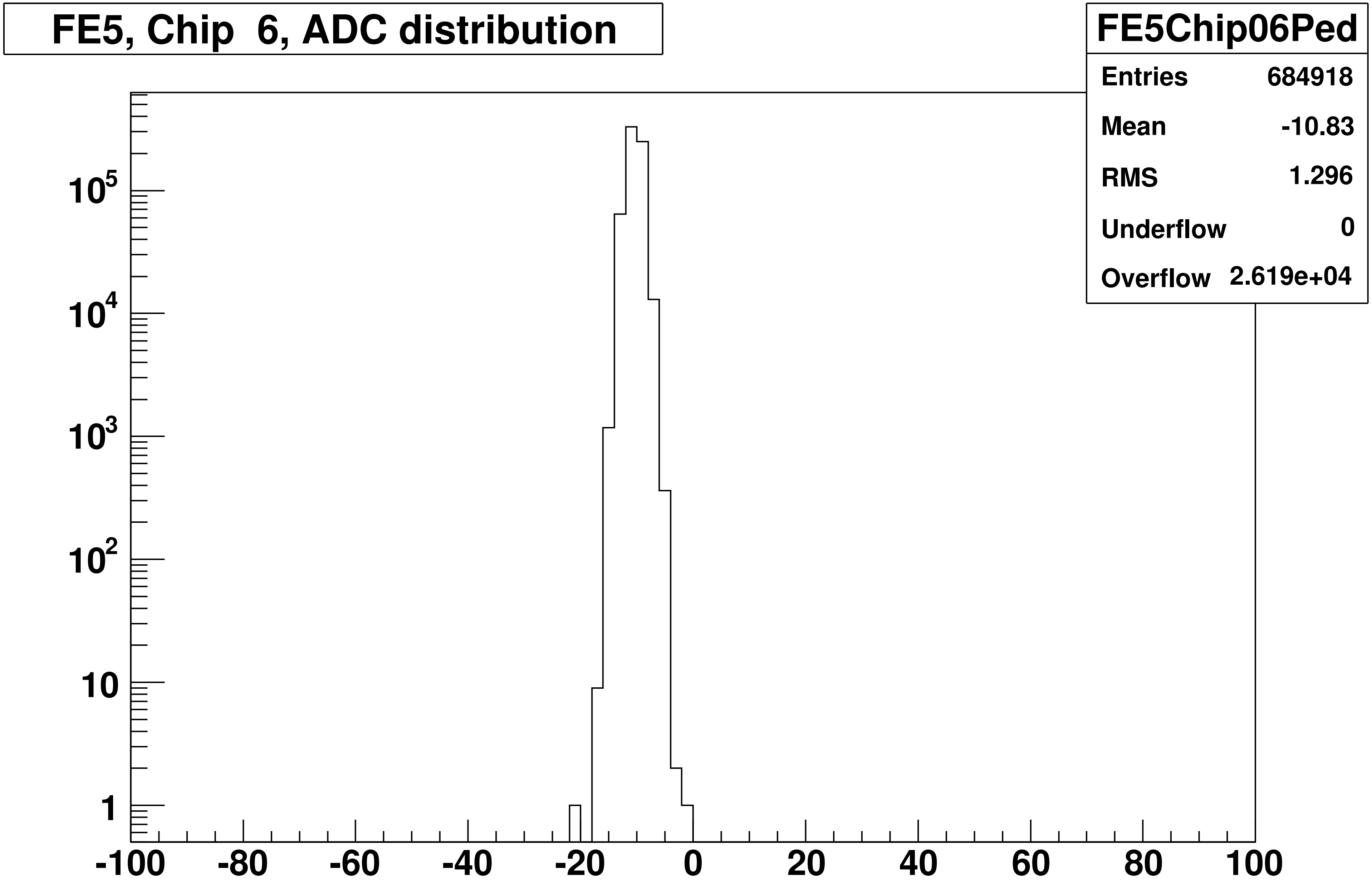

FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 684918
Mean 751.1
RMS 3745
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

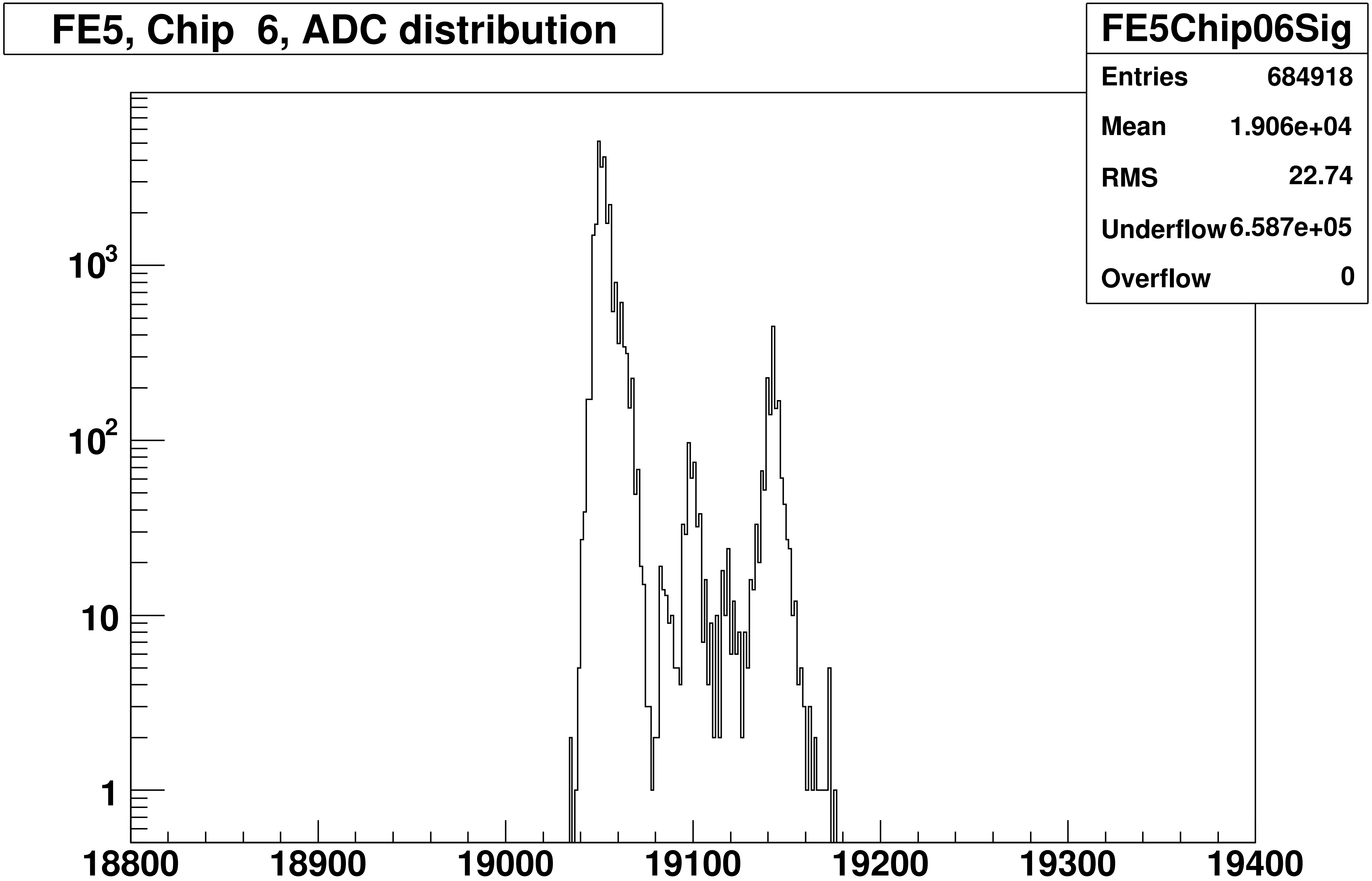

FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 684918
Mean -16.23
RMS 1.8
Underflow 18
Overflow 2.759e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



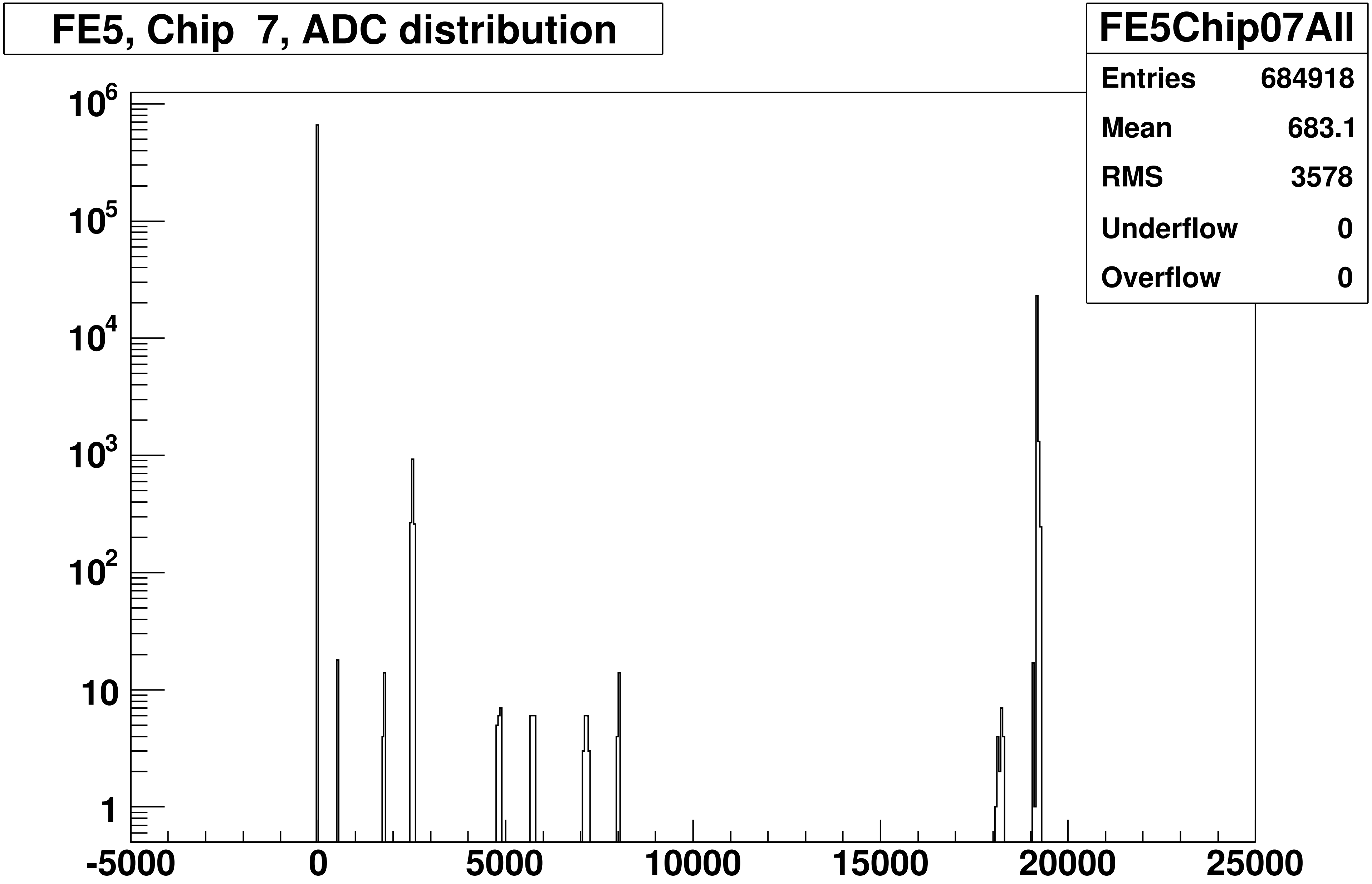
FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 684918
Mean 1.903e+04
RMS 5.569
Underflow 6.573e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 684918
Mean 718.4
RMS 3657
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

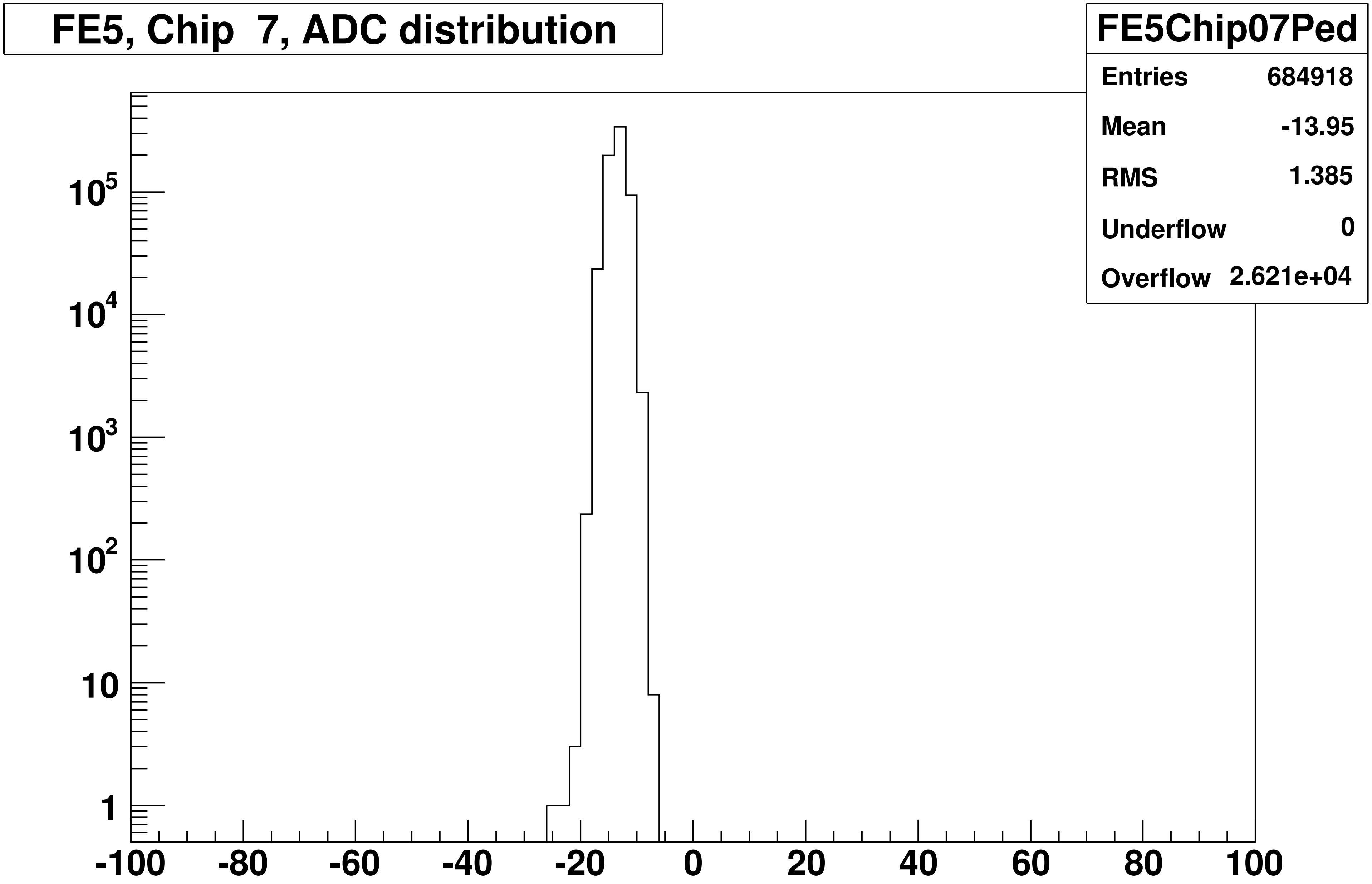

FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 684918
Mean -10.83
RMS 1.296
Underflow 0
Overflow 2.619e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

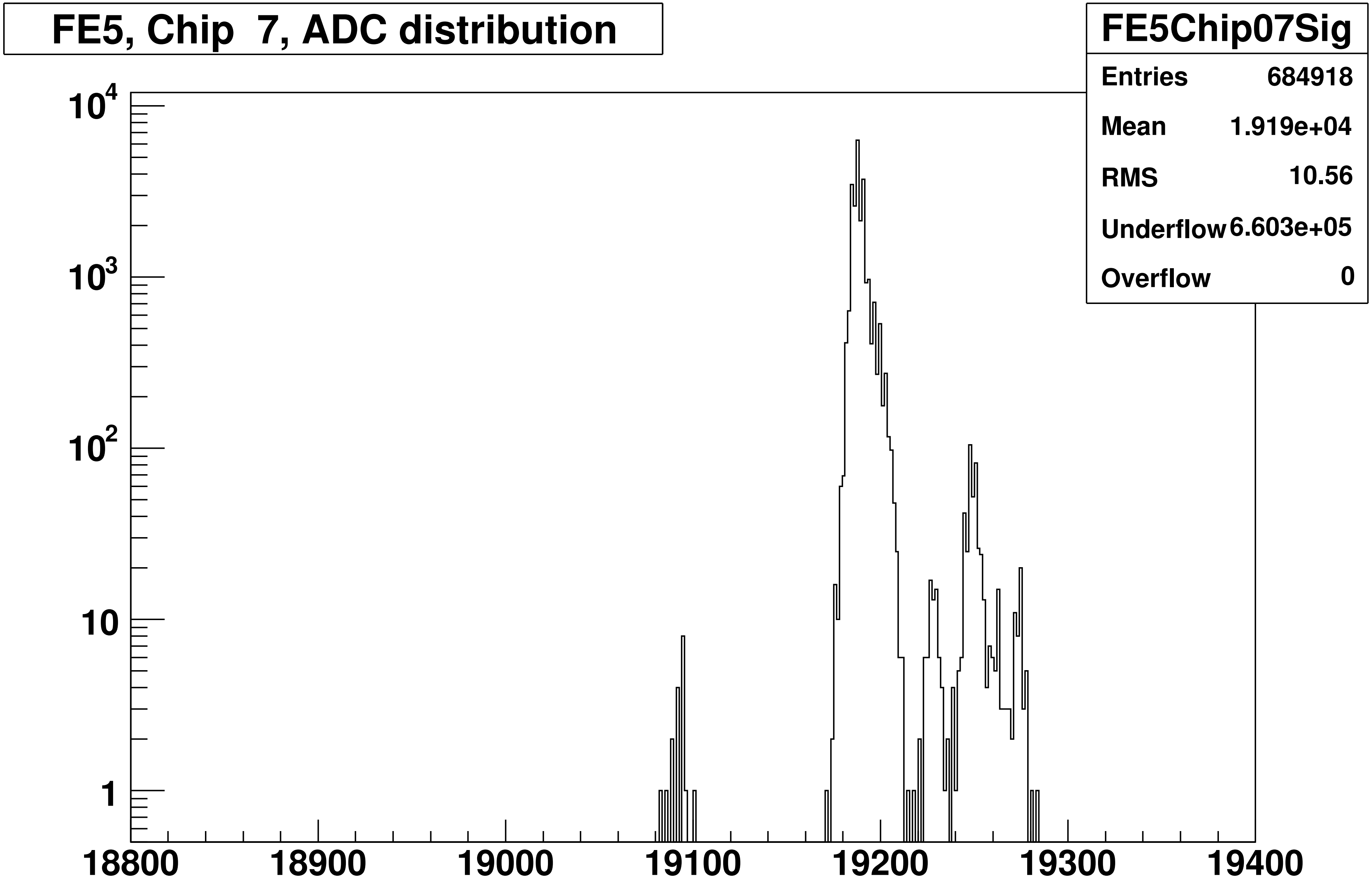


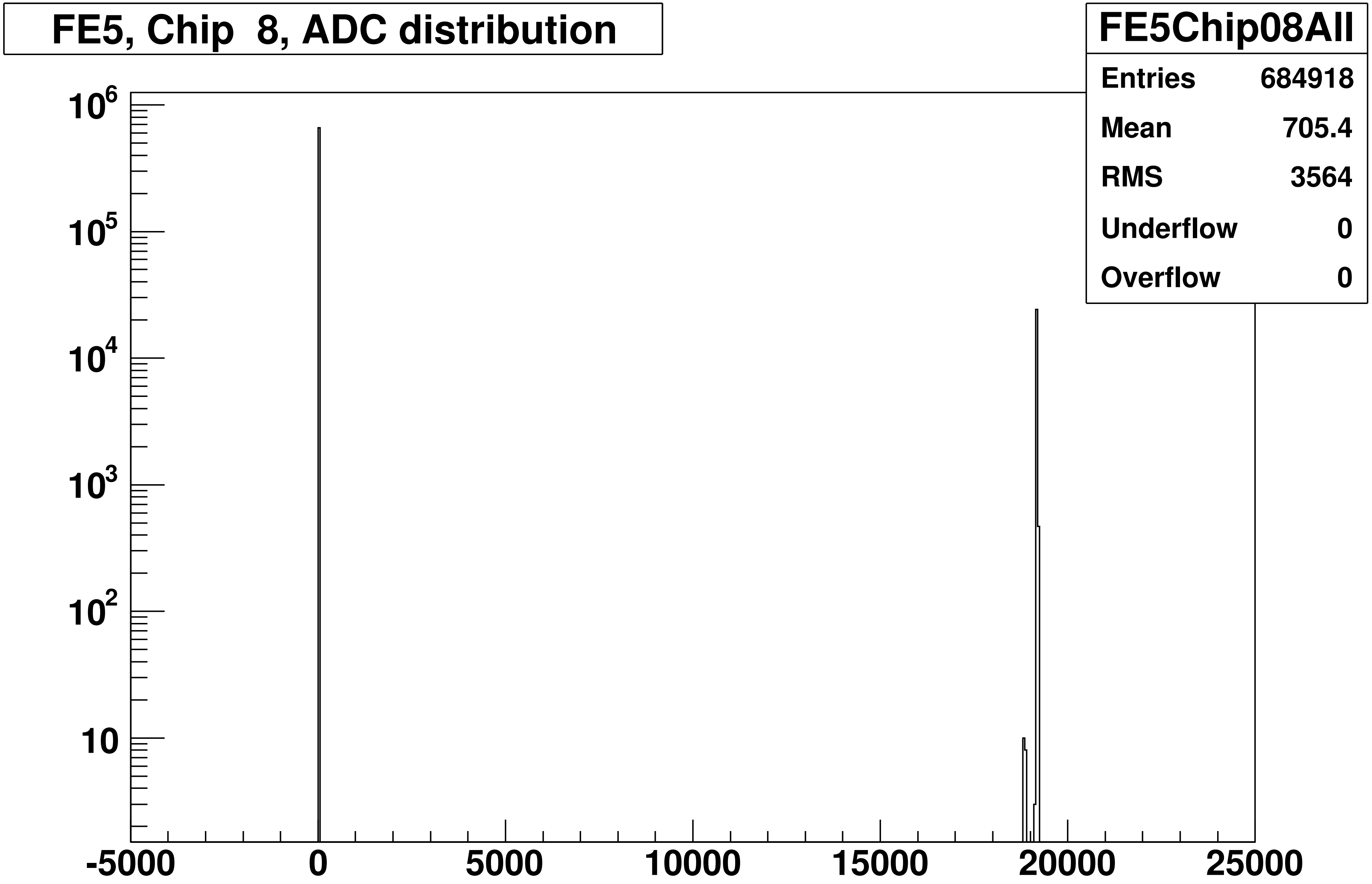
FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 684918
Mean 1.906e+04
RMS 22.74
Underflow 6.587e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

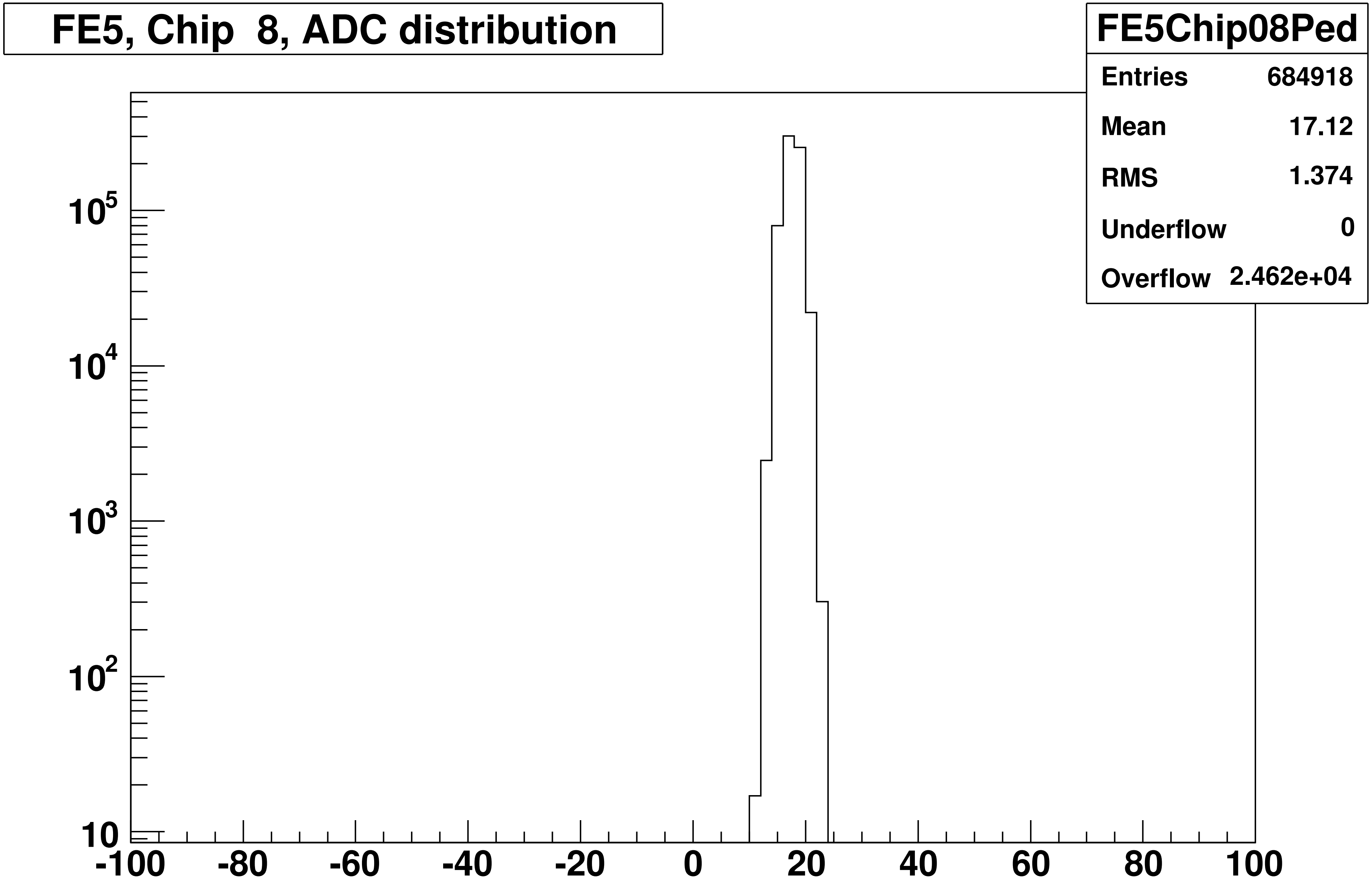


FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 684918
Mean 683.1
RMS 3578
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

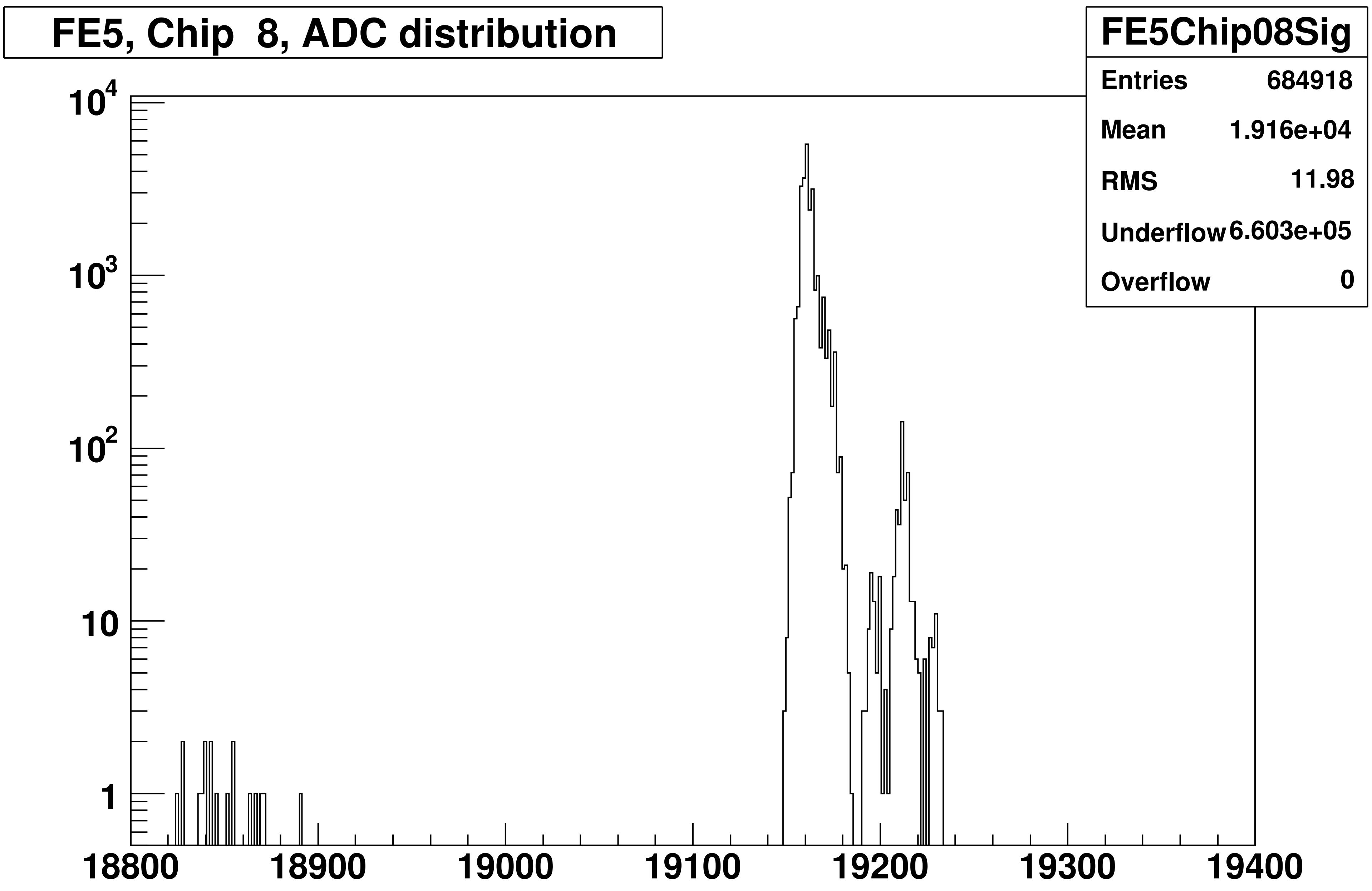

FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 684918
Mean -13.95
RMS 1.385
Underflow 0
Overflow 2.621e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

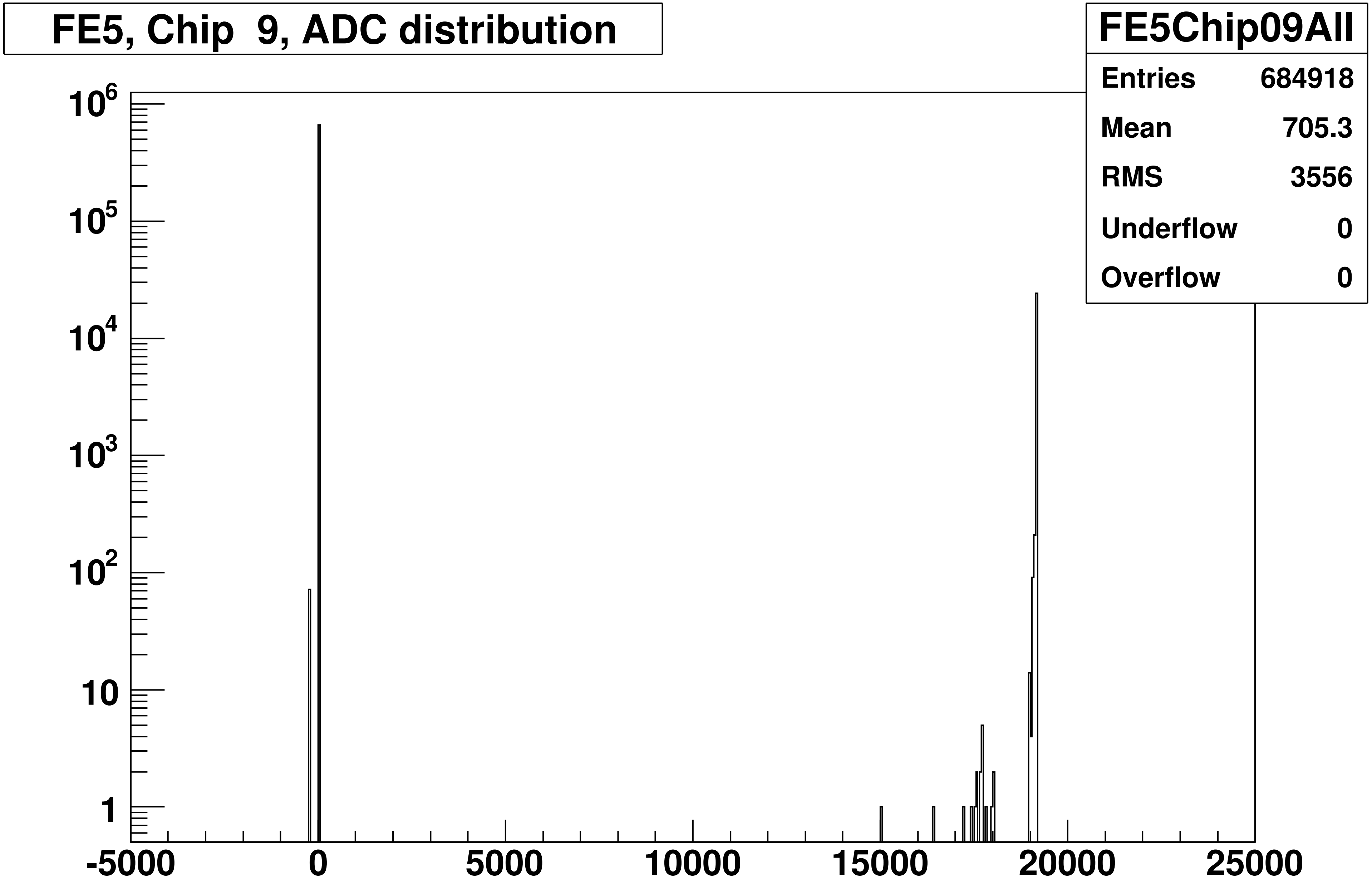

FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 684918
Mean 1.919e+04
RMS 10.56
Underflow 6.603e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

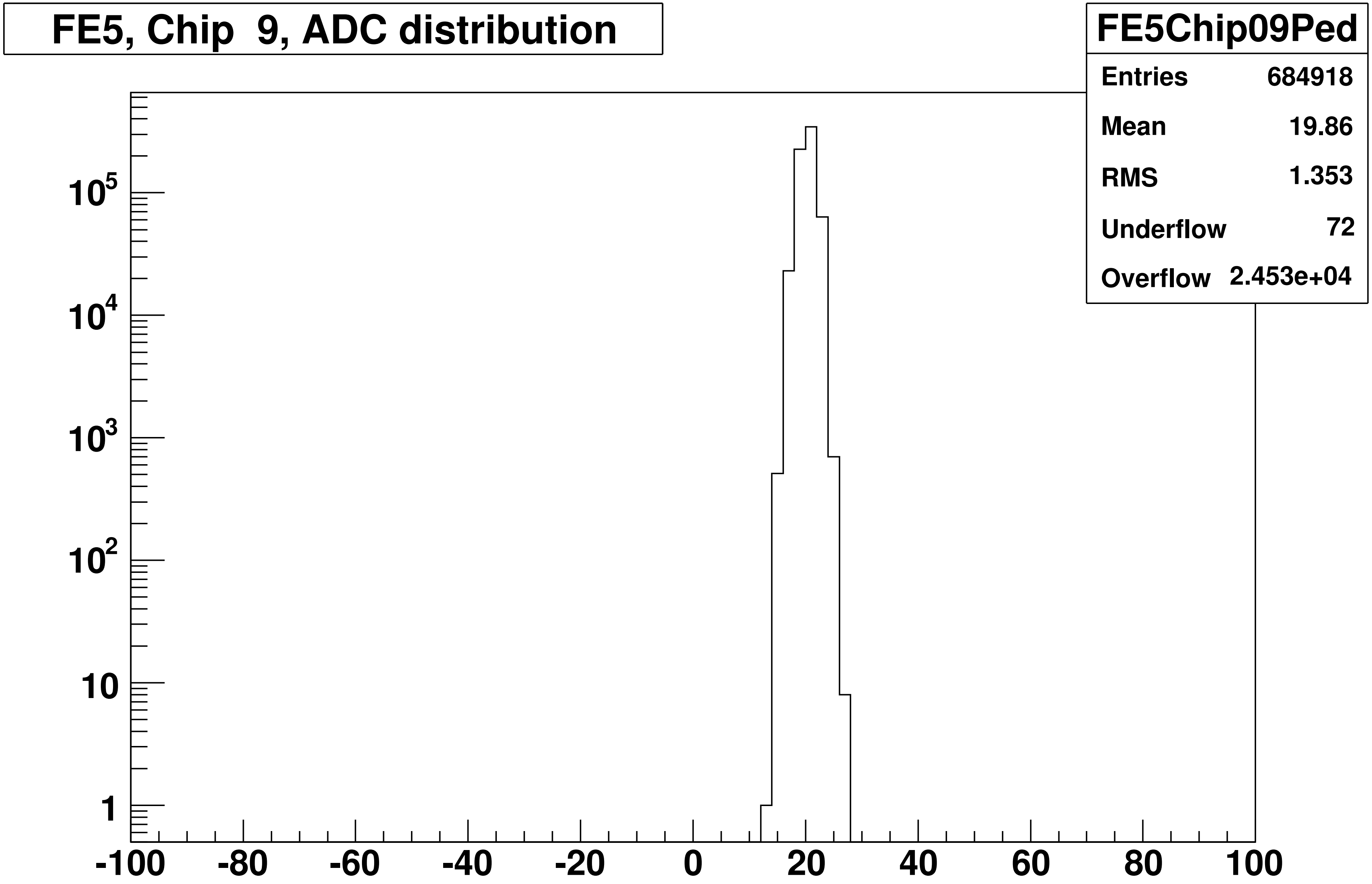

FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 684918
Mean 705.4
RMS 3564
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



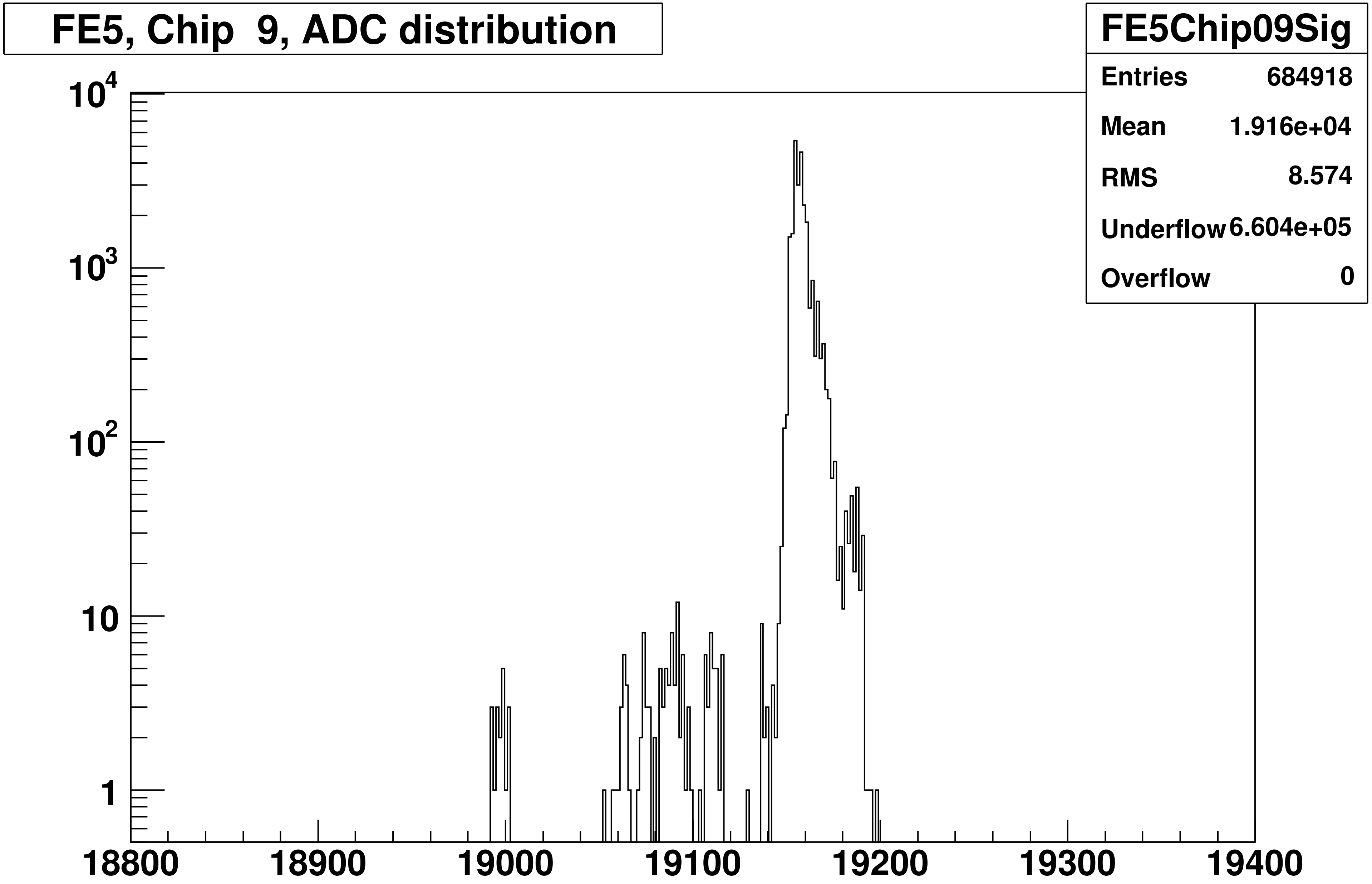
FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 684918
Mean 17.12
RMS 1.374
Underflow 0
Overflow 2.462e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

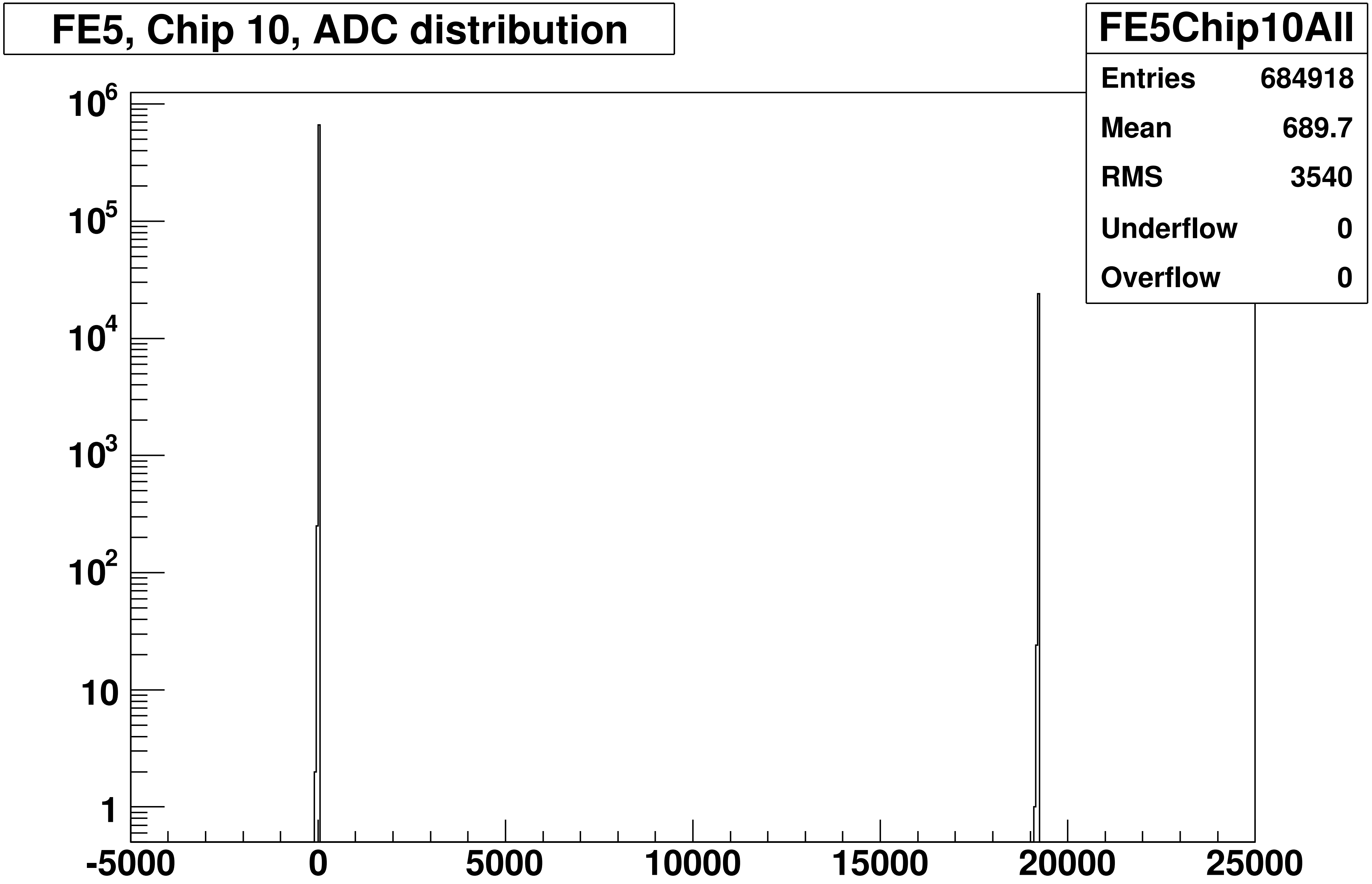

FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 684918
Mean 1.916e+04
RMS 11.98
Underflow 6.603e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

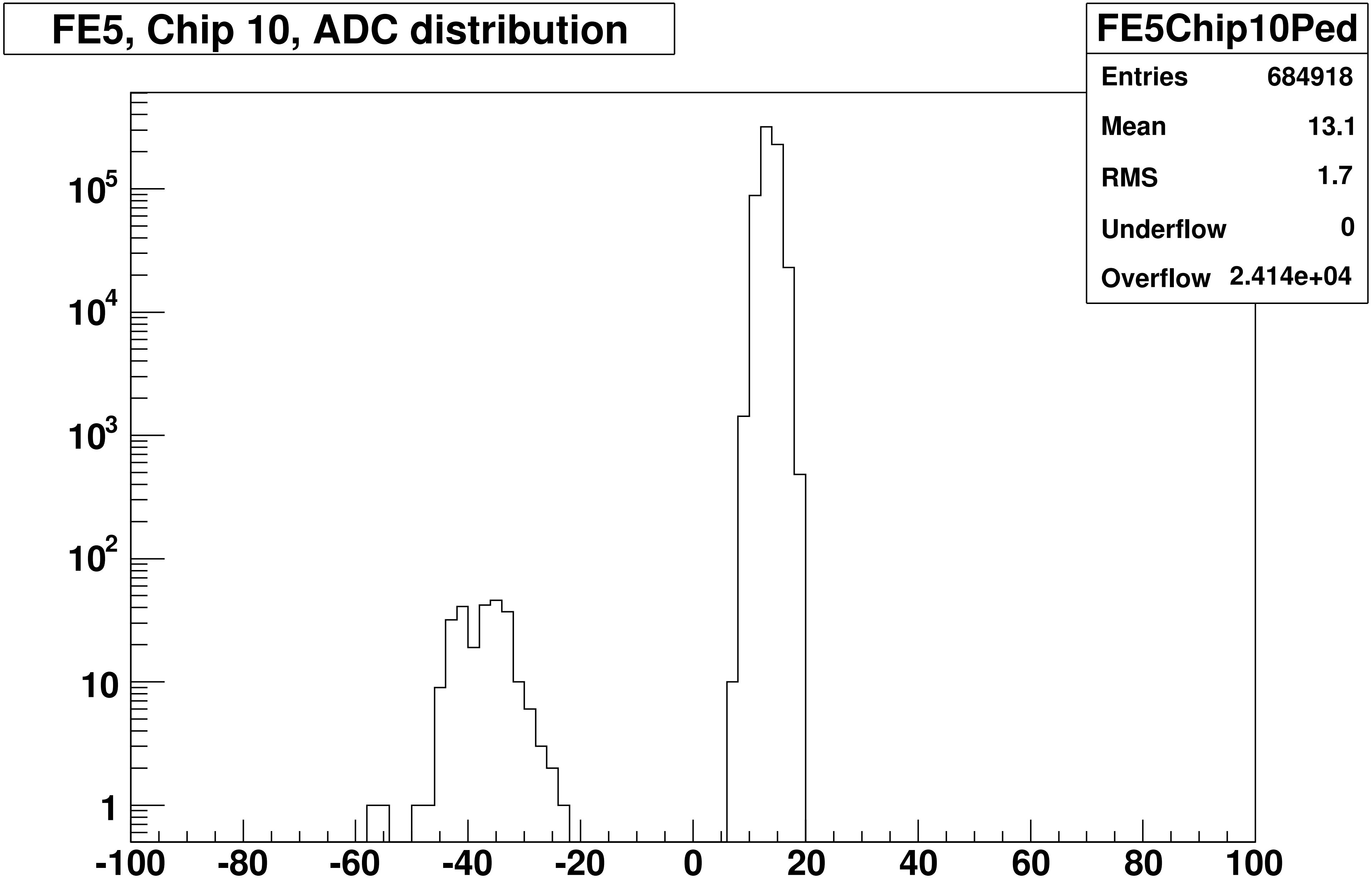

FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 684918
Mean 705.3
RMS 3556
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



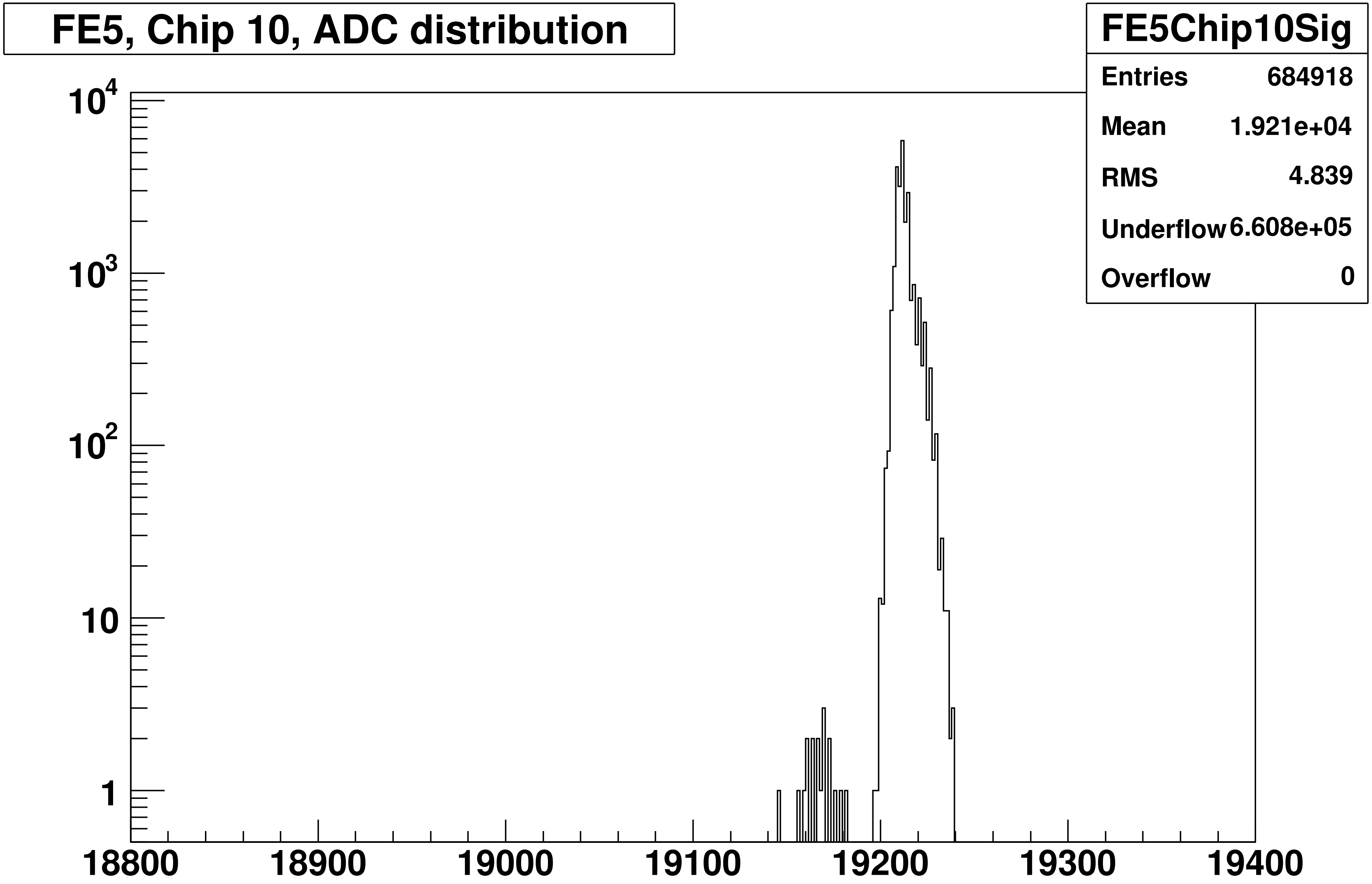
FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 684918
Mean 19.86
RMS 1.353
Underflow 72
Overflow 2.453e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 684918
Mean 1.916e+04
RMS 8.574
Underflow 6.604e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

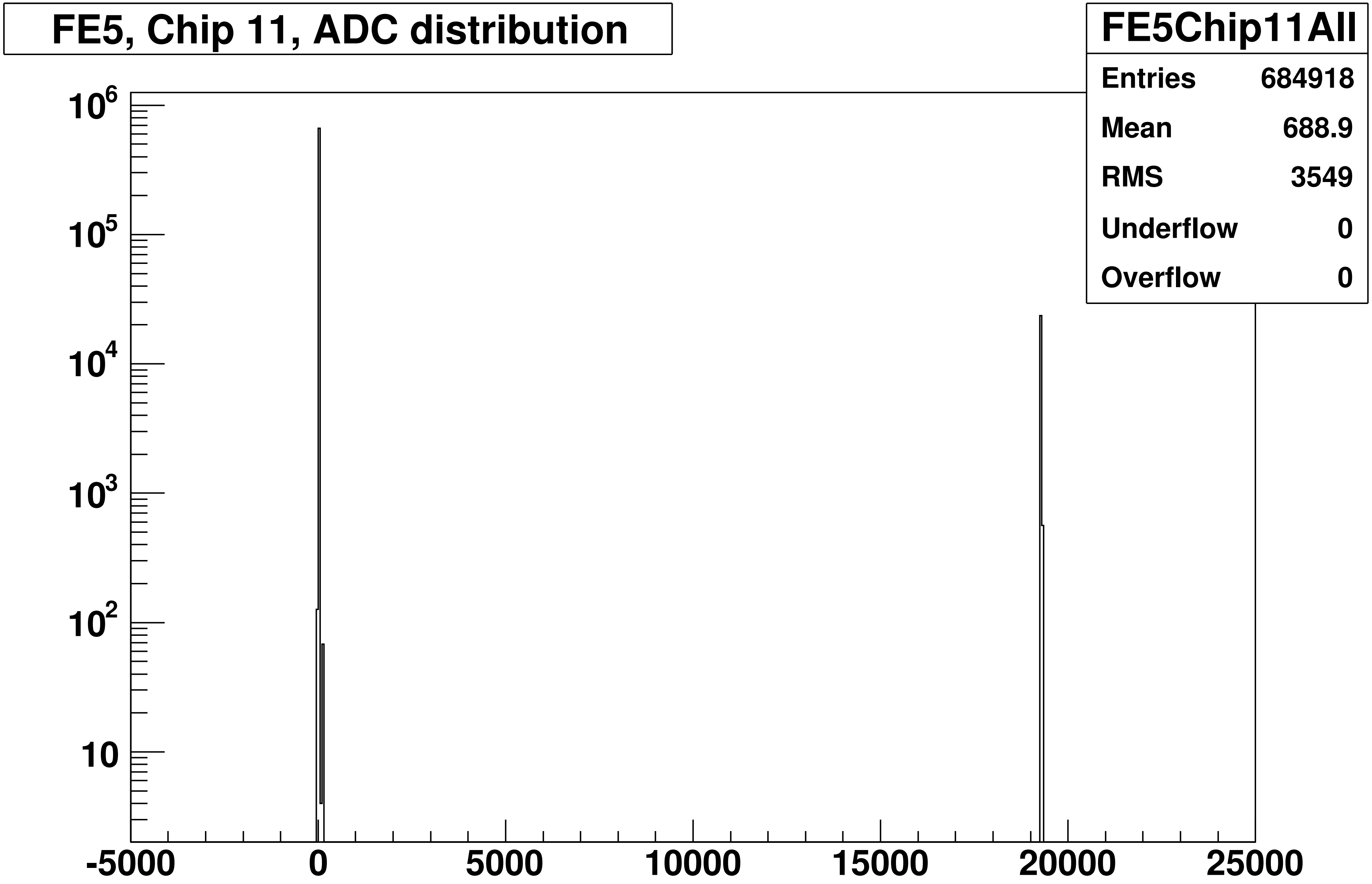

FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 684918
Mean 689.7
RMS 3540
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

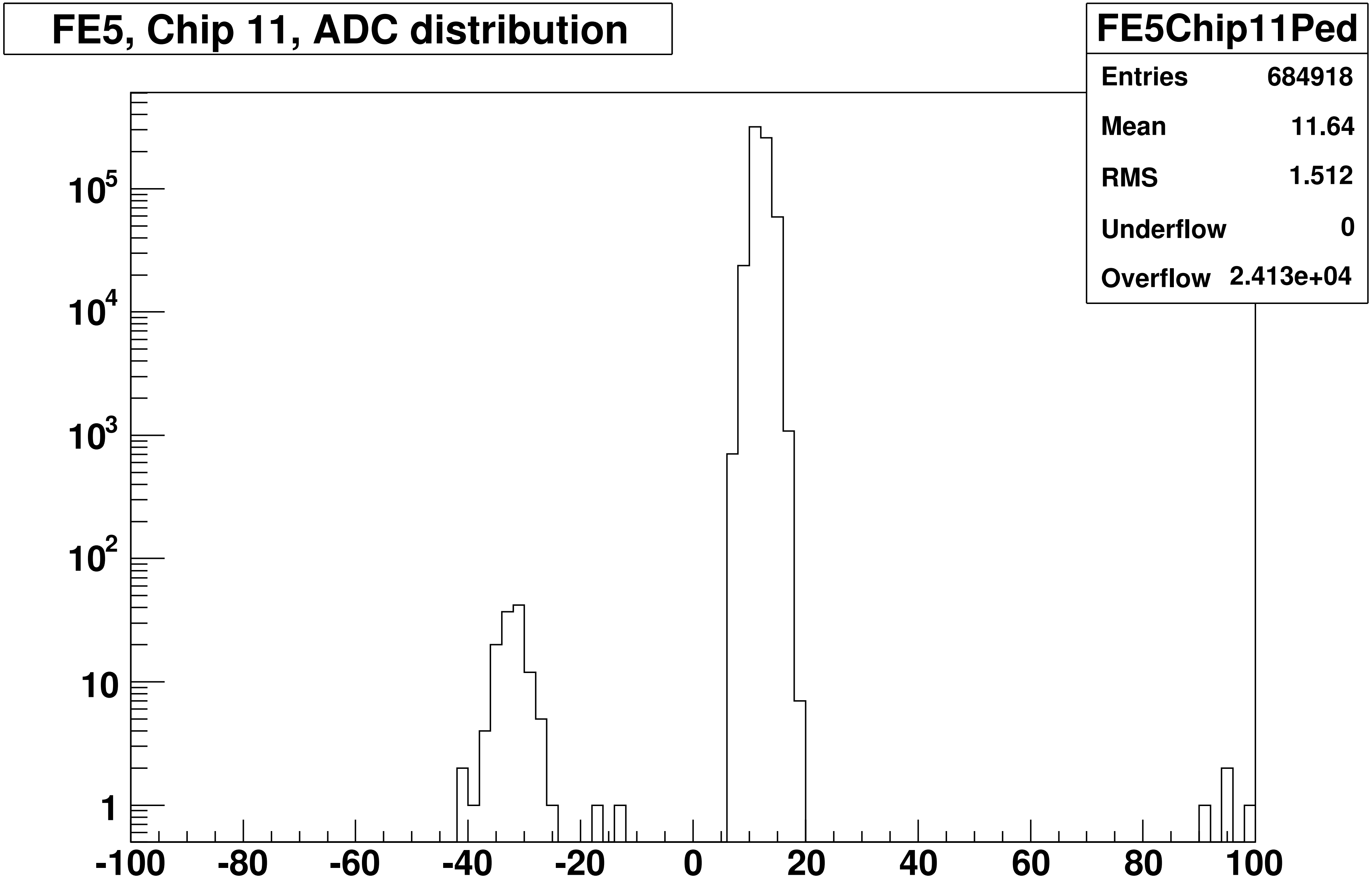


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 684918
Mean 13.1
RMS 1.7
Underflow 0
Overflow 2.414e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

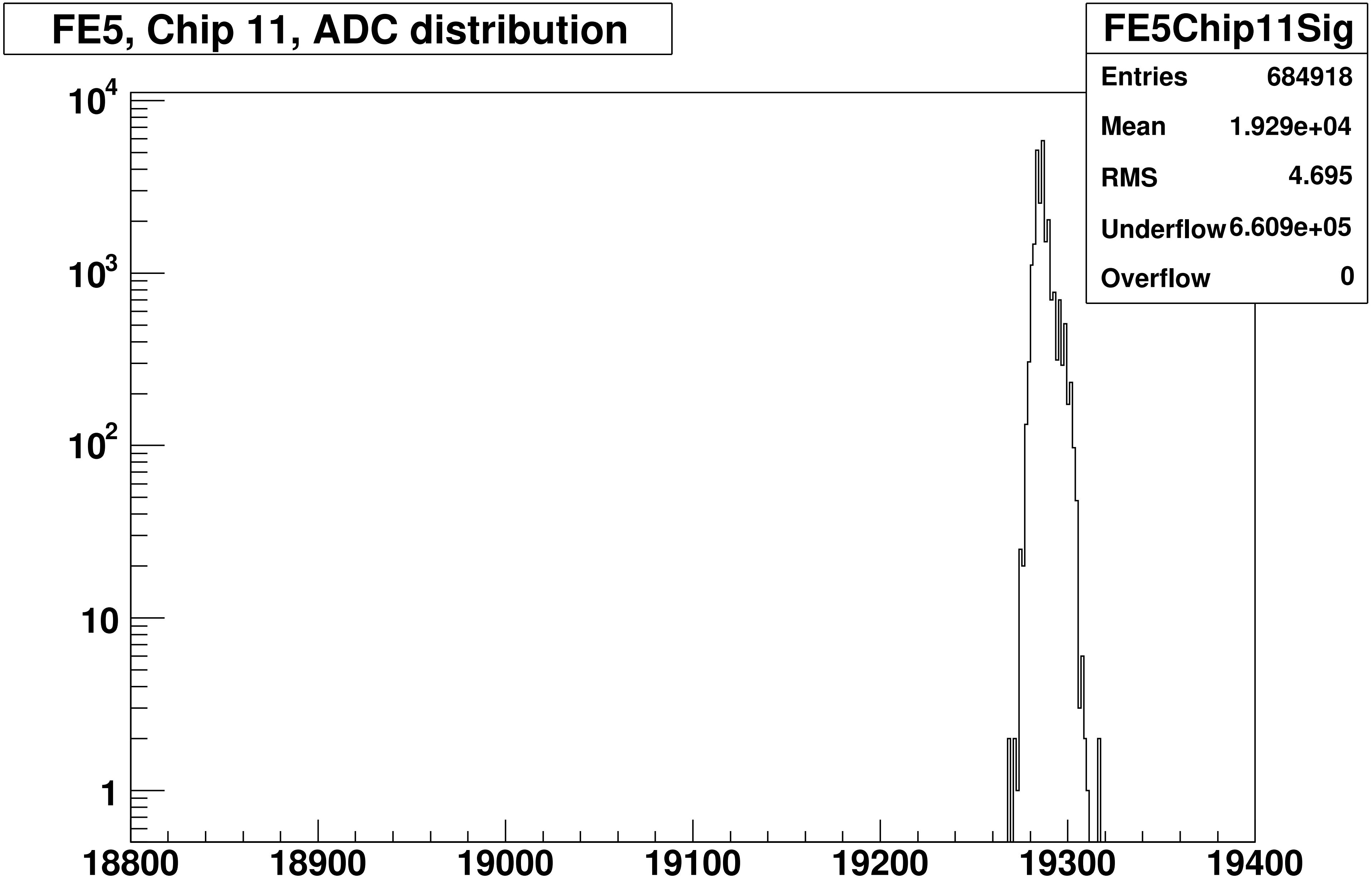


FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 684918
Mean 1.921e+04
RMS 4.839
Underflow 6.608e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

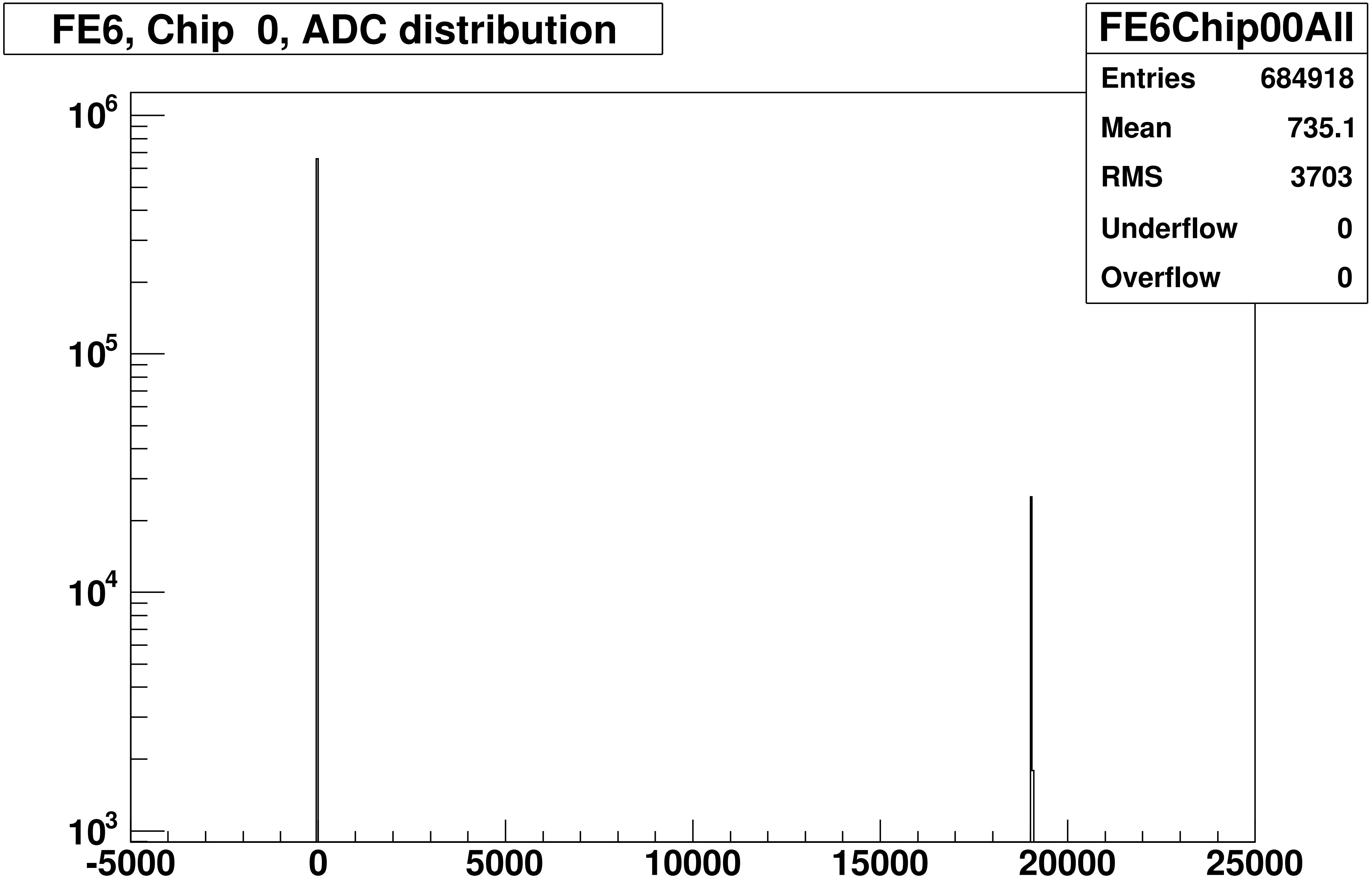

FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 684918
Mean 688.9
RMS 3549
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

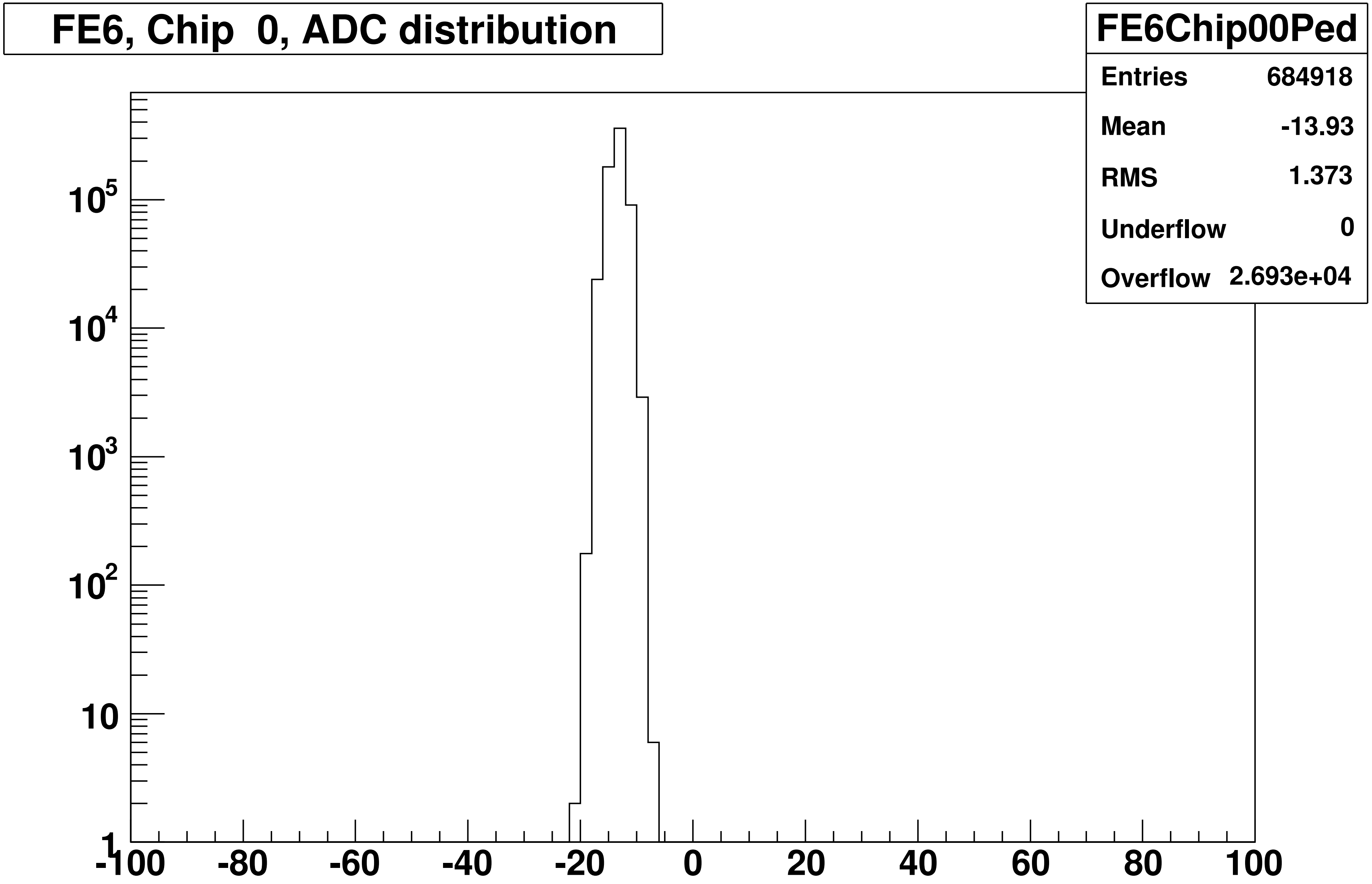


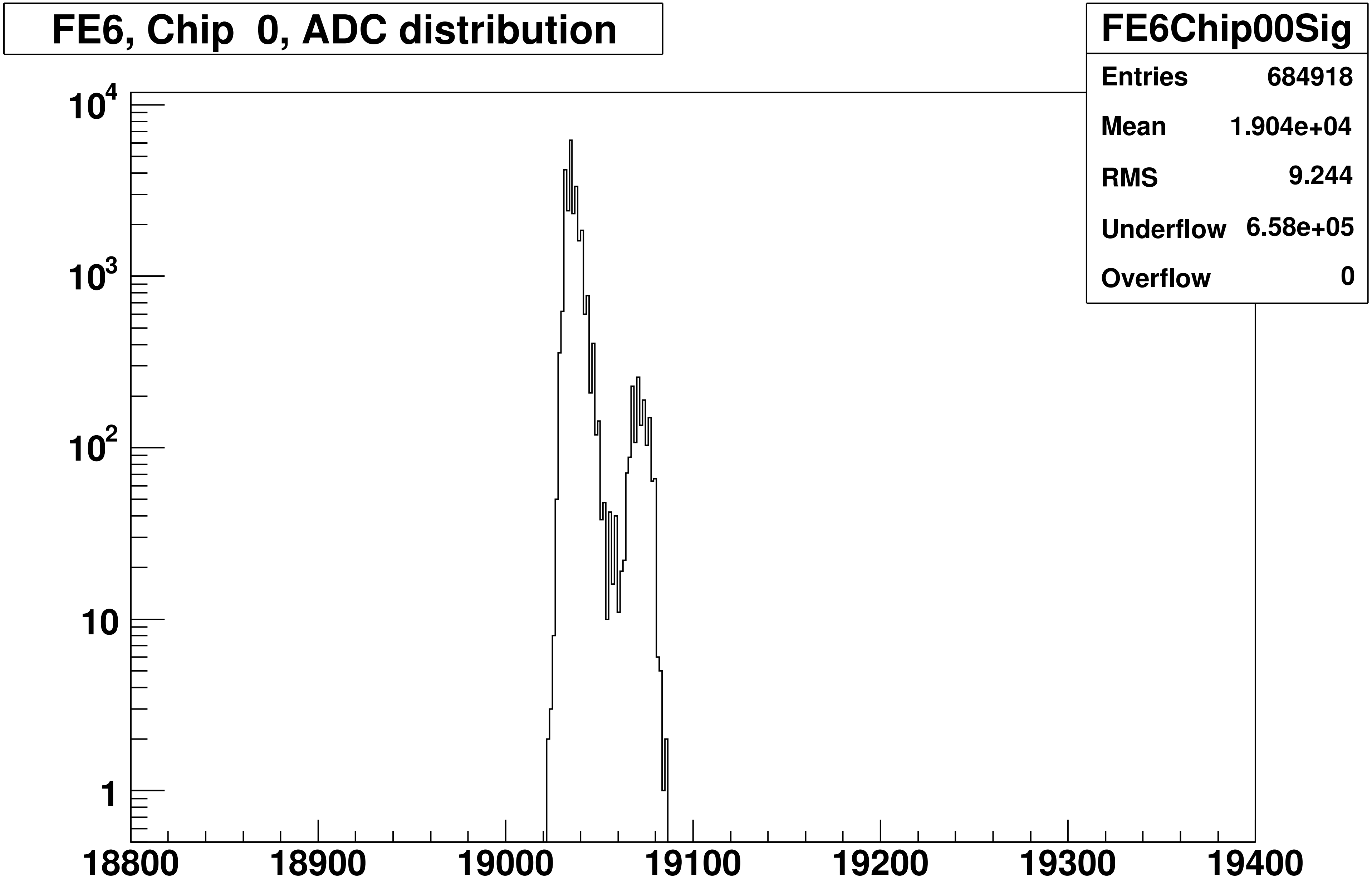
FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 684918
Mean 11.64
RMS 1.512
Underflow 0
Overflow 2.413e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

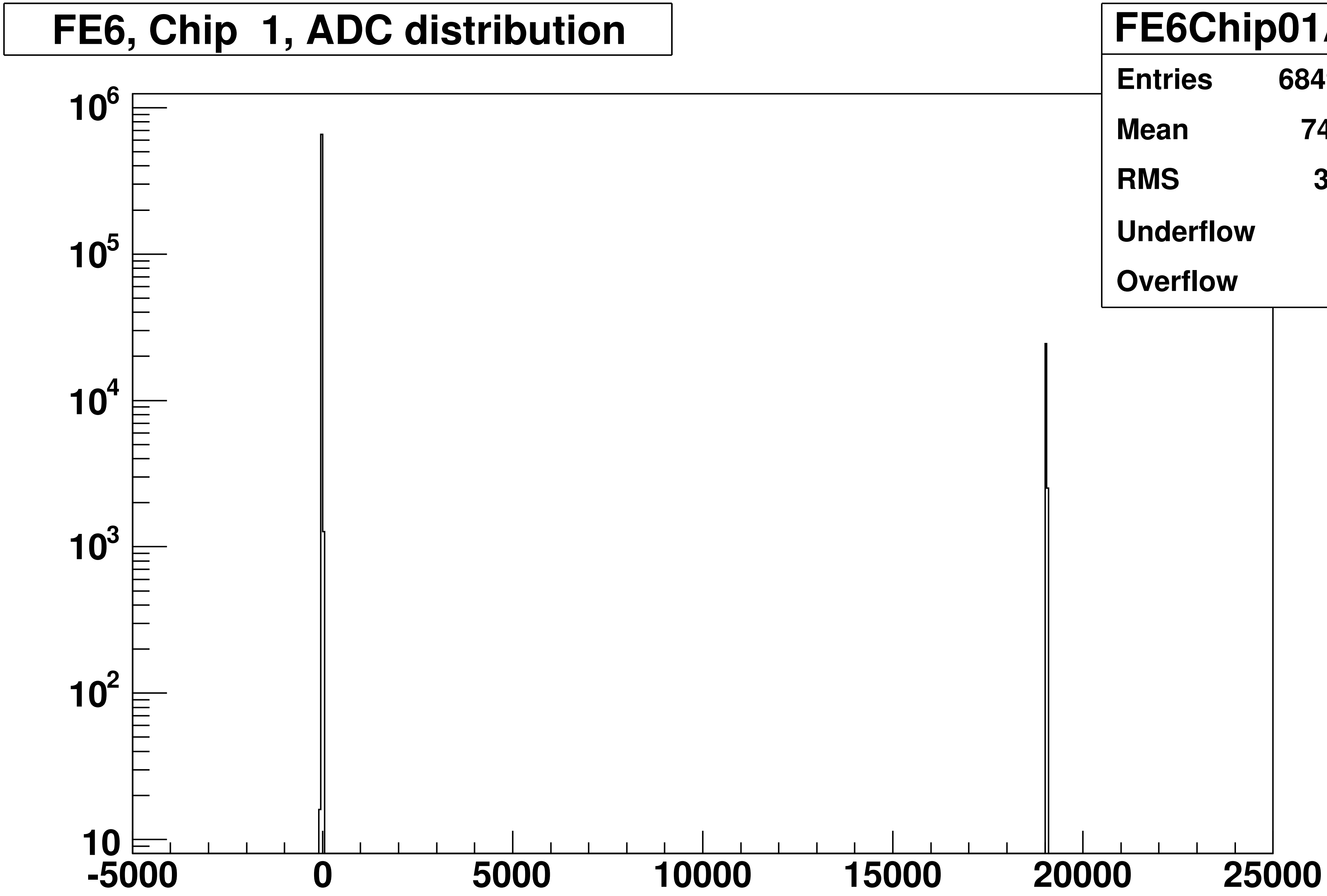


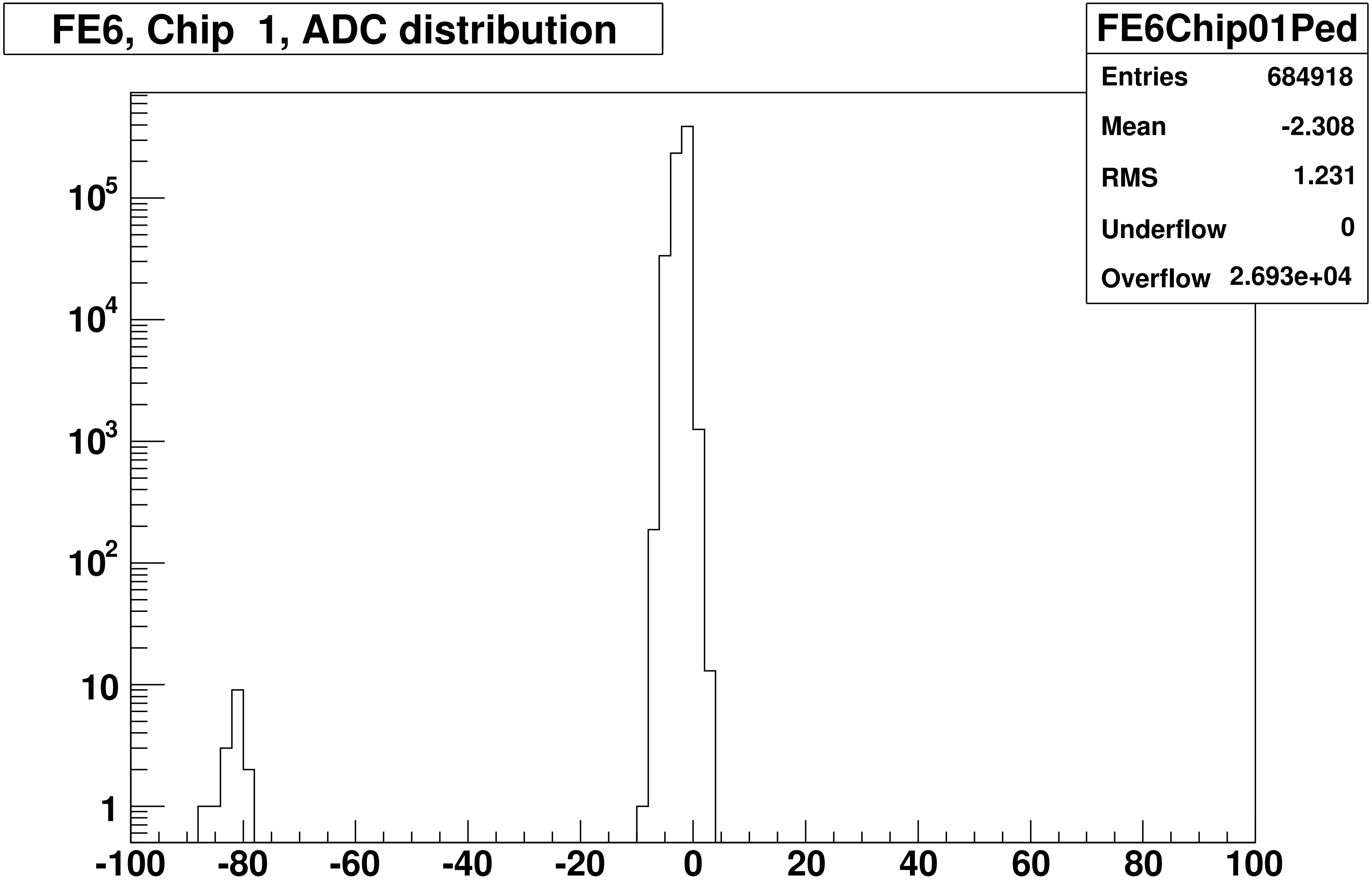
FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 684918
Mean 1.929e+04
RMS 4.695
Underflow 6.609e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 684918
Mean 735.1
RMS 3703
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

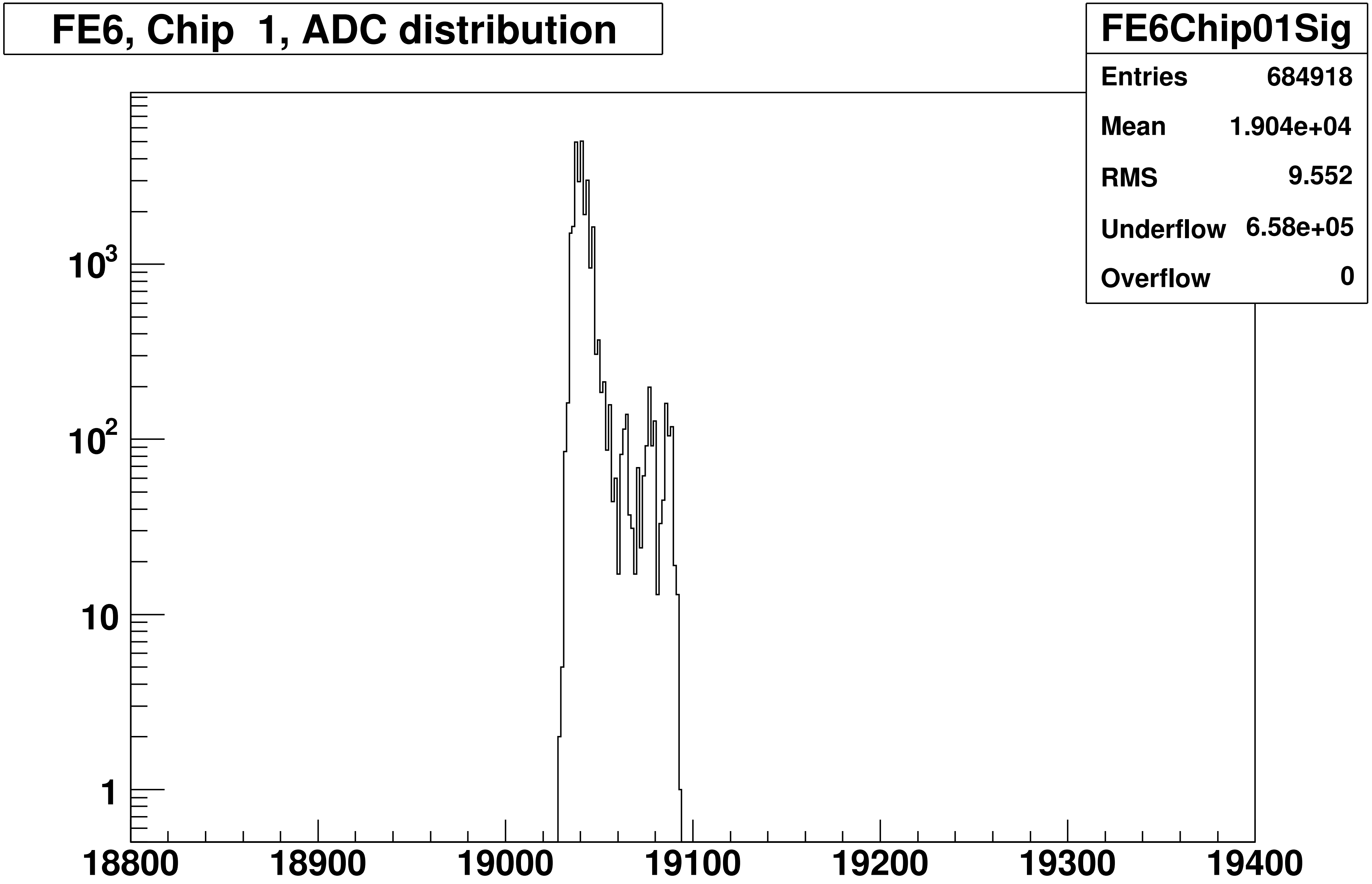

FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 684918
Mean -13.93
RMS 1.373
Underflow 0
Overflow 2.693e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

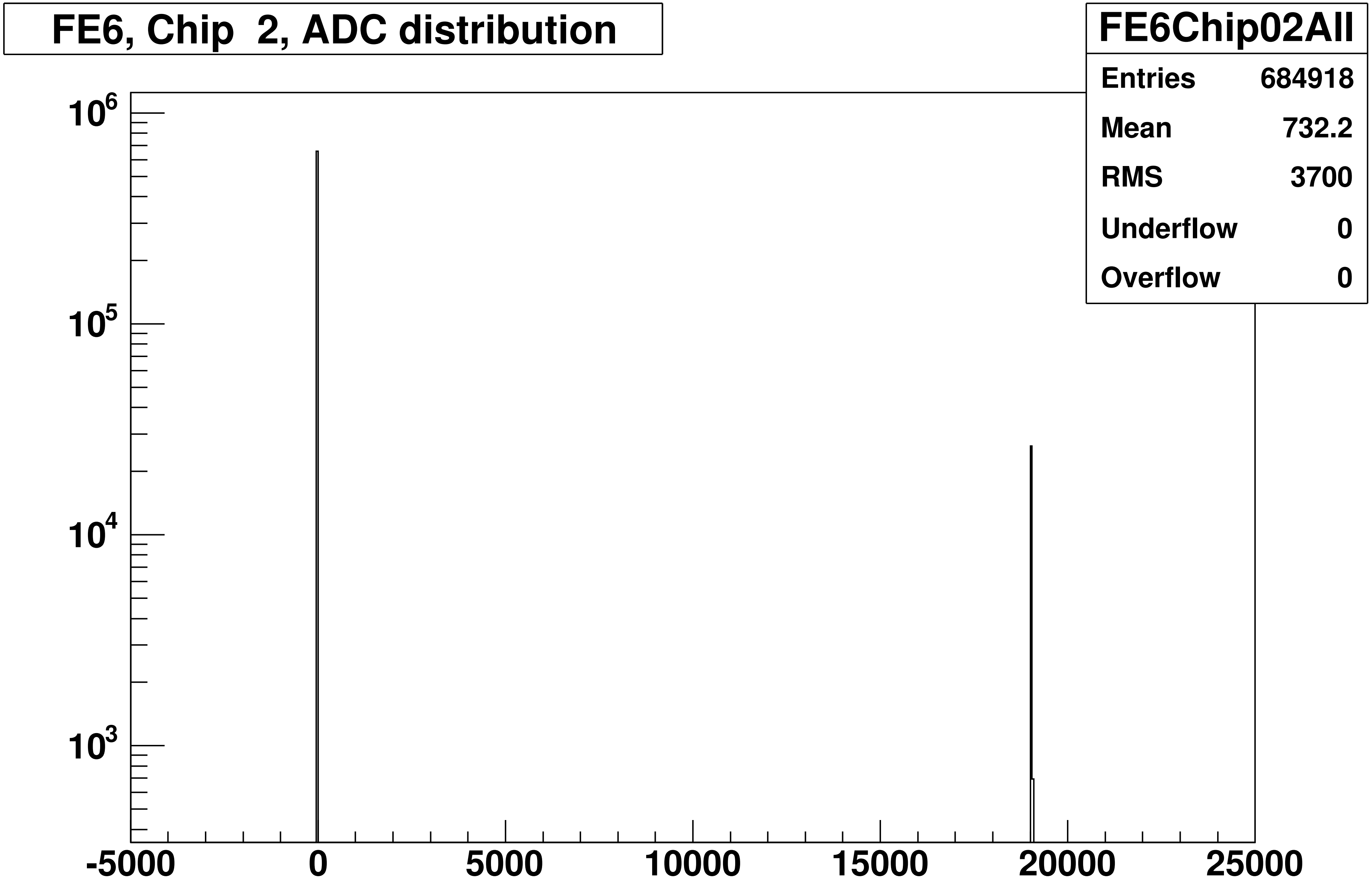

FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 684918
Mean 1.904e+04
RMS 9.244
Underflow 6.58e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

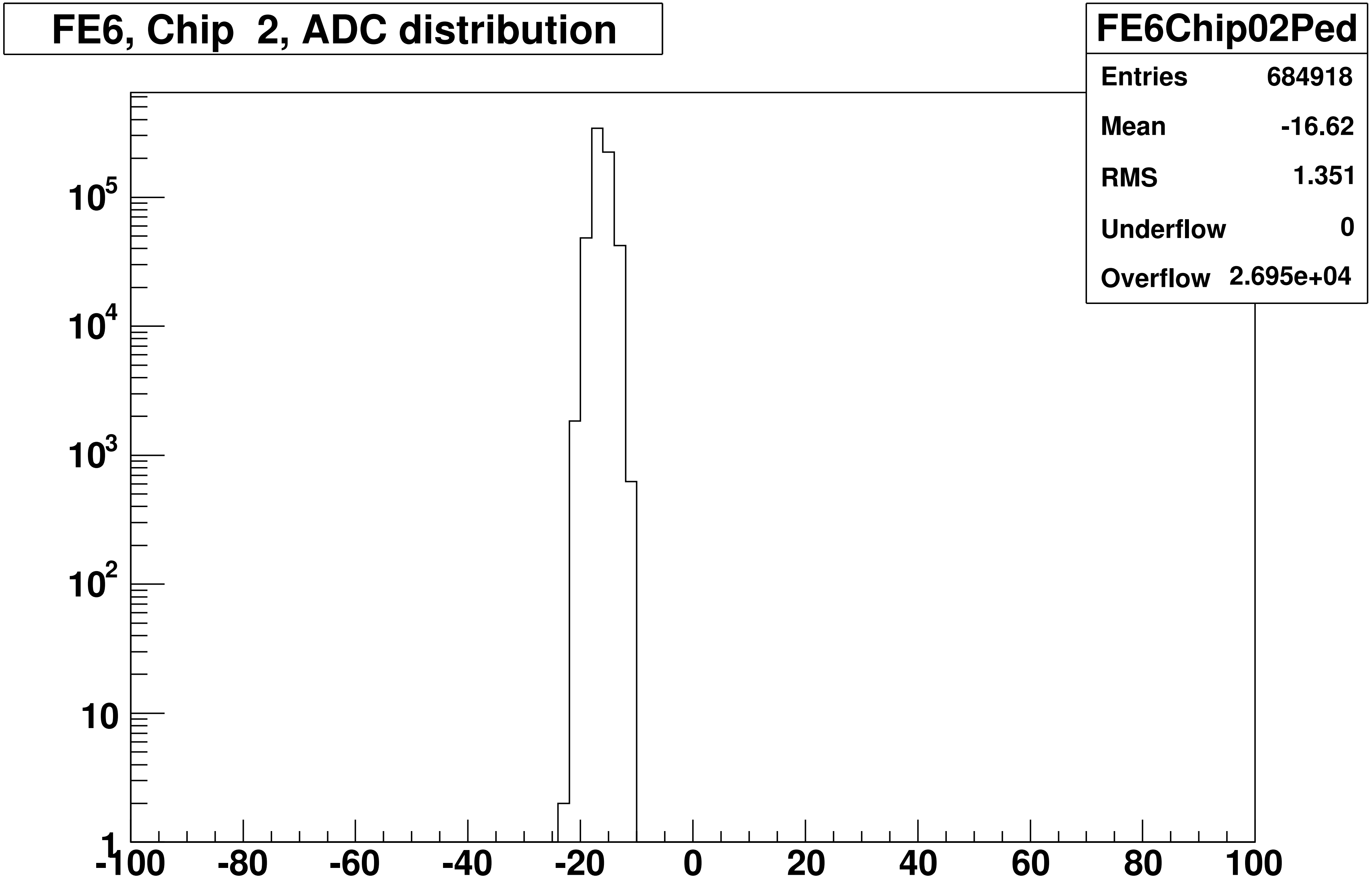

FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 684918
Mean 746.5
RMS 3701
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

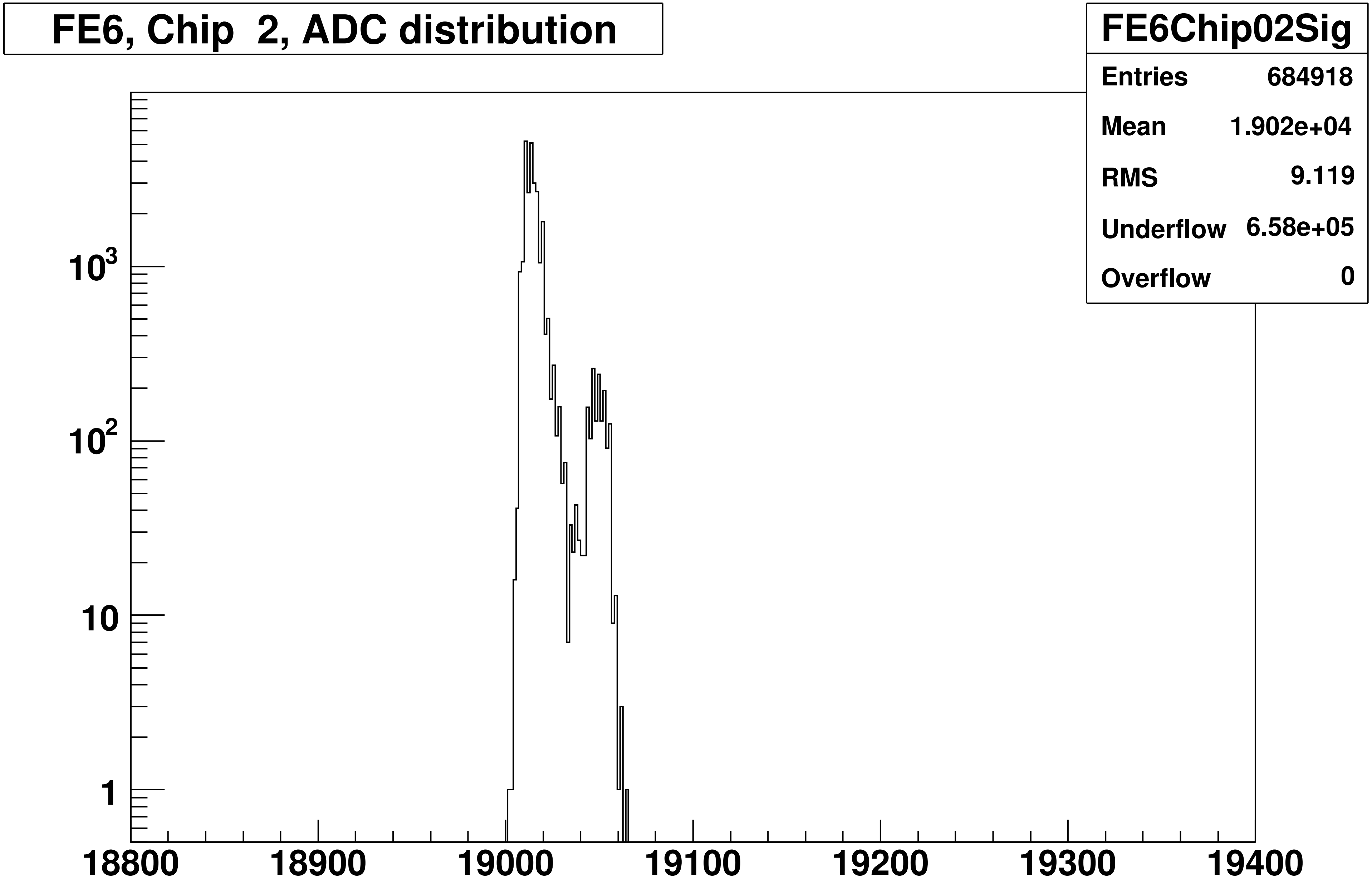


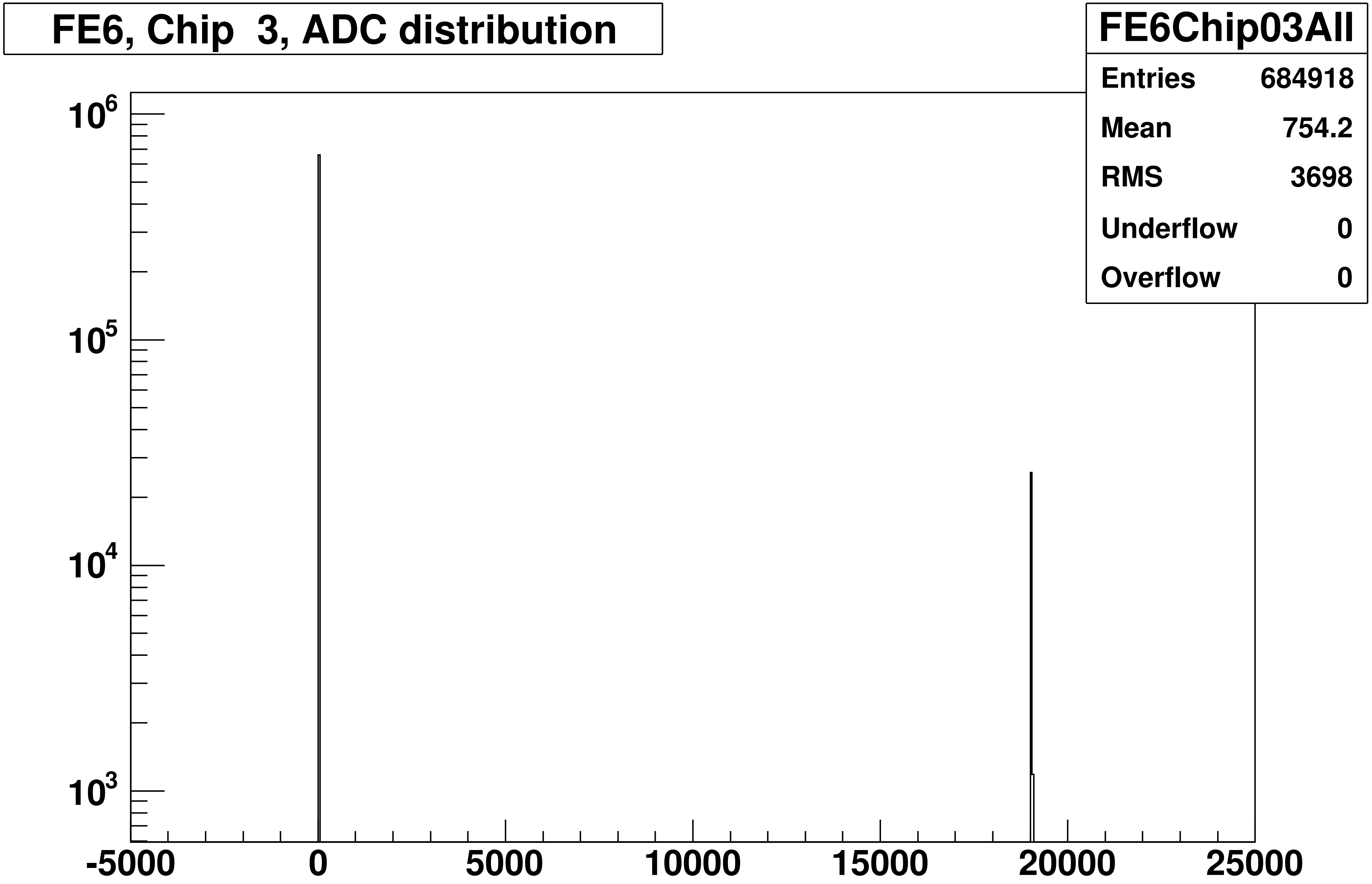
FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 684918
Mean -2.308
RMS 1.231
Underflow 0
Overflow 2.693e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

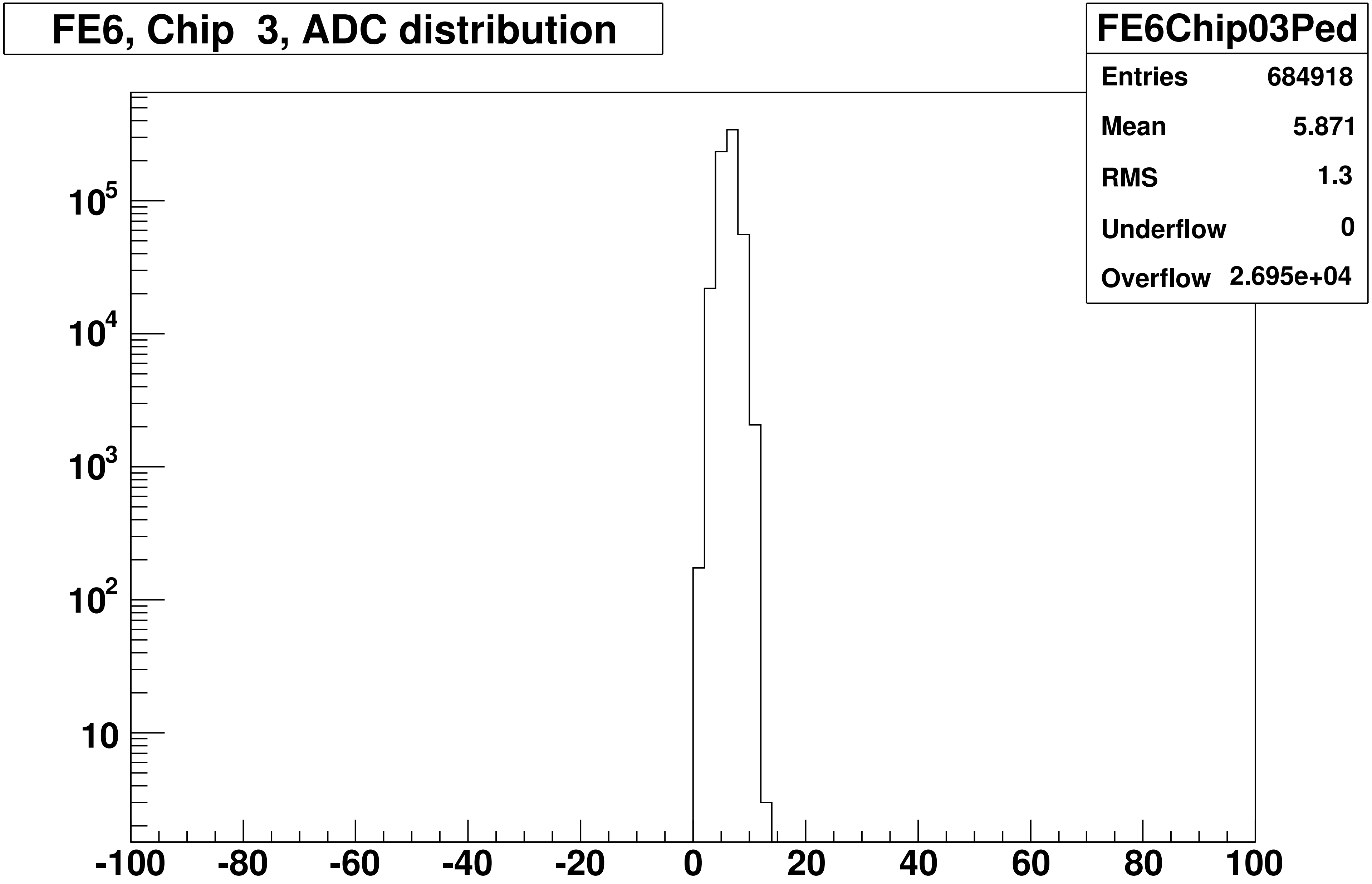

FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 684918
Mean 1.904e+04
RMS 9.552
Underflow 6.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

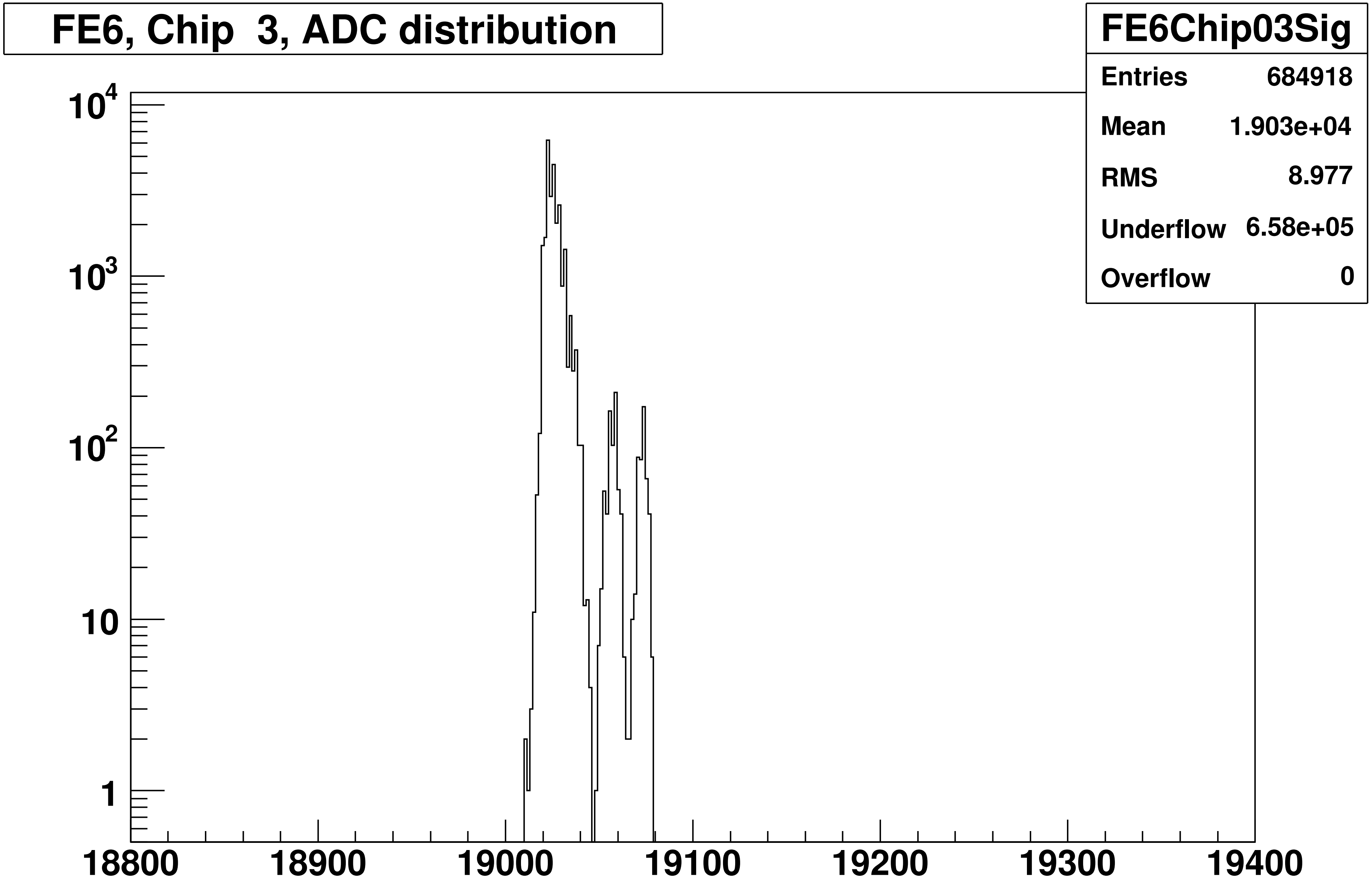

FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 684918
Mean 732.2
RMS 3700
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

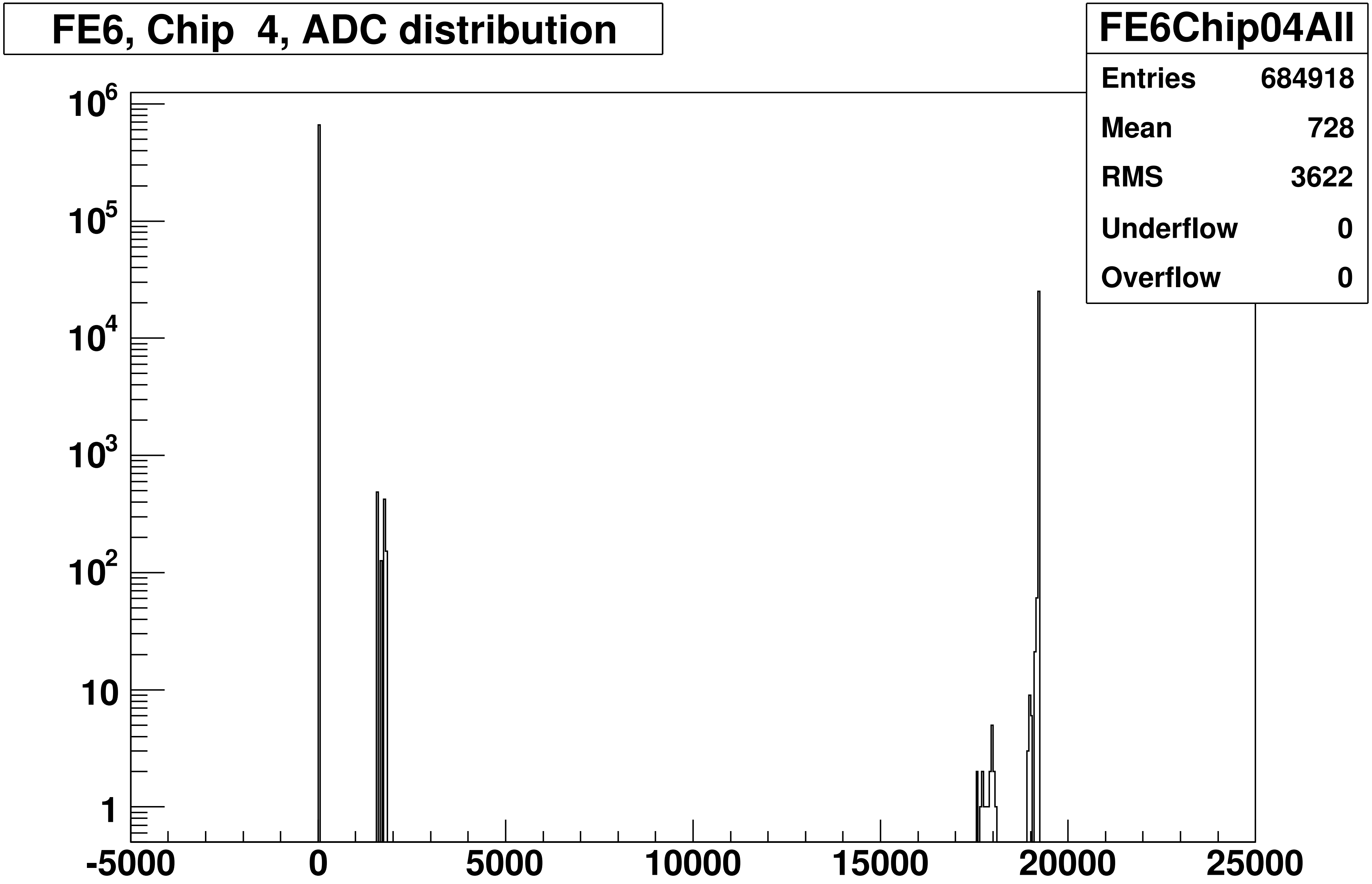

FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 684918
Mean -16.62
RMS 1.351
Underflow 0
Overflow 2.695e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

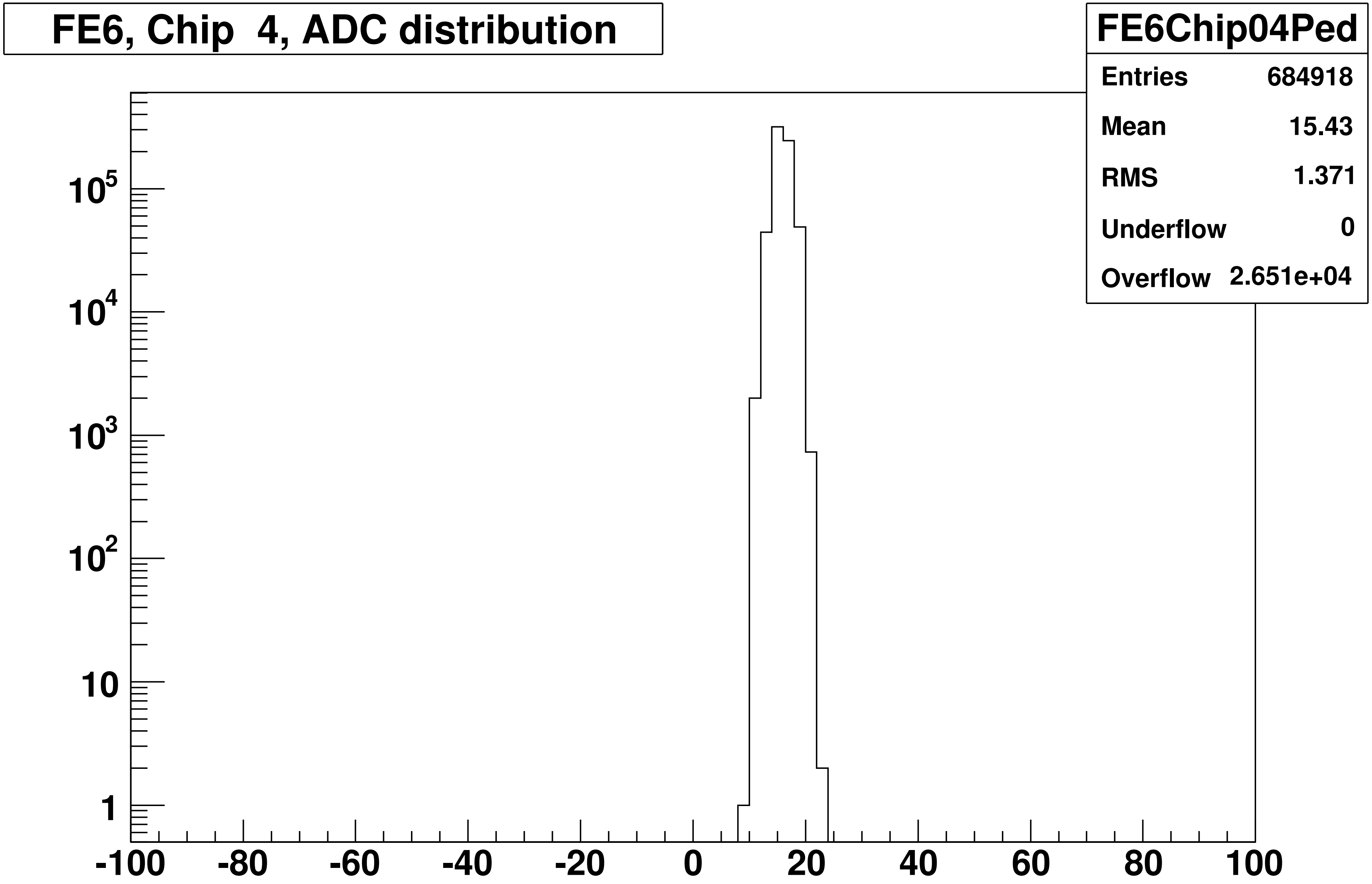

FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 684918
Mean 1.902e+04
RMS 9.119
Underflow 6.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

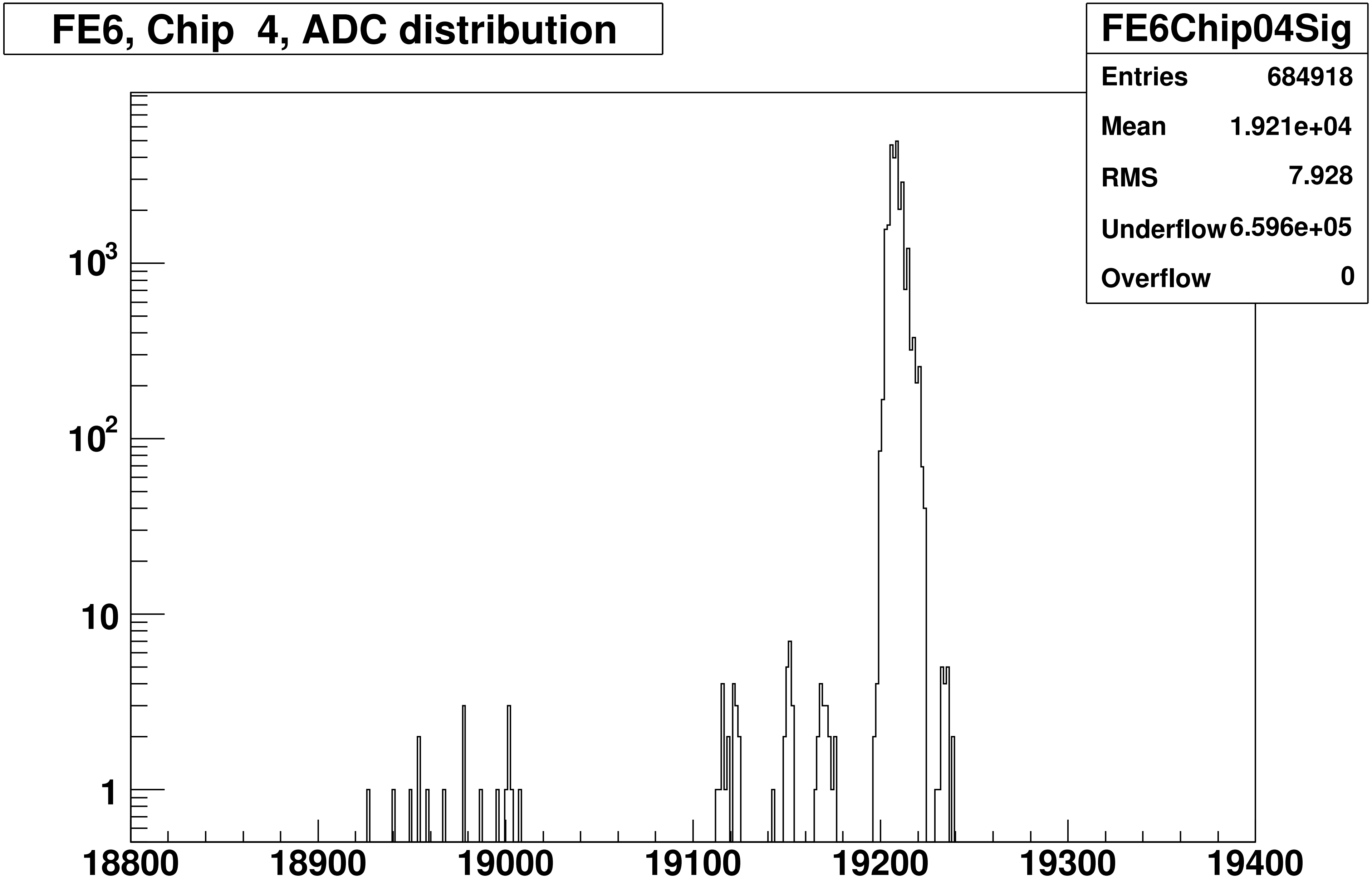

FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 684918
Mean 754.2
RMS 3698
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

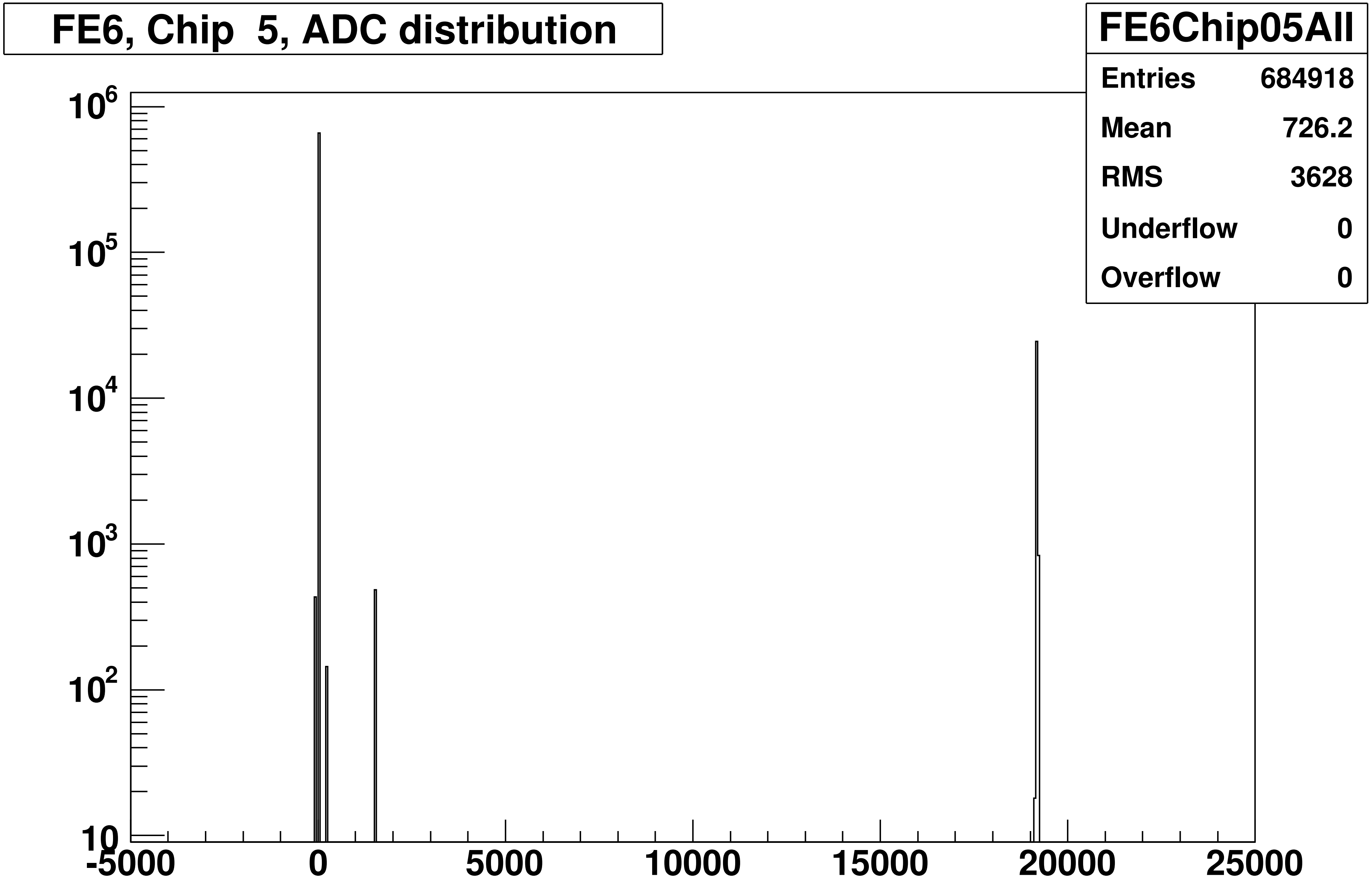

FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 684918
Mean 5.871
RMS 1.3
Underflow 0
Overflow 2.695e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

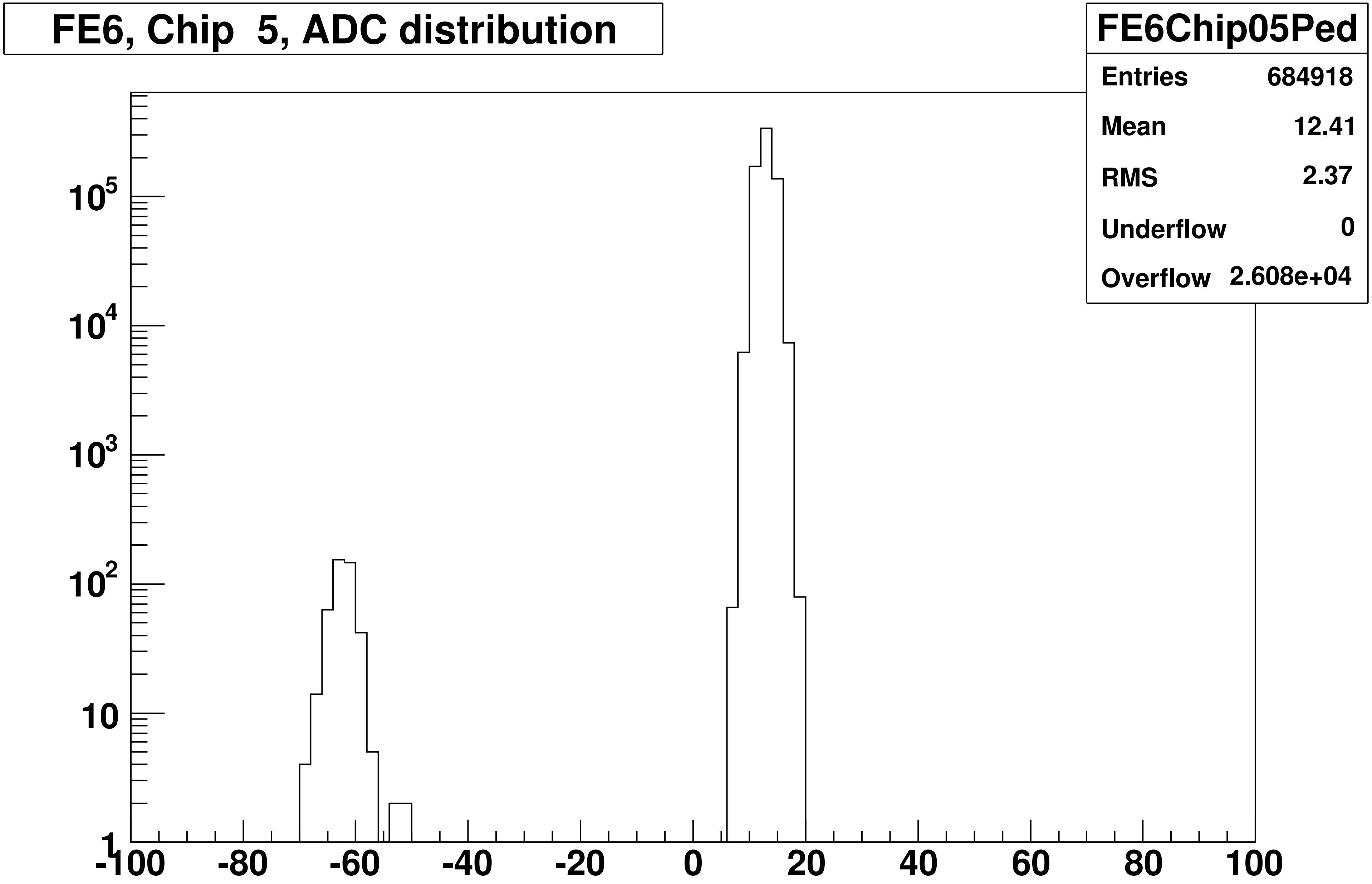

FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 684918
Mean 1.903e+04
RMS 8.977
Underflow 6.58e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

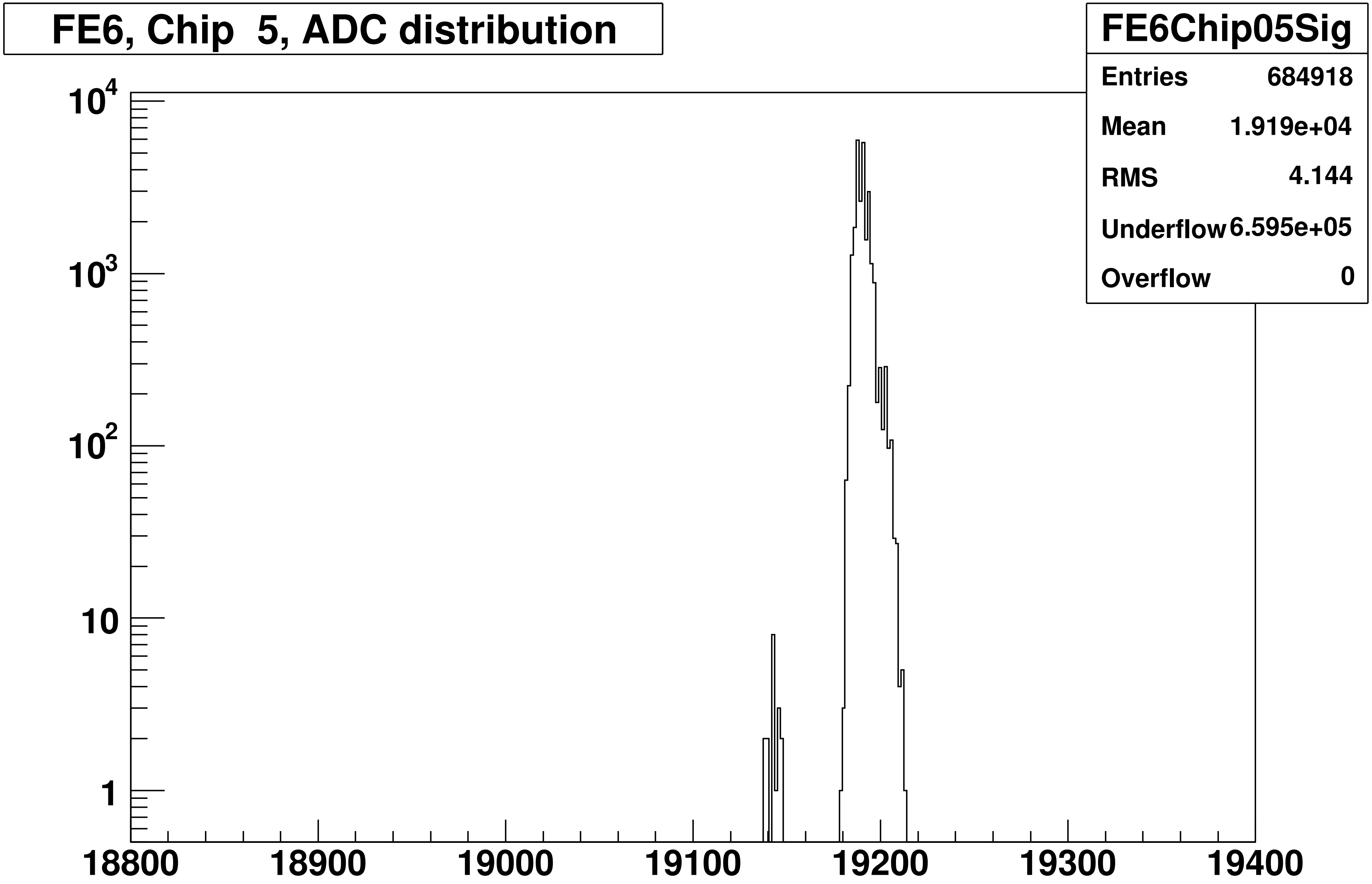

FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 684918
Mean 728
RMS 3622
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

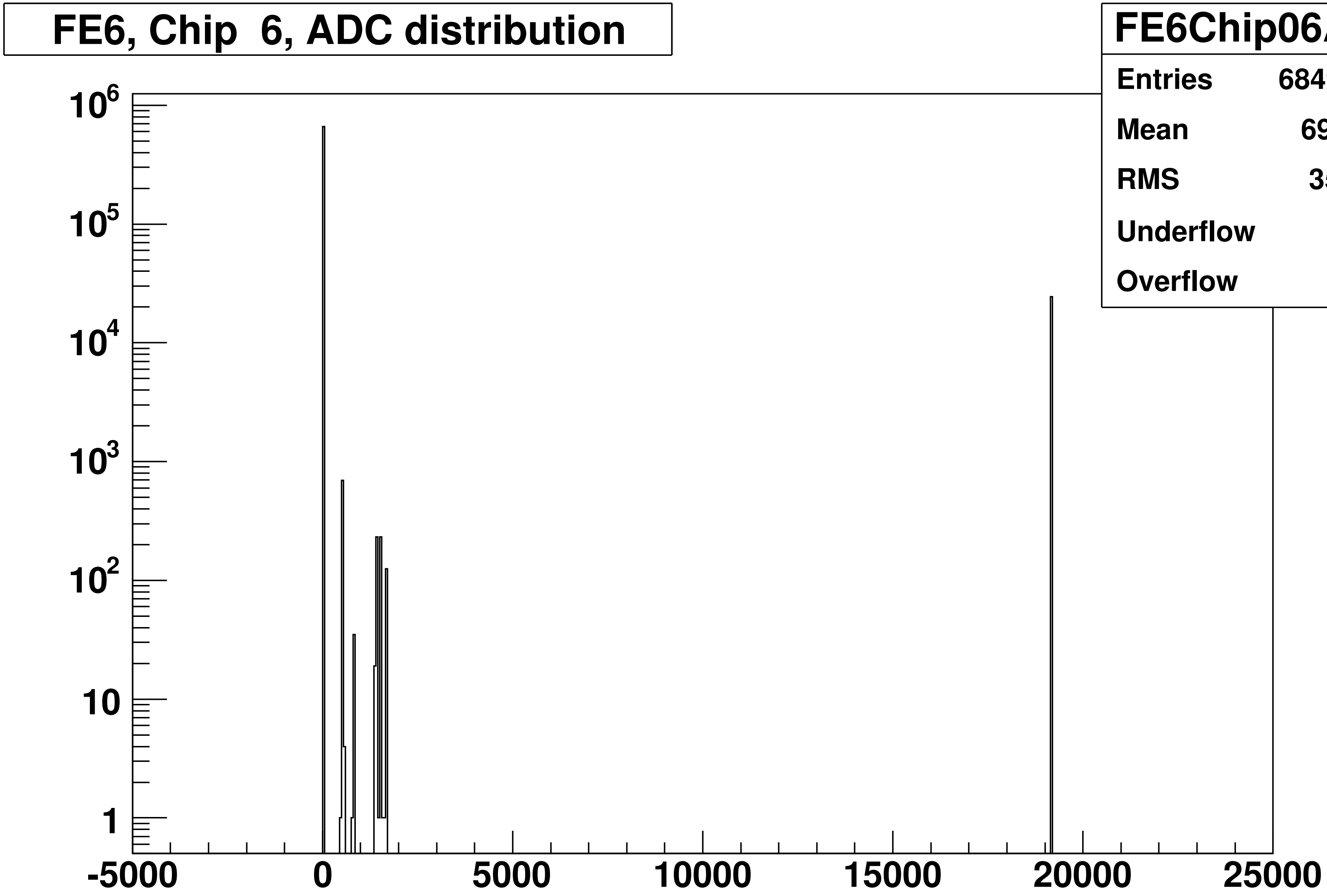

FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 684918
Mean 15.43
RMS 1.371
Underflow 0
Overflow 2.651e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

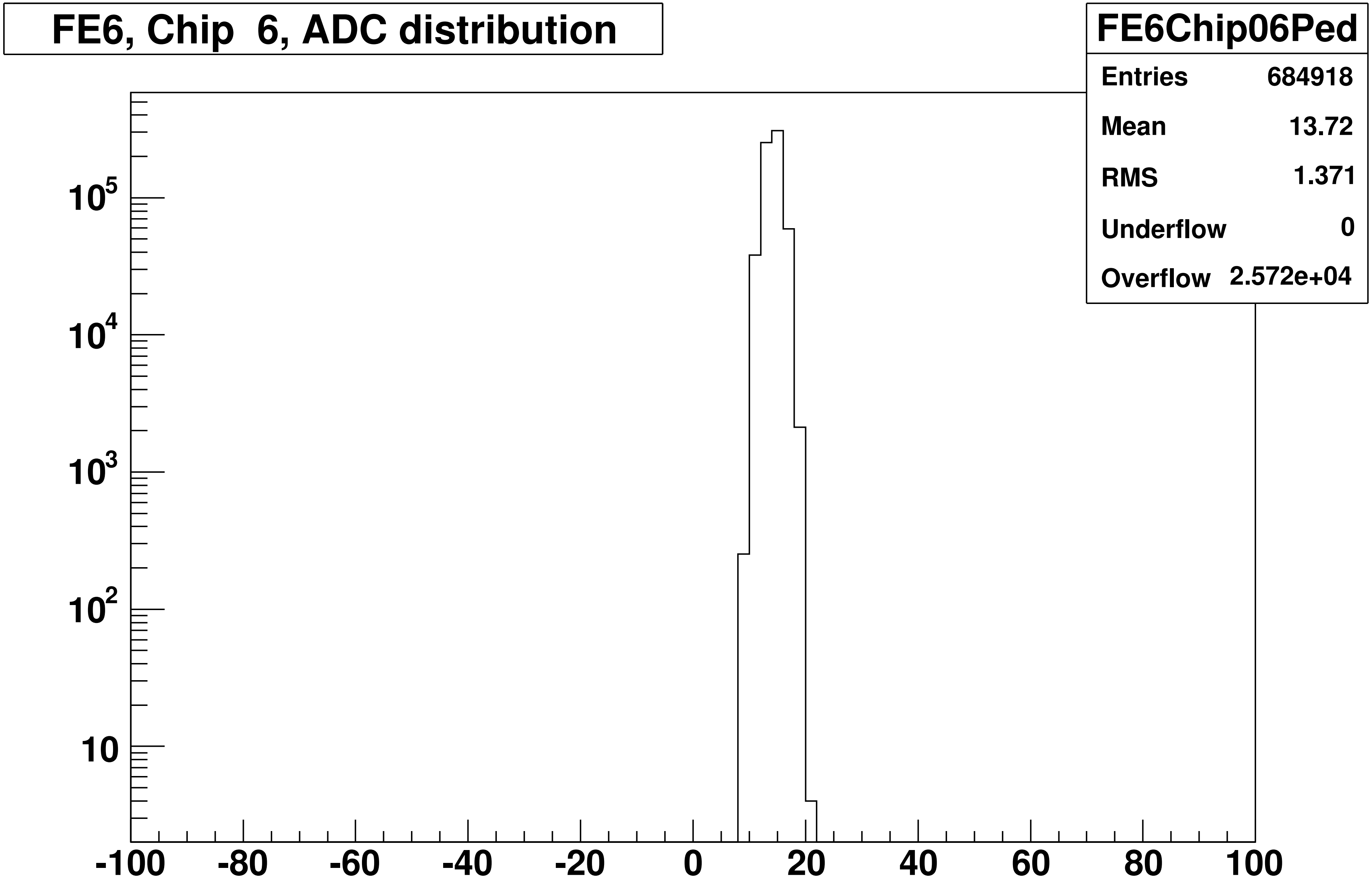

FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 684918
Mean 1.921e+04
RMS 7.928
Underflow 6.596e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

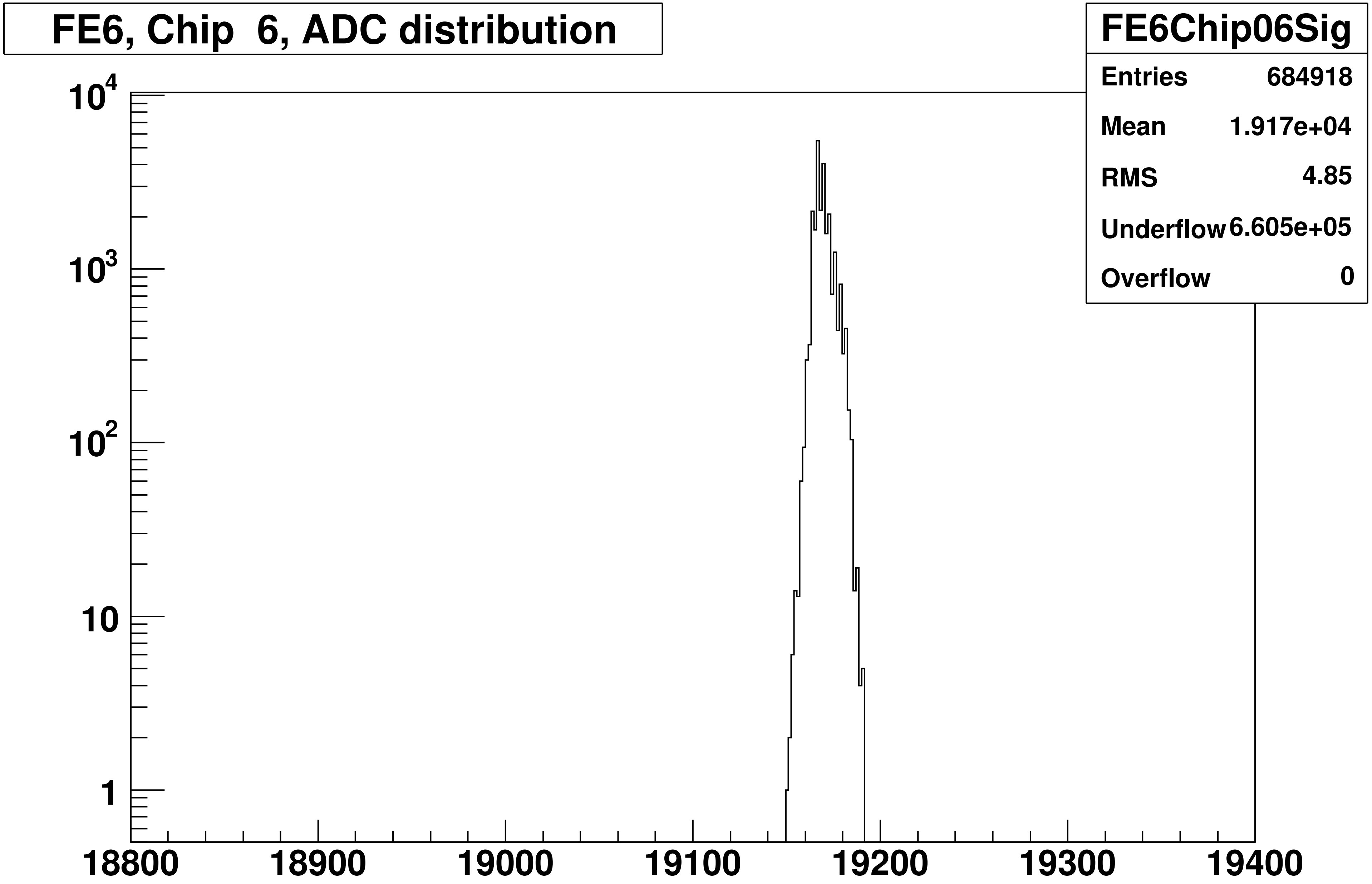

FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 684918
Mean 726.2
RMS 3628
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

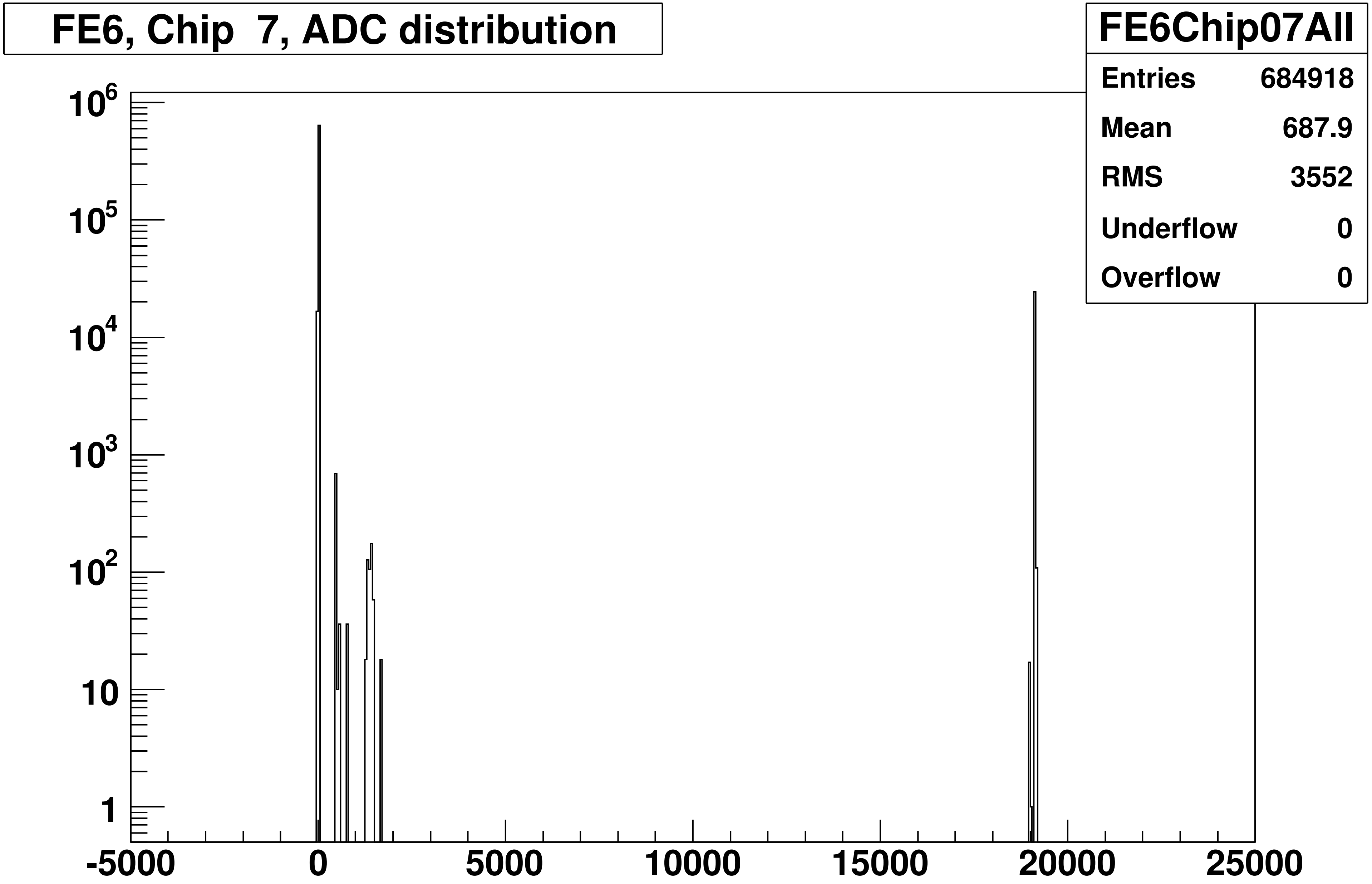

FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 684918
Mean 12.41
RMS 2.37
Underflow 0
Overflow 2.608e+04
1
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

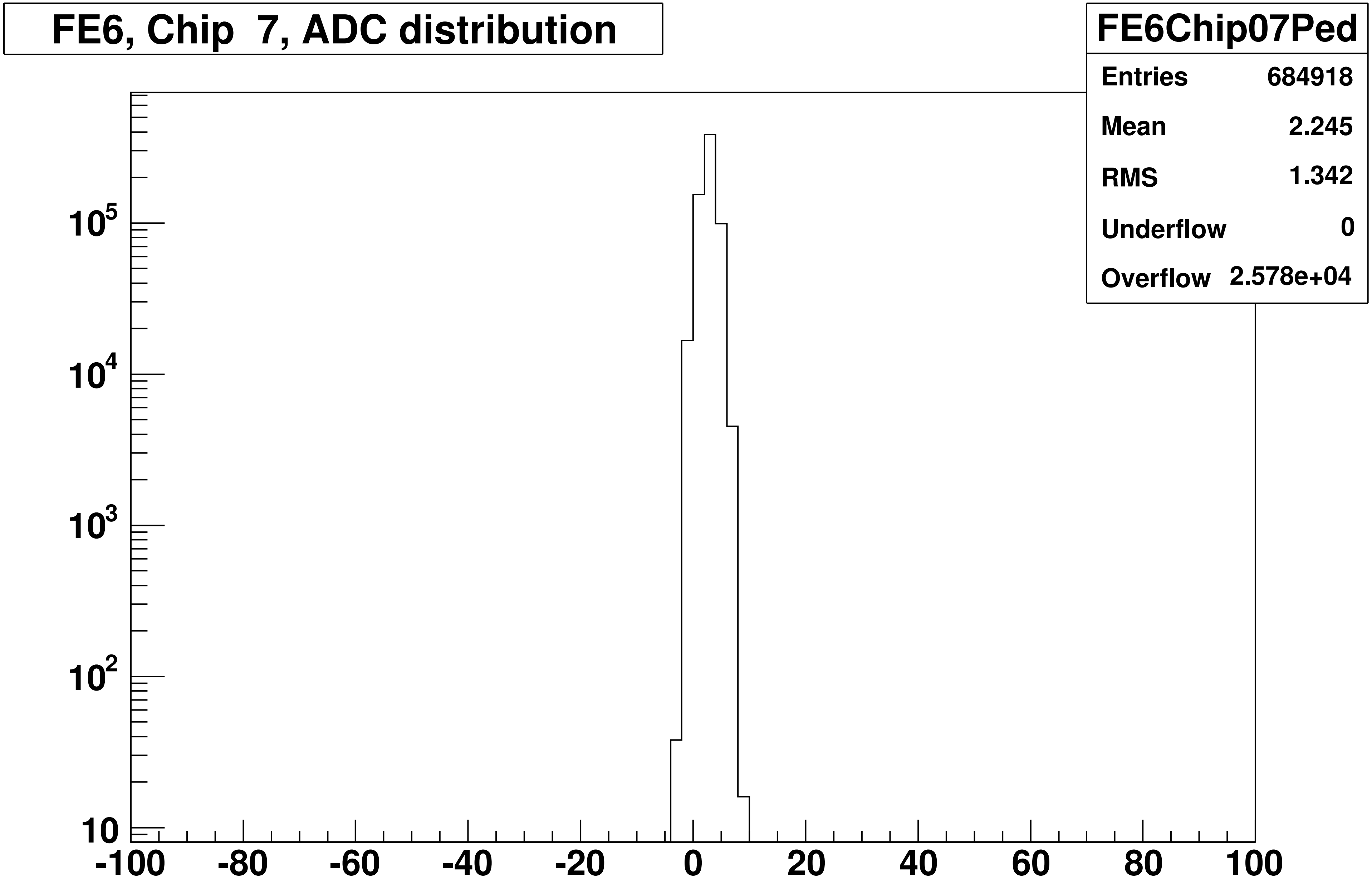

FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 684918
Mean 1.919e+04
RMS 4.144
Underflow 6.595e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

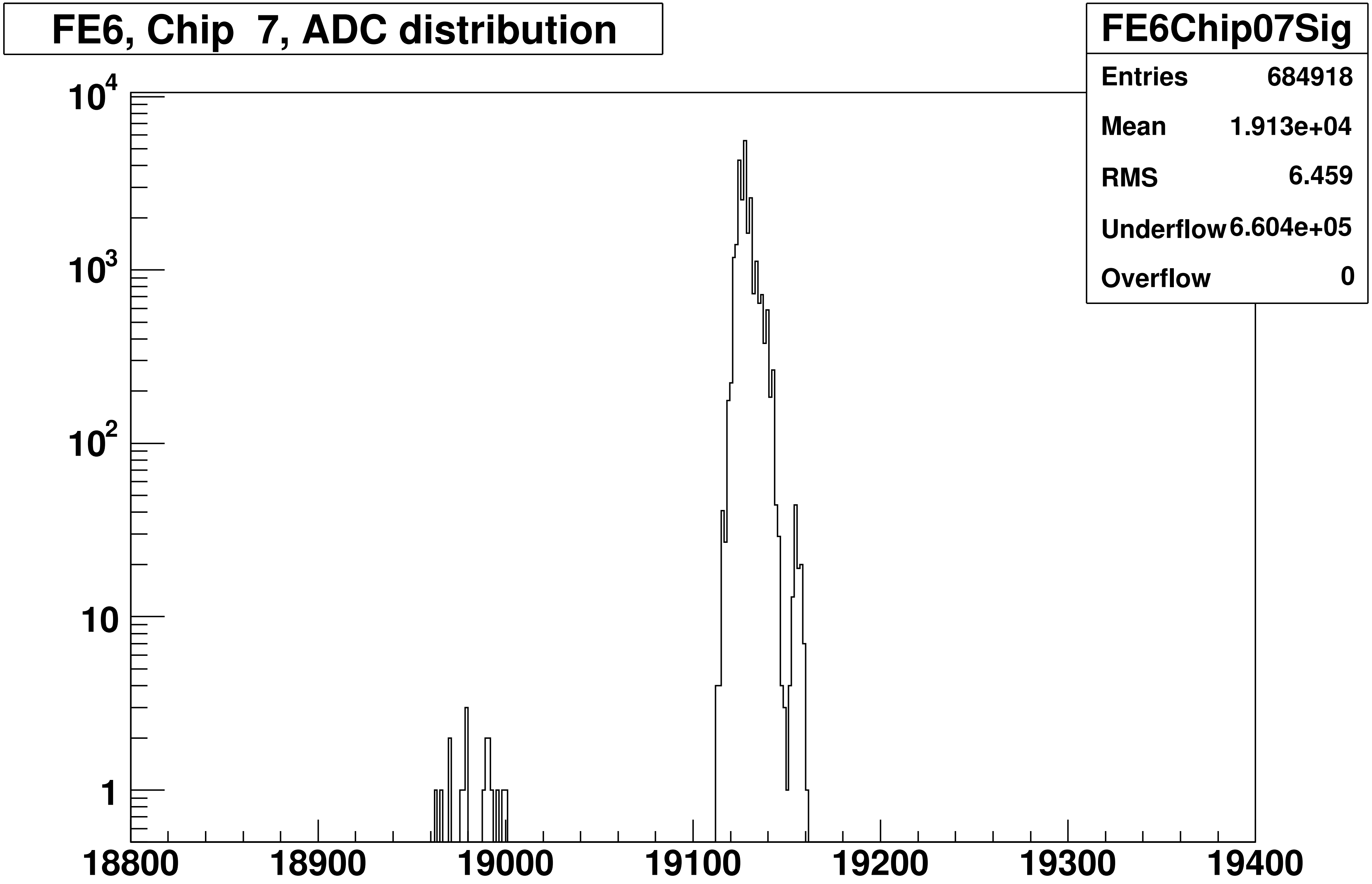

FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 684918
Mean 697.3
RMS 3549
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 684918
Mean 13.72
RMS 1.371
Underflow 0
Overflow 2.572e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

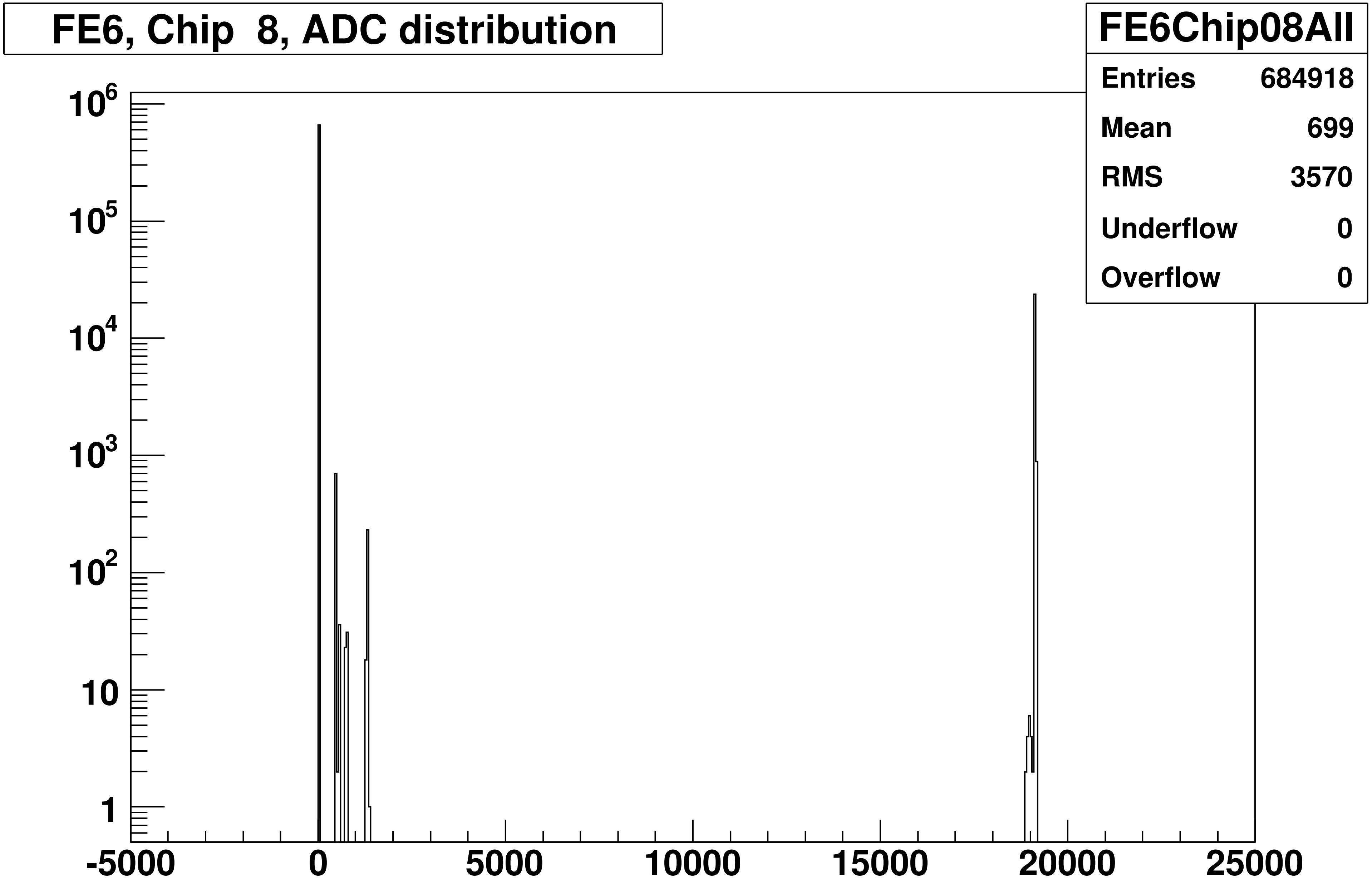

FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 684918
Mean 1.917e+04
RMS 4.85
Underflow 6.605e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 684918
Mean 687.9
RMS 3552
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

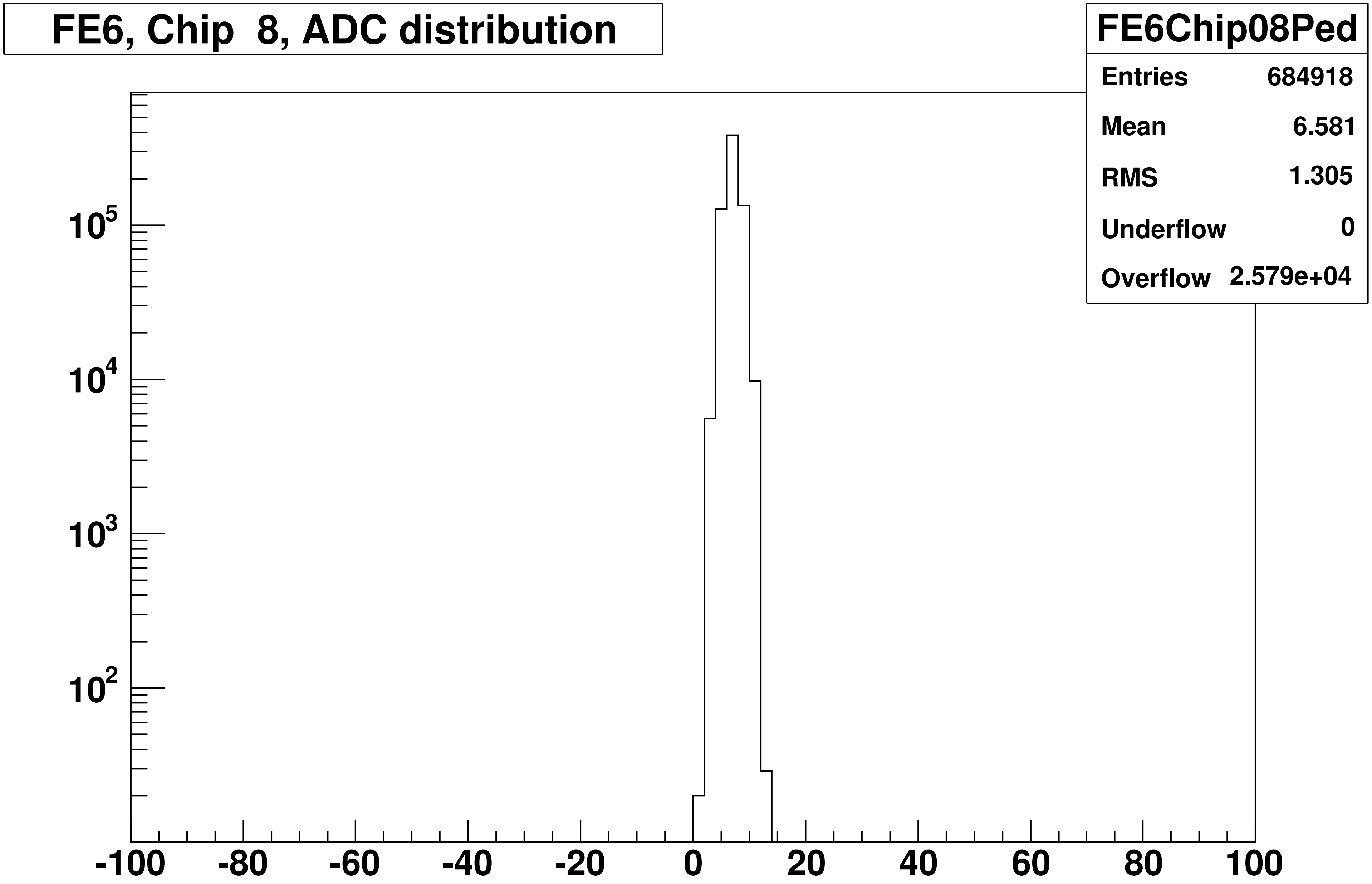

FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 684918
Mean 2.245
RMS 1.342
Underflow 0
Overflow 2.578e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

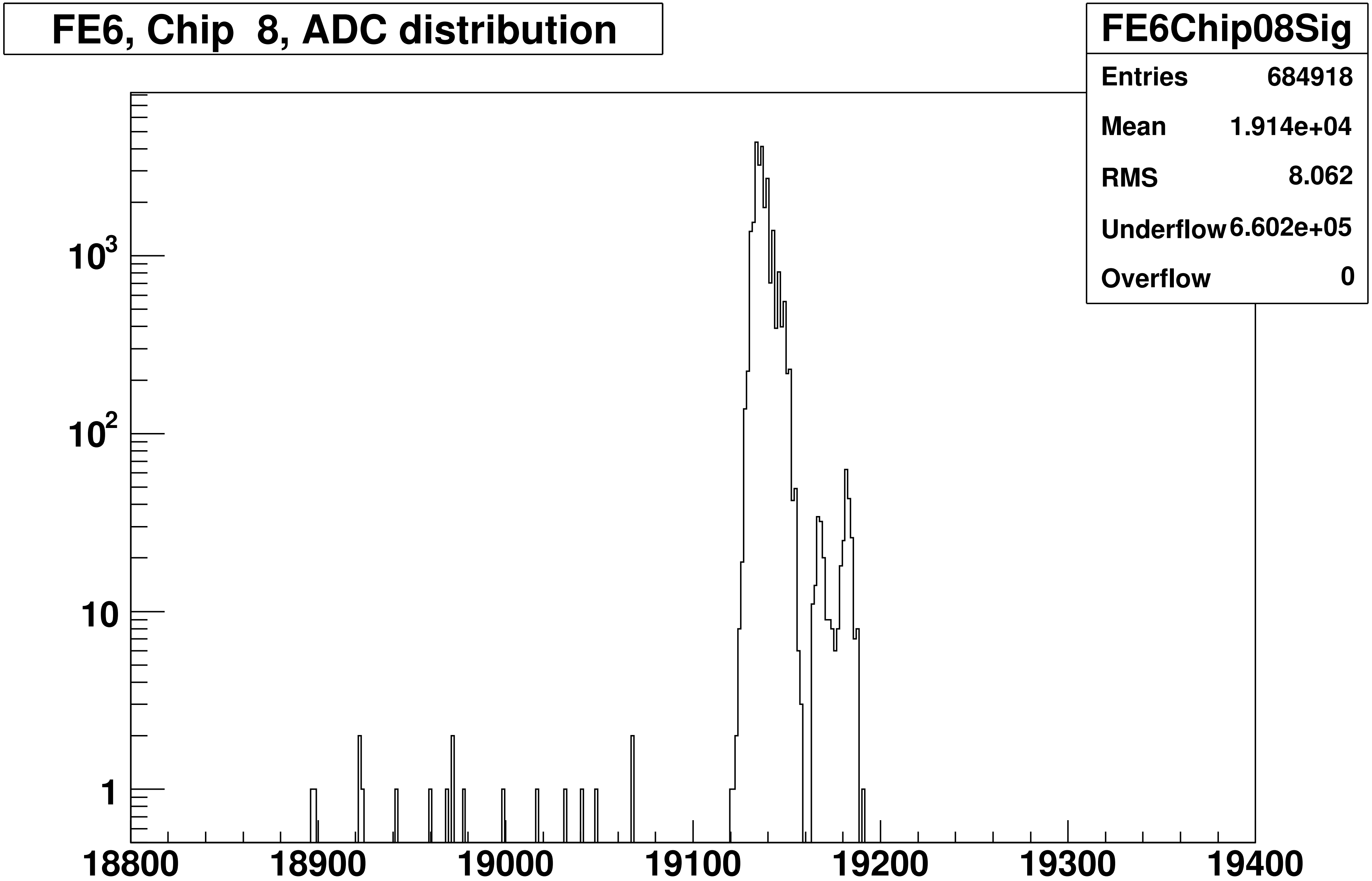


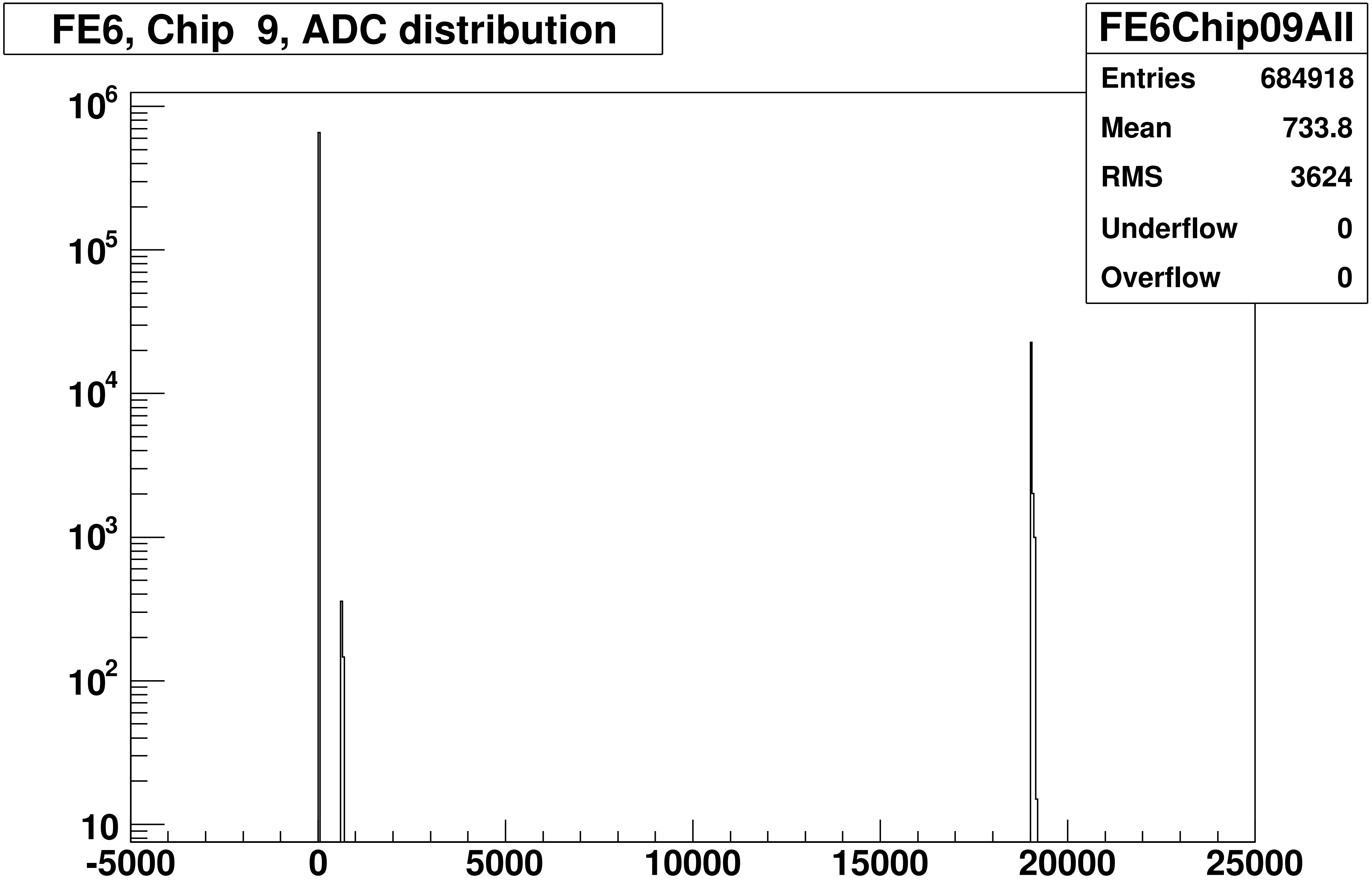
FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 684918
Mean 1.913e+04
RMS 6.459
Underflow 6.604e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



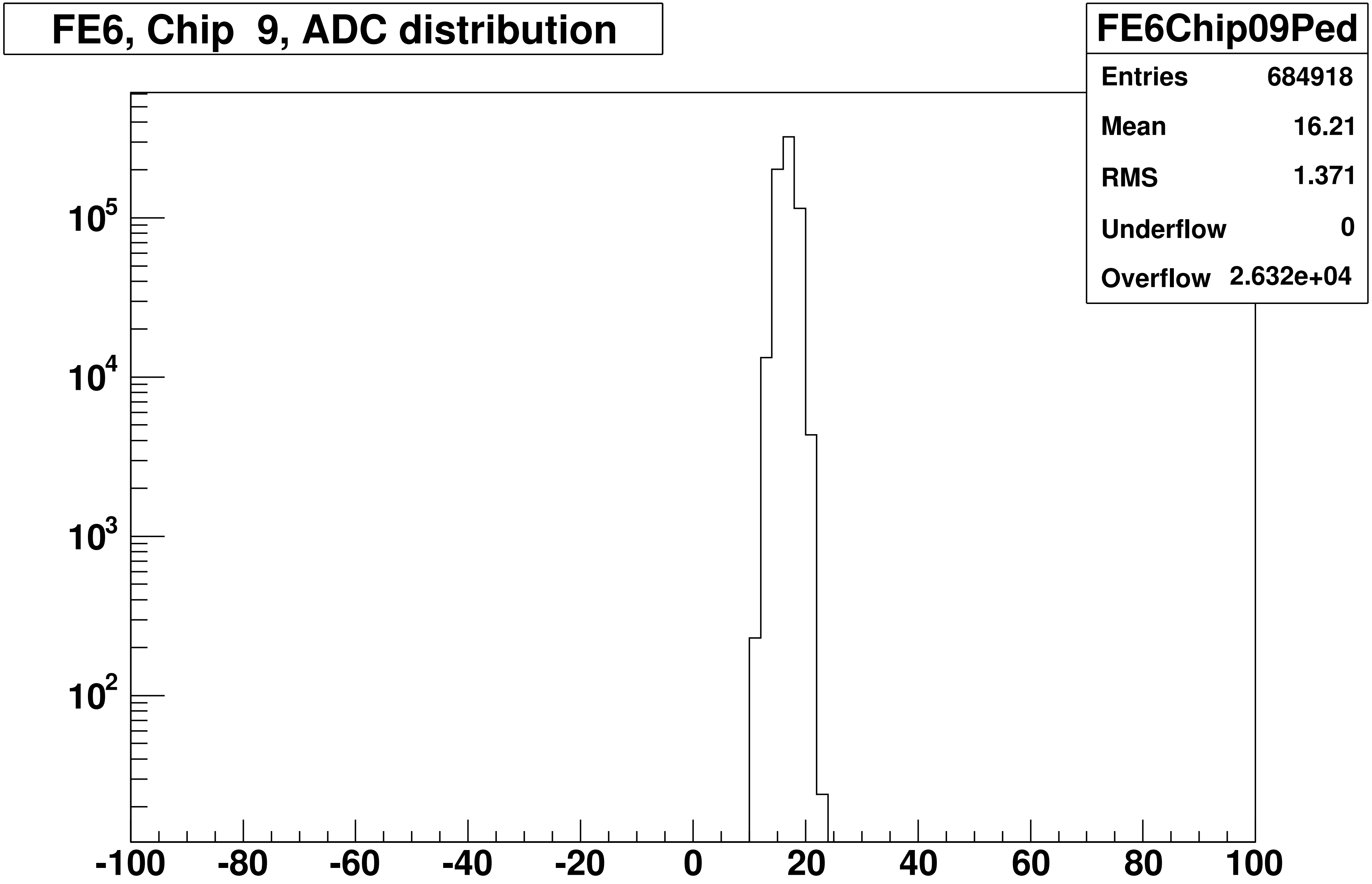
FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 684918
Mean 699
RMS 3570
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

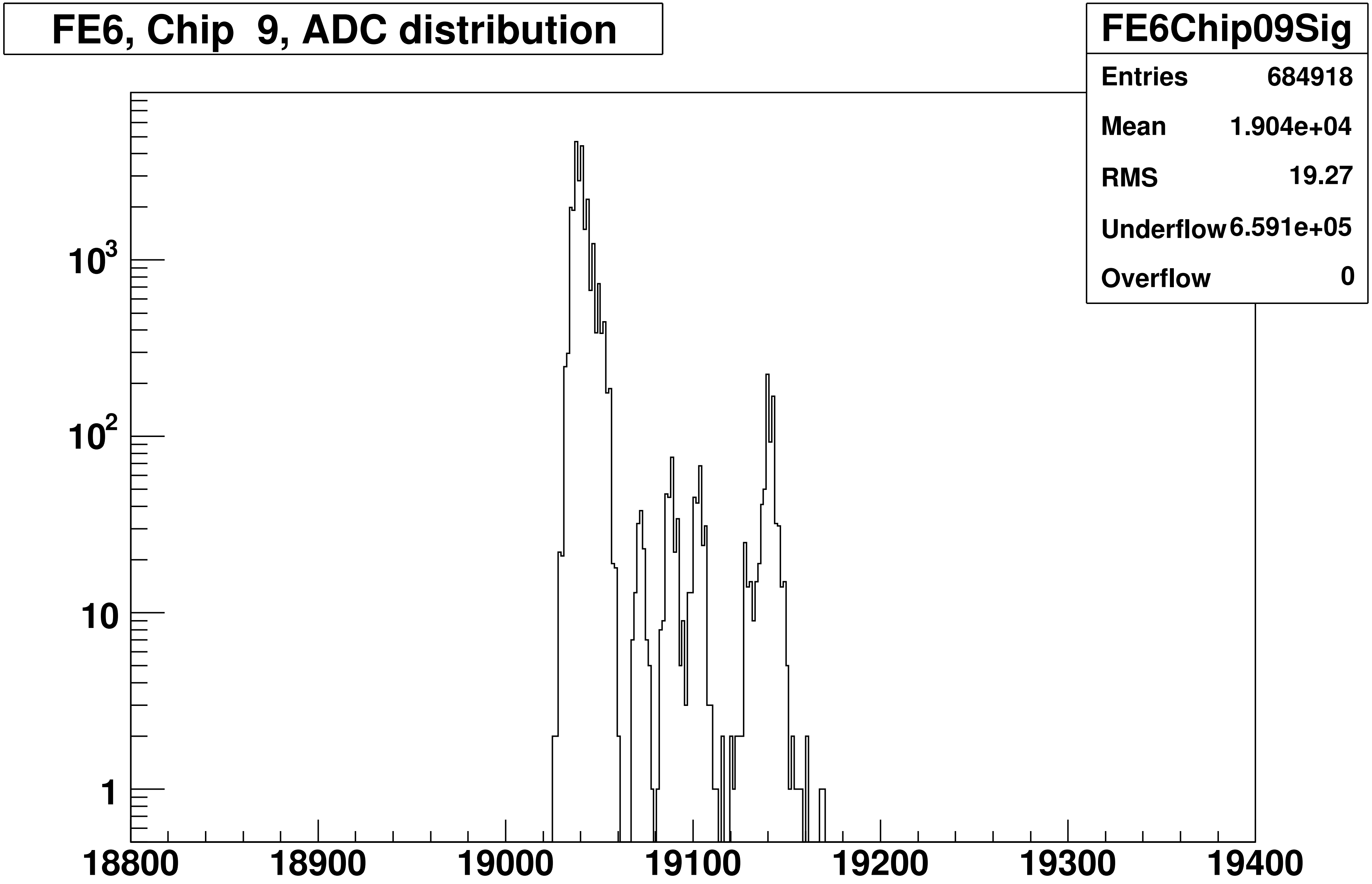

FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 684918
Mean 6.581
RMS 1.305
Underflow 0
Overflow 2.579e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 684918
Mean 1.914e+04
RMS 8.062
Underflow 6.602e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400

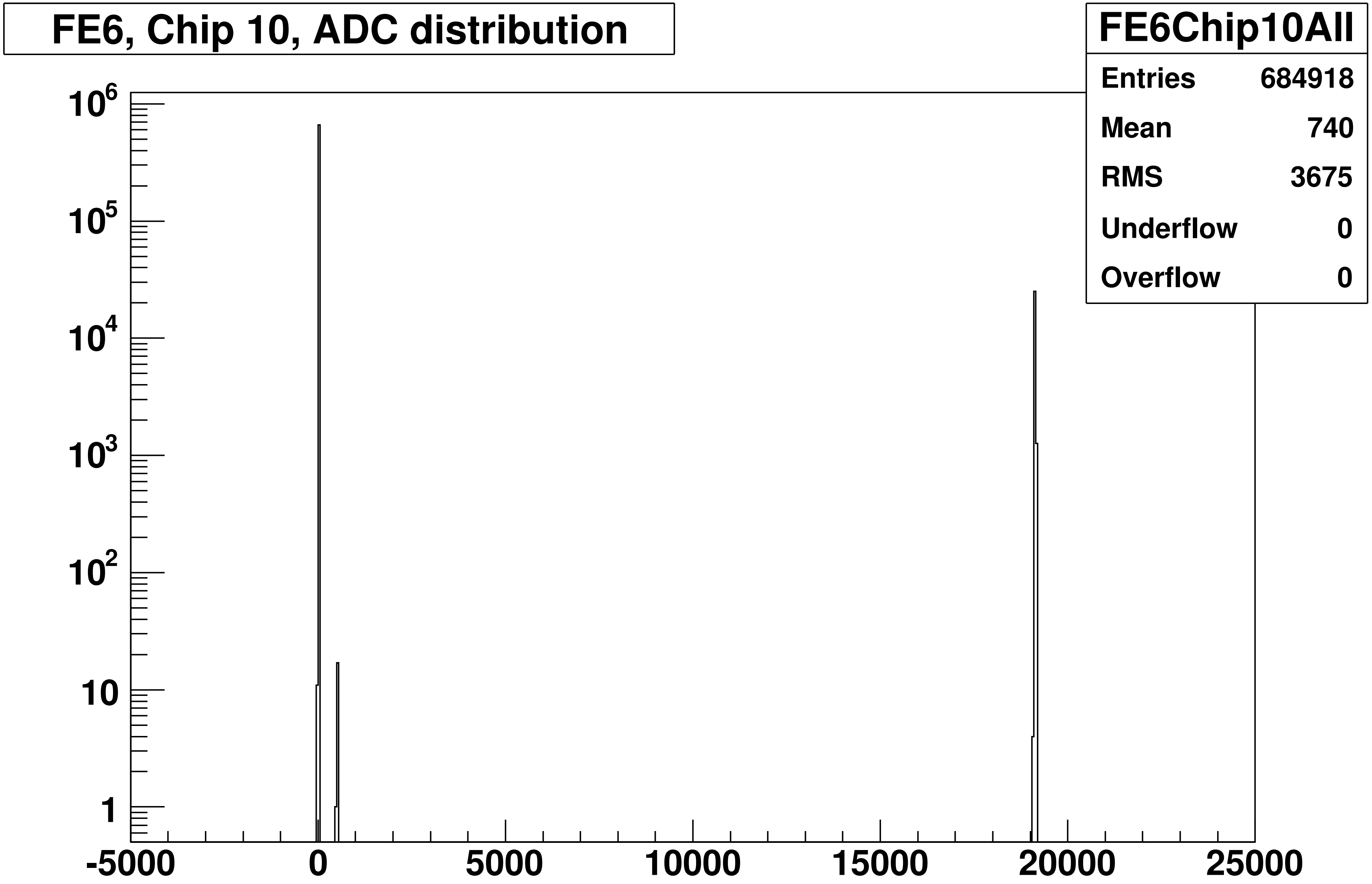


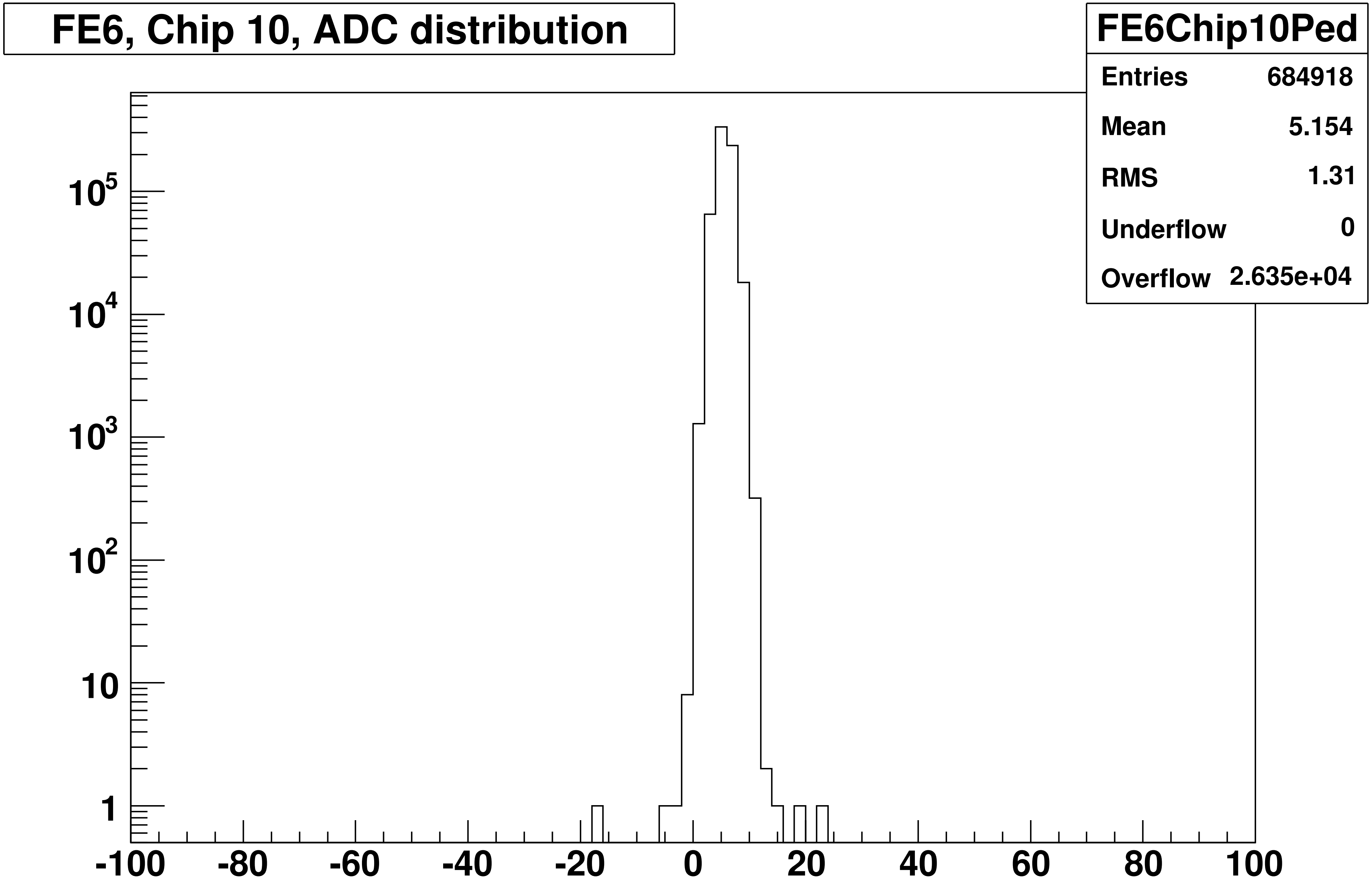
FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 684918
Mean 733.8
RMS 3624
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

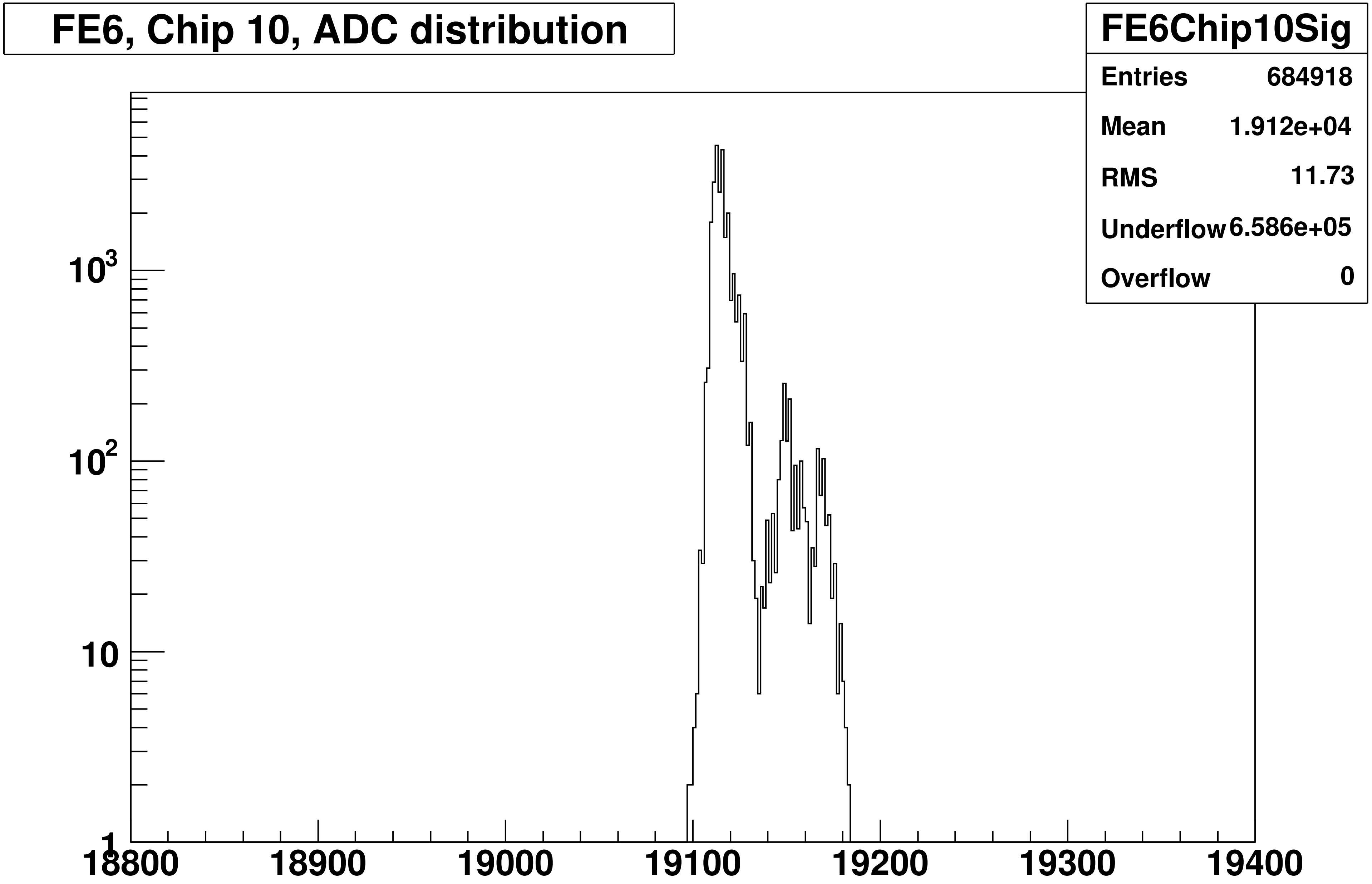

FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 684918
Mean 16.21
RMS 1.371
Underflow 0
Overflow 2.632e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

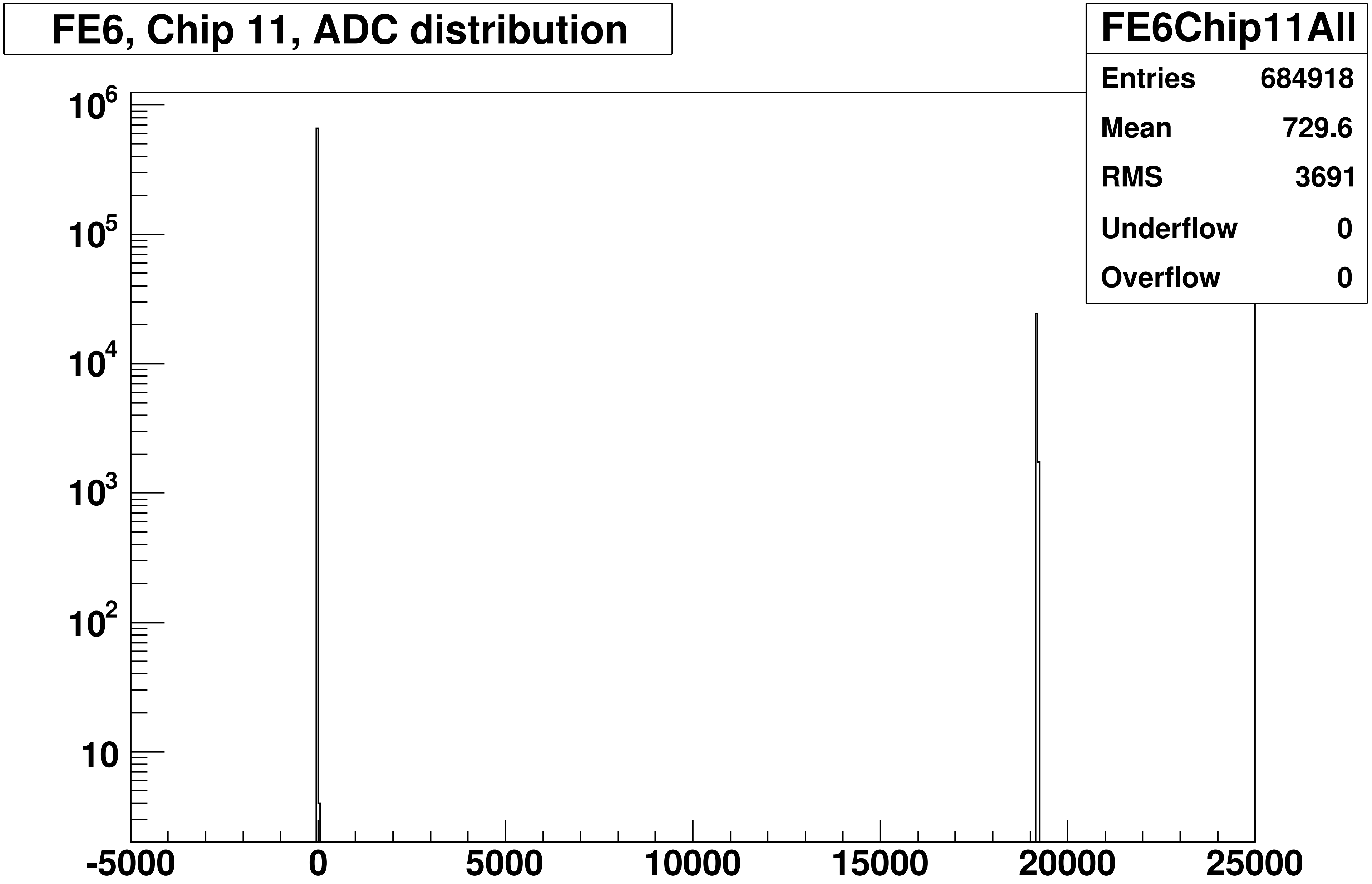


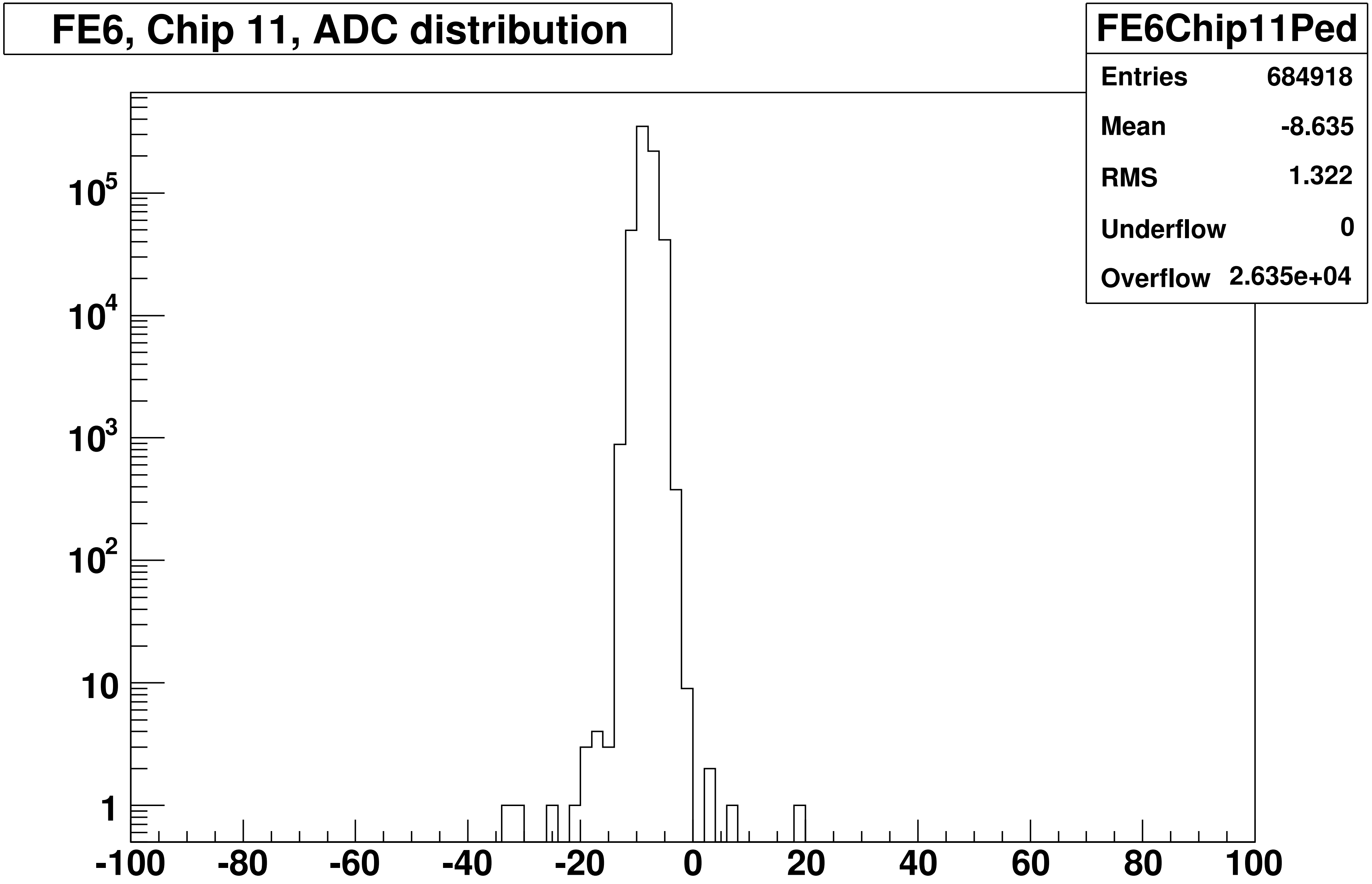
FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 684918
Mean 1.904e+04
RMS 19.27
Underflow 6.591e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 684918
Mean 740
RMS 3675
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

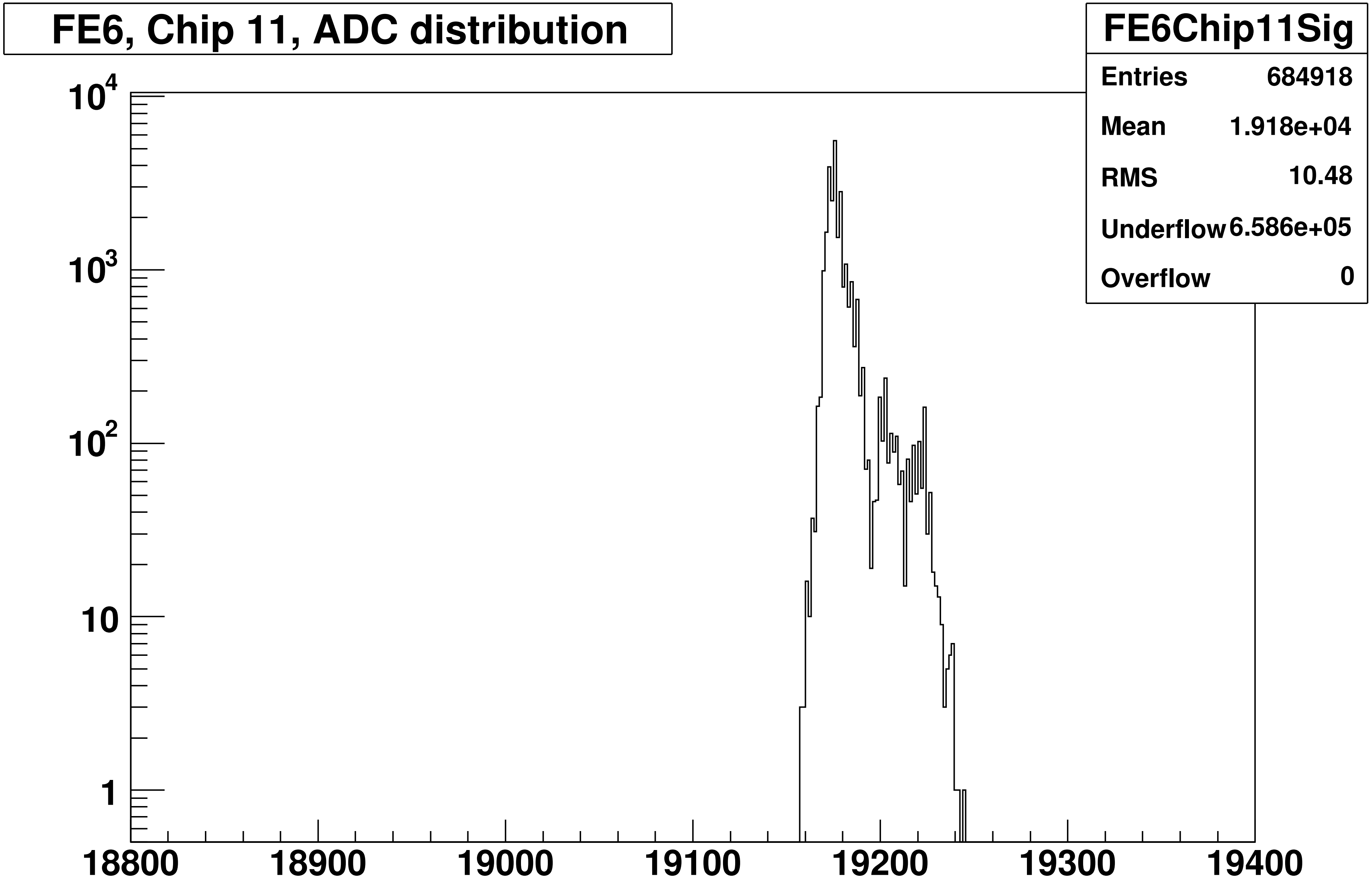

FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 684918
Mean 5.154
RMS 1.31
Underflow 0
Overflow 2.635e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

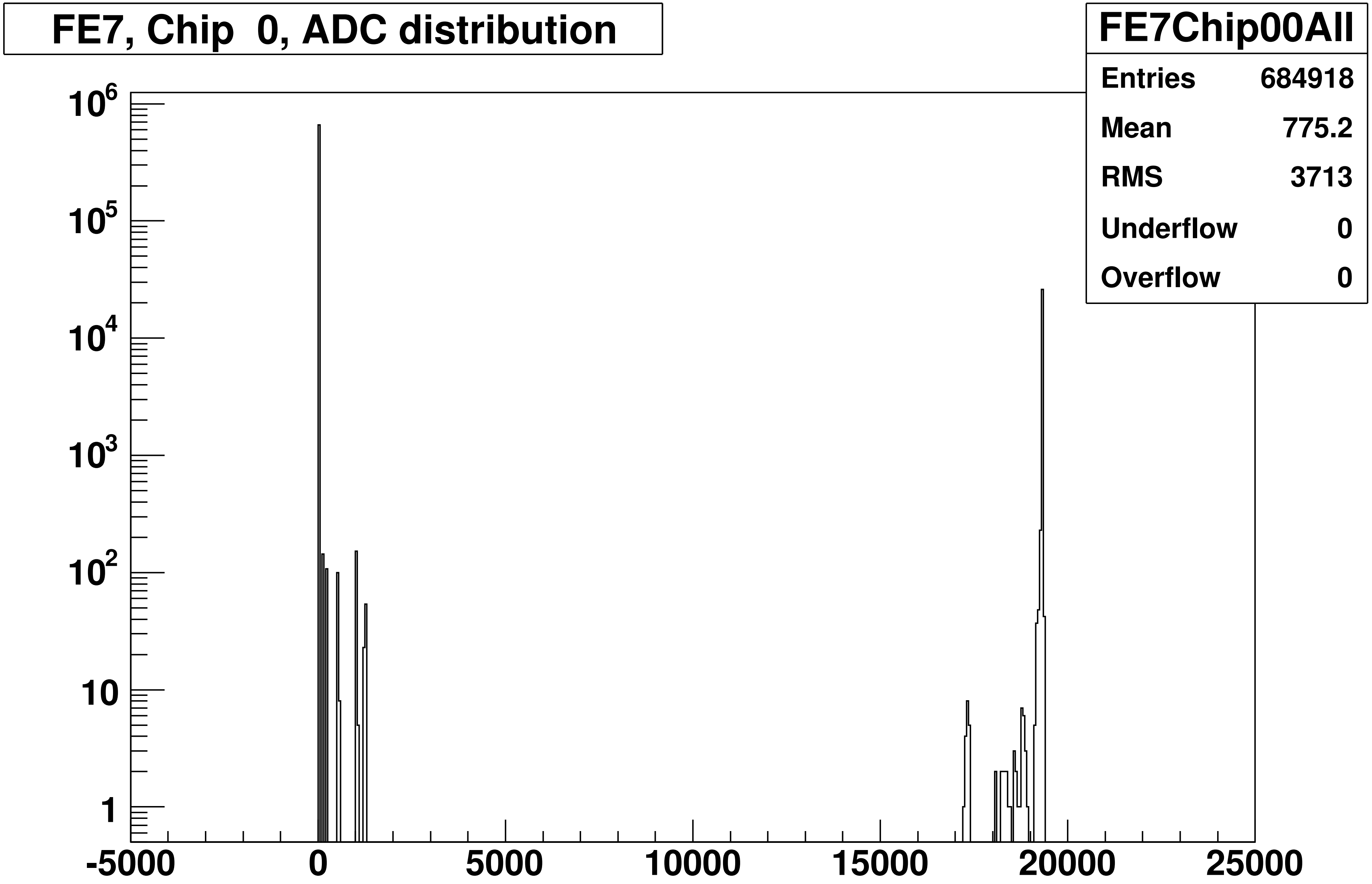

FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 684918
Mean 1.912e+04
RMS 11.73
Underflow 6.586e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 684918
Mean 729.6
RMS 3691
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

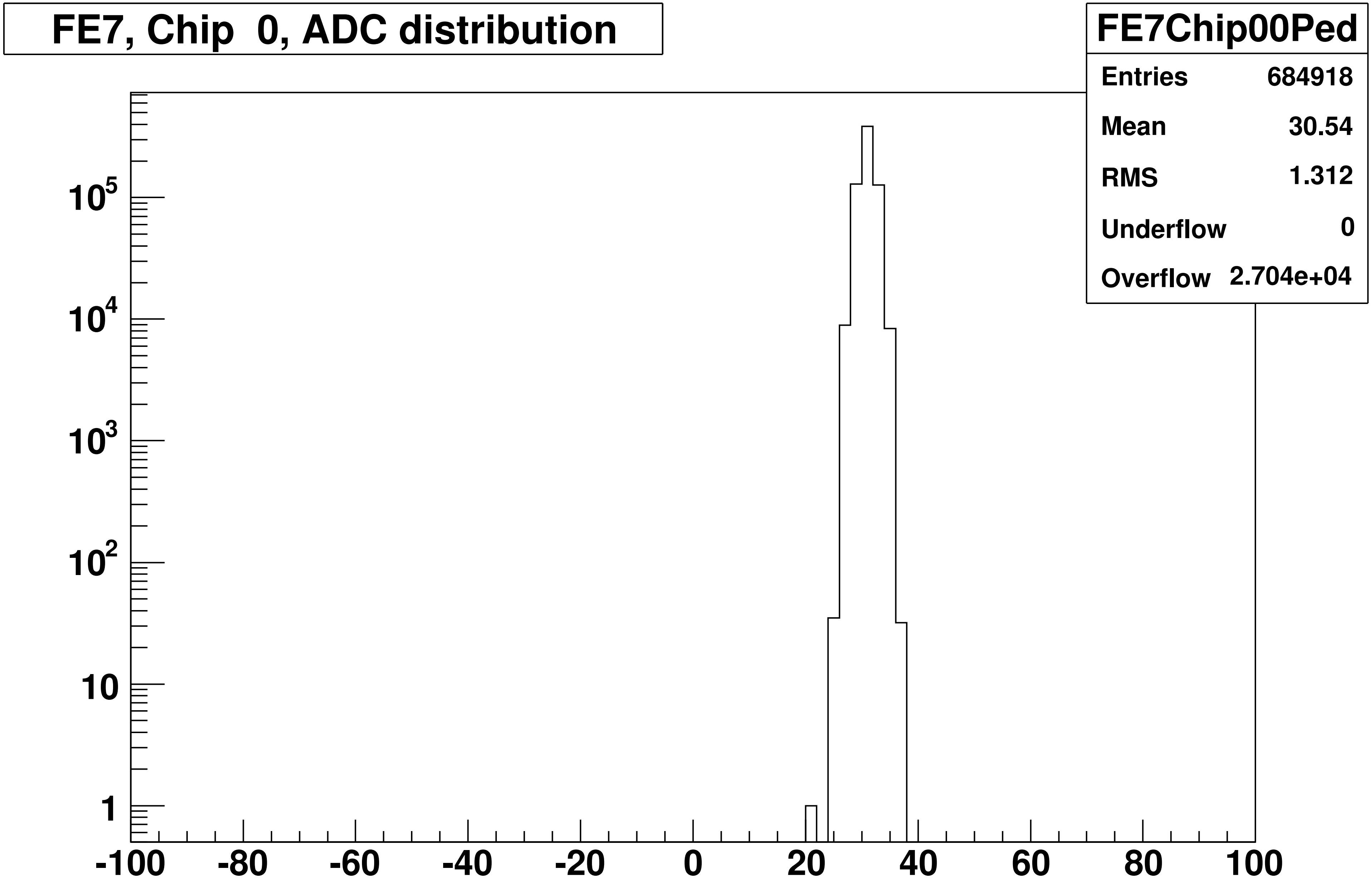


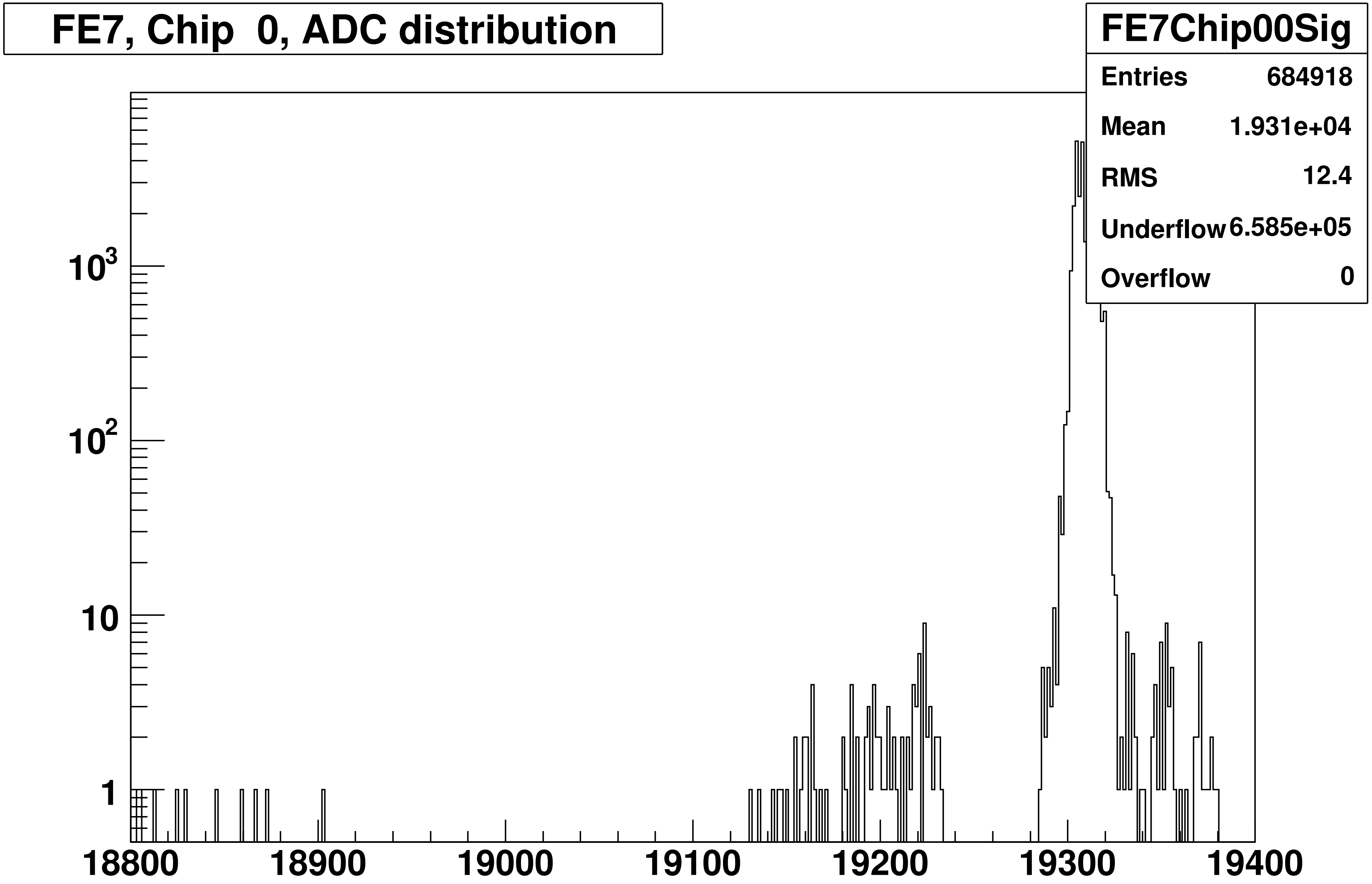
FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 684918
Mean -8.635
RMS 1.322
Underflow 0
Overflow 2.635e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

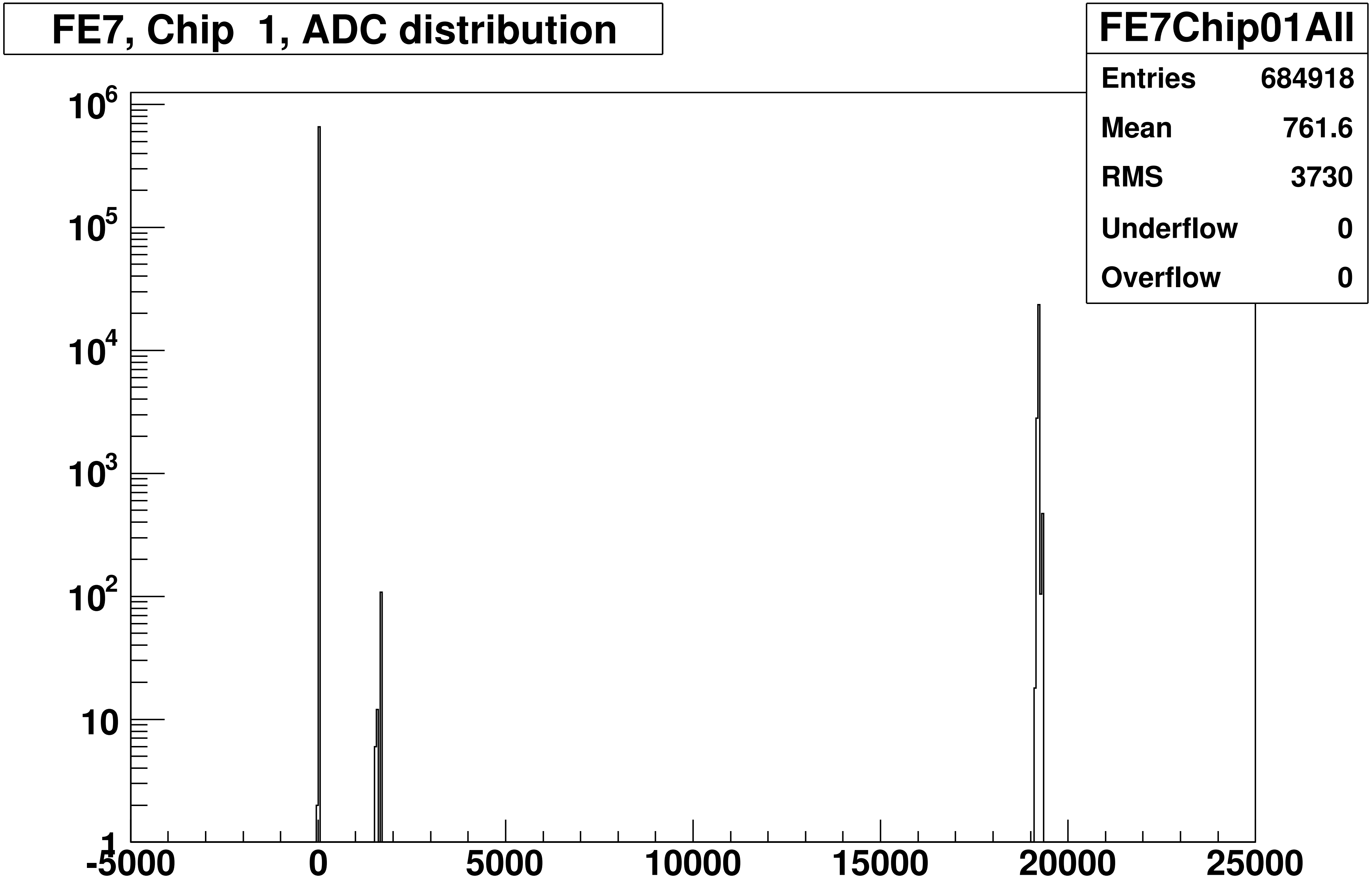

FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 684918
Mean 1.918e+04
RMS 10.48
Underflow 6.586e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



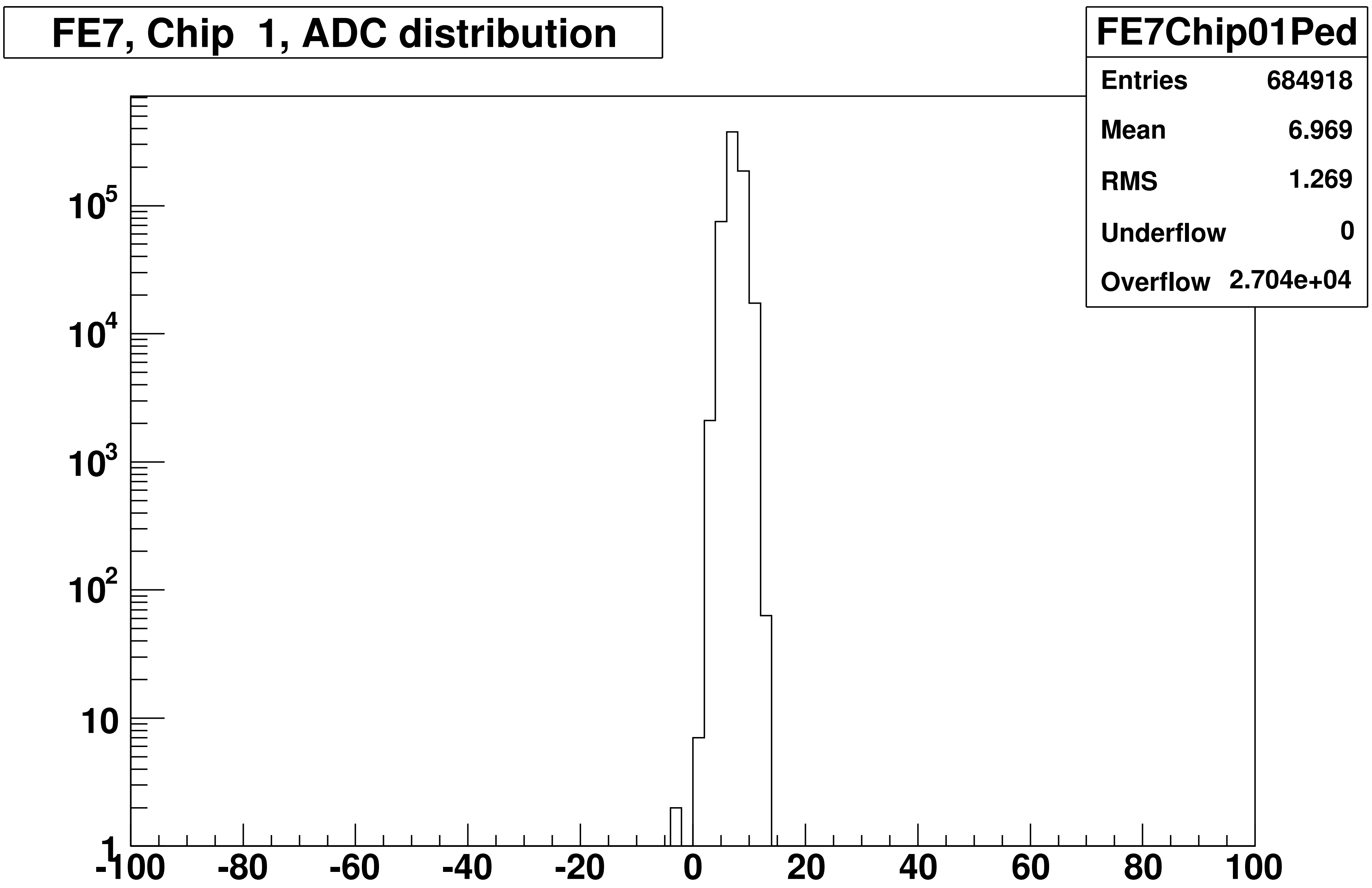
FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 684918
Mean 775.2
RMS 3713
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

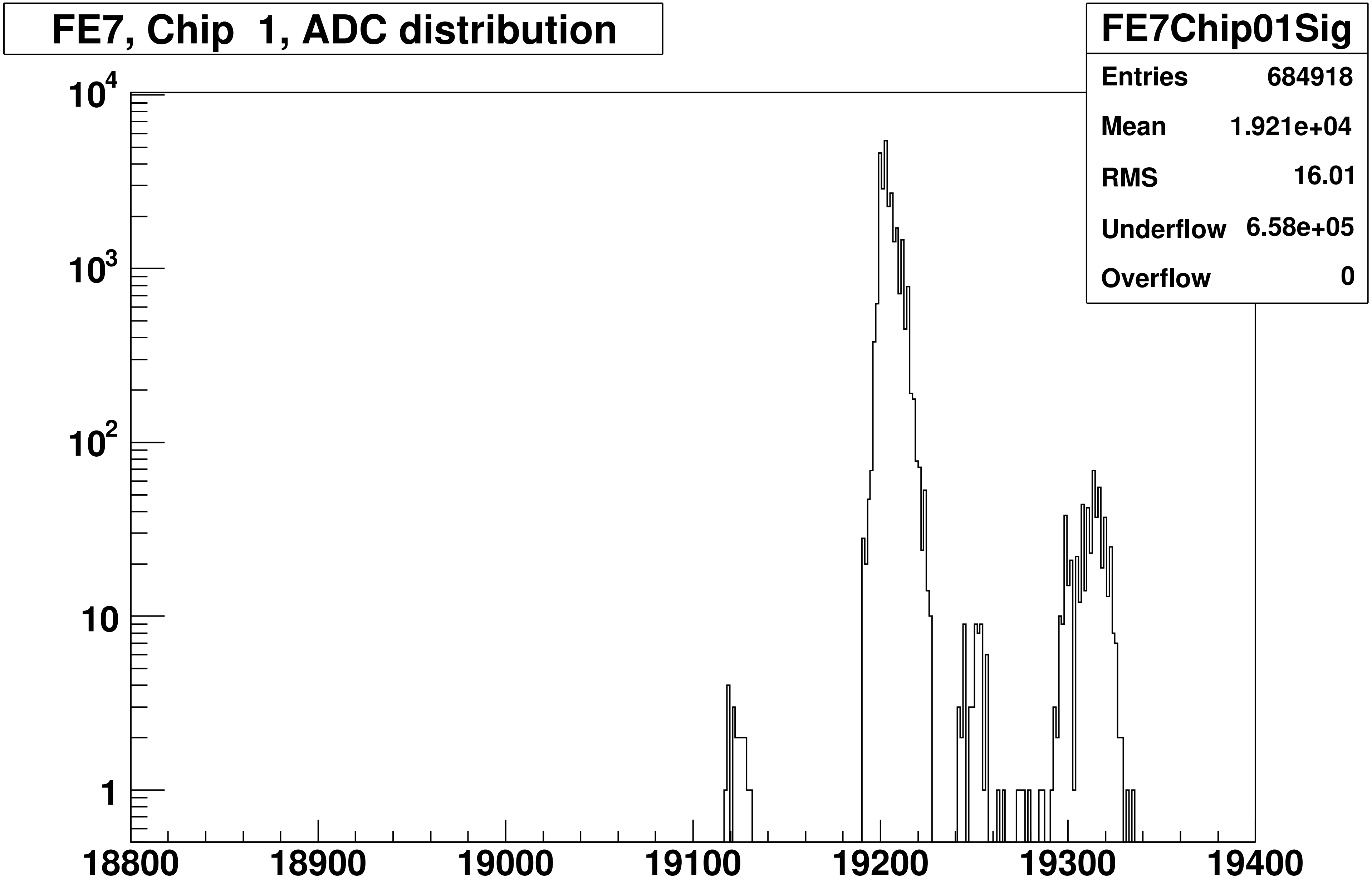

FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 684918
Mean 30.54
RMS 1.312
Underflow 0
Overflow 2.704e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

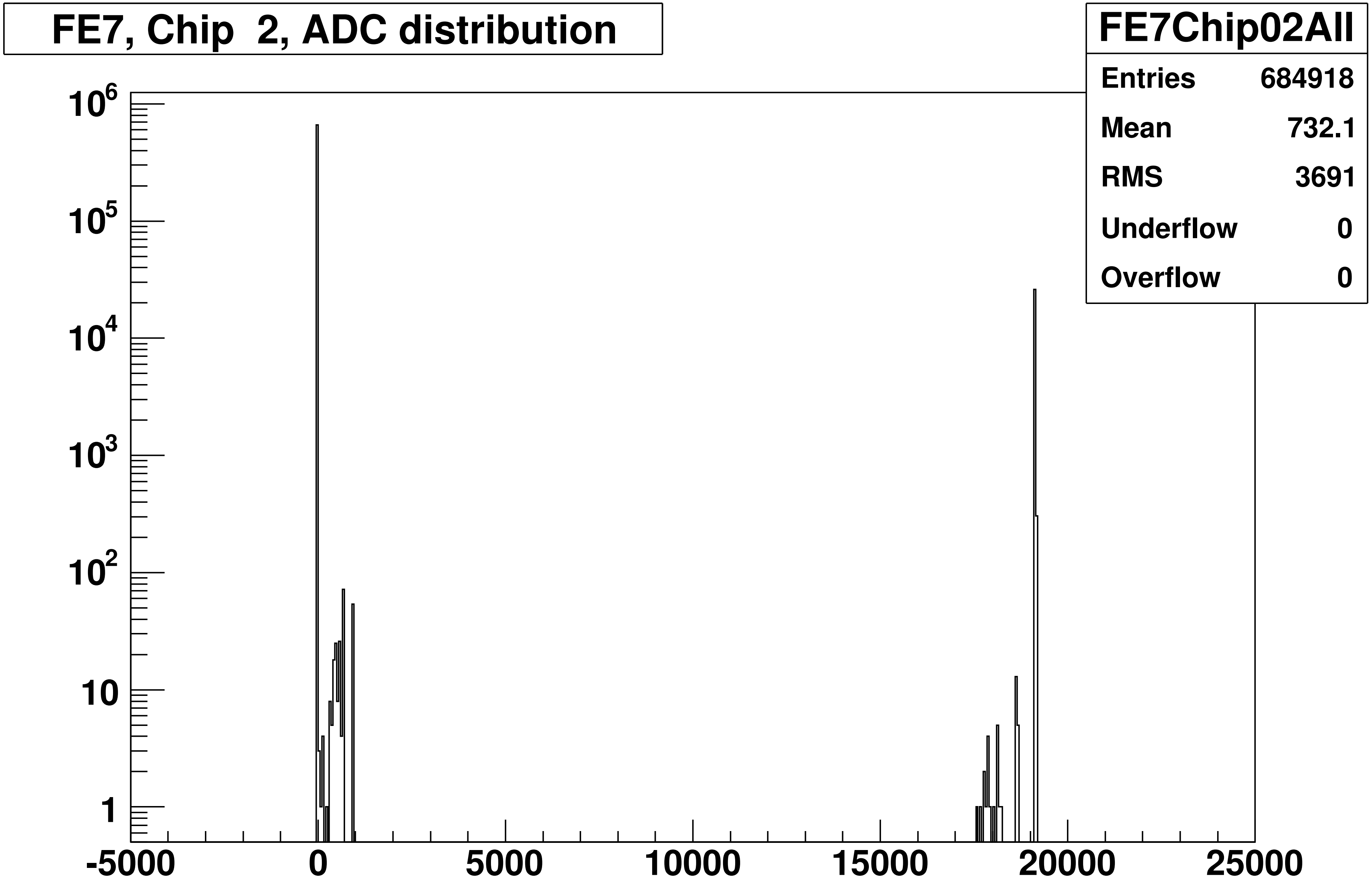

FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 684918
Mean 1.931e+04
RMS 12.4
Underflow 6.585e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

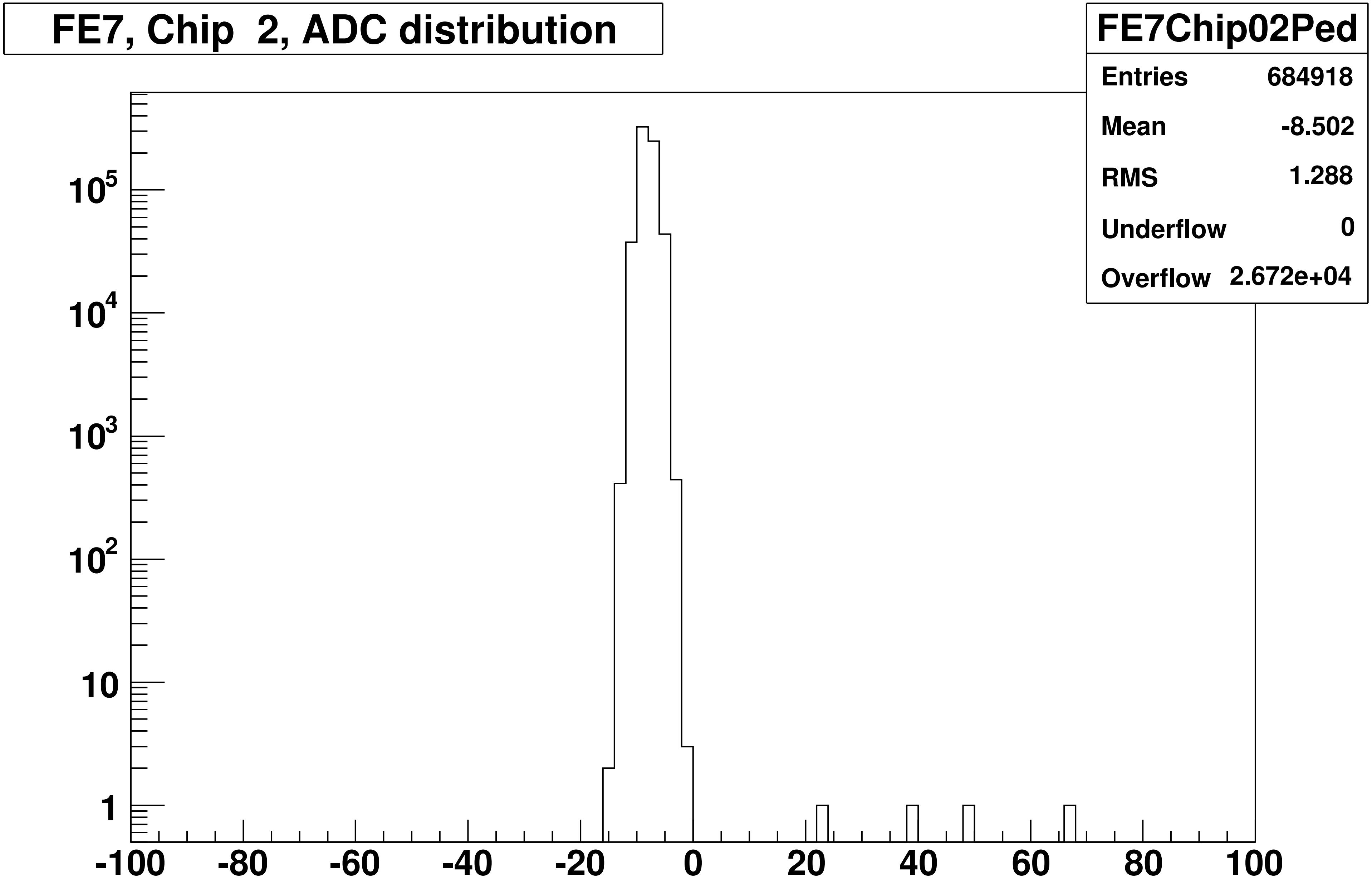


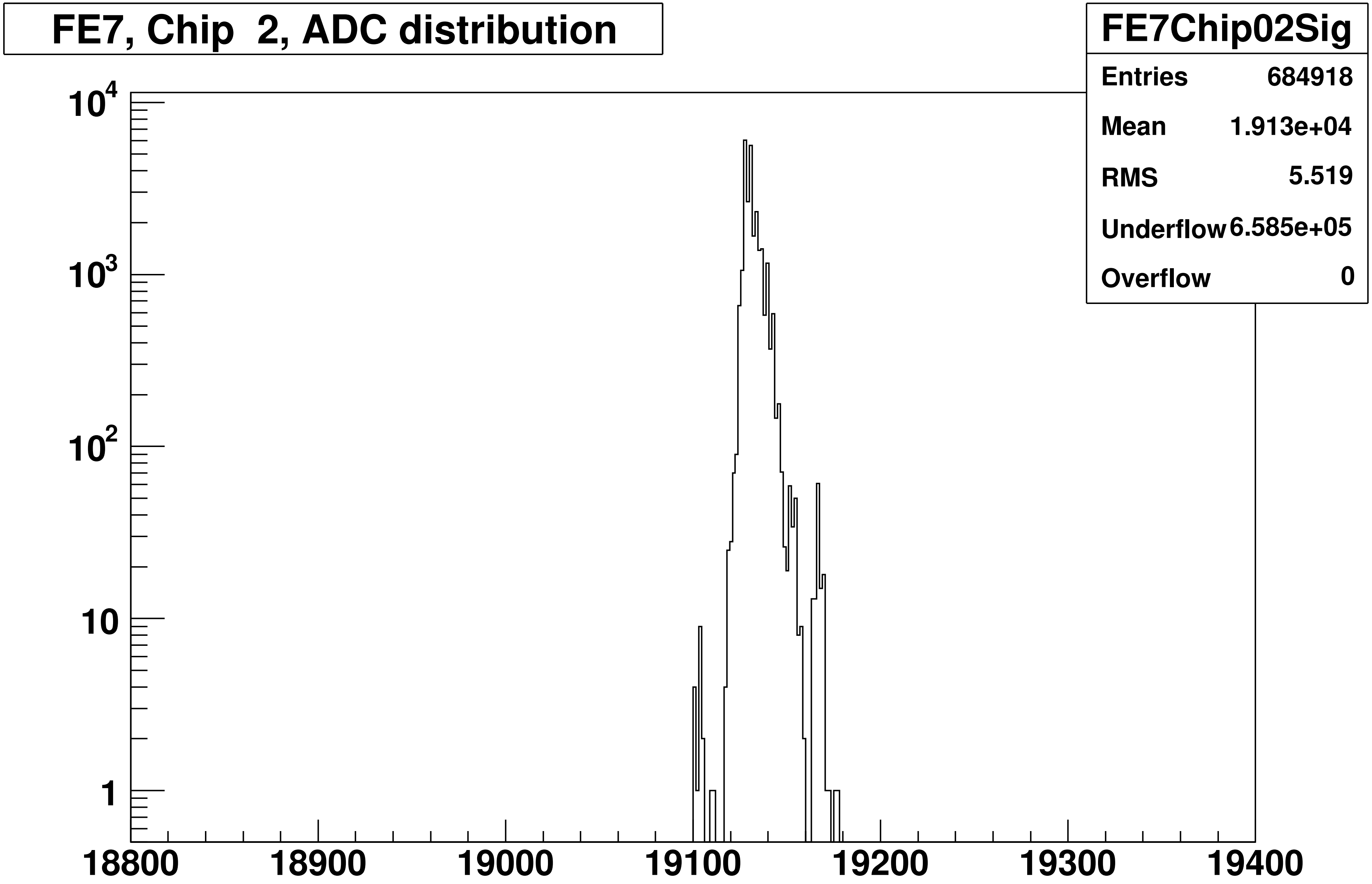
FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 684918
Mean 761.6
RMS 3730
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

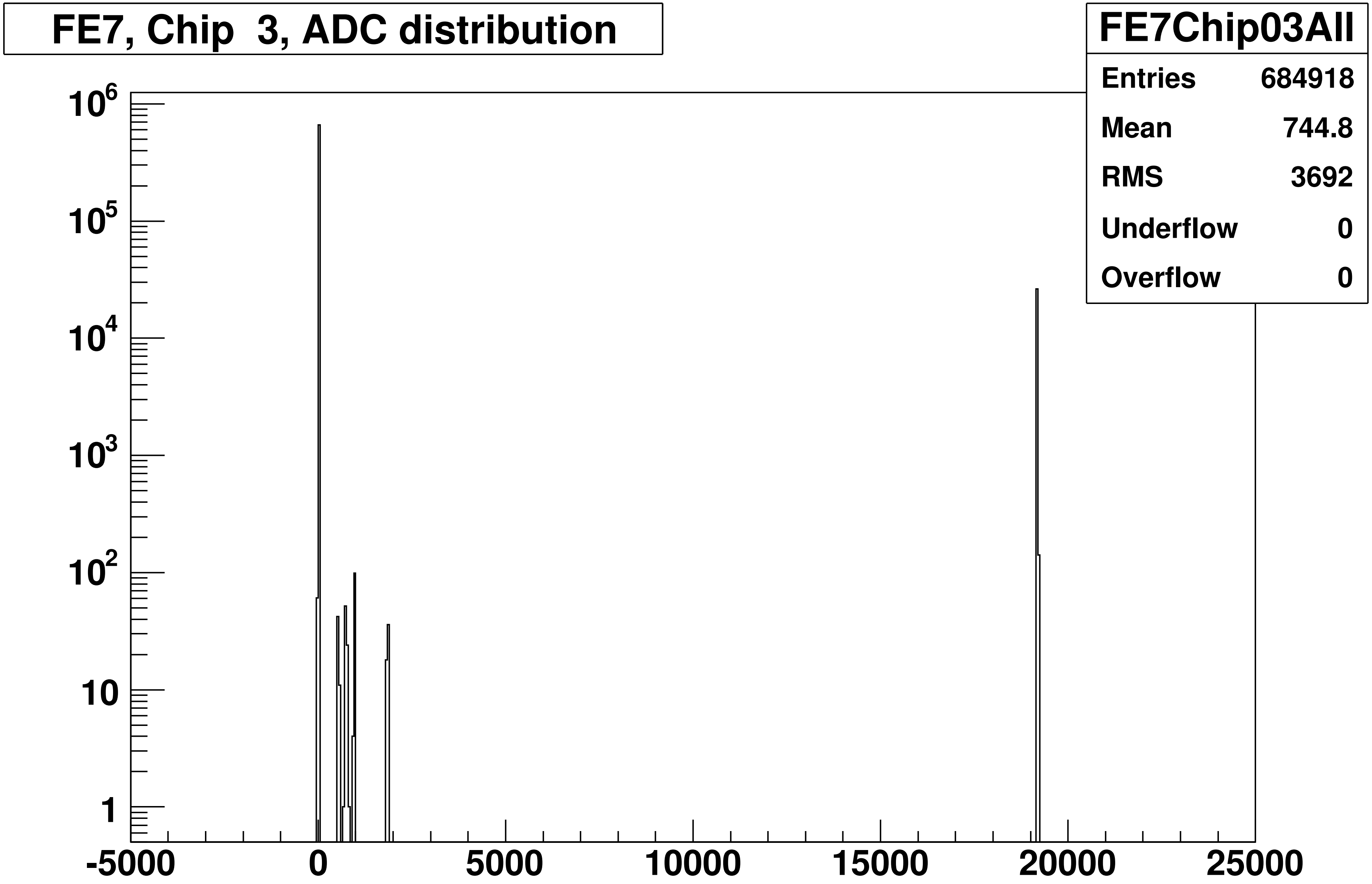

FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 684918
Mean 6.969
RMS 1.269
Underflow 0
Overflow 2.704e+04
1
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

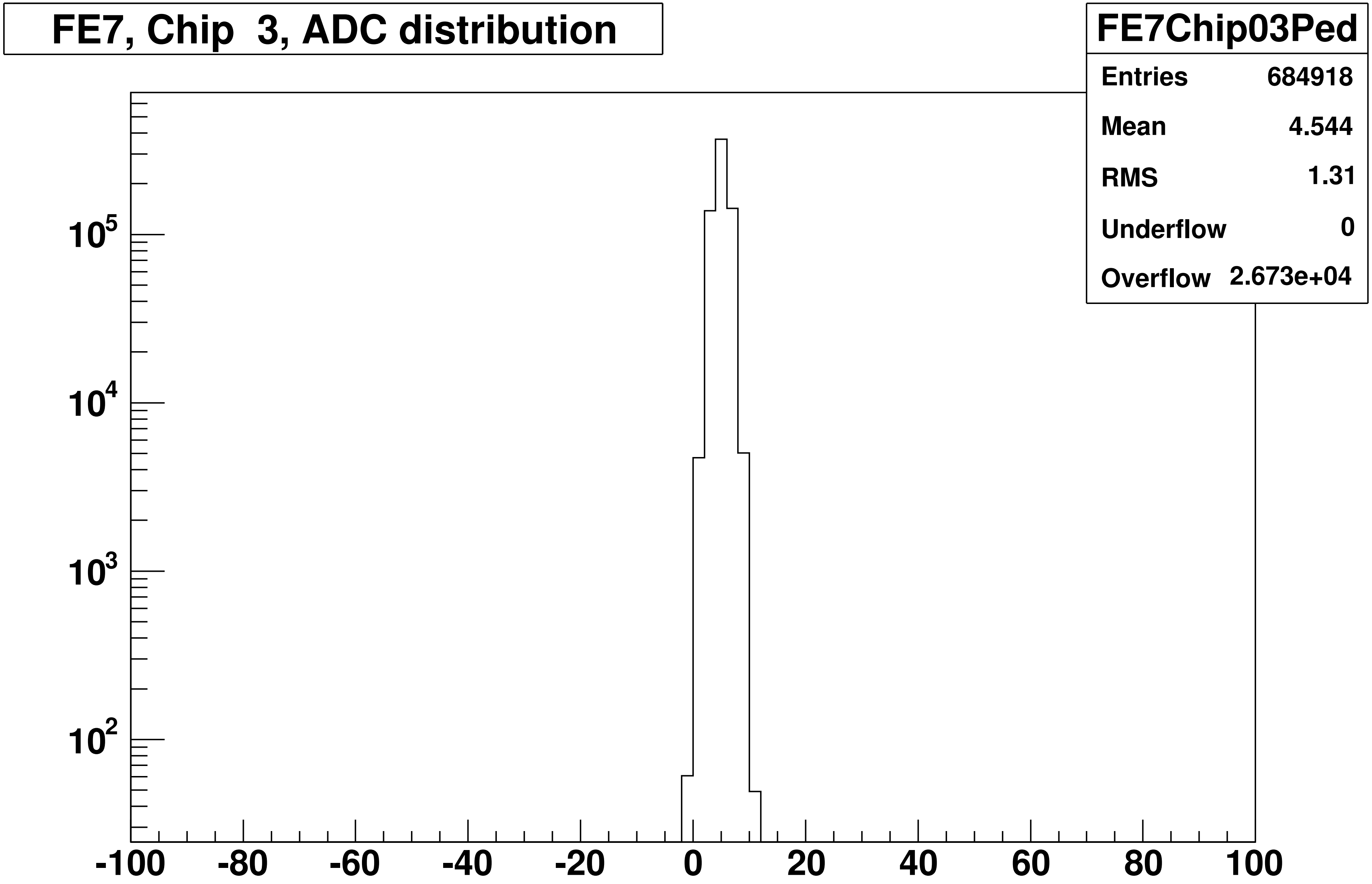

FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 684918
Mean 1.921e+04
RMS 16.01
Underflow 6.58e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

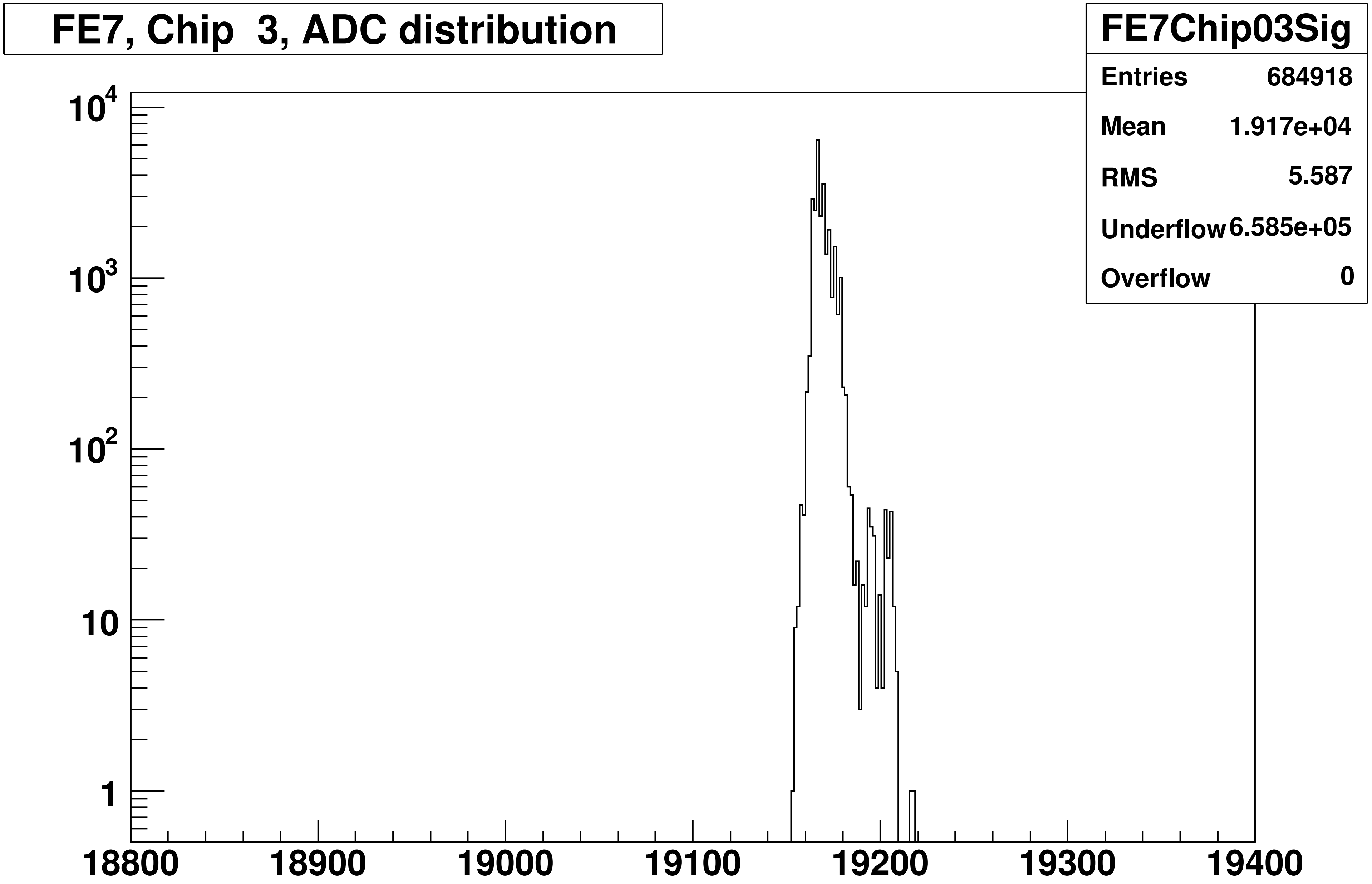

FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 684918
Mean 732.1
RMS 3691
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 684918
Mean -8.502
RMS 1.288
Underflow 0
Overflow 2.672e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

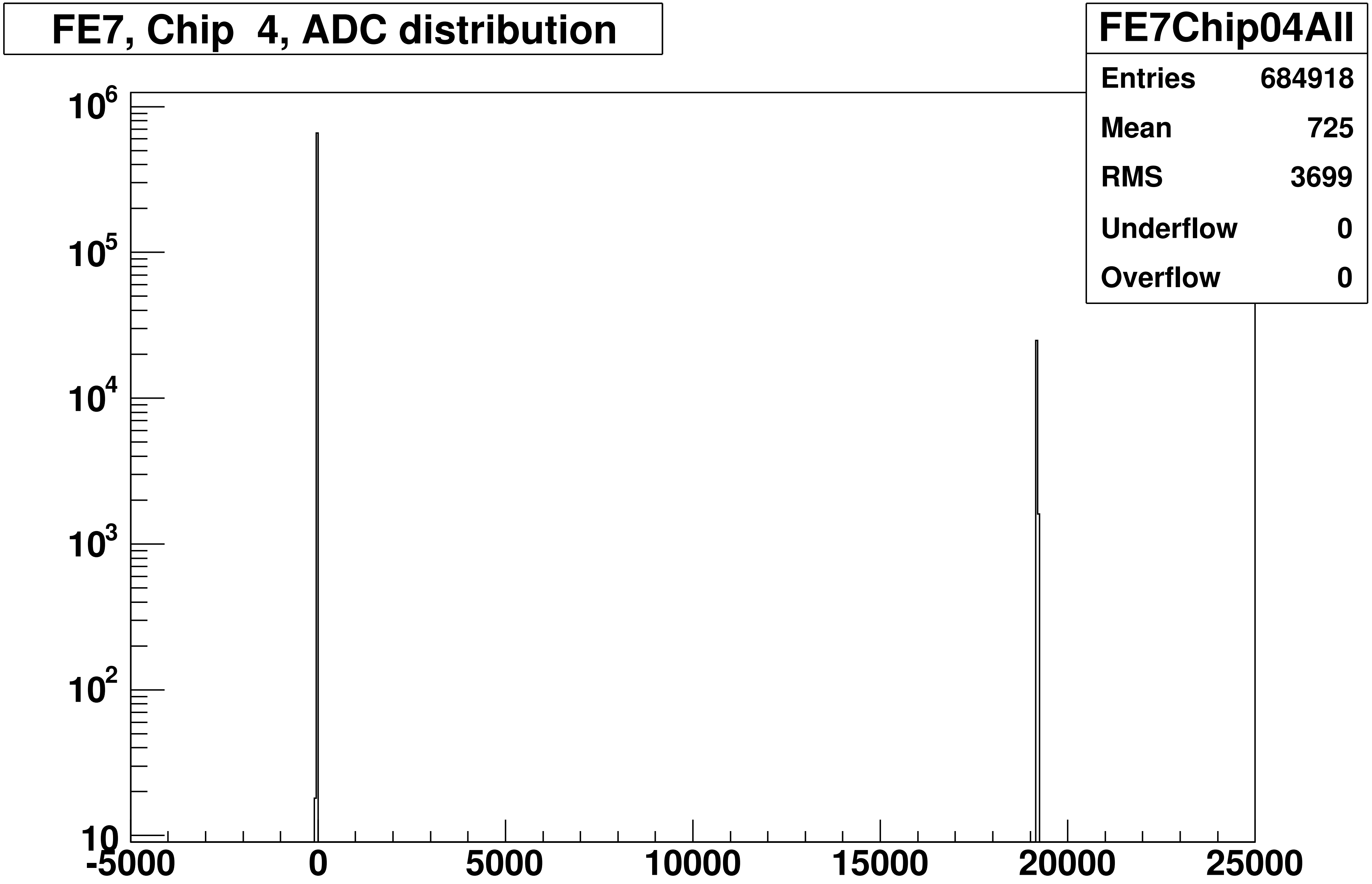

FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 684918
Mean 1.913e+04
RMS 5.519
Underflow 6.585e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

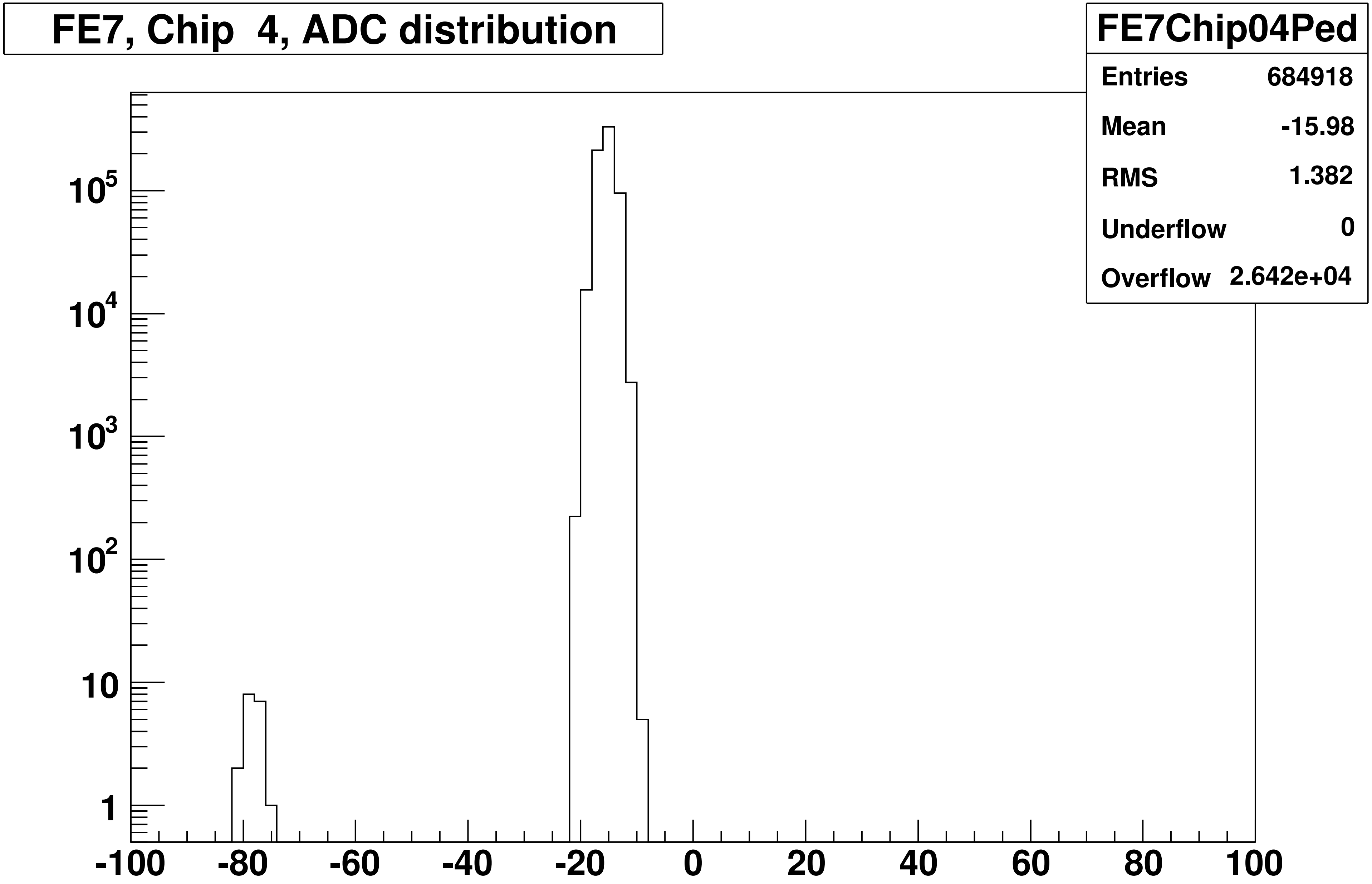

FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 684918
Mean 744.8
RMS 3692
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

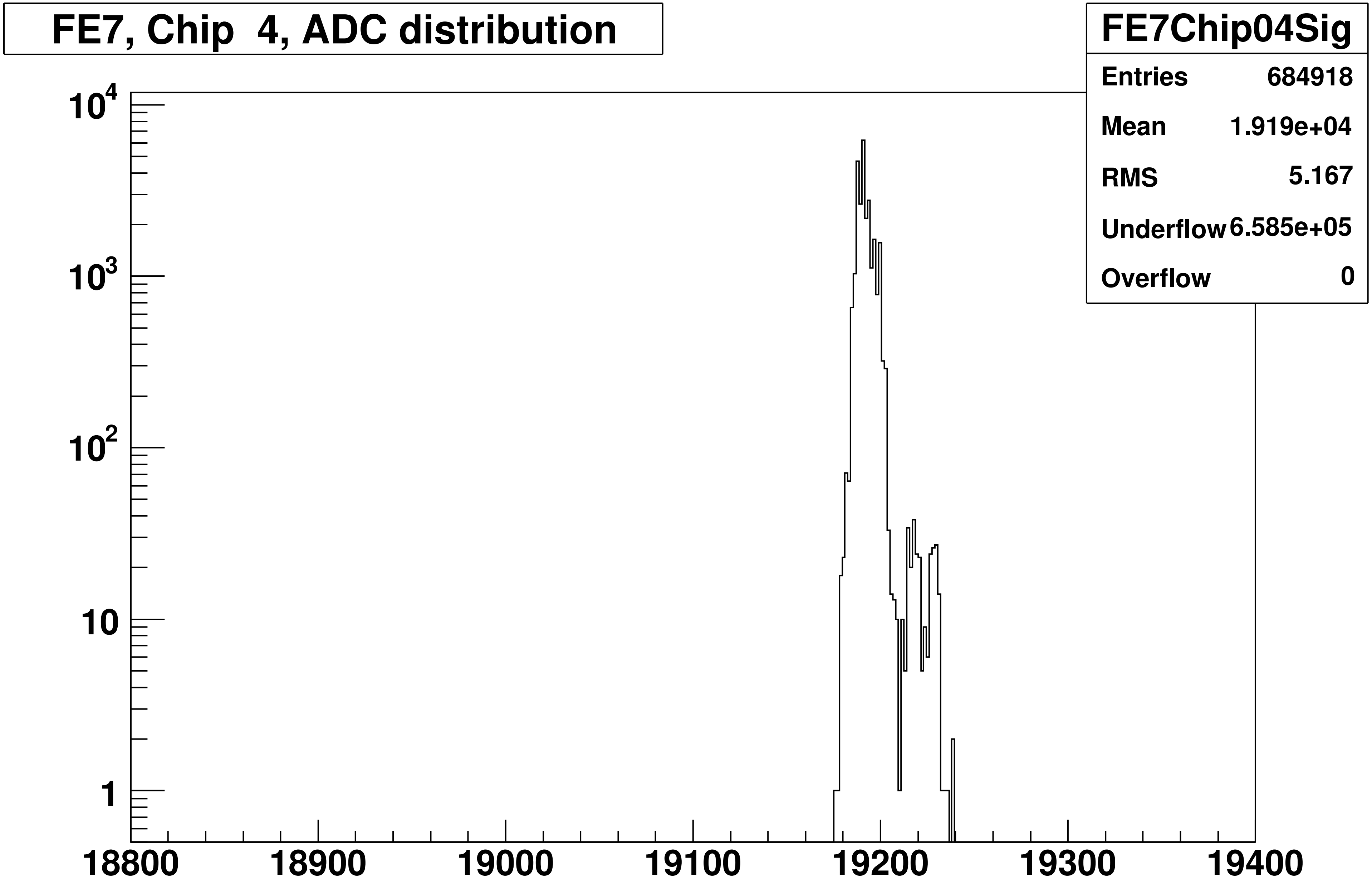

FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 684918
Mean 4.544
RMS 1.31
Underflow 0
Overflow 2.673e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

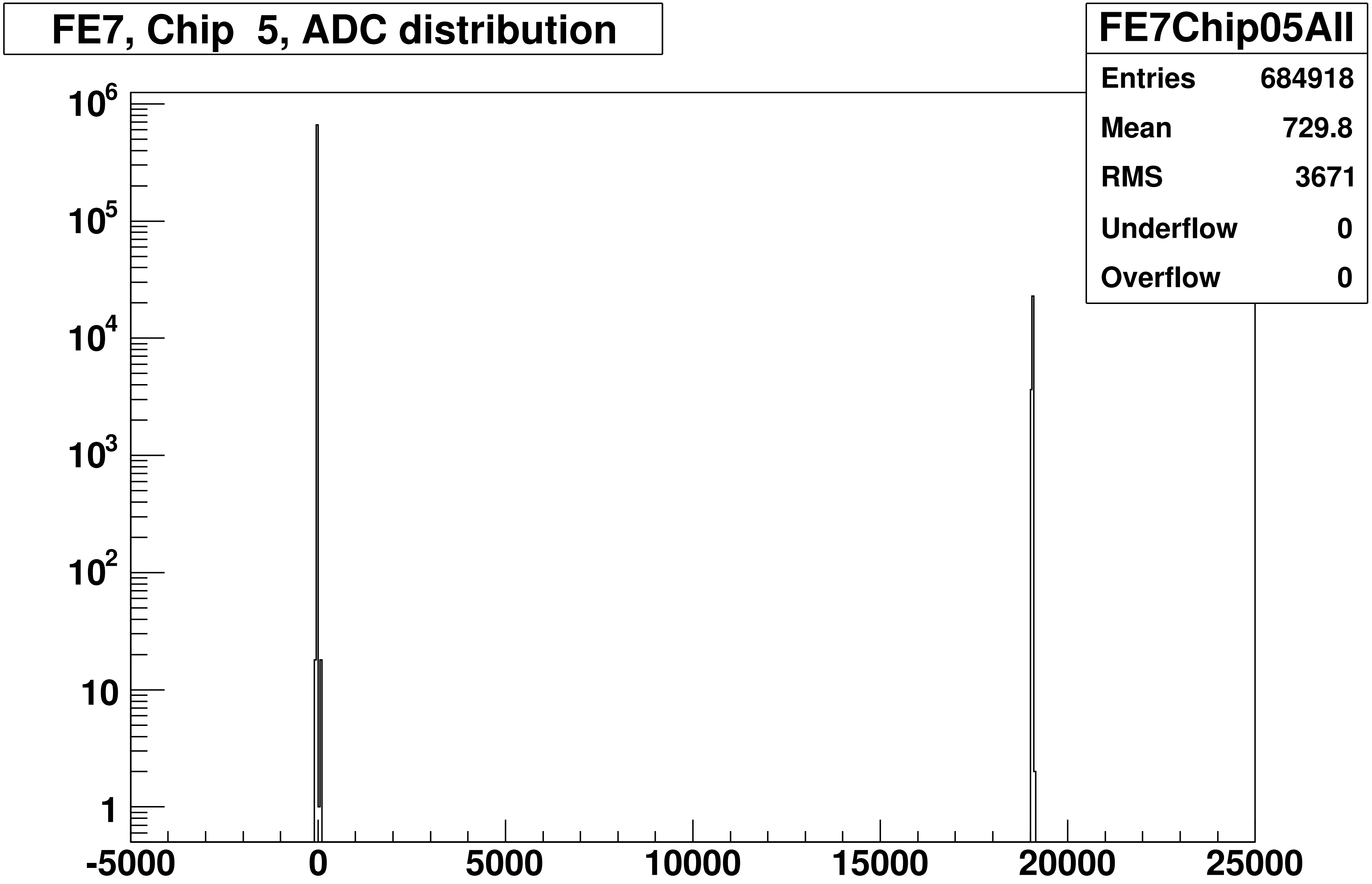


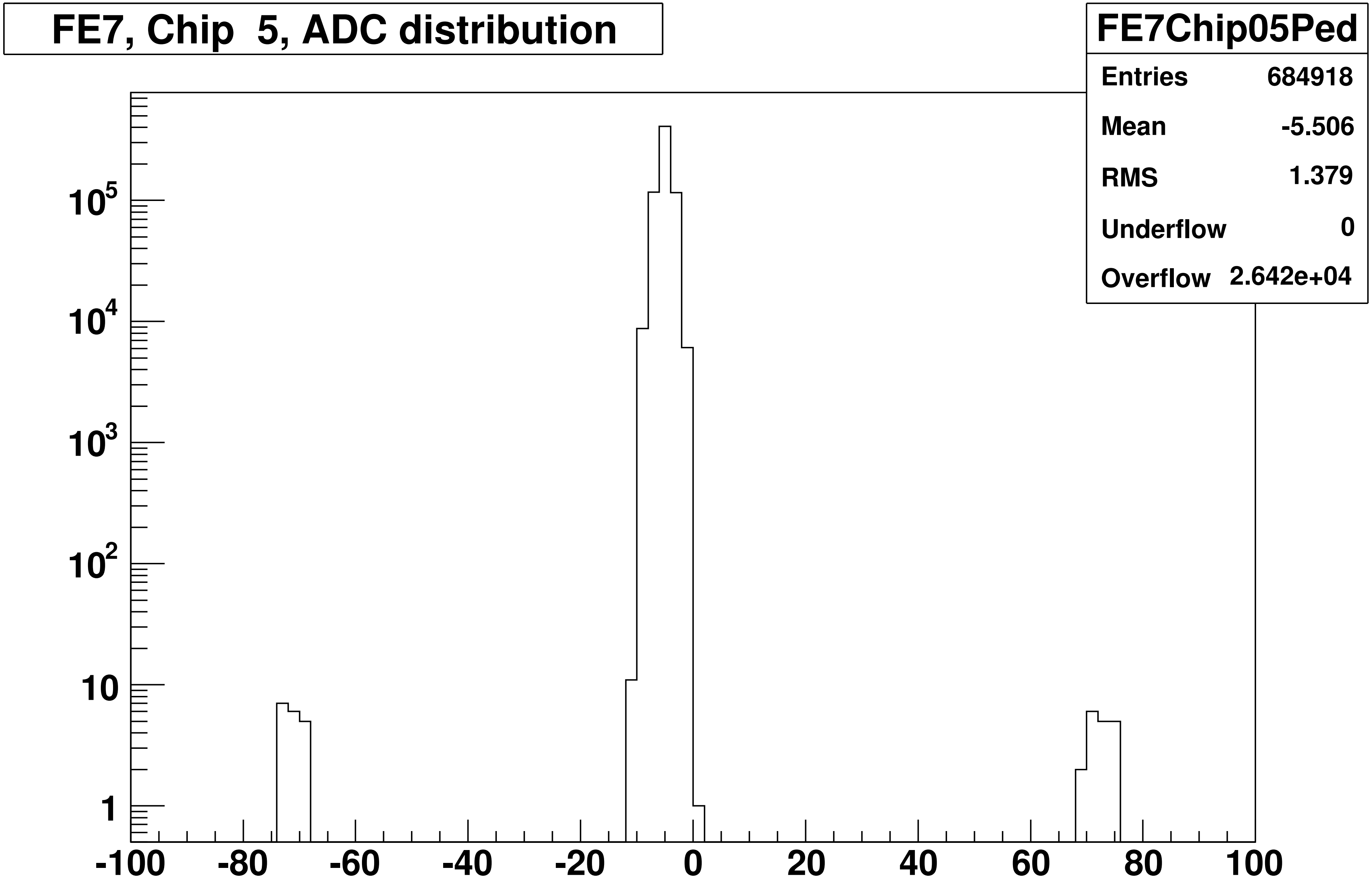
FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 684918
Mean 1.917e+04
RMS 5.587
Underflow 6.585e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 684918
Mean 725
RMS 3699
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

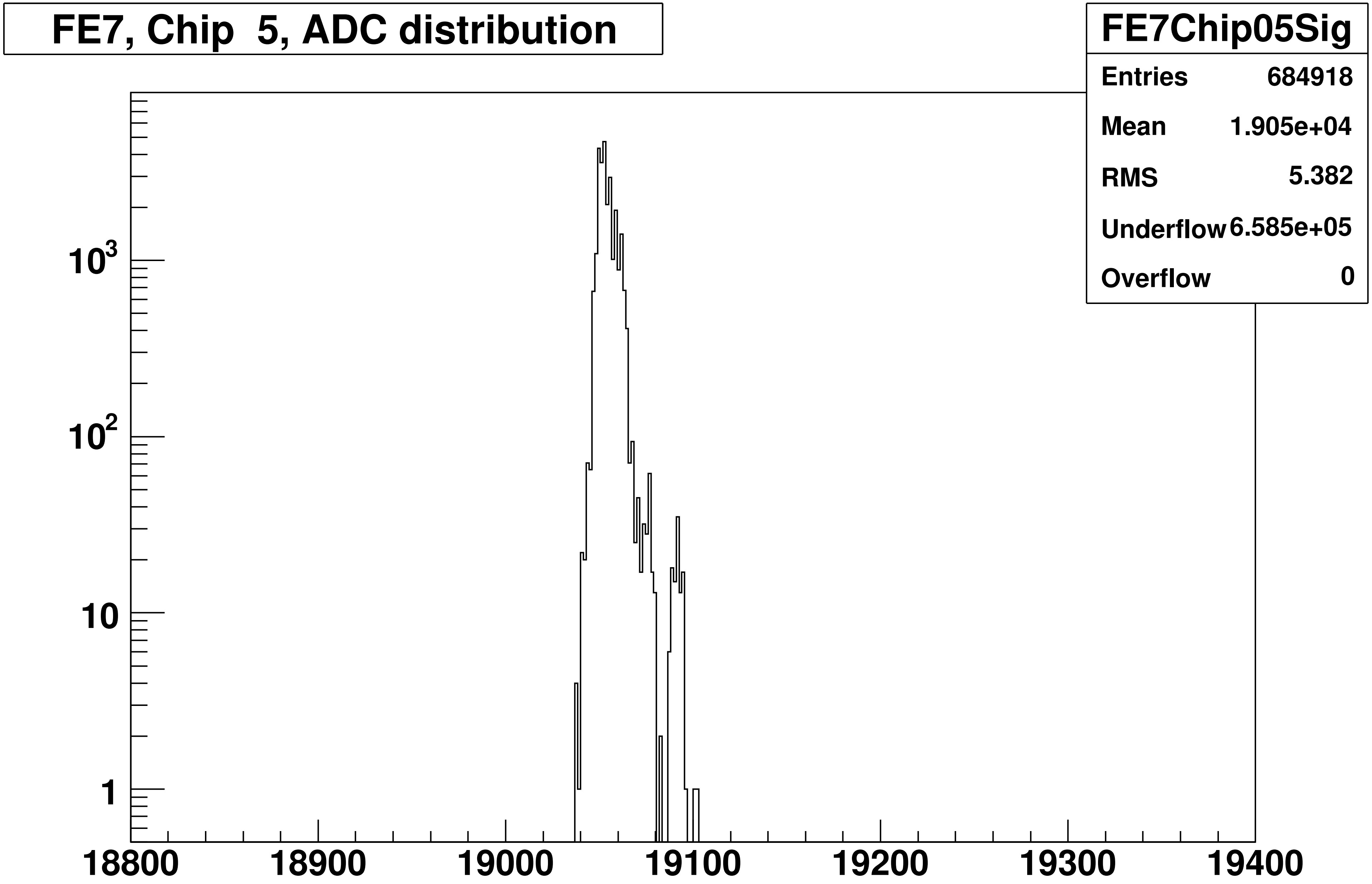

FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 684918
Mean -15.98
RMS 1.382
Underflow 0
Overflow 2.642e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 684918
Mean 1.919e+04
RMS 5.167
Underflow 6.585e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

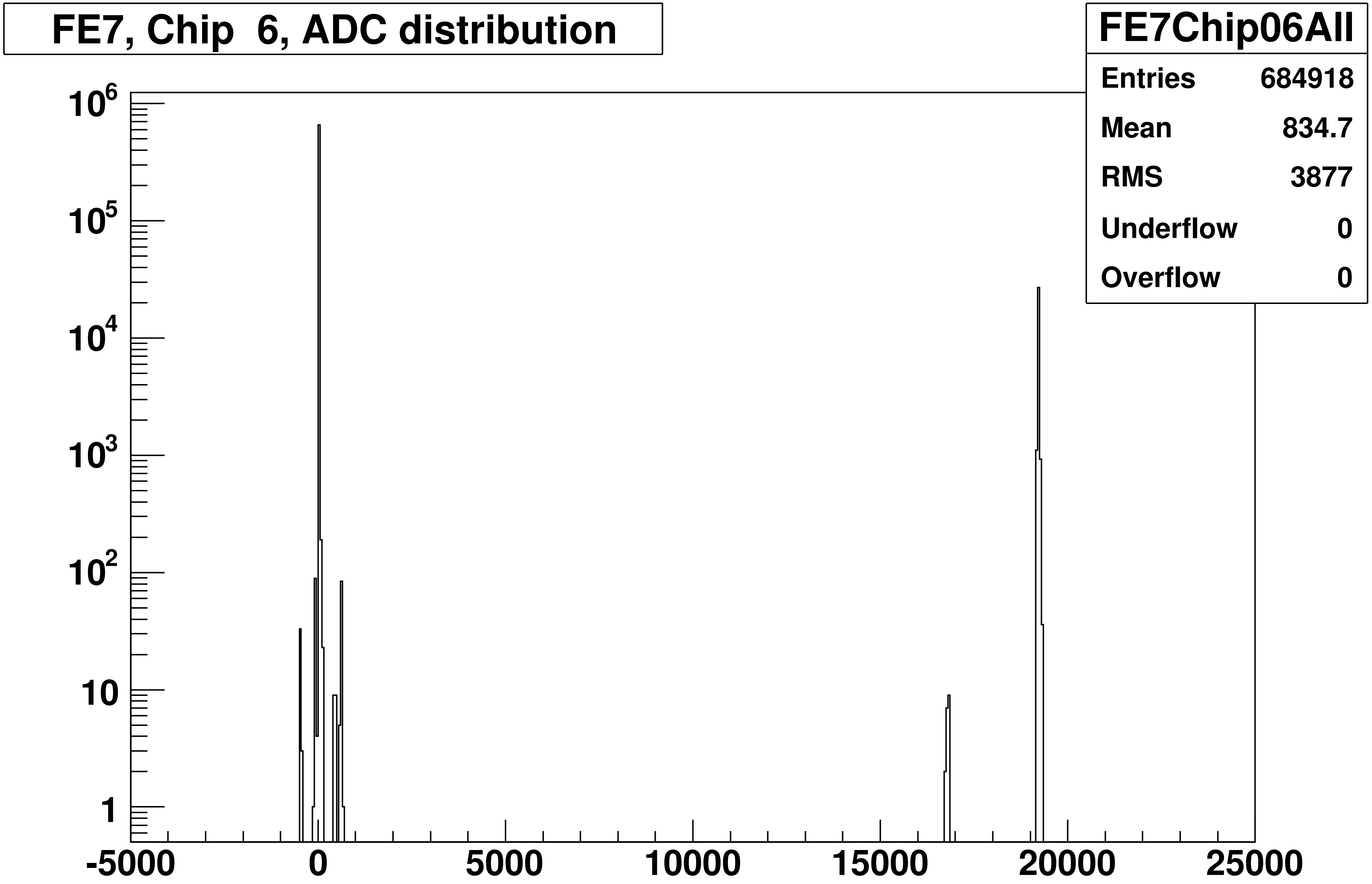

FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 684918
Mean 729.8
RMS 3671
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

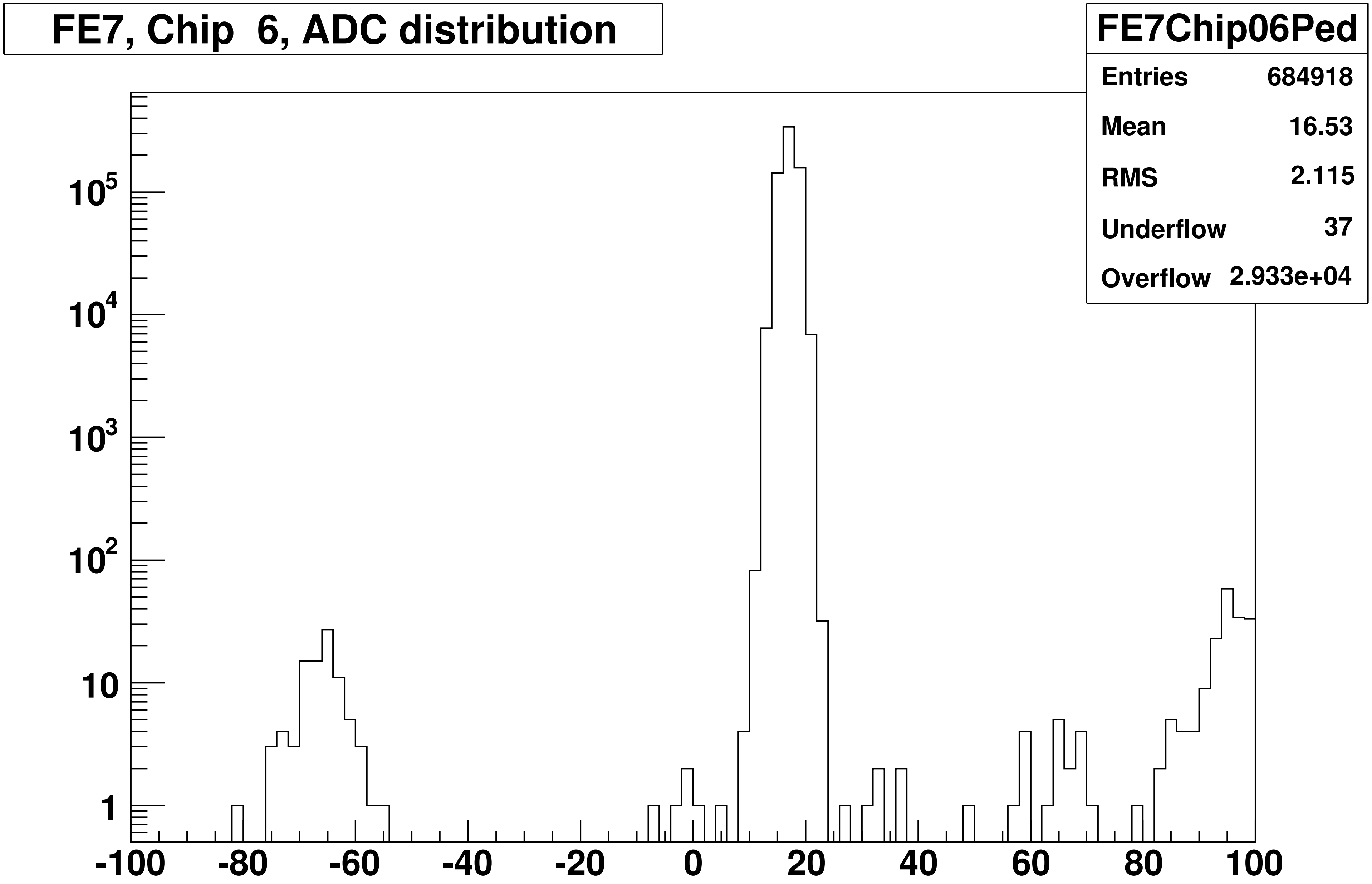


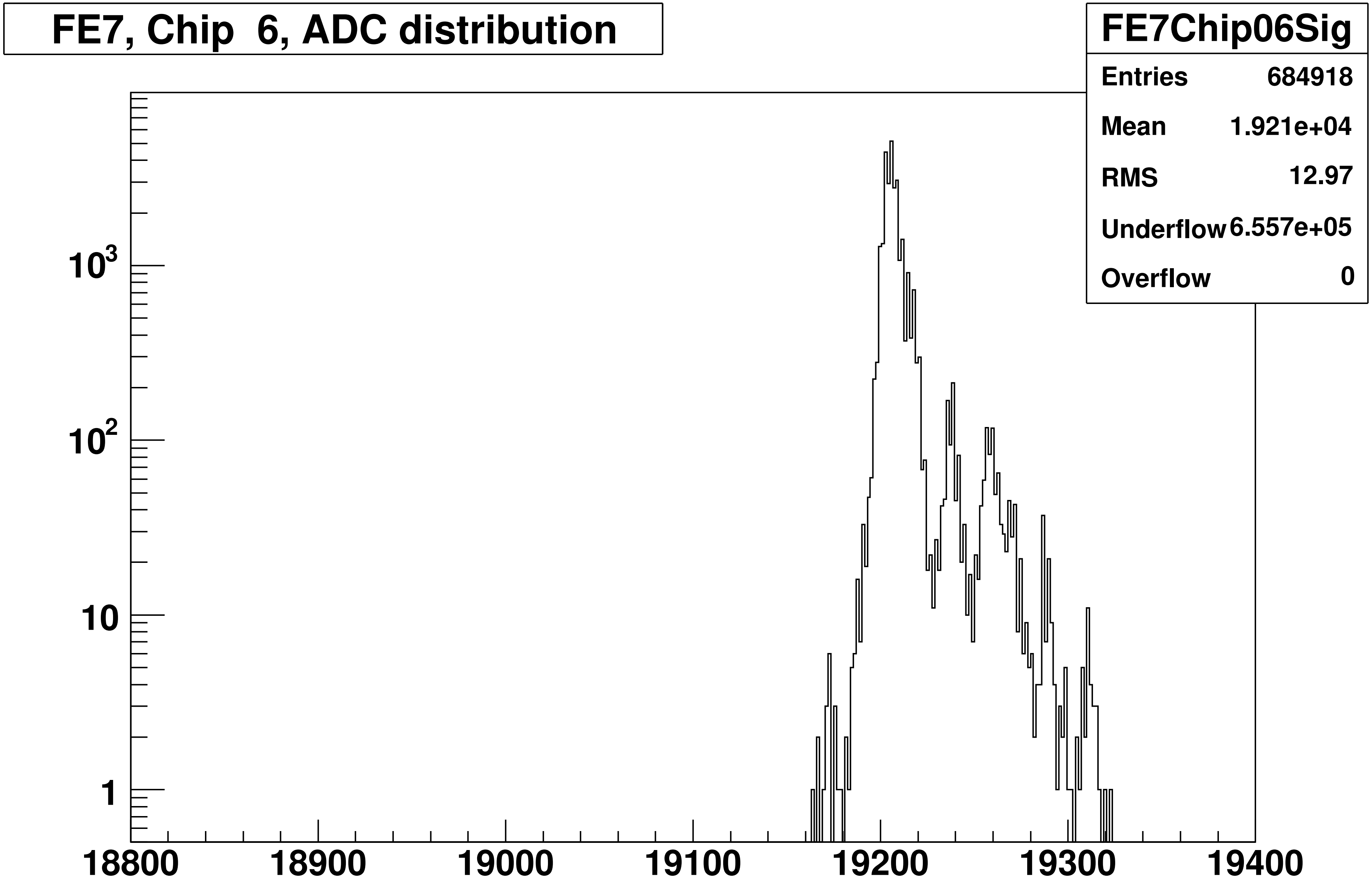
FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 684918
Mean -5.506
RMS 1.379
Underflow 0
Overflow 2.642e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

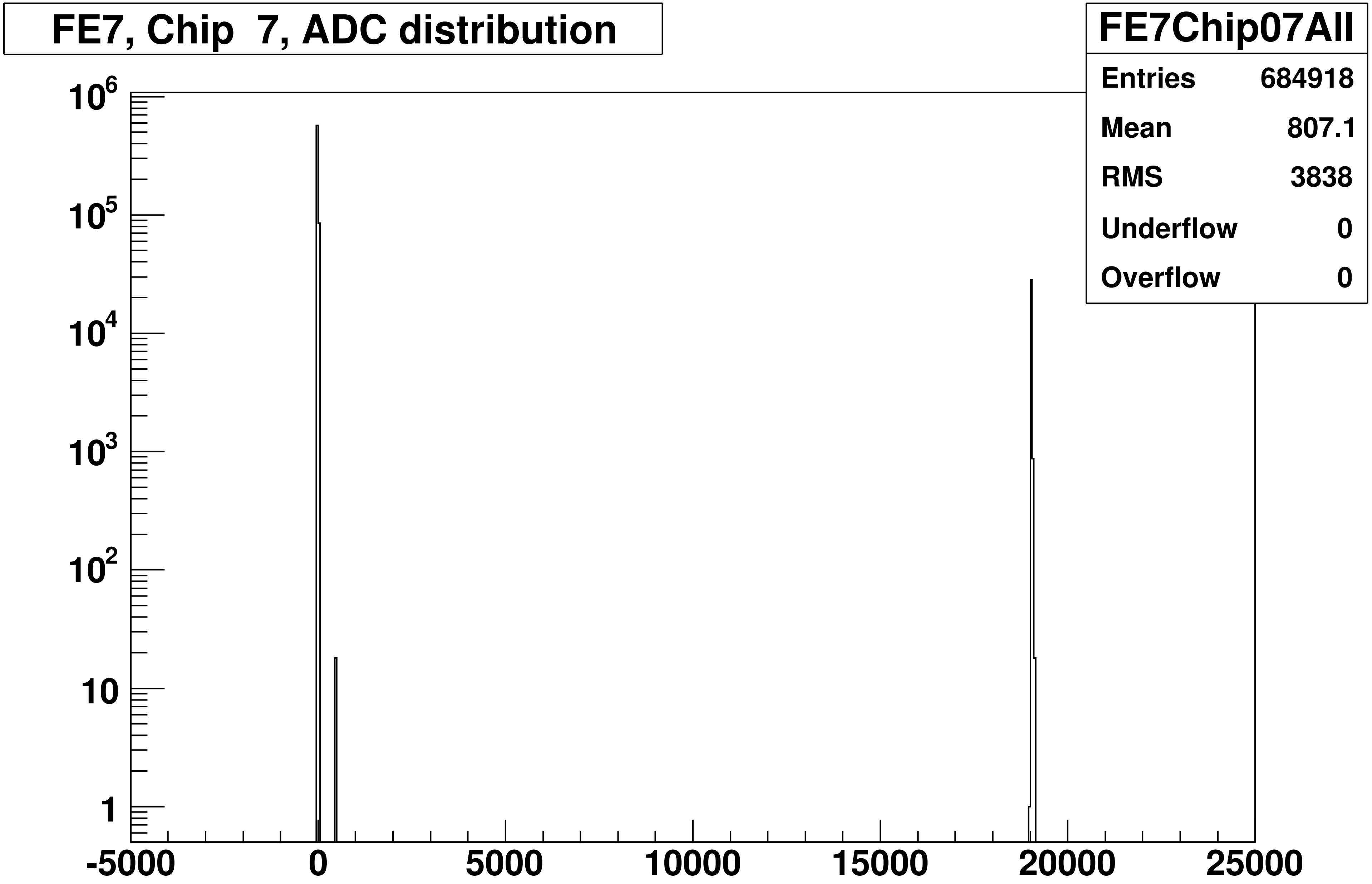


FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 684918
Mean 1.905e+04
RMS 5.382
Underflow 6.585e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400

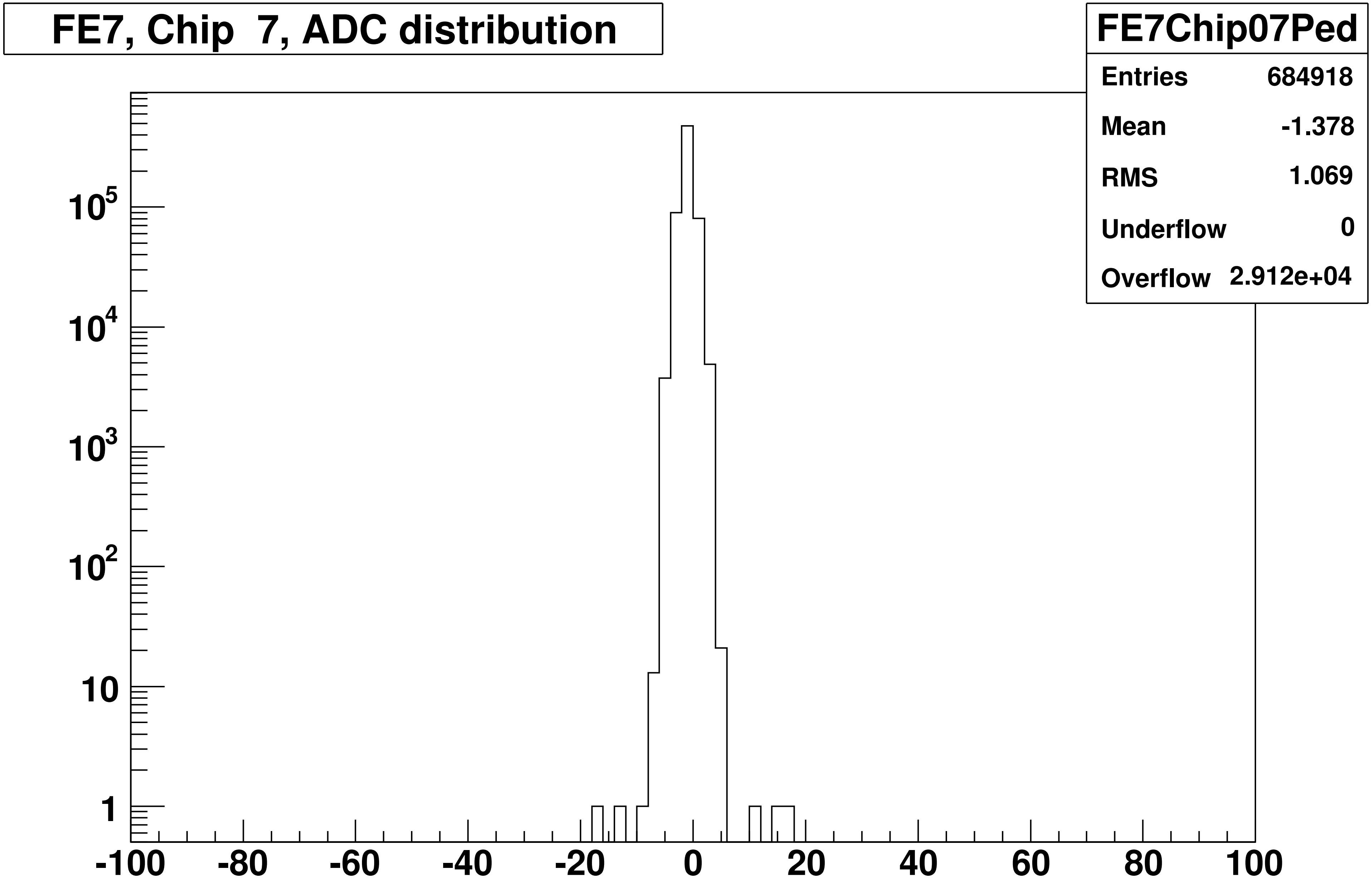

FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 684918
Mean 834.7
RMS 3877
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

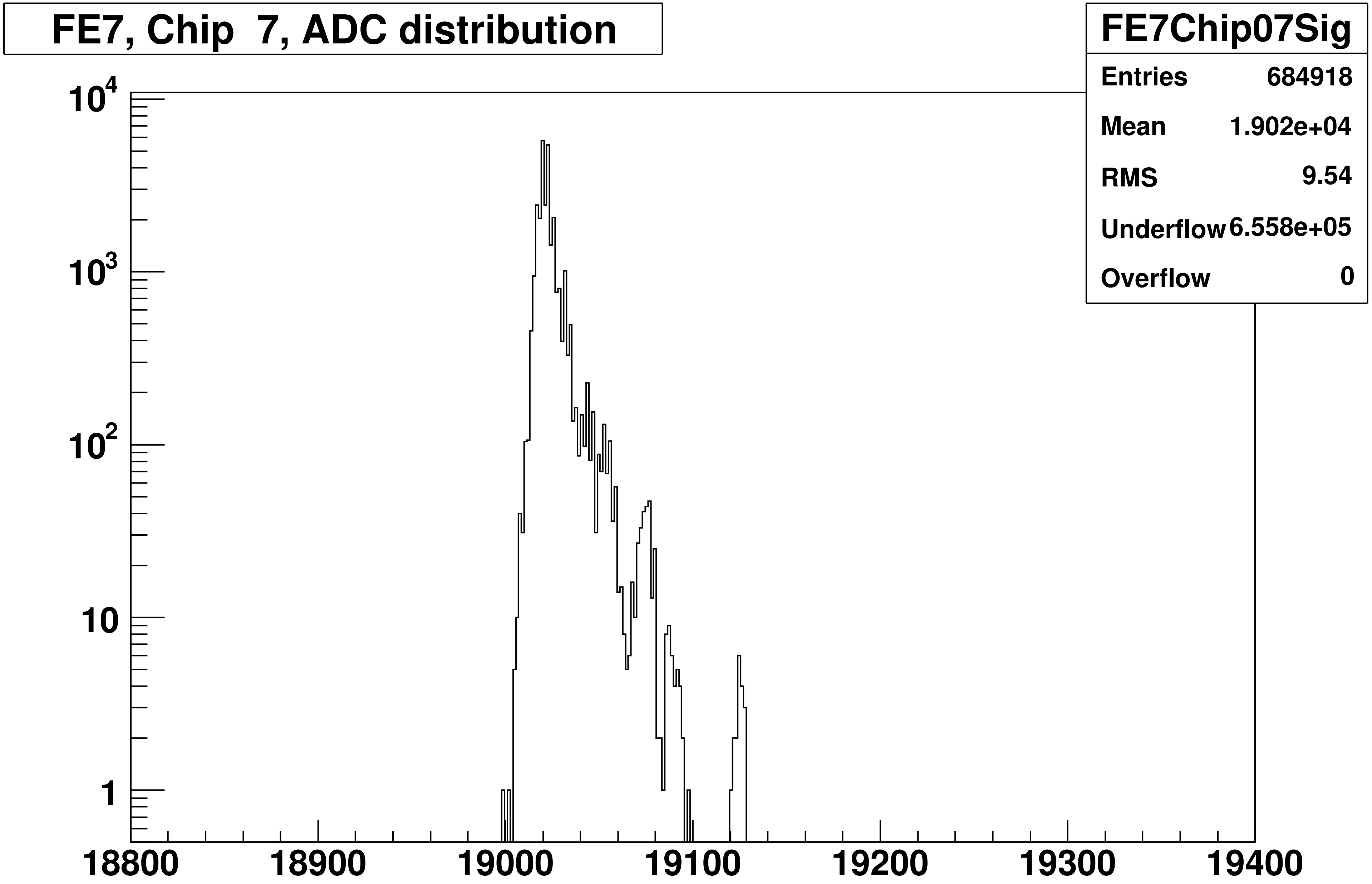

FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 684918
Mean 16.53
RMS 2.115
Underflow 37
Overflow 2.933e+04
1
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

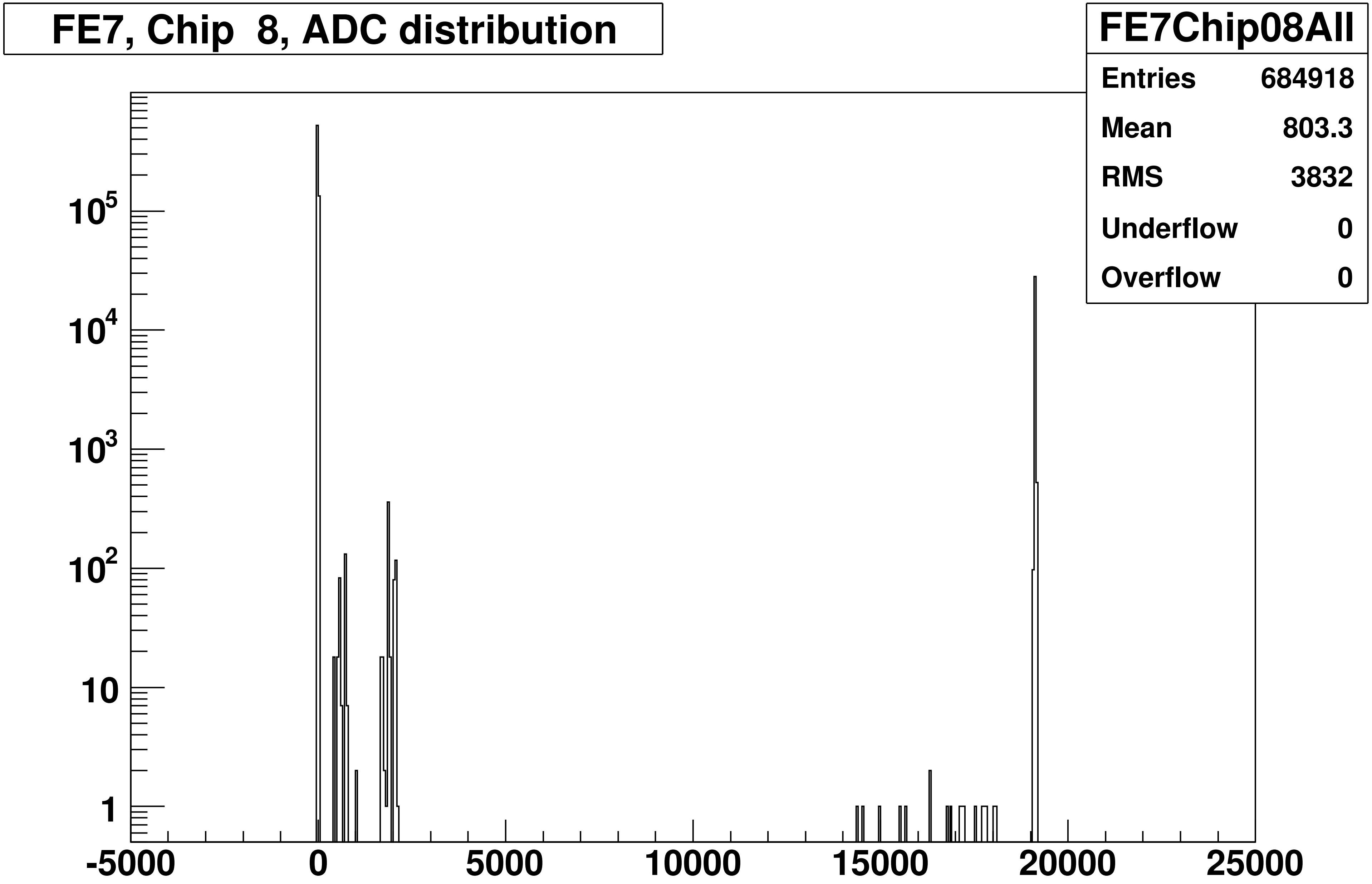

FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 684918
Mean 1.921e+04
RMS 12.97
Underflow 6.557e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



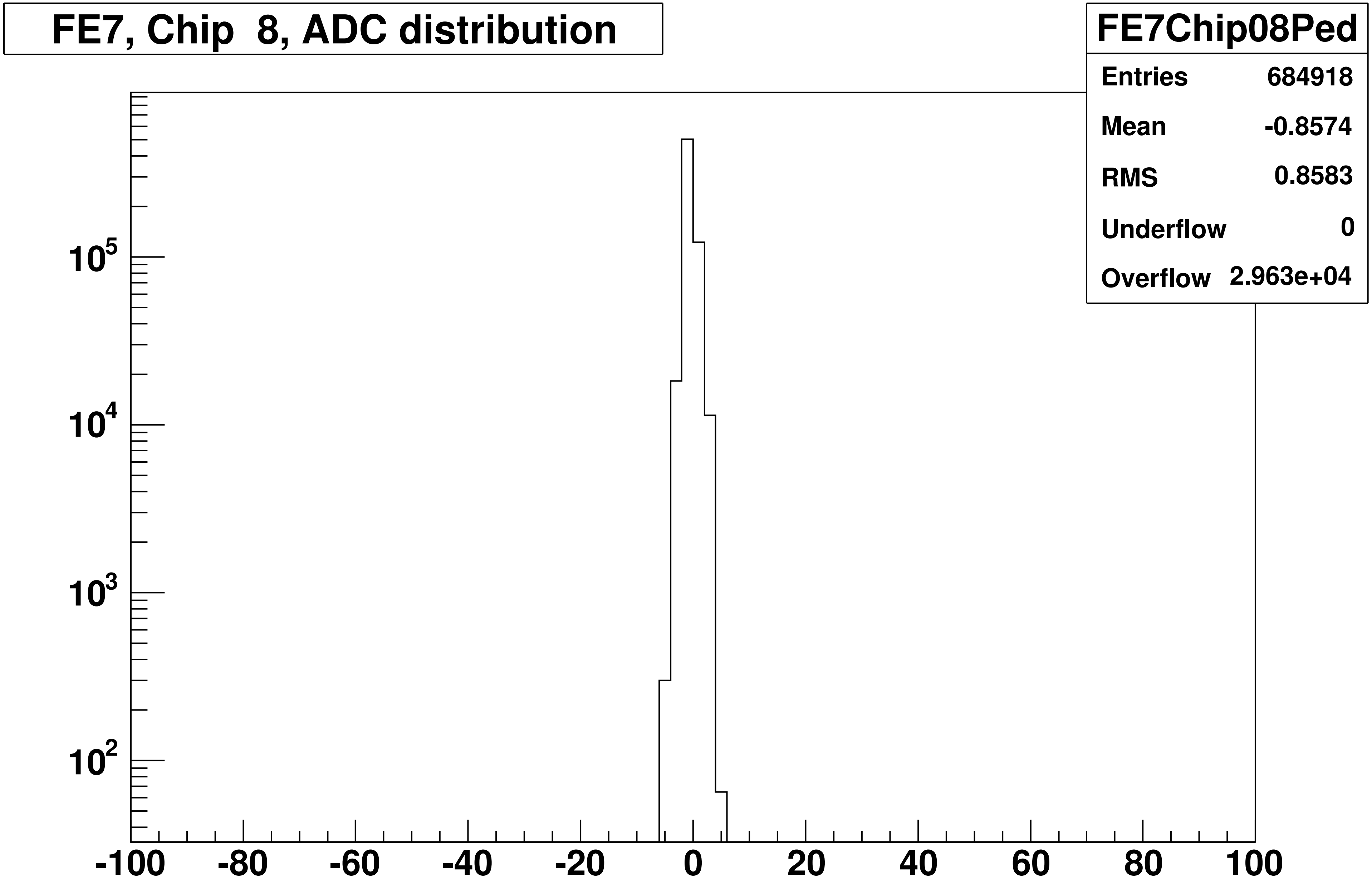
FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 684918
Mean 807.1
RMS 3838
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

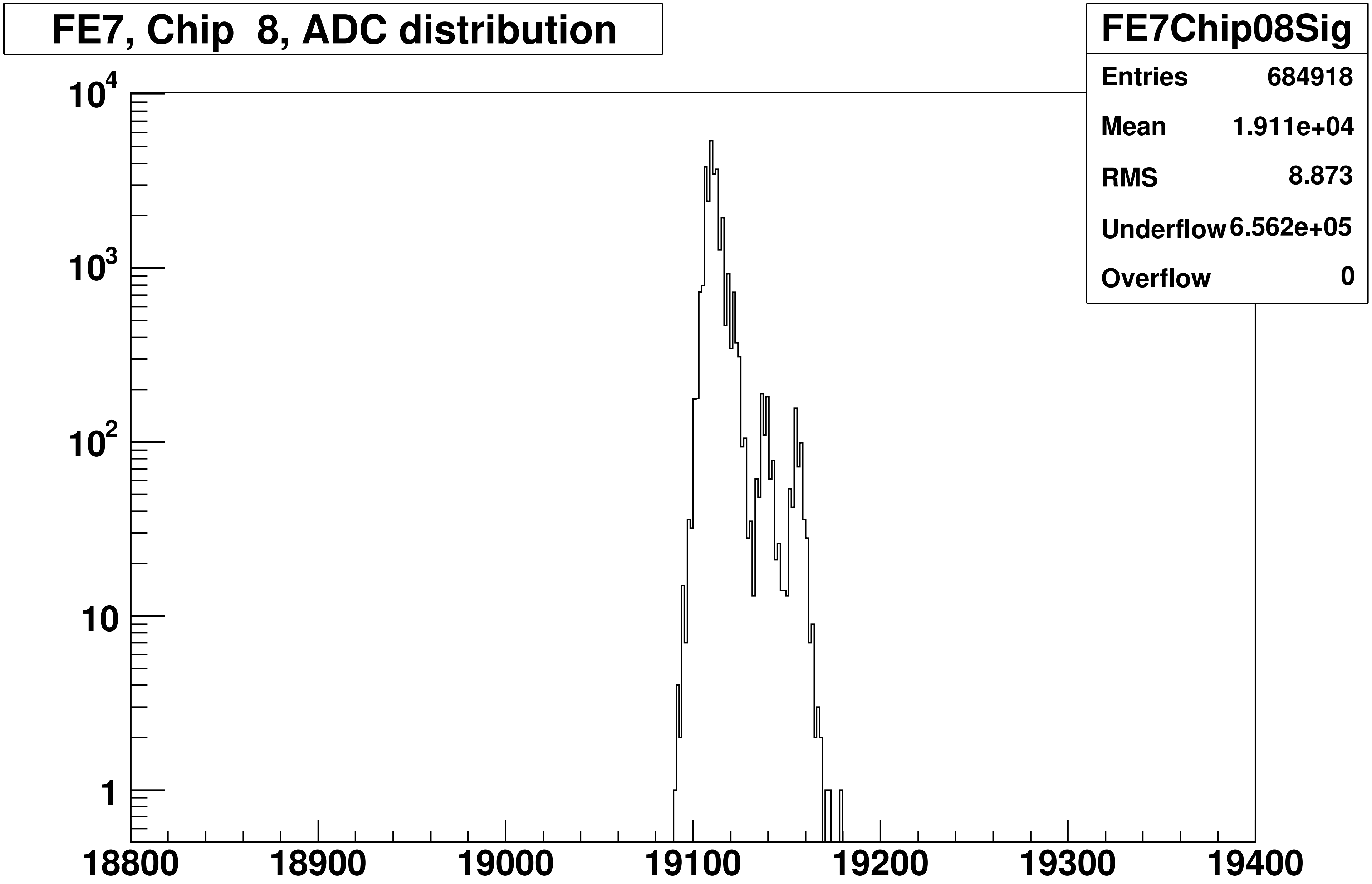

FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 684918
Mean -1.378
RMS 1.069
Underflow 0
Overflow 2.912e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

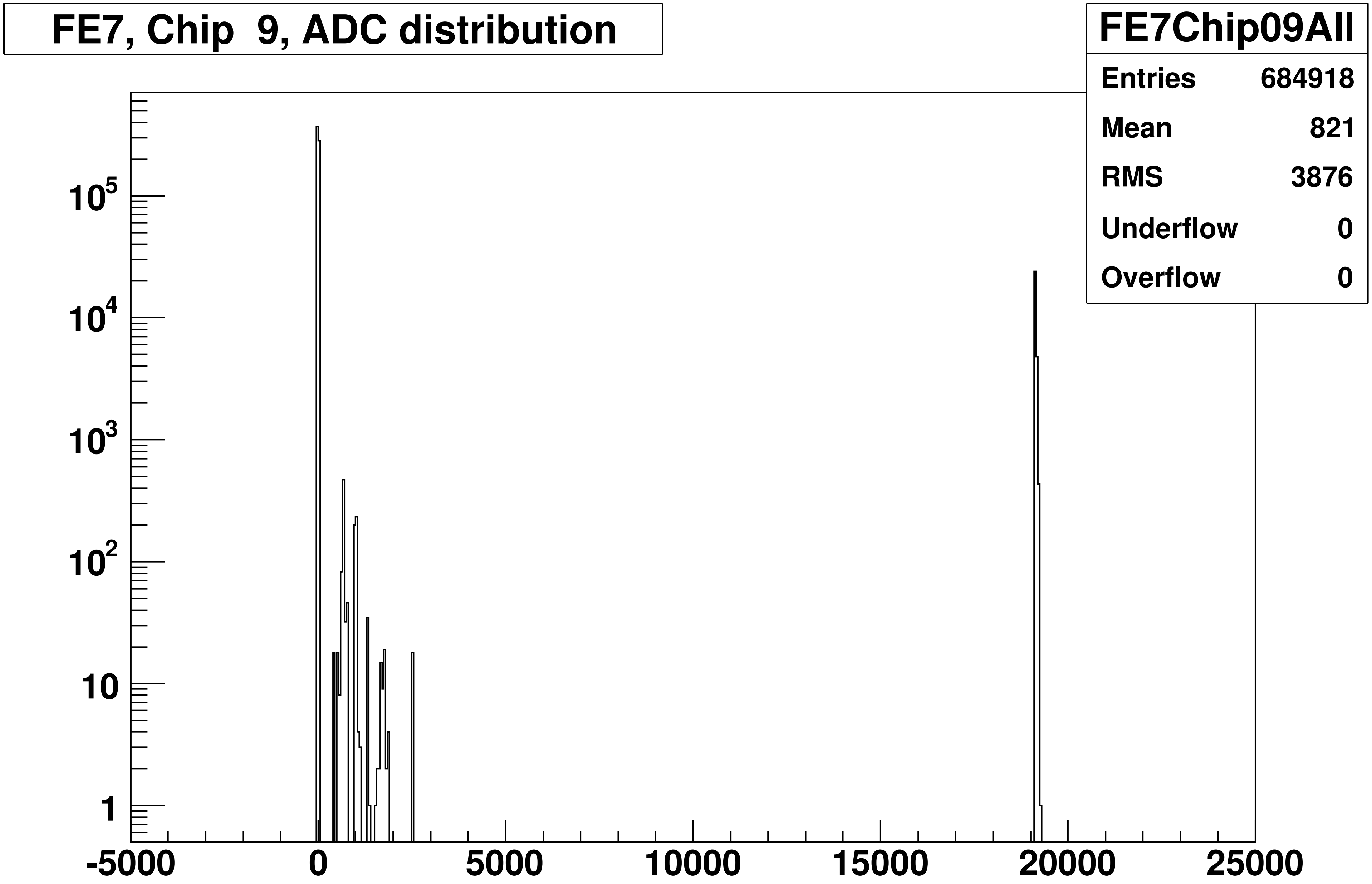

FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 684918
Mean 1.902e+04
RMS 9.54
Underflow 6.558e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



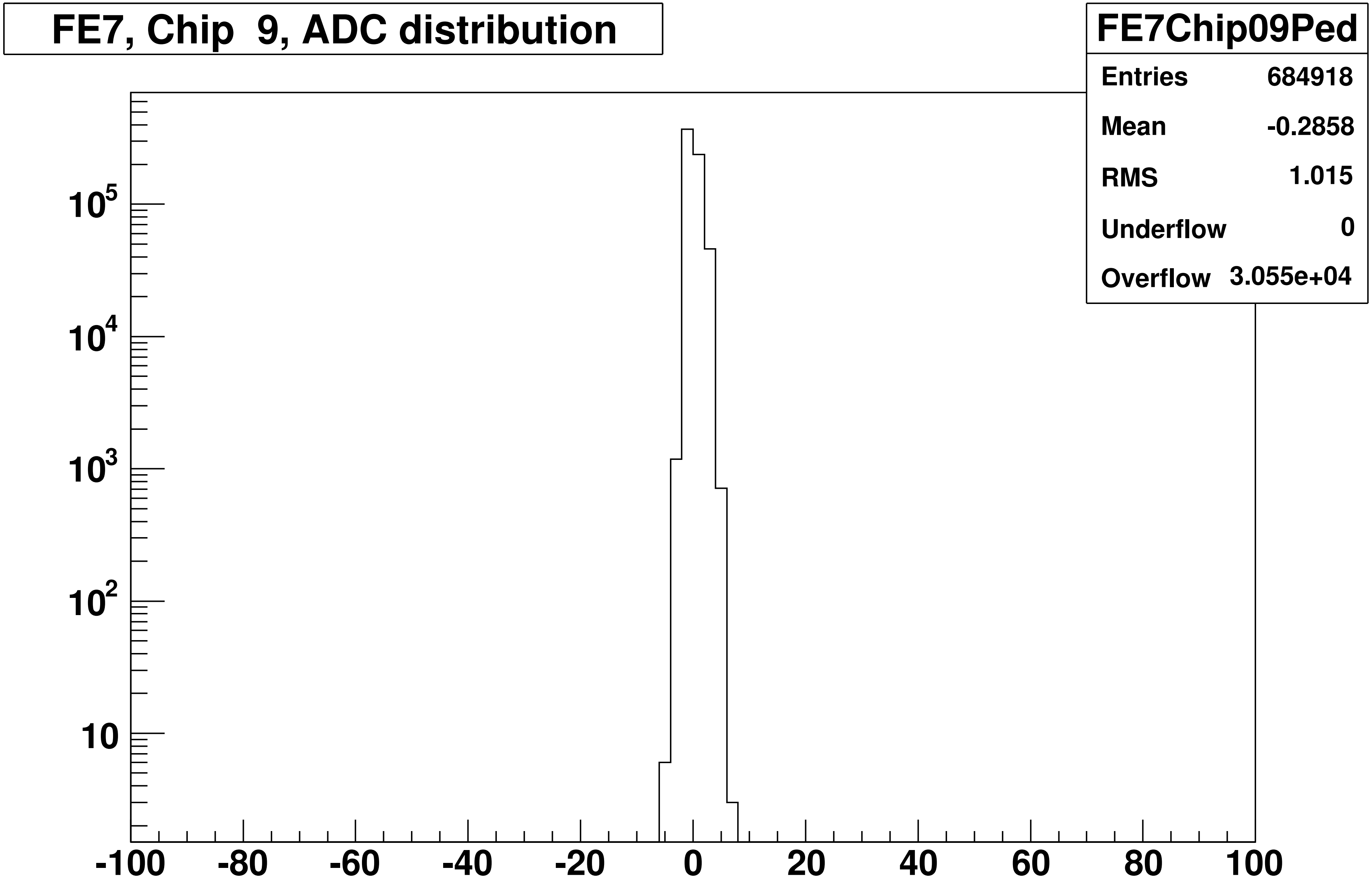
FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 684918
Mean 803.3
RMS 3832
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 684918
Mean -0.8574
RMS 0.8583
Underflow 0
Overflow 2.963e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

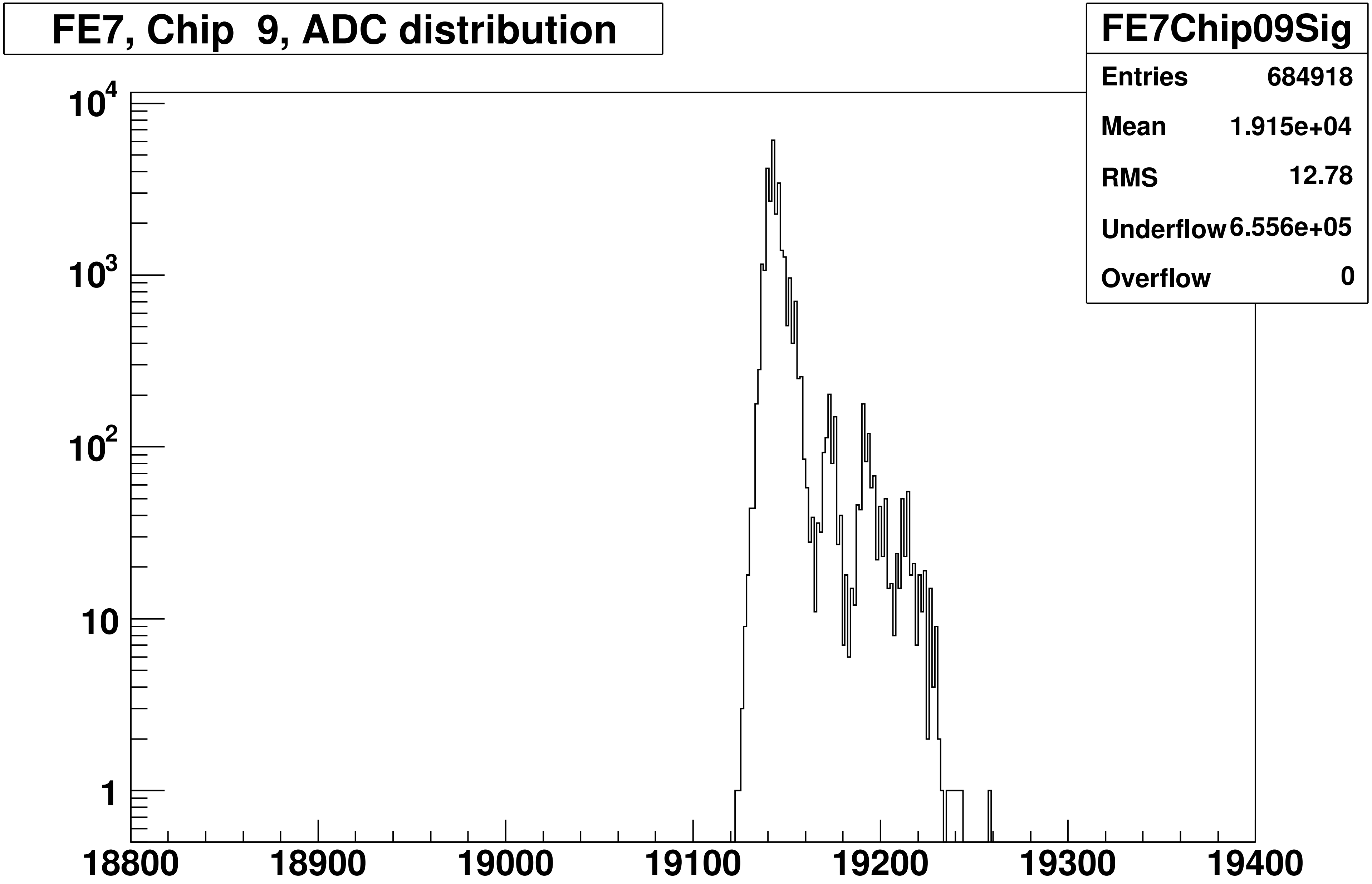

FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 684918
Mean 1.911e+04
RMS 8.873
Underflow 6.562e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

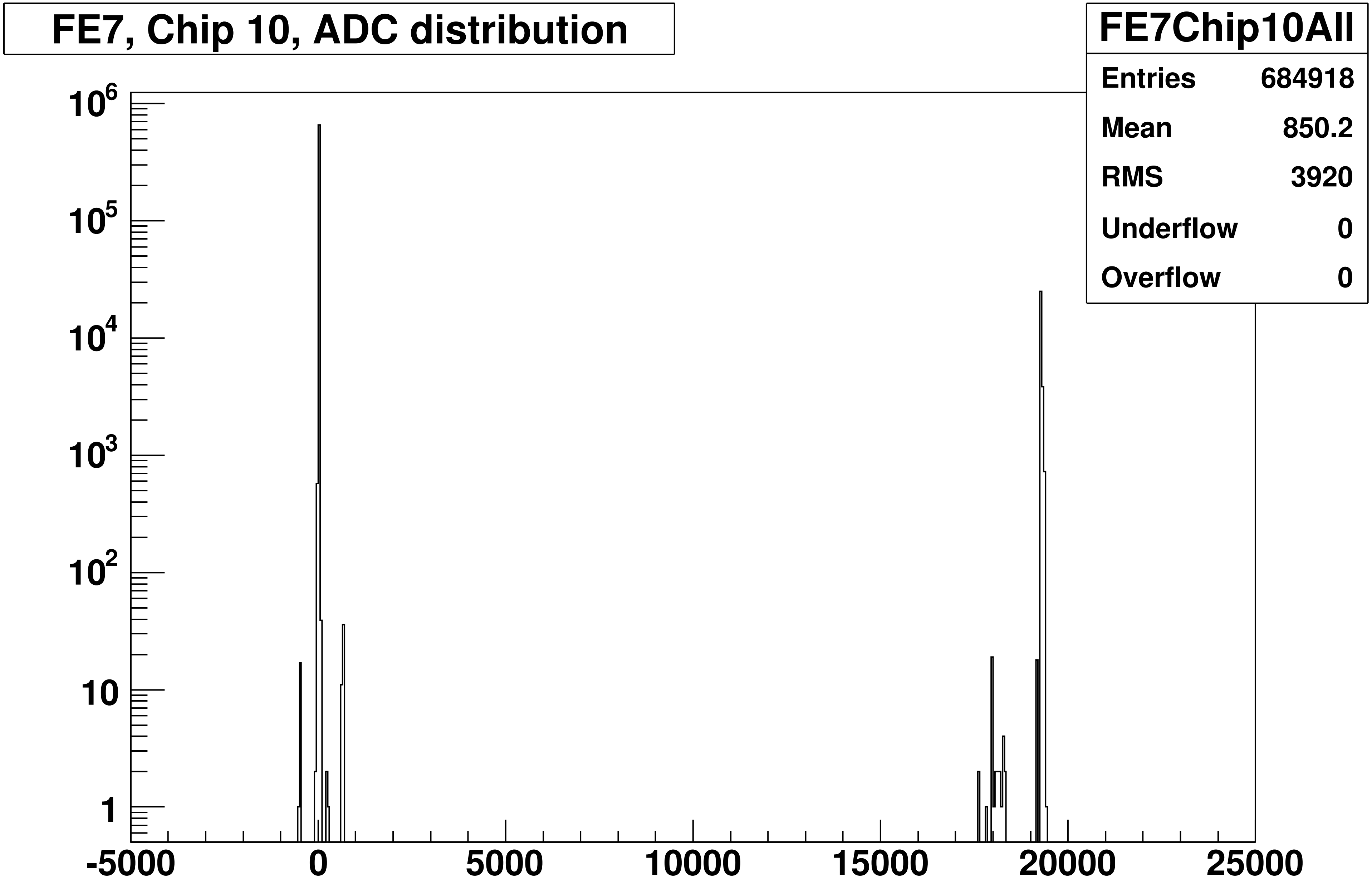


FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 684918
Mean 821
RMS 3876
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

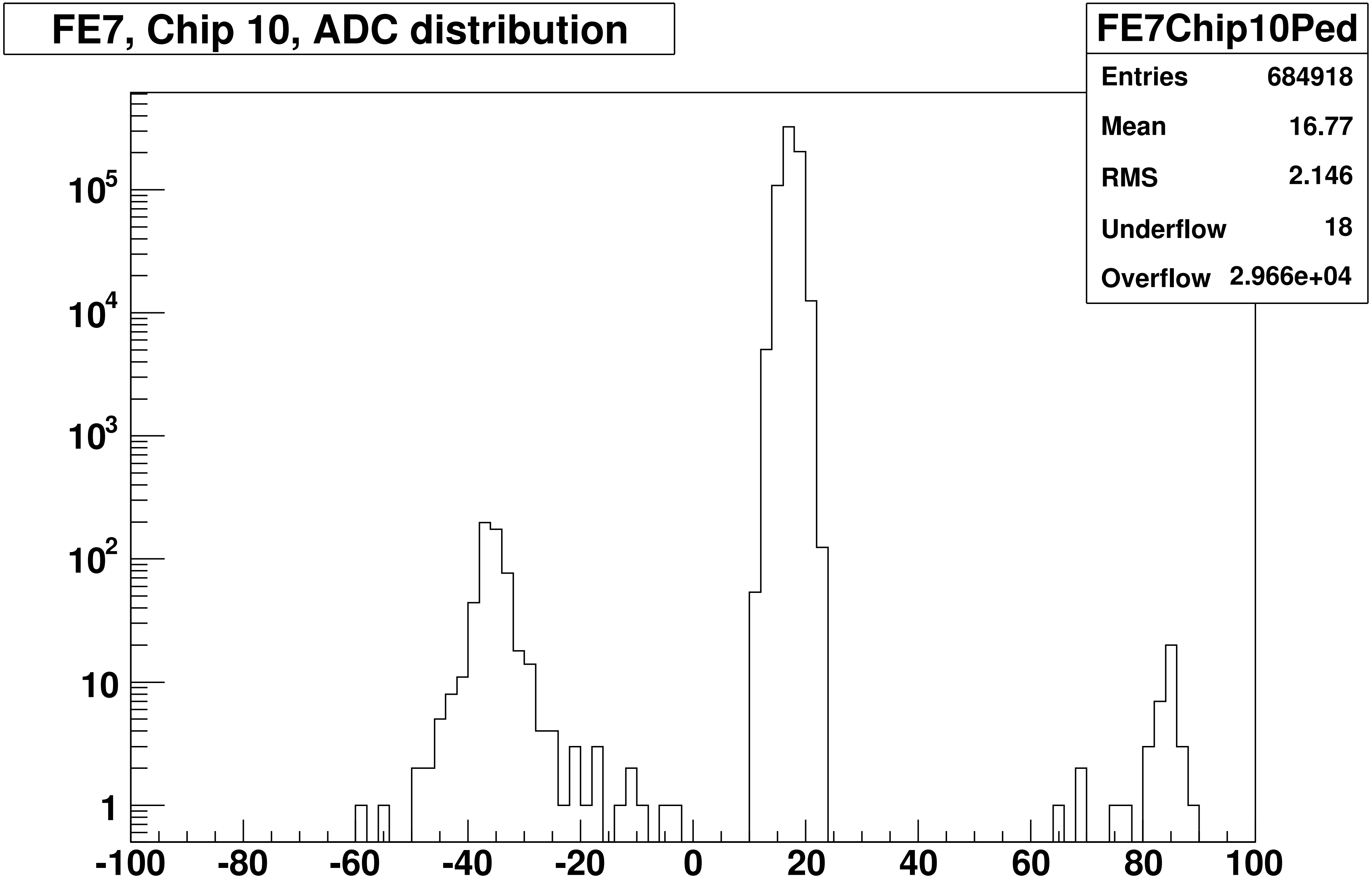


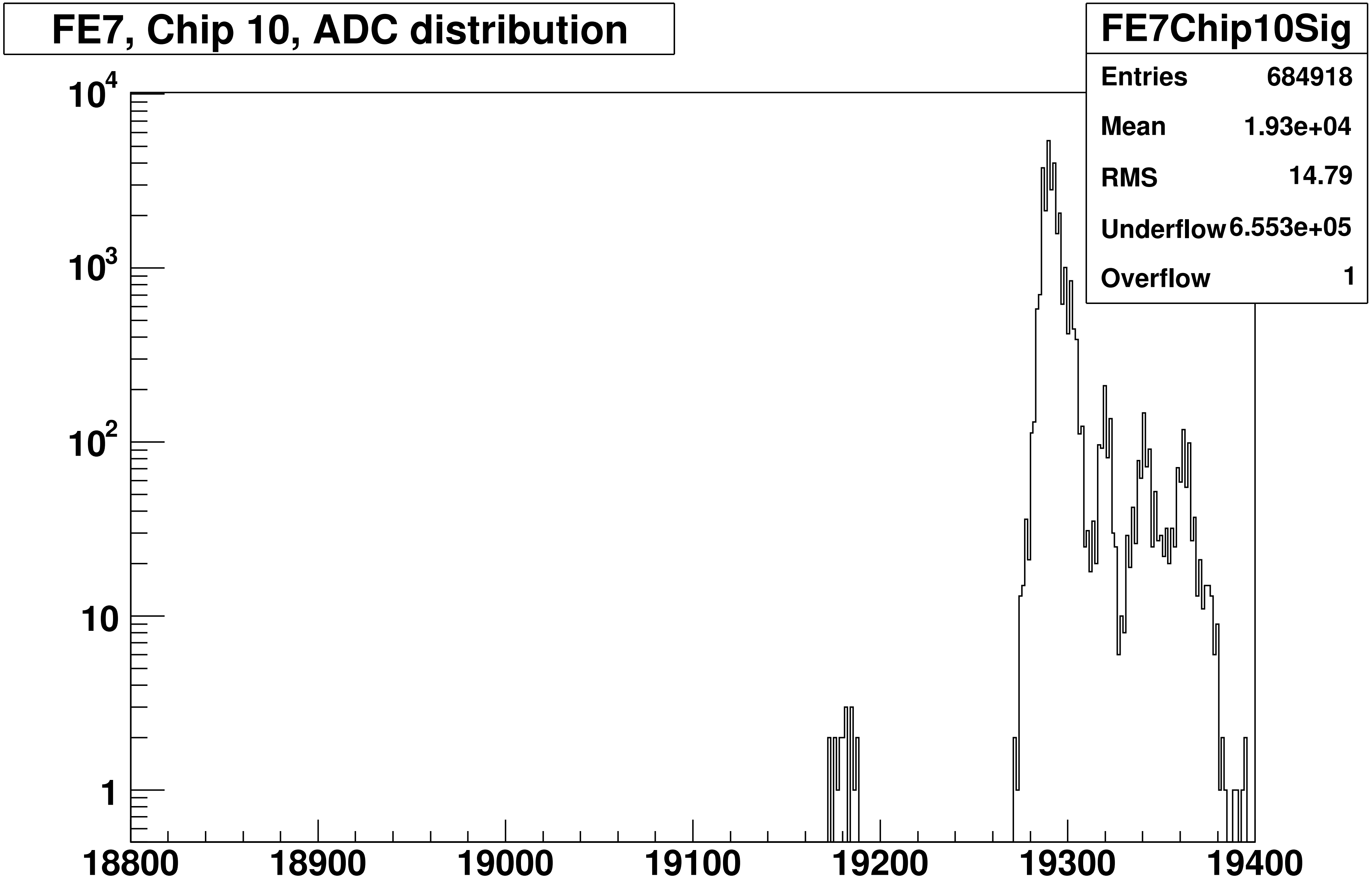
FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 684918
Mean -0.2858
RMS 1.015
Underflow 0
Overflow 3.055e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

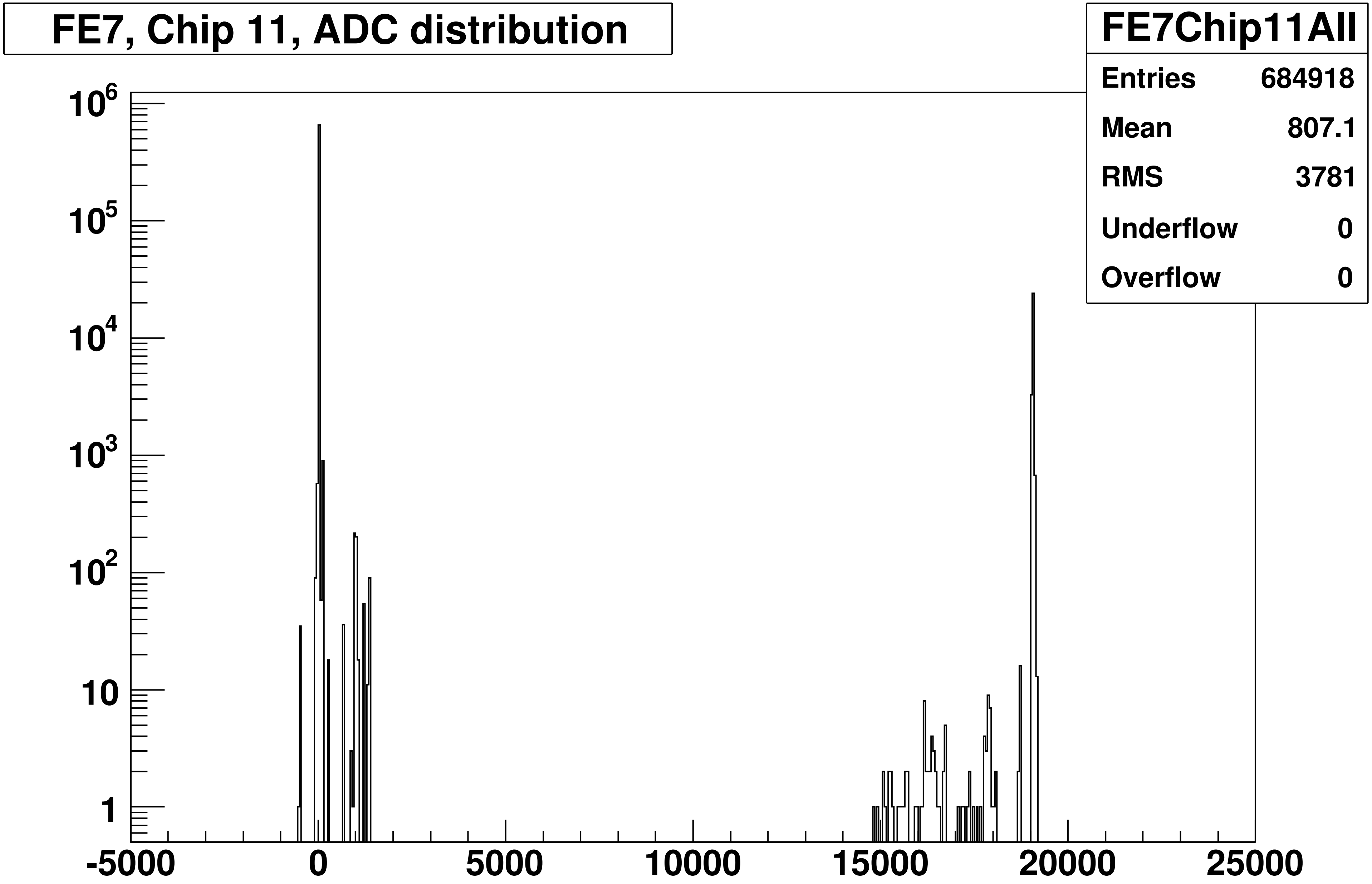

FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 684918
Mean 1.915e+04
RMS 12.78
Underflow 6.556e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

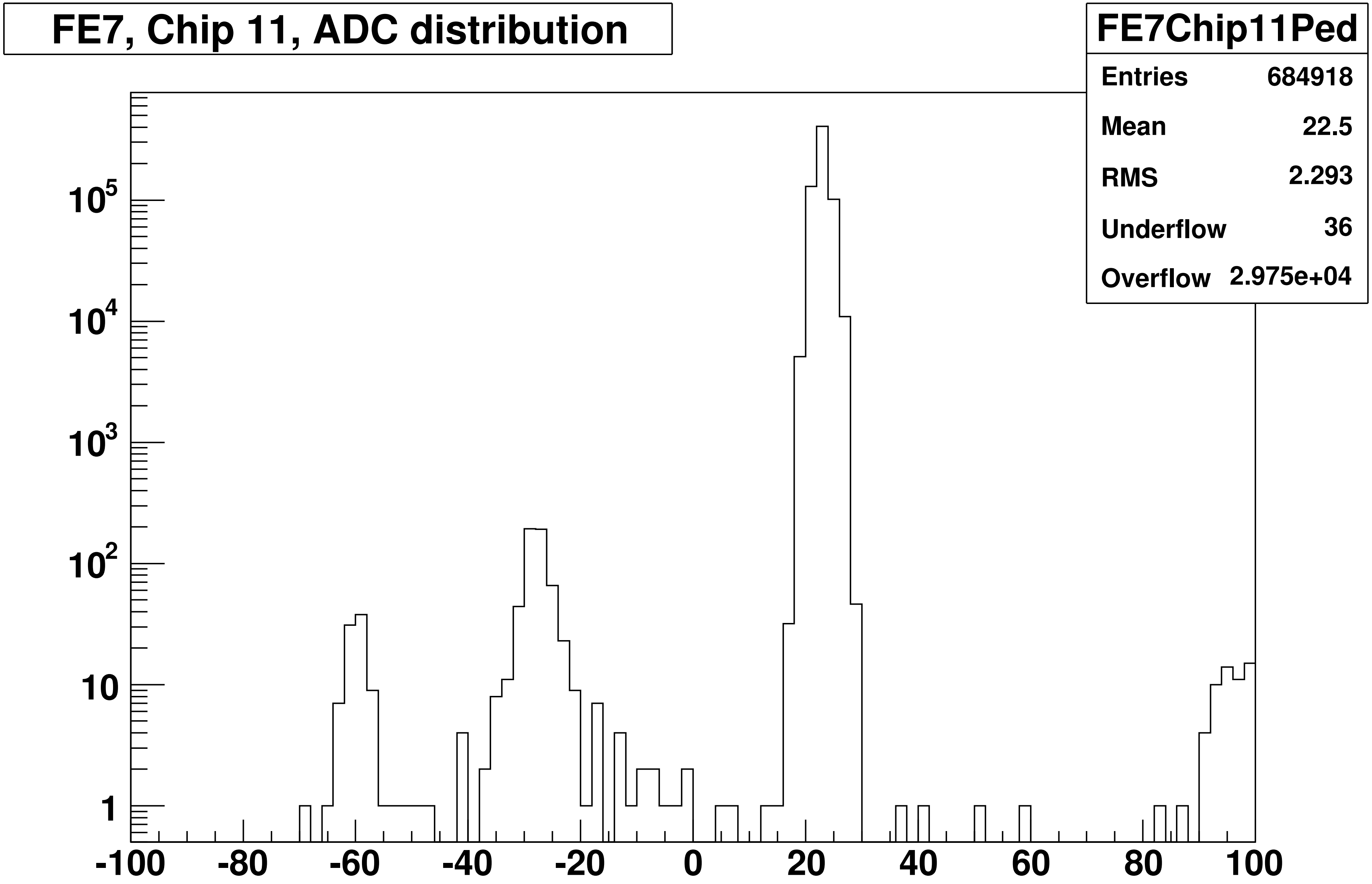


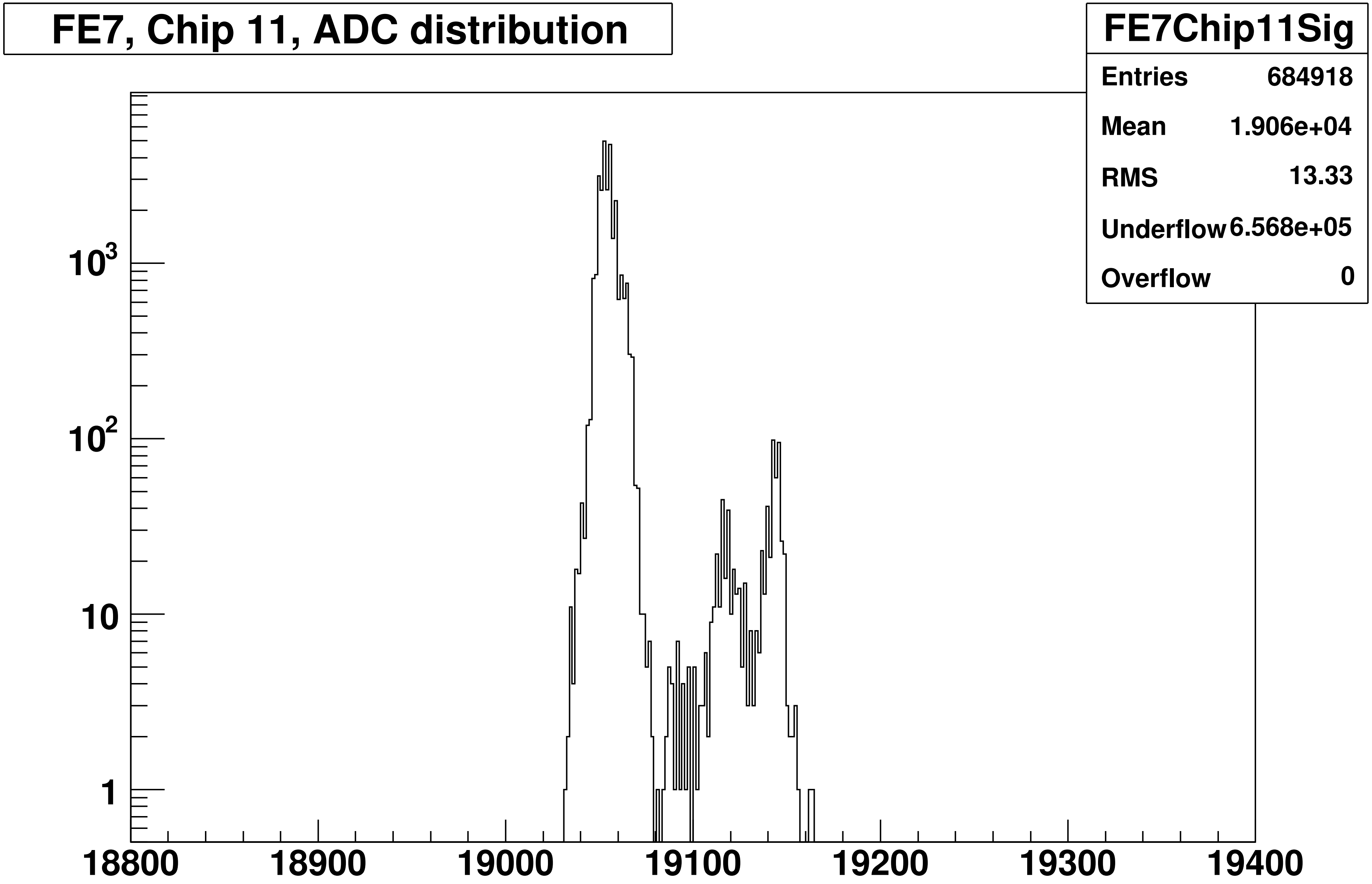
FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 684918
Mean 850.2
RMS 3920
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

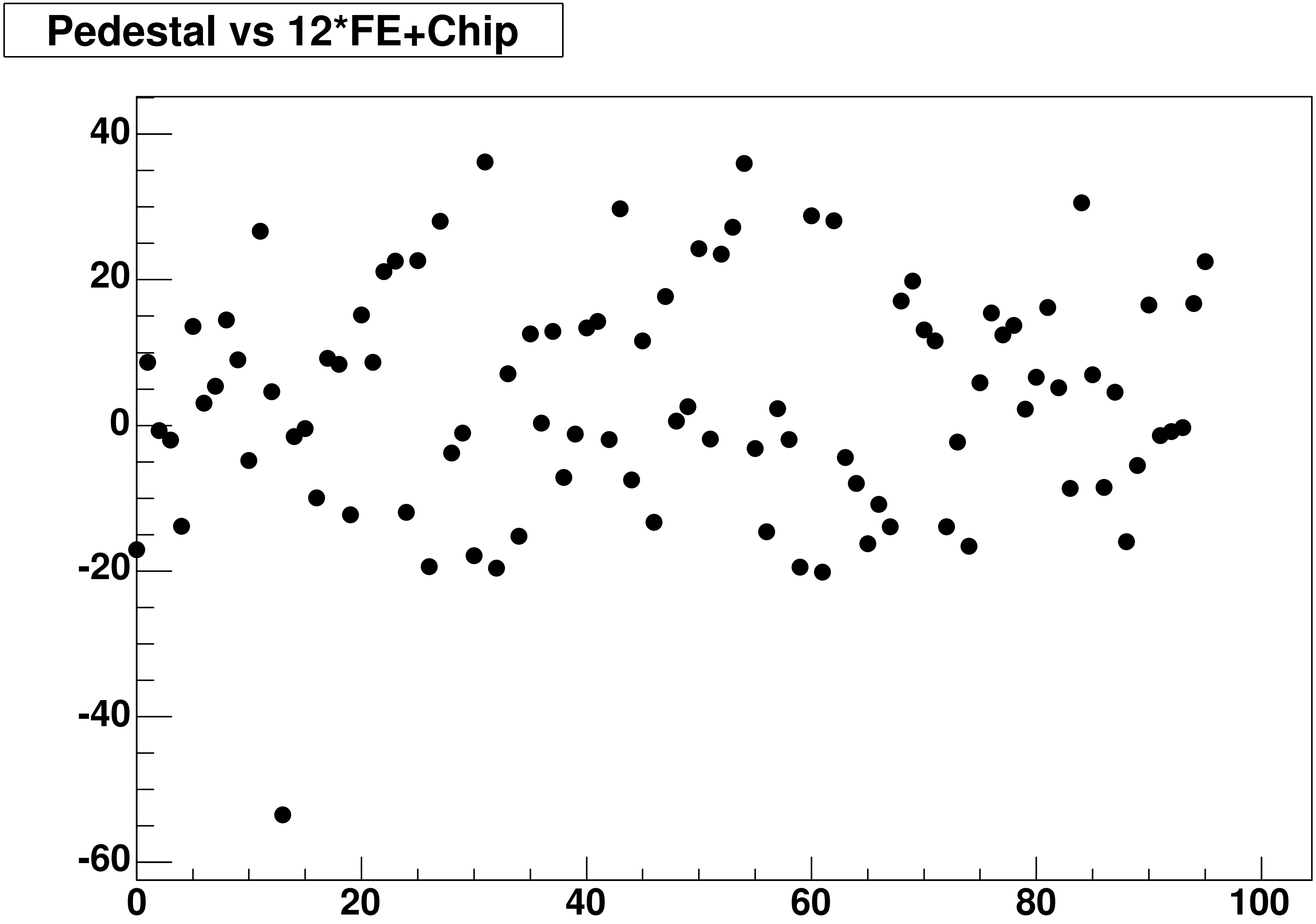

FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 684918
Mean 16.77
RMS 2.146
Underflow 18
Overflow 2.966e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

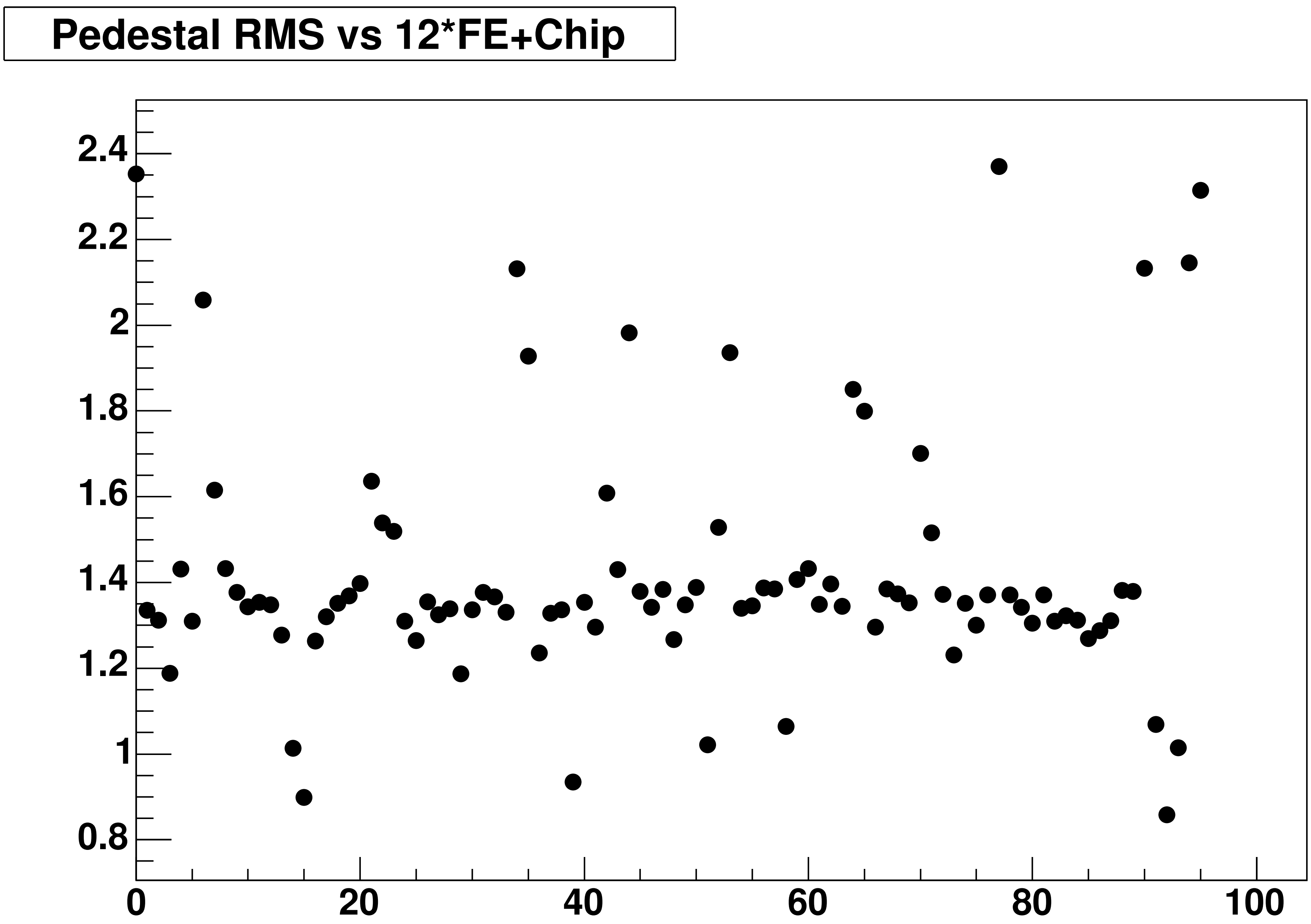

FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 684918
Mean 1.93e+04
RMS 14.79
Underflow 6.553e+05
Overflow 1
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

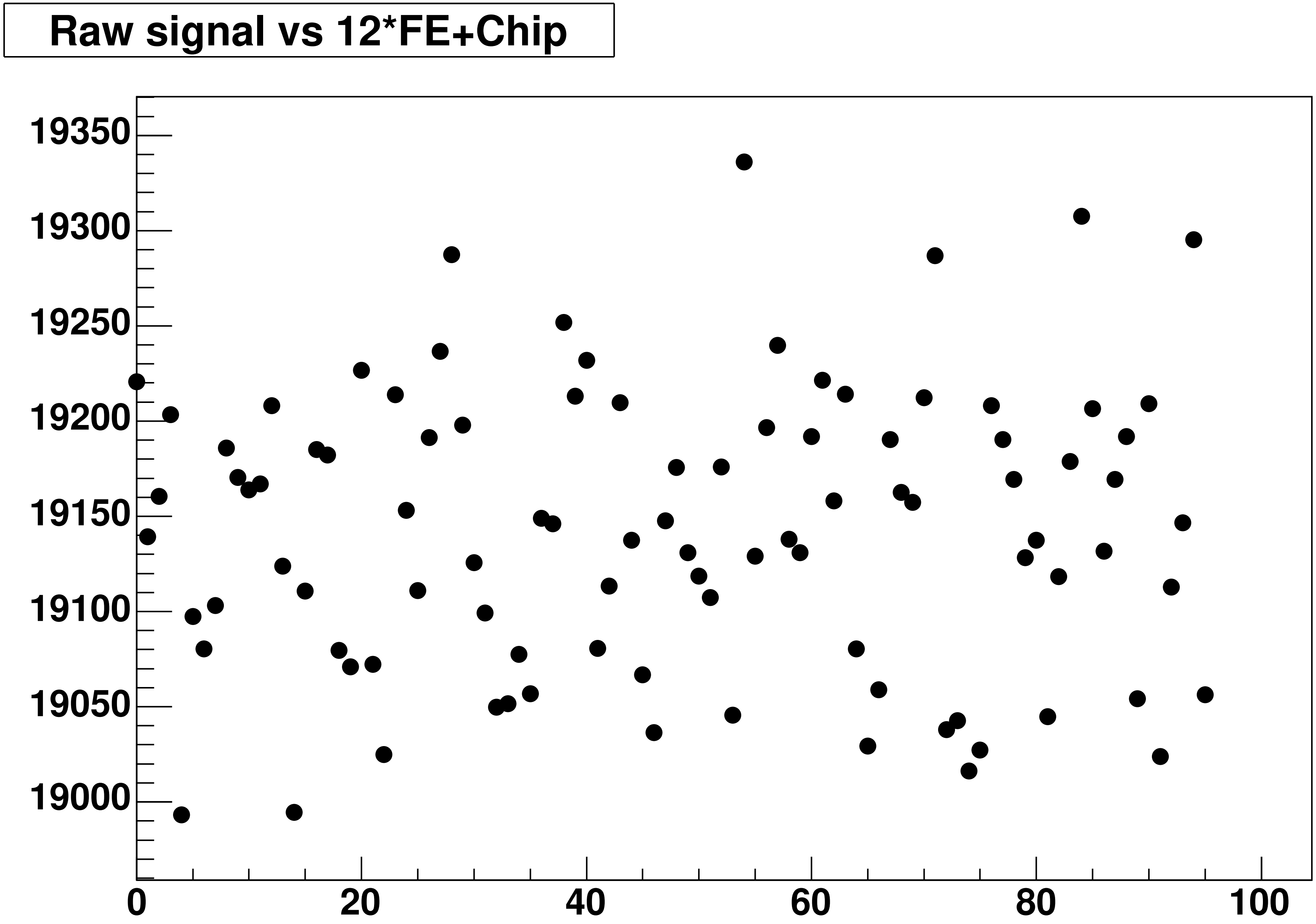

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 684918
Mean 807.1
RMS 3781
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 684918
Mean 22.5
RMS 2.293
Underflow 36
Overflow 2.975e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

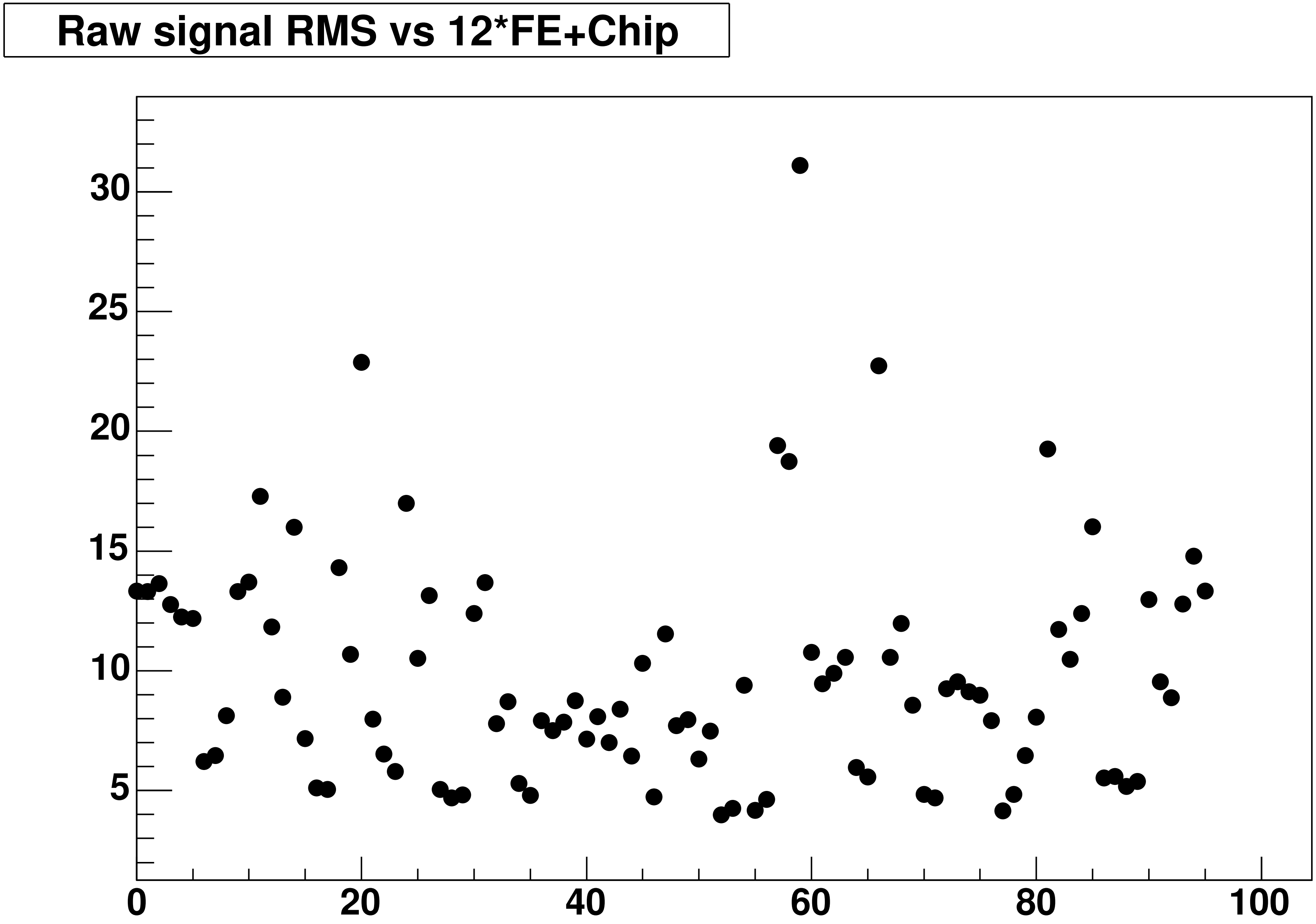

FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 684918
Mean 1.906e+04
RMS 13.33
Underflow 6.568e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


Pedestal vs 12*FE+Chip
40
20
0
-20
-40
-60
0
20
40
60
80
100

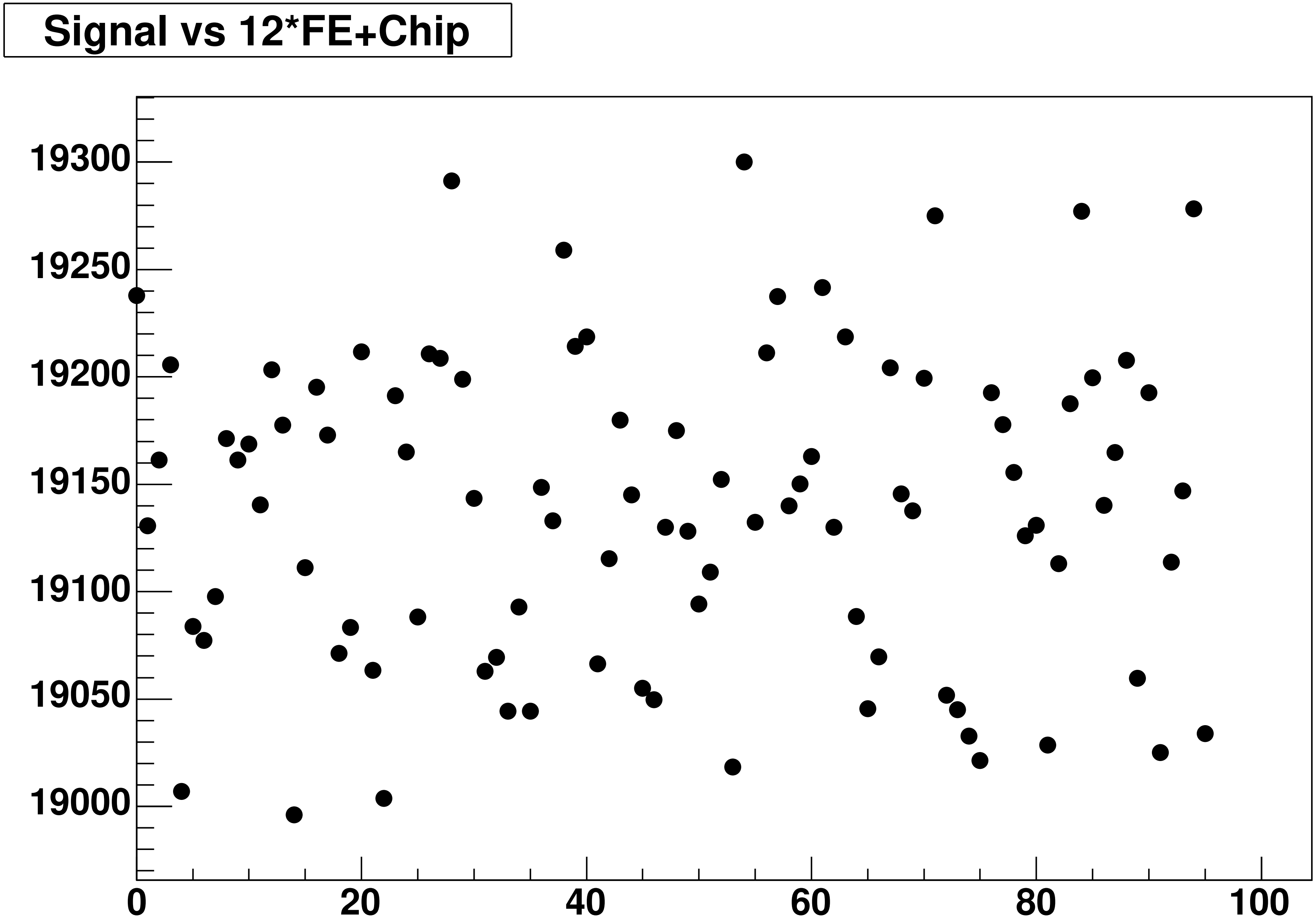

Pedestal RMS vs 12*FE+Chip
2.4
2.2
2
1.8
1.6
1.4
1.2
1
0.8
0
20
40
60
80
100

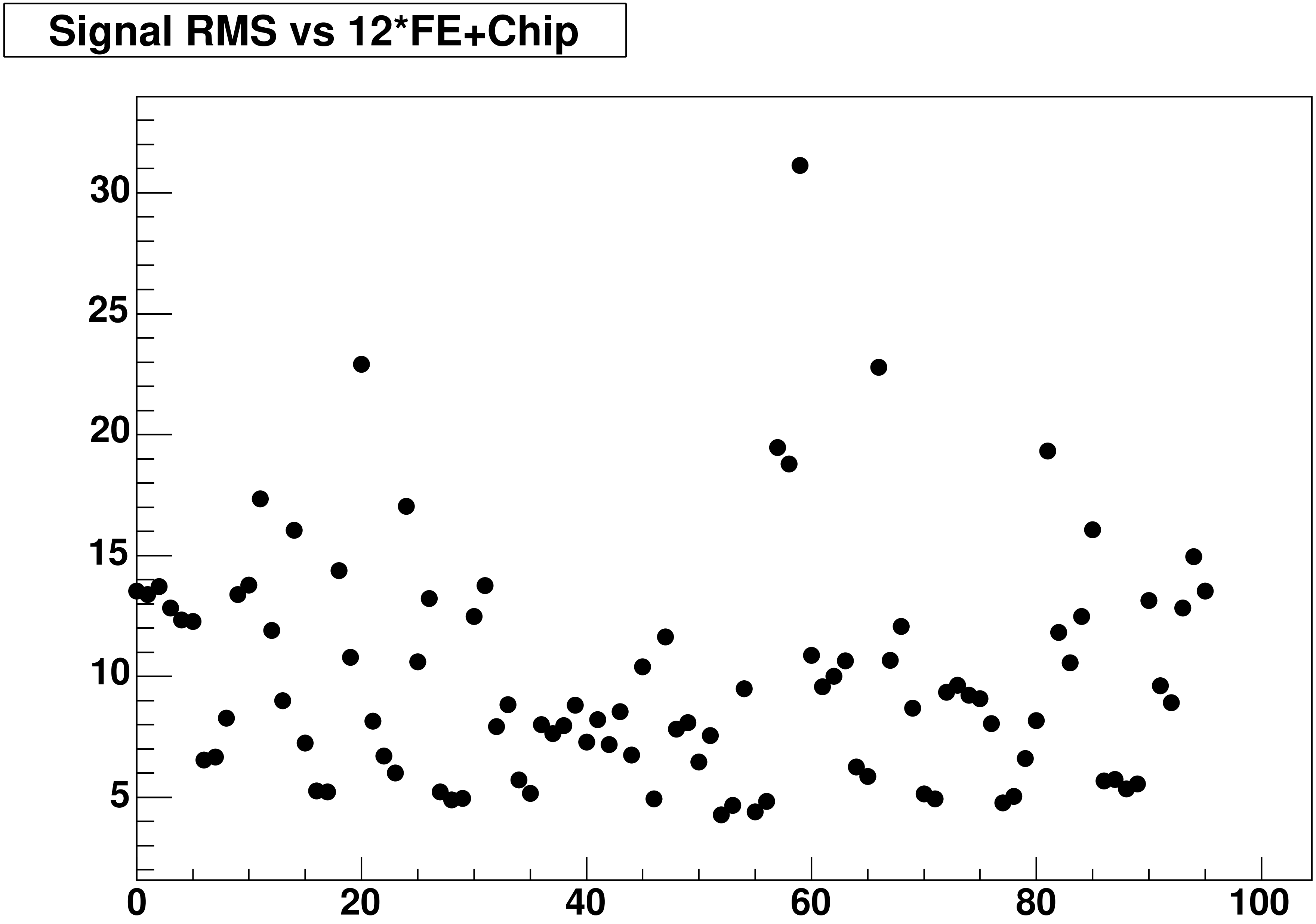


Raw signal vs 12*FE+Chip
19350
19300
19250
19200
19150
19100
19050
19000
0
20
40
60
80
100



Raw signal RMS vs 12*FE+Chip
30
25
20
15
10
5
0
20
40
60
80
100


Signal vs 12*FE+Chip
19300
19250
19200
19150
19100
19050
19000
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
30
25
20
15
10
5
0
20
40
60
80
100