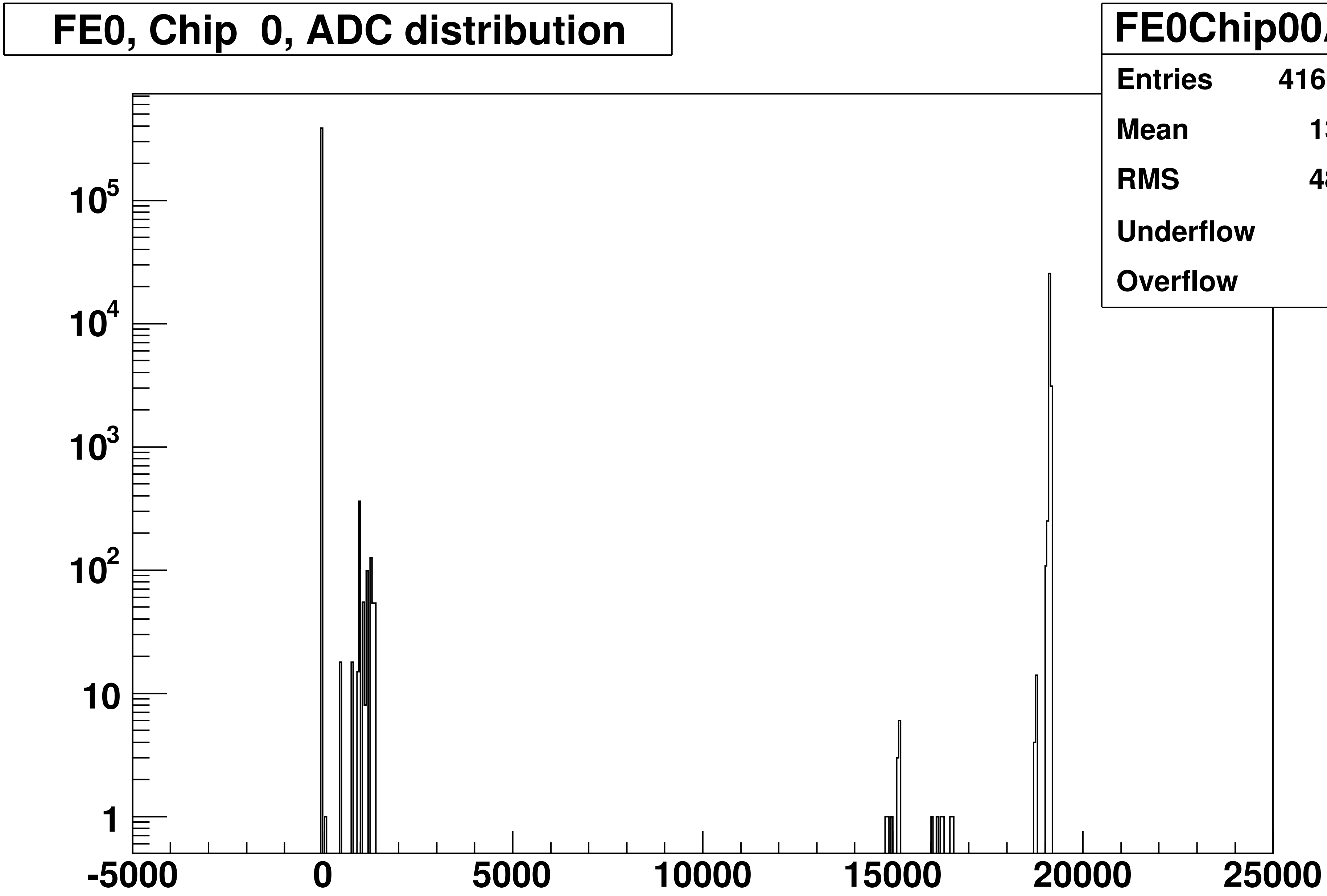
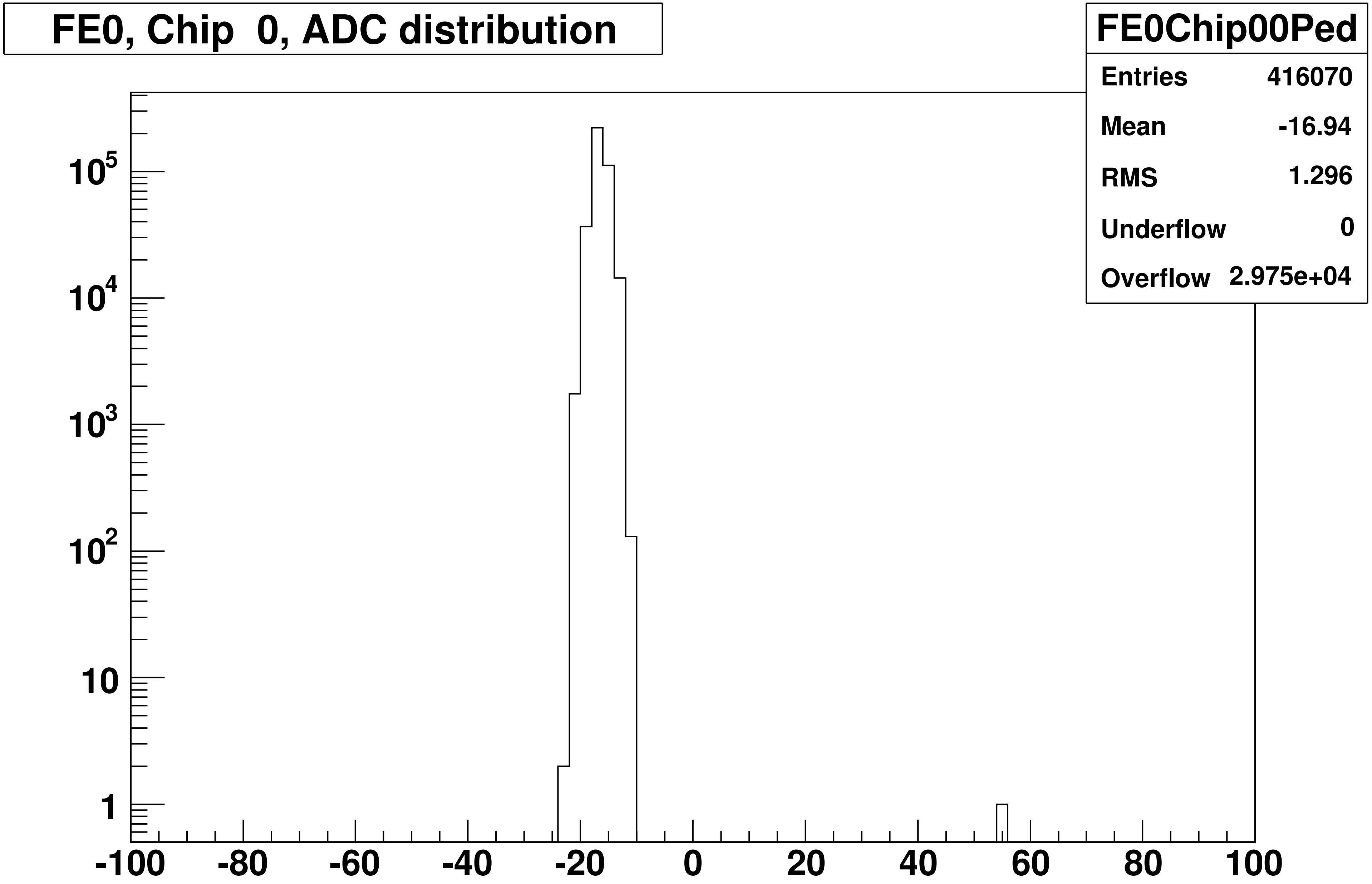
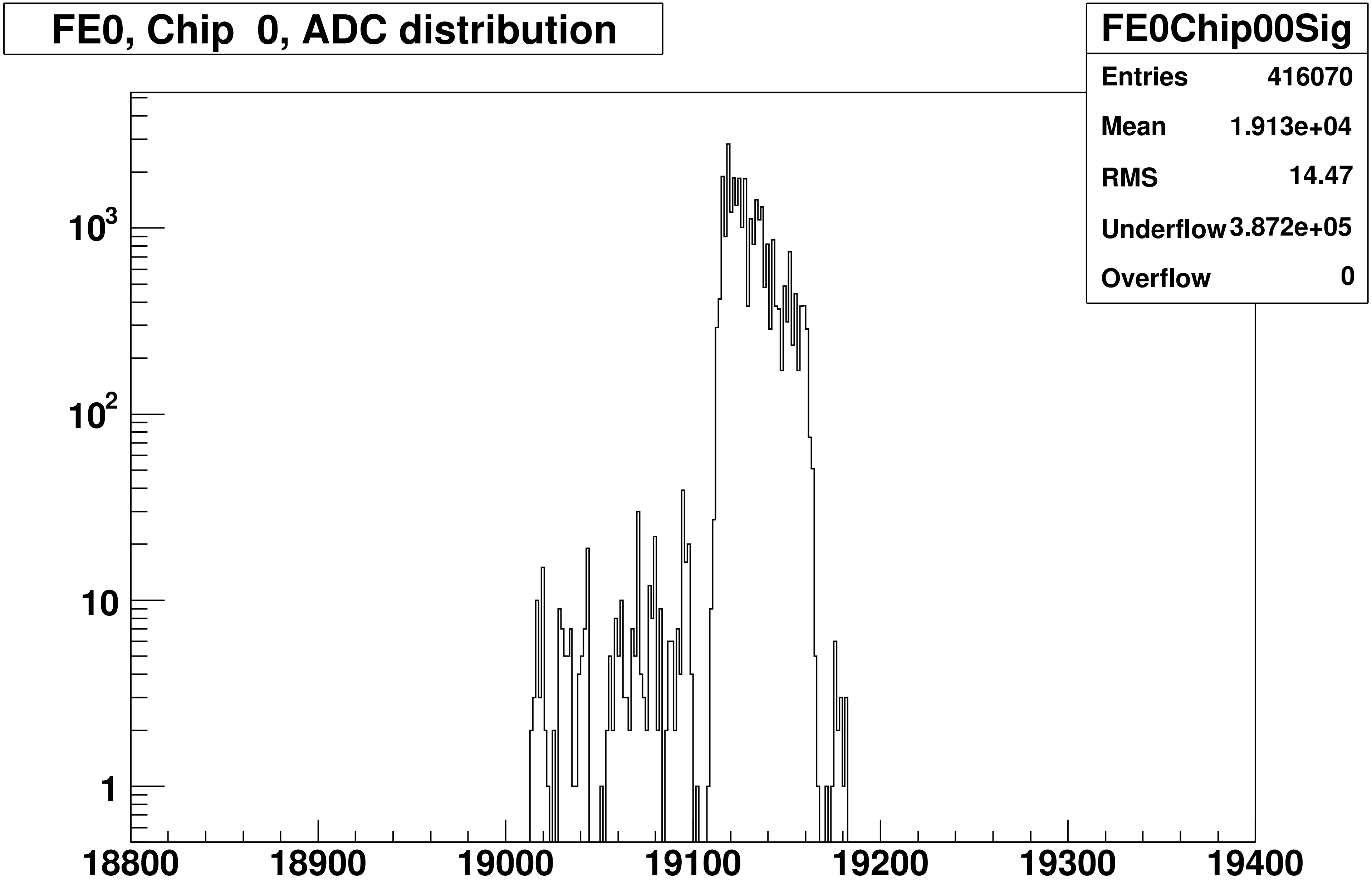

FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 416070
Mean 1317
RMS 4870
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 416070
Mean -16.94
RMS 1.296
Underflow 0
Overflow 2.975e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 416070
Mean 1.913e+04
RMS 14.47
Underflow 3.872e+05
Overflow 0
1
10
10^2
10^3
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18900
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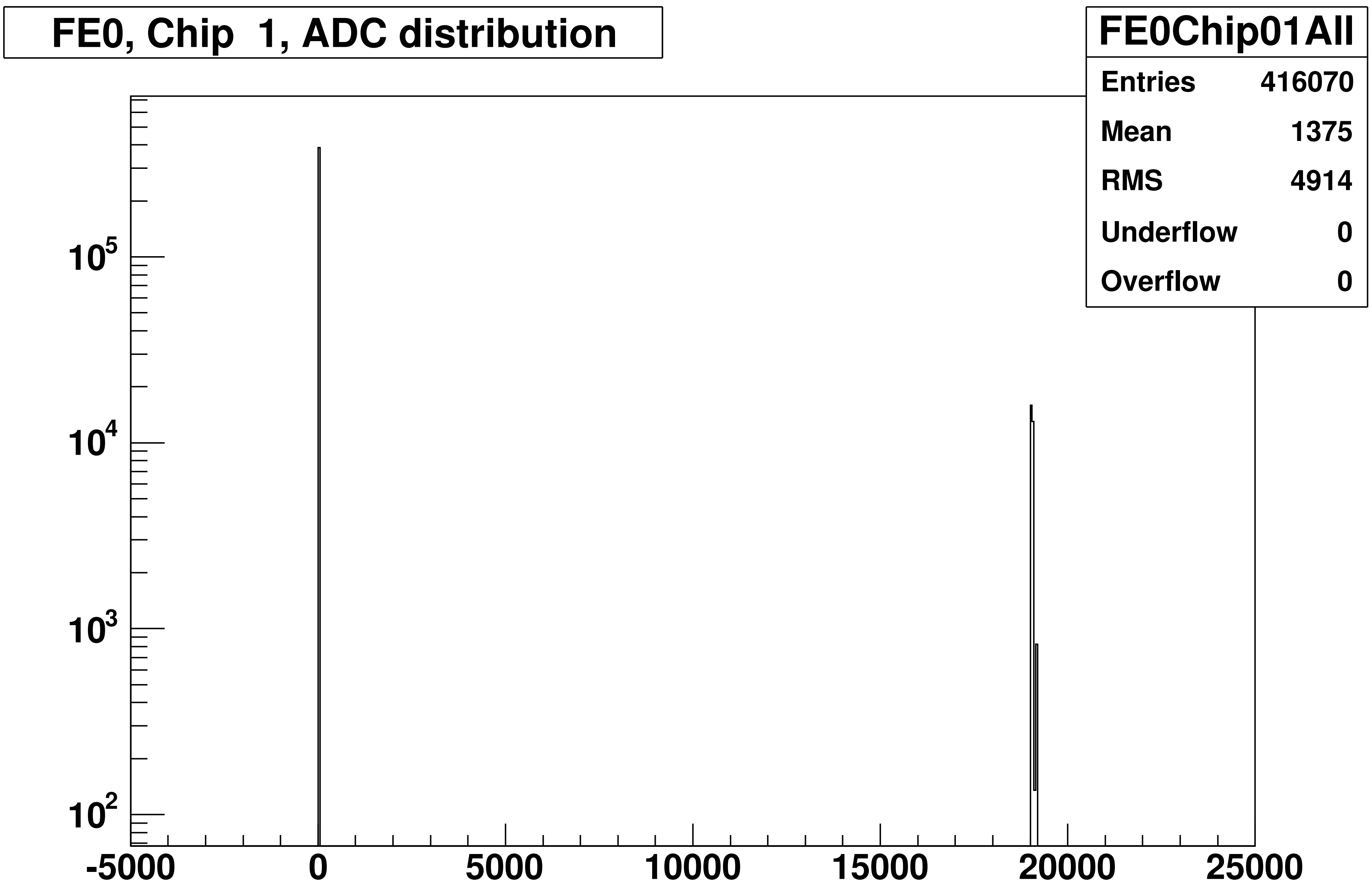
19100
19200
19300
19400


FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 416070
Mean 1375
RMS 4914
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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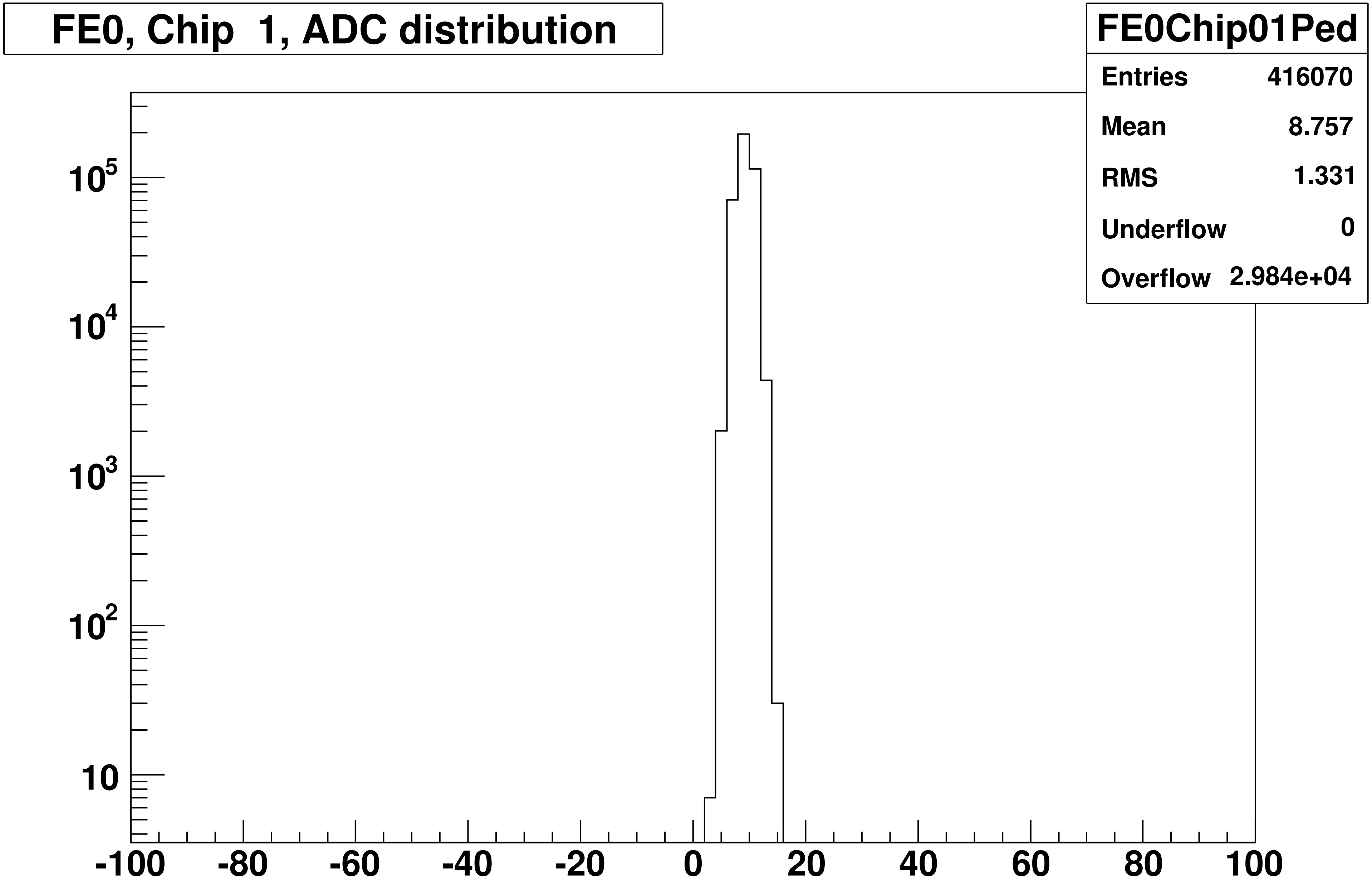
10000
15000
20000
25000



FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 416070
Mean 8.757
RMS 1.331
Underflow 0
Overflow 2.984e+04
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10^2
10^3
10^4
10^5
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-80
-60
-40
-20
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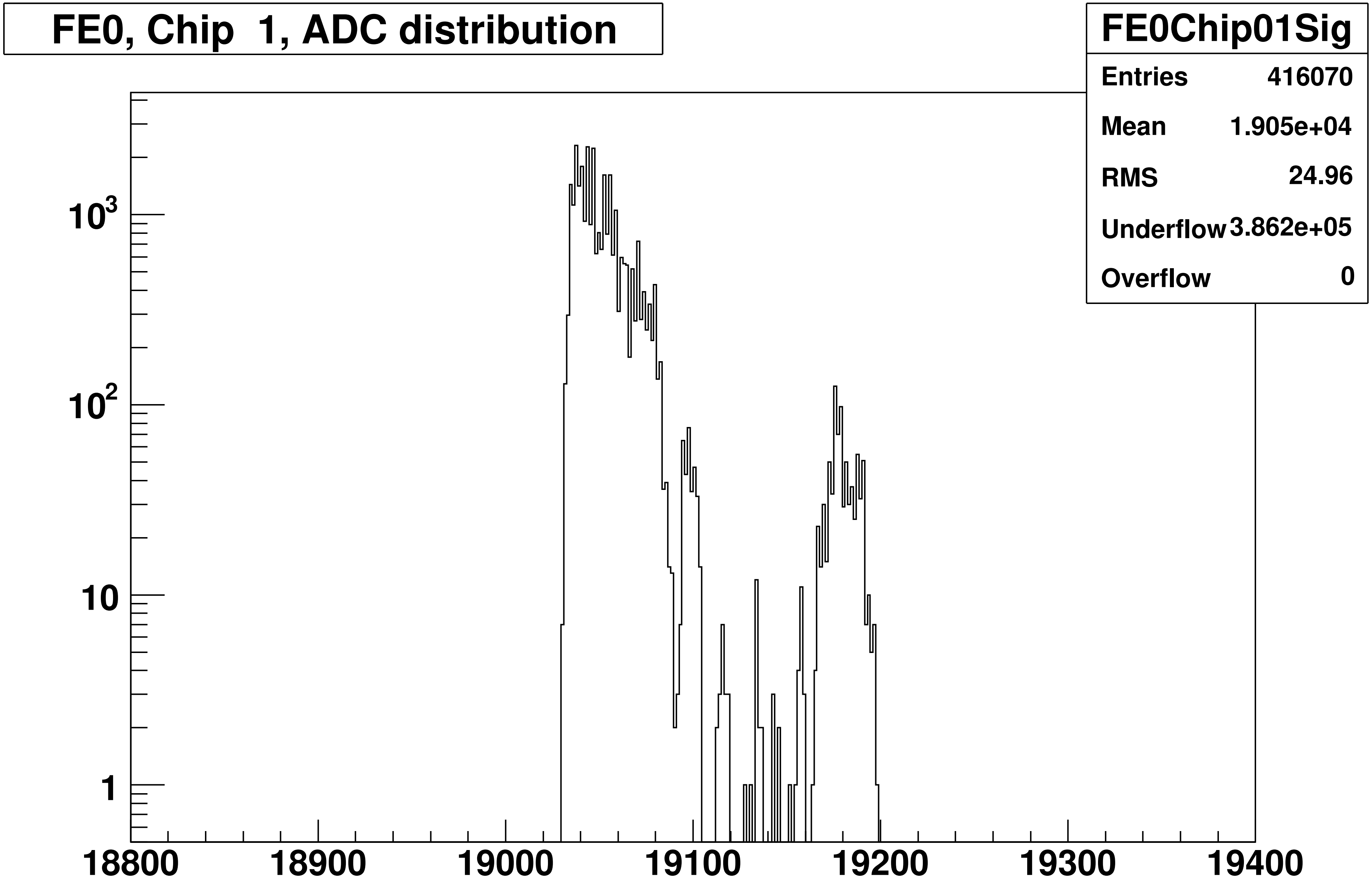
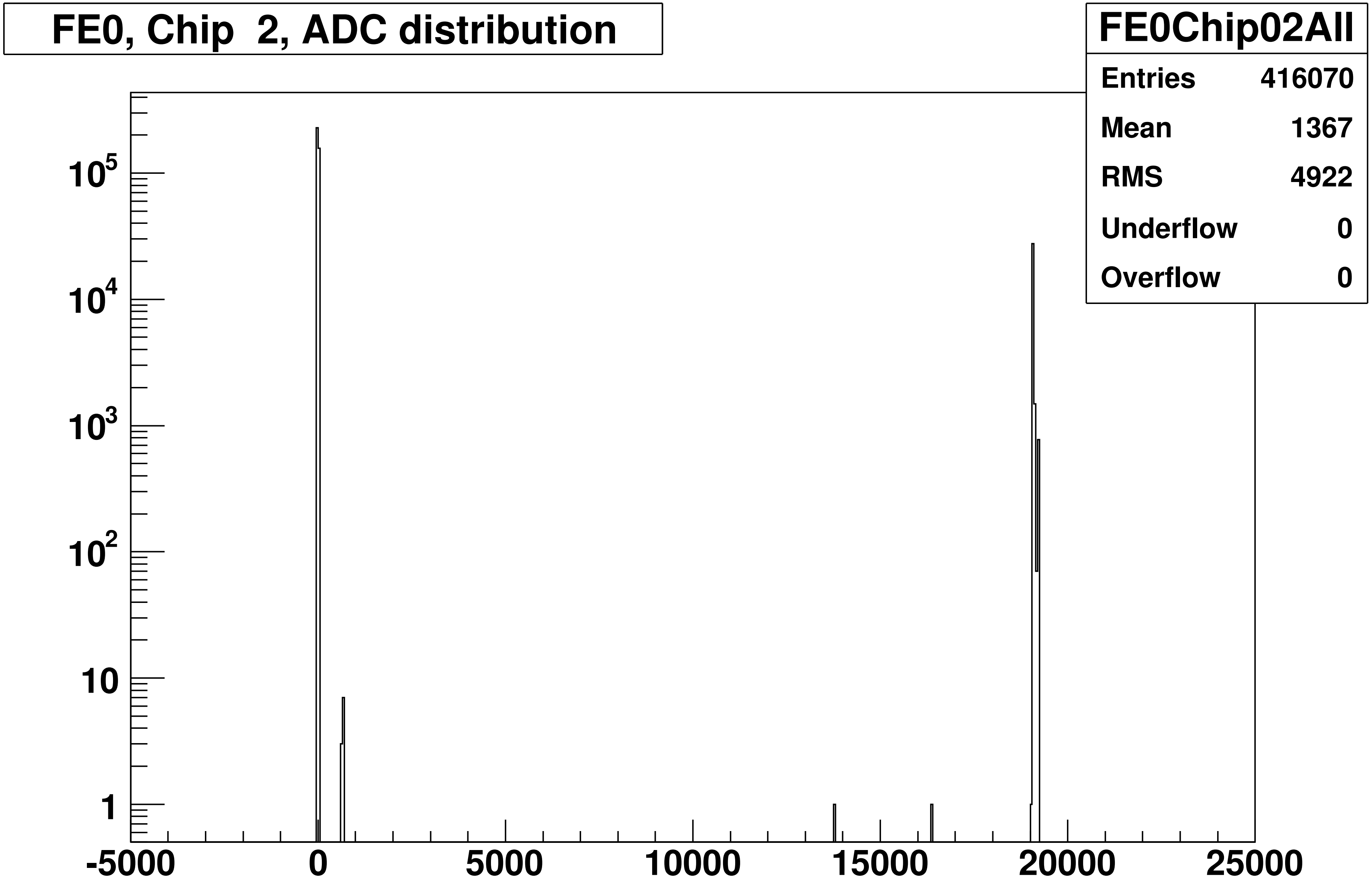
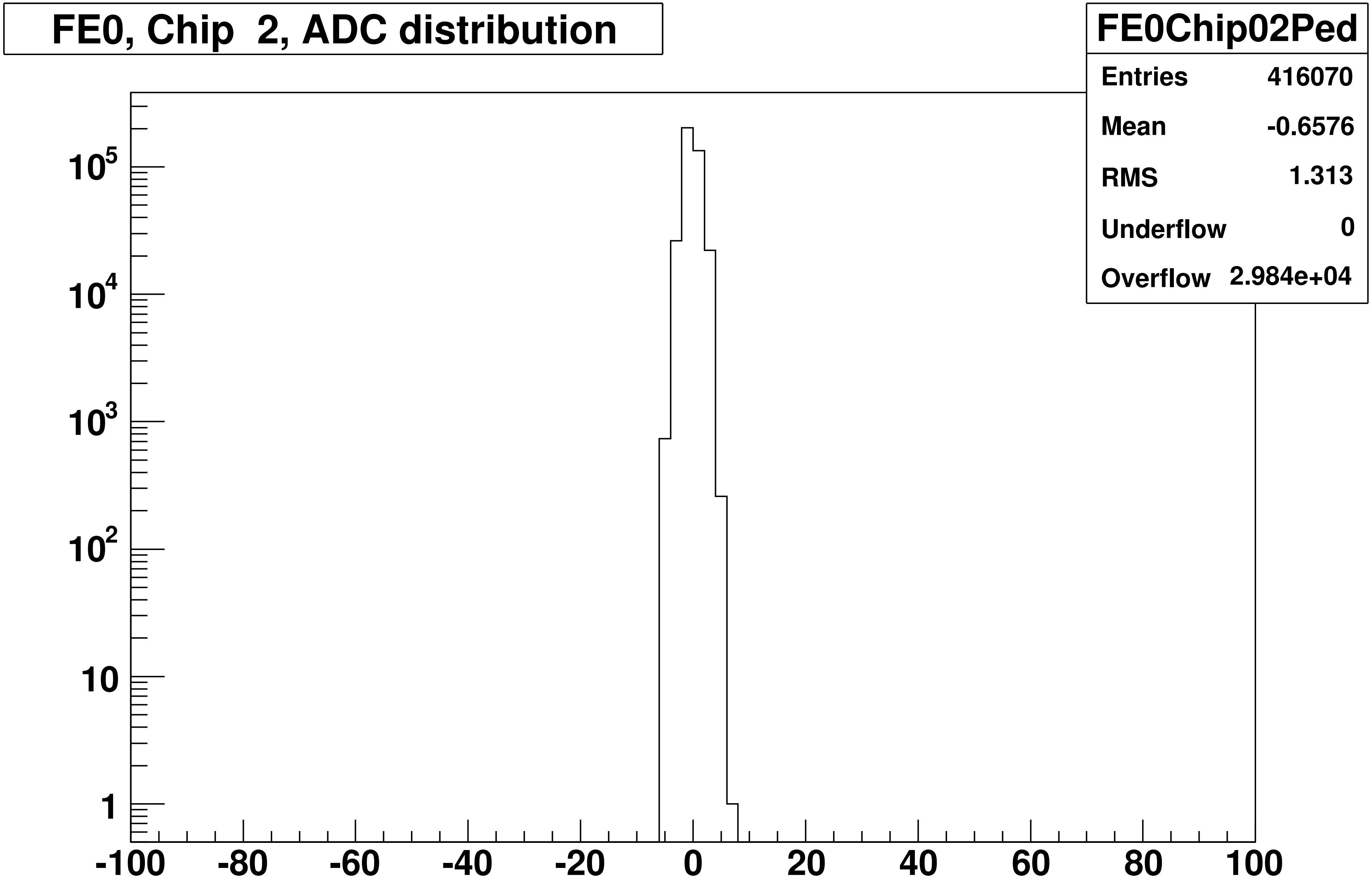
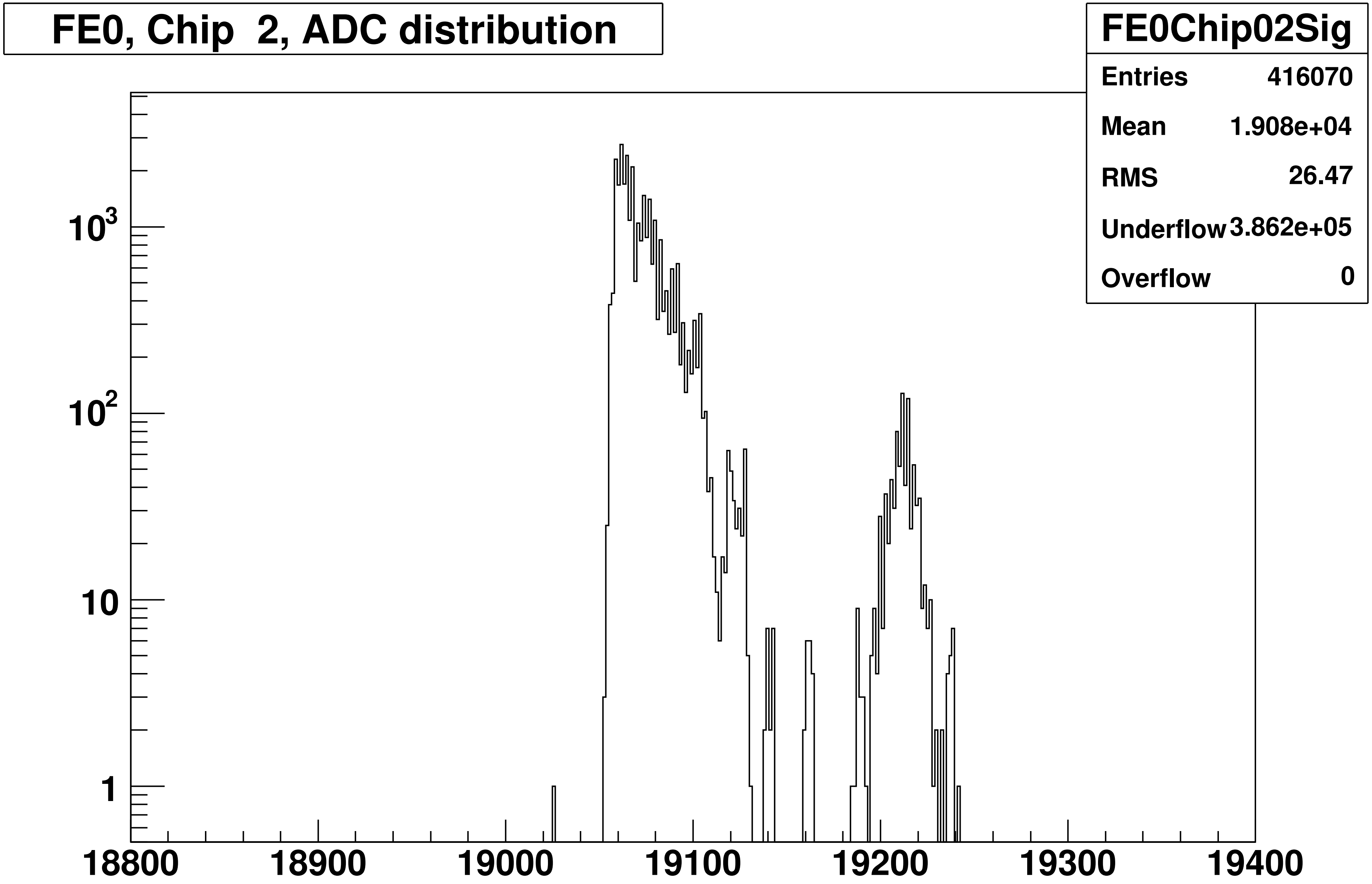
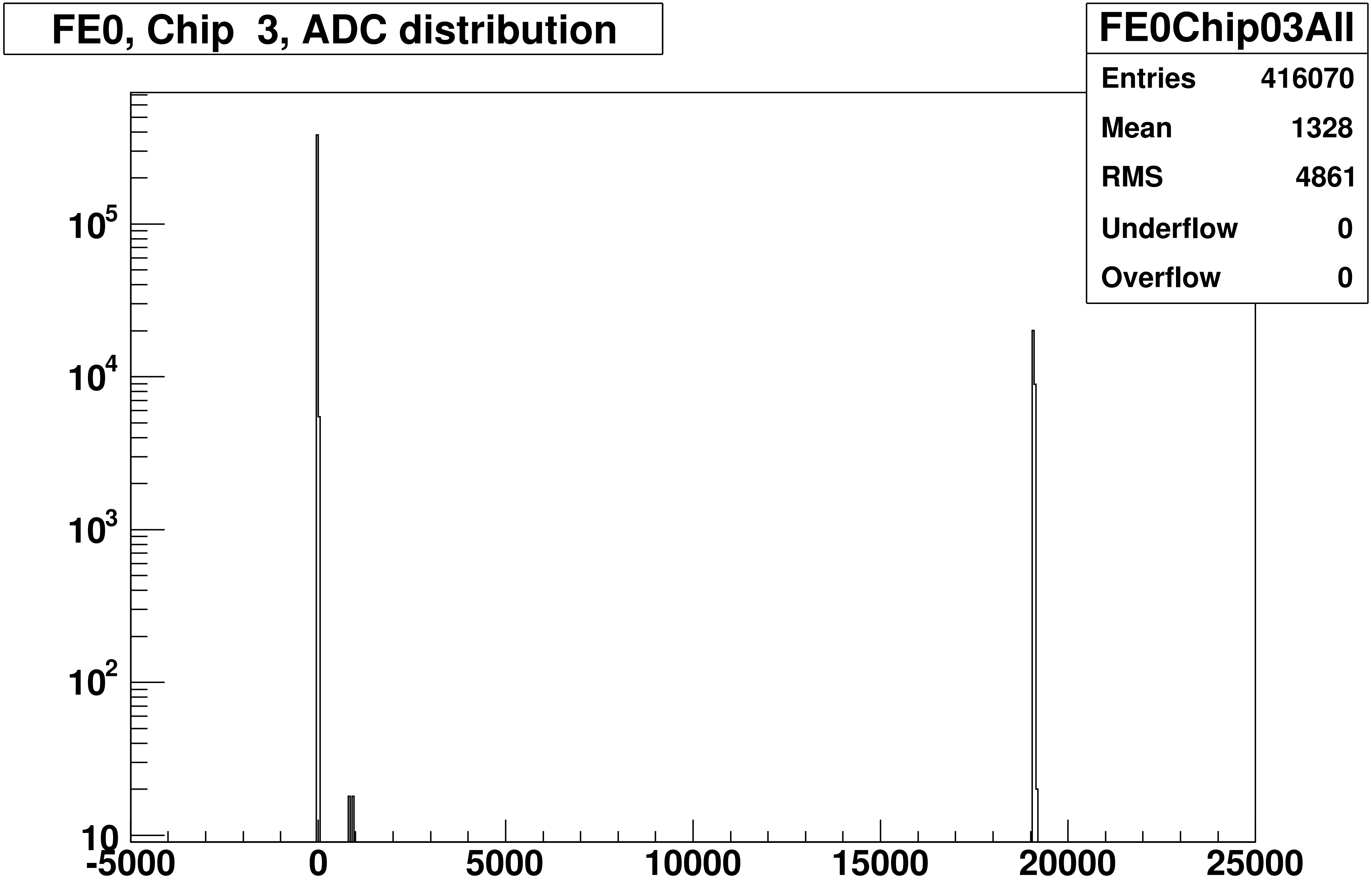
20
40
60
80
100

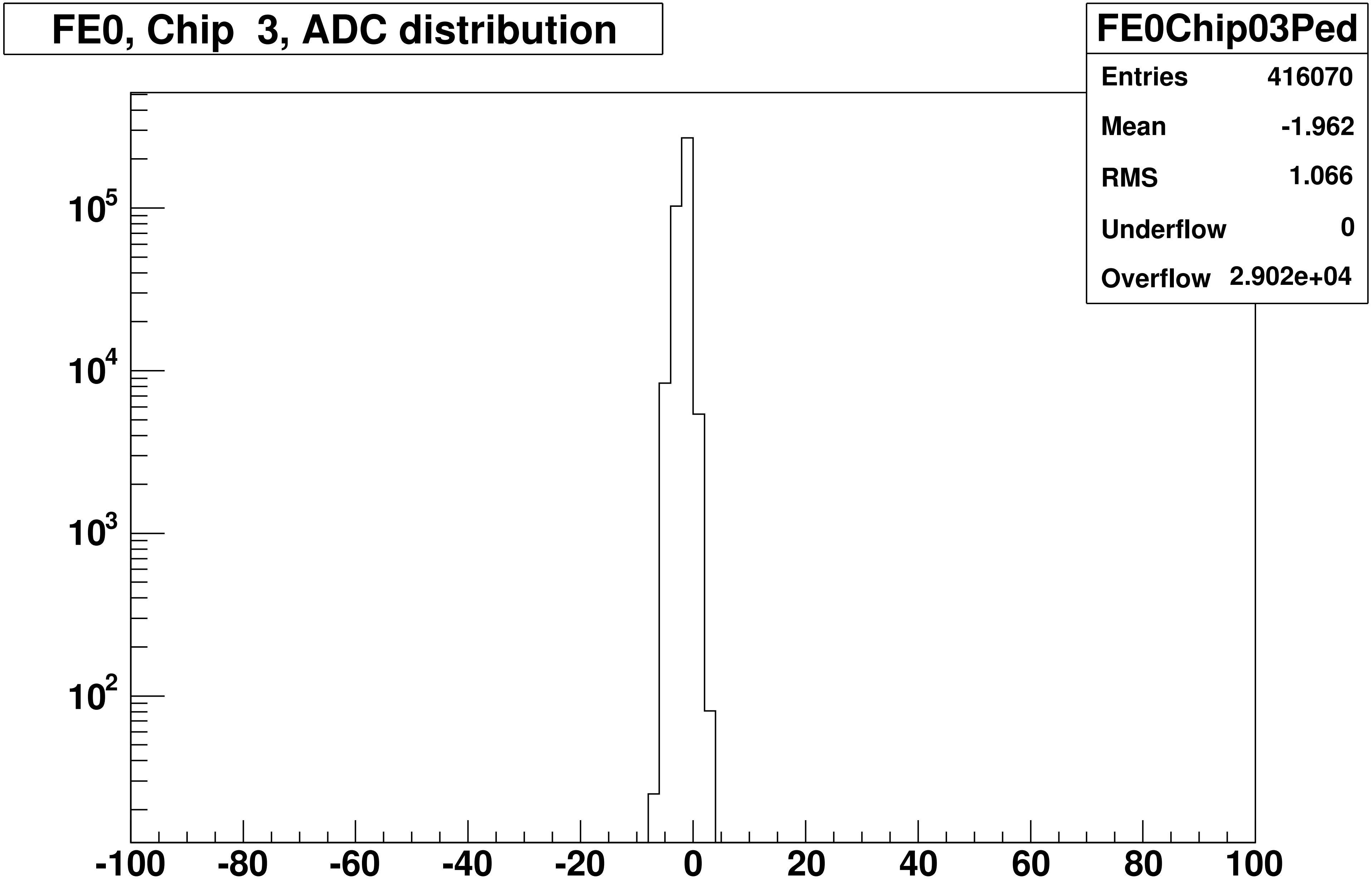

FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 416070
Mean 1.905e+04
RMS 24.96
Underflow 3.862e+05
Overflow 0
10³
10²
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 416070
Mean 1367
RMS 4922
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

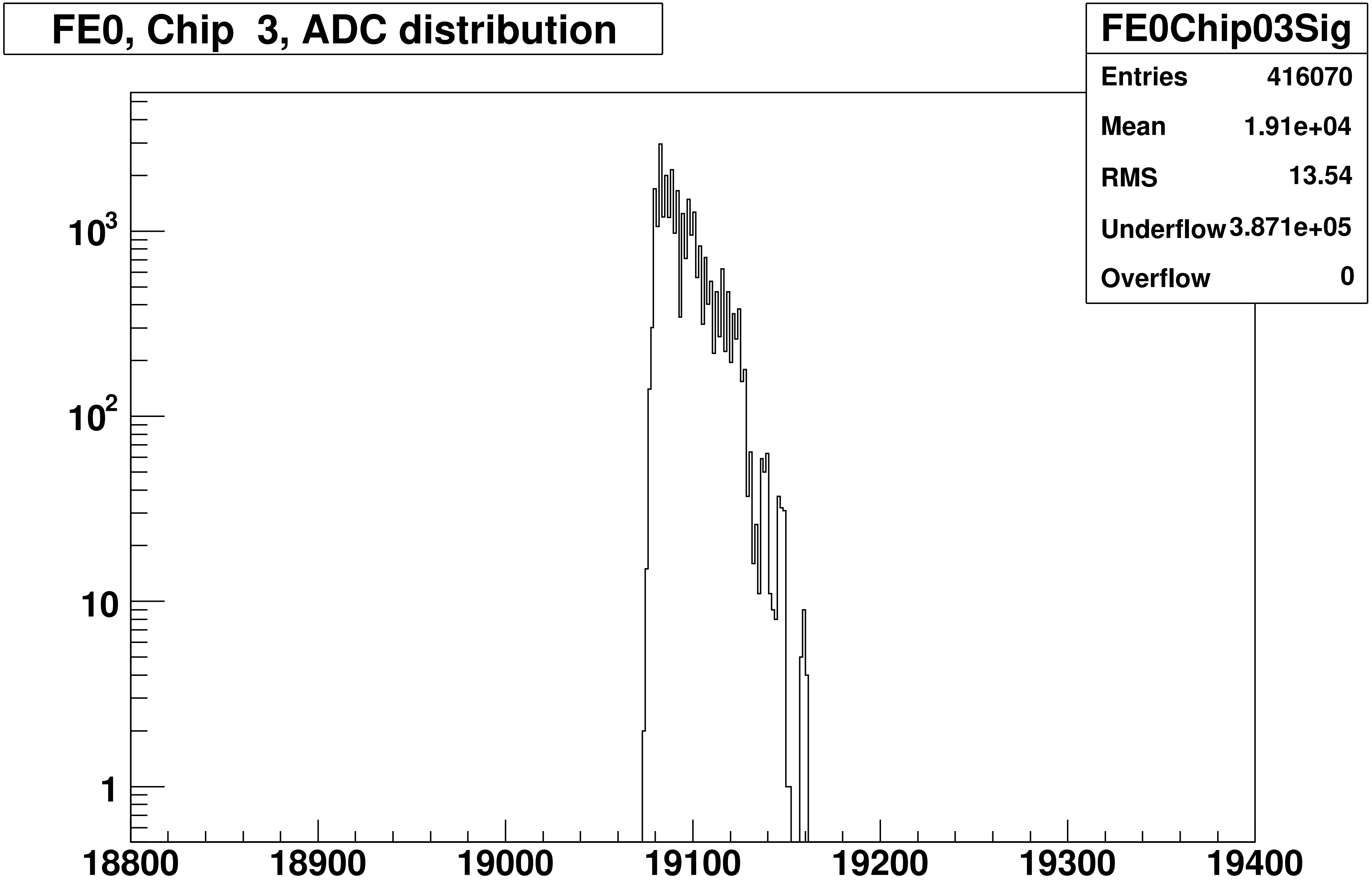

FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 416070
Mean -0.6576
RMS 1.313
Underflow 0
Overflow 2.984e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 416070
Mean 1.908e+04
RMS 26.47
Underflow 3.862e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

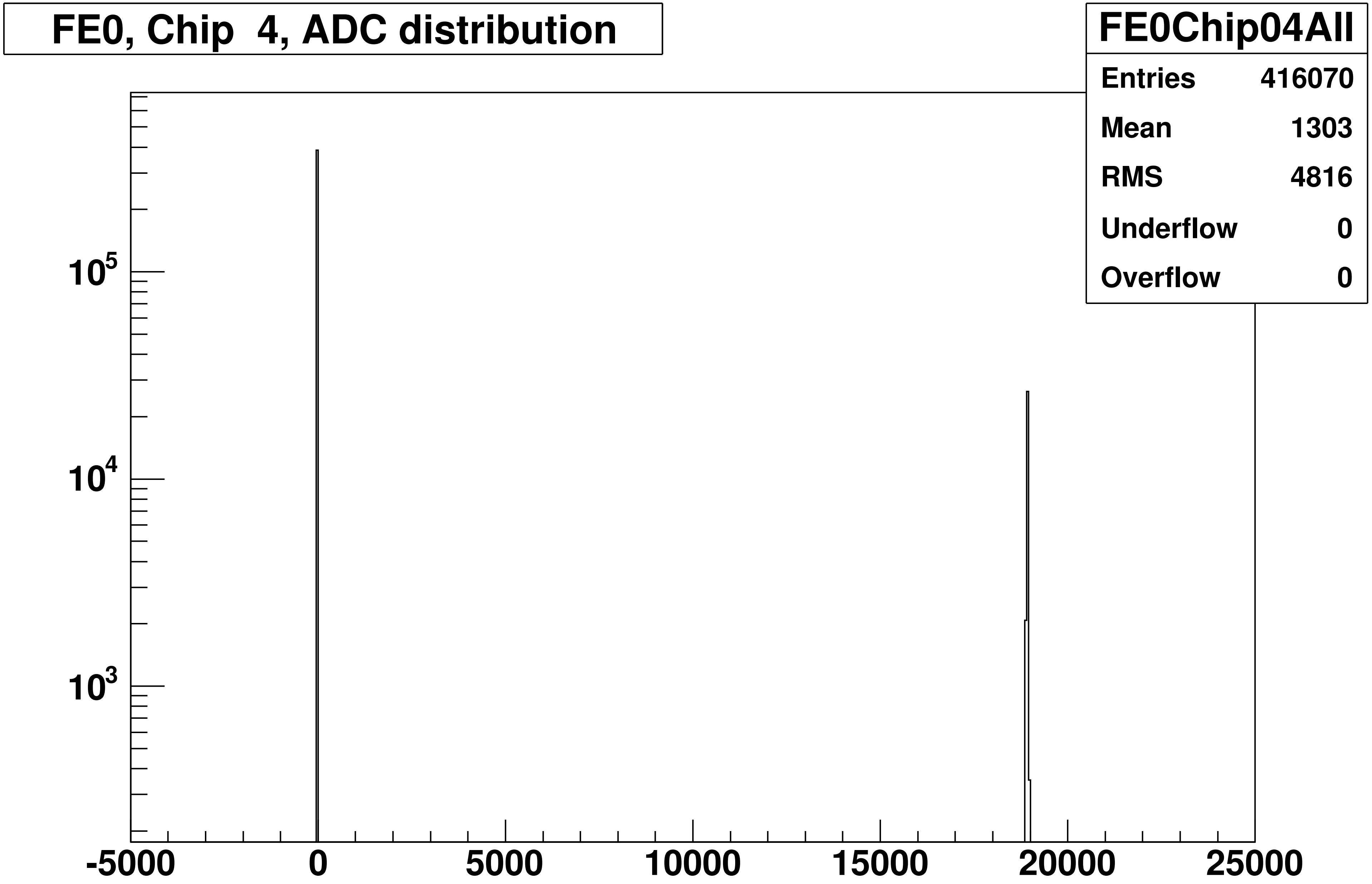

FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 416070
Mean 1328
RMS 4861
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



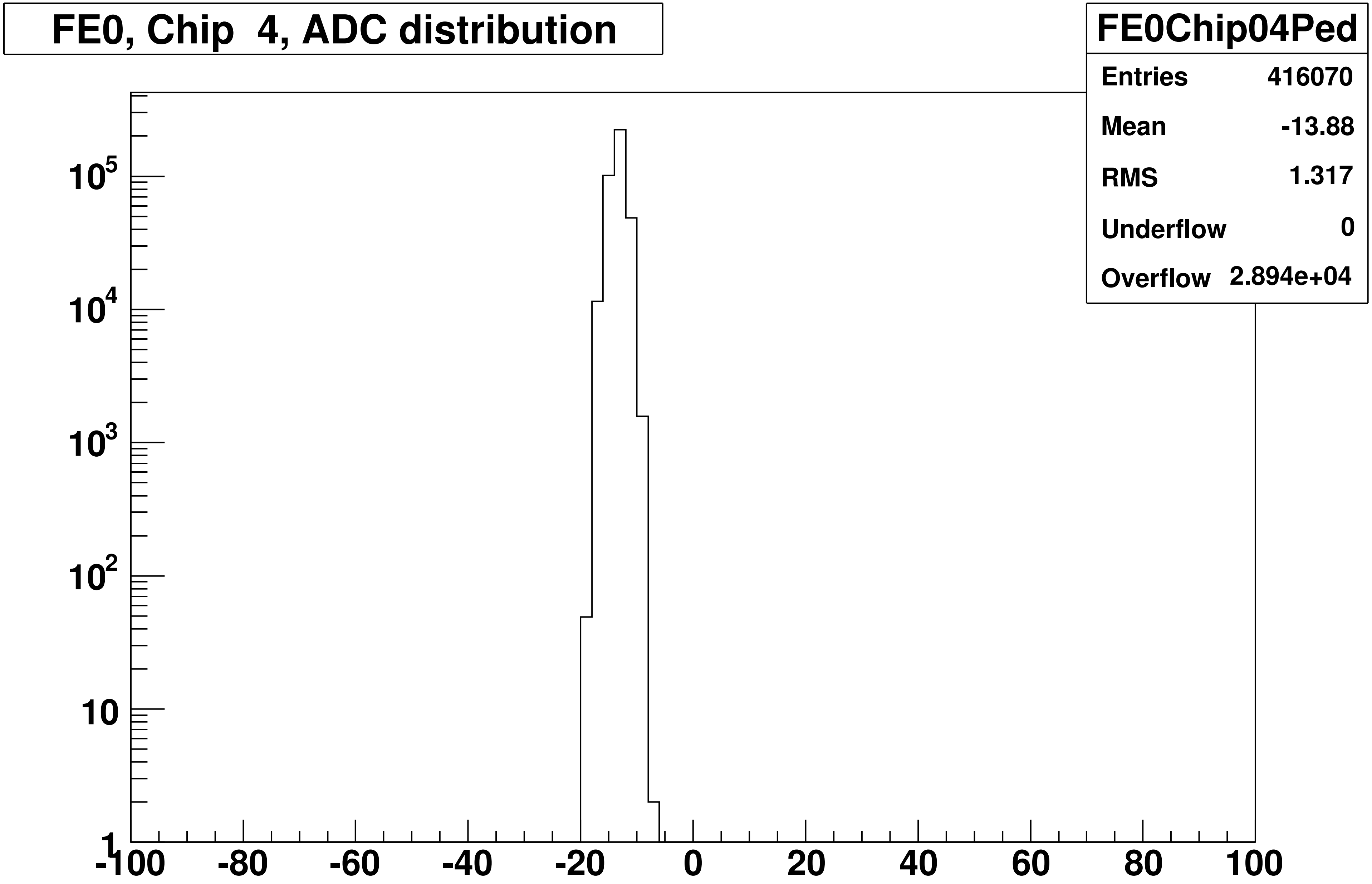
FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 416070
Mean -1.962
RMS 1.066
Underflow 0
Overflow 2.902e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

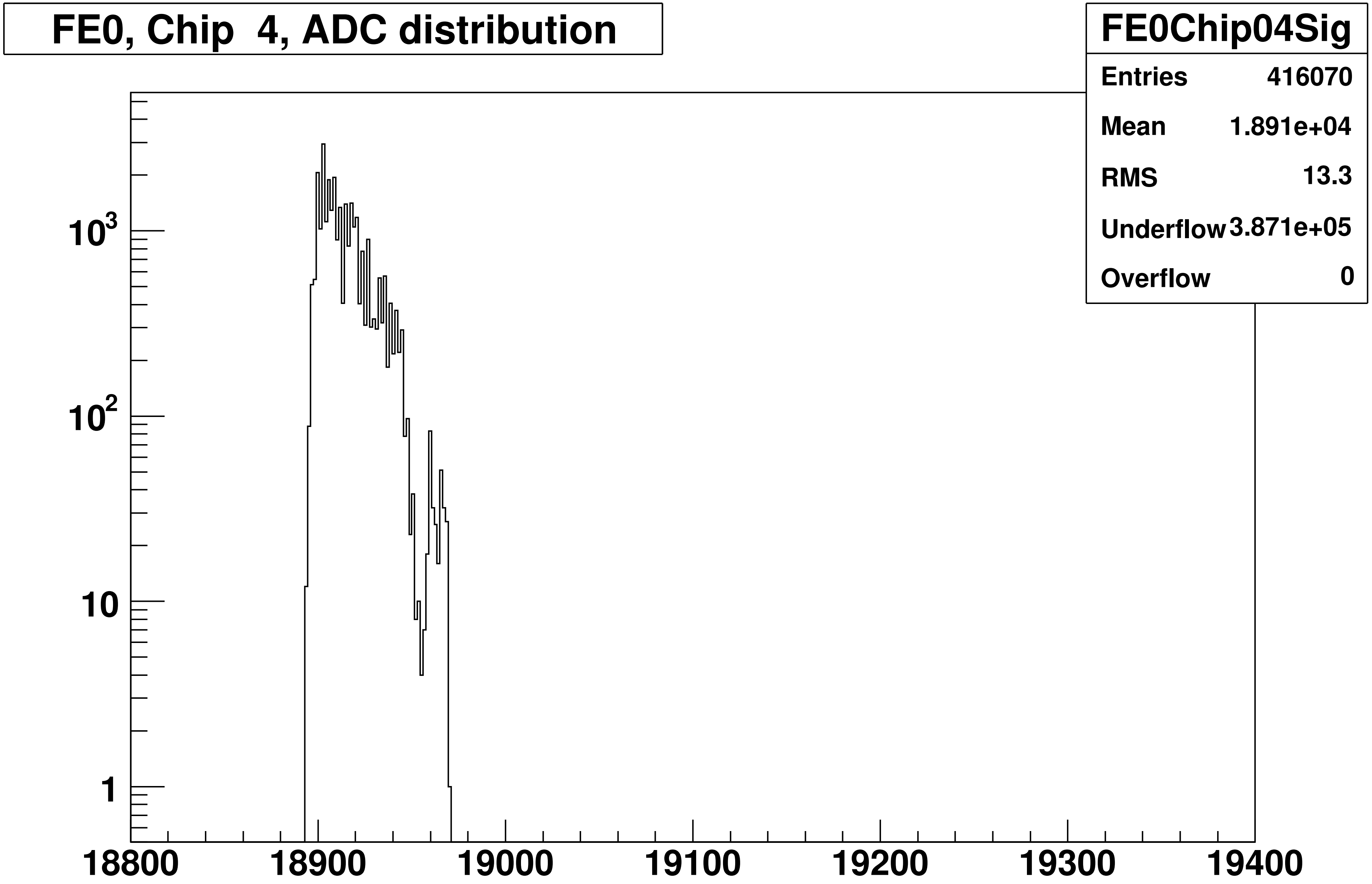


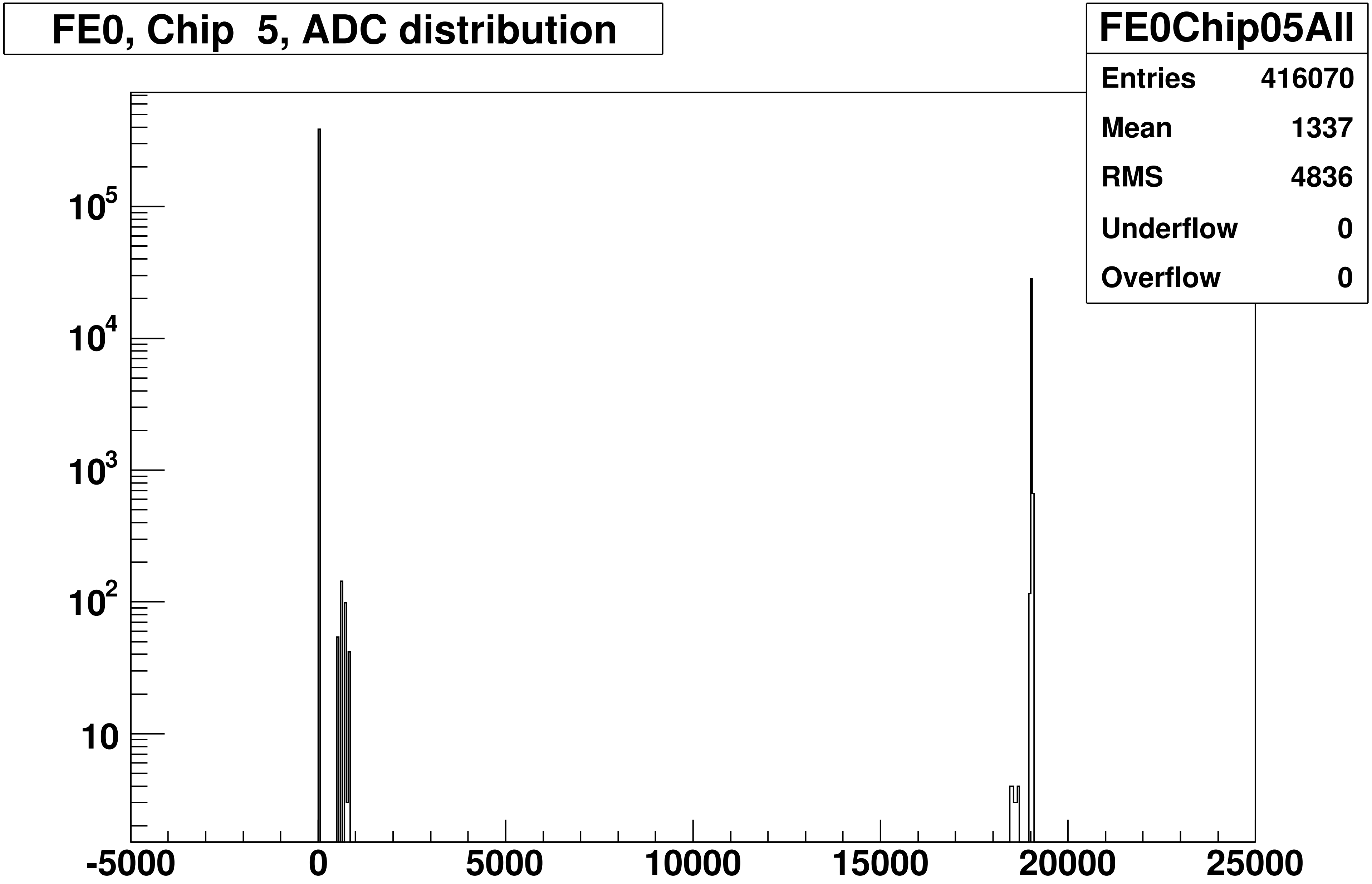
FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 416070
Mean 1.91e+04
RMS 13.54
Underflow 3.871e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



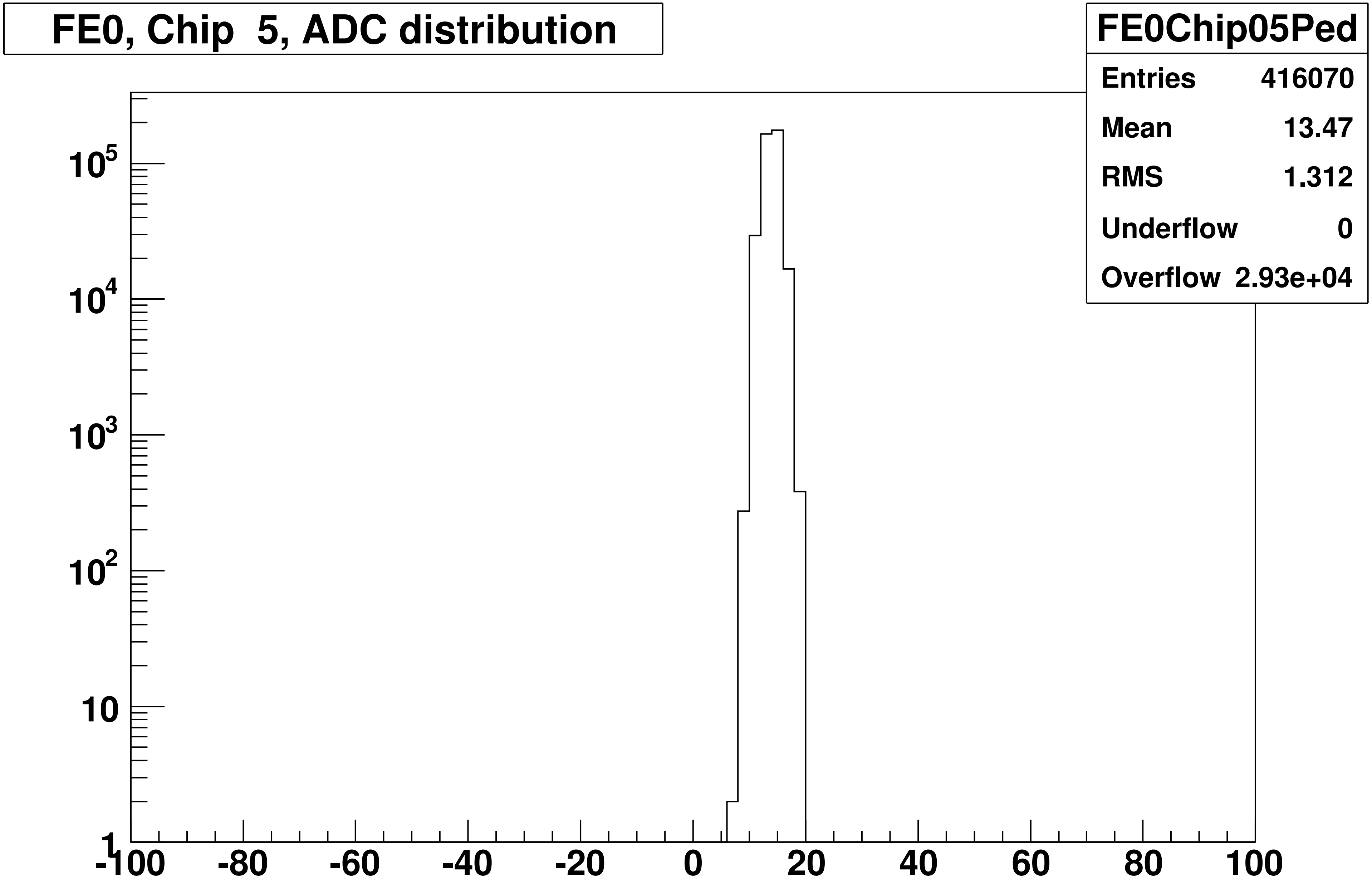
FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 416070
Mean 1303
RMS 4816
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 416070
Mean -13.88
RMS 1.317
Underflow 0
Overflow 2.894e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 416070
Mean 1.891e+04
RMS 13.3
Underflow 3.871e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 416070
Mean 1337
RMS 4836
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 416070
Mean 13.47
RMS 1.312
Underflow 0
Overflow 2.93e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
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-40
-20
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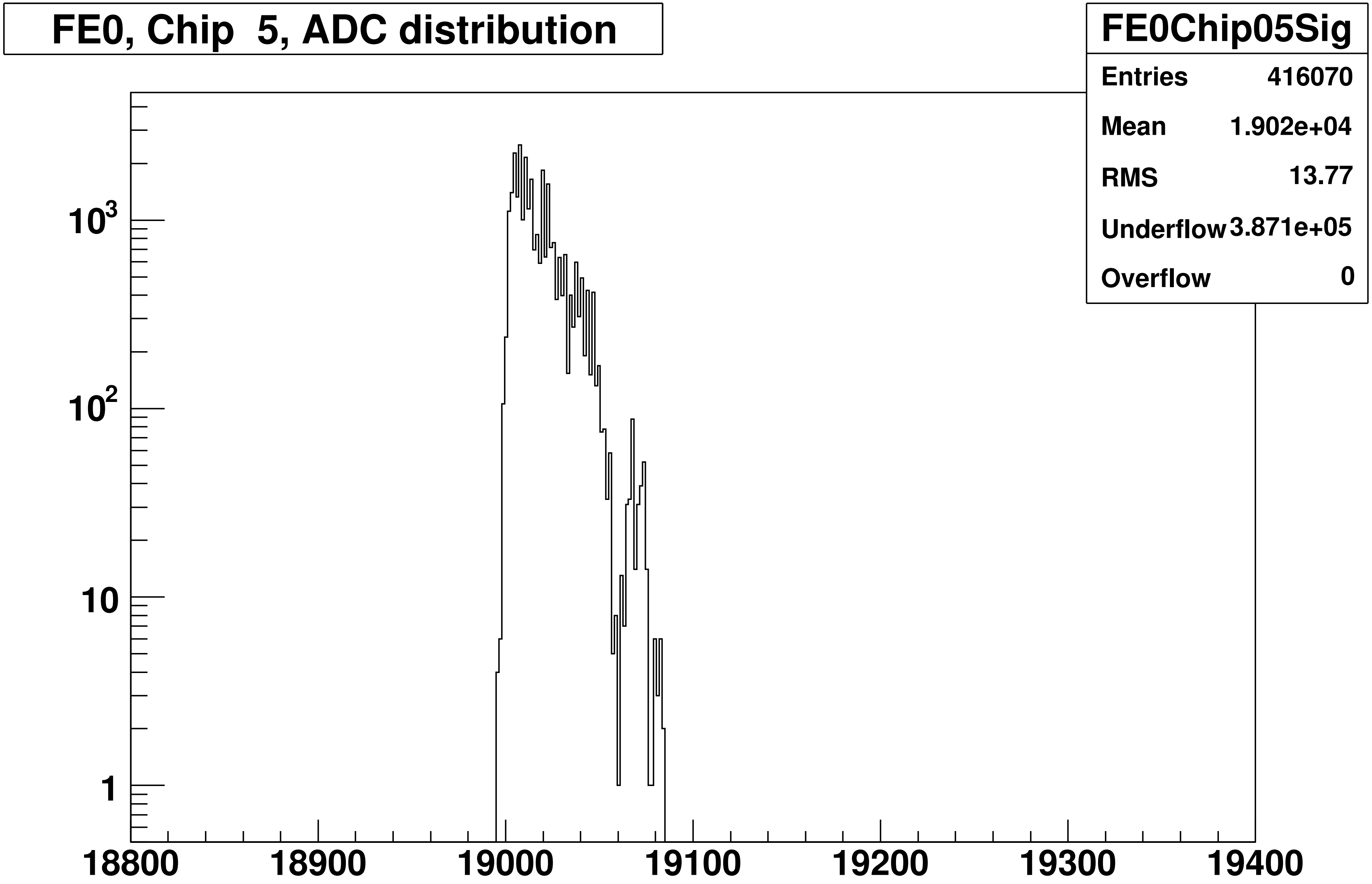
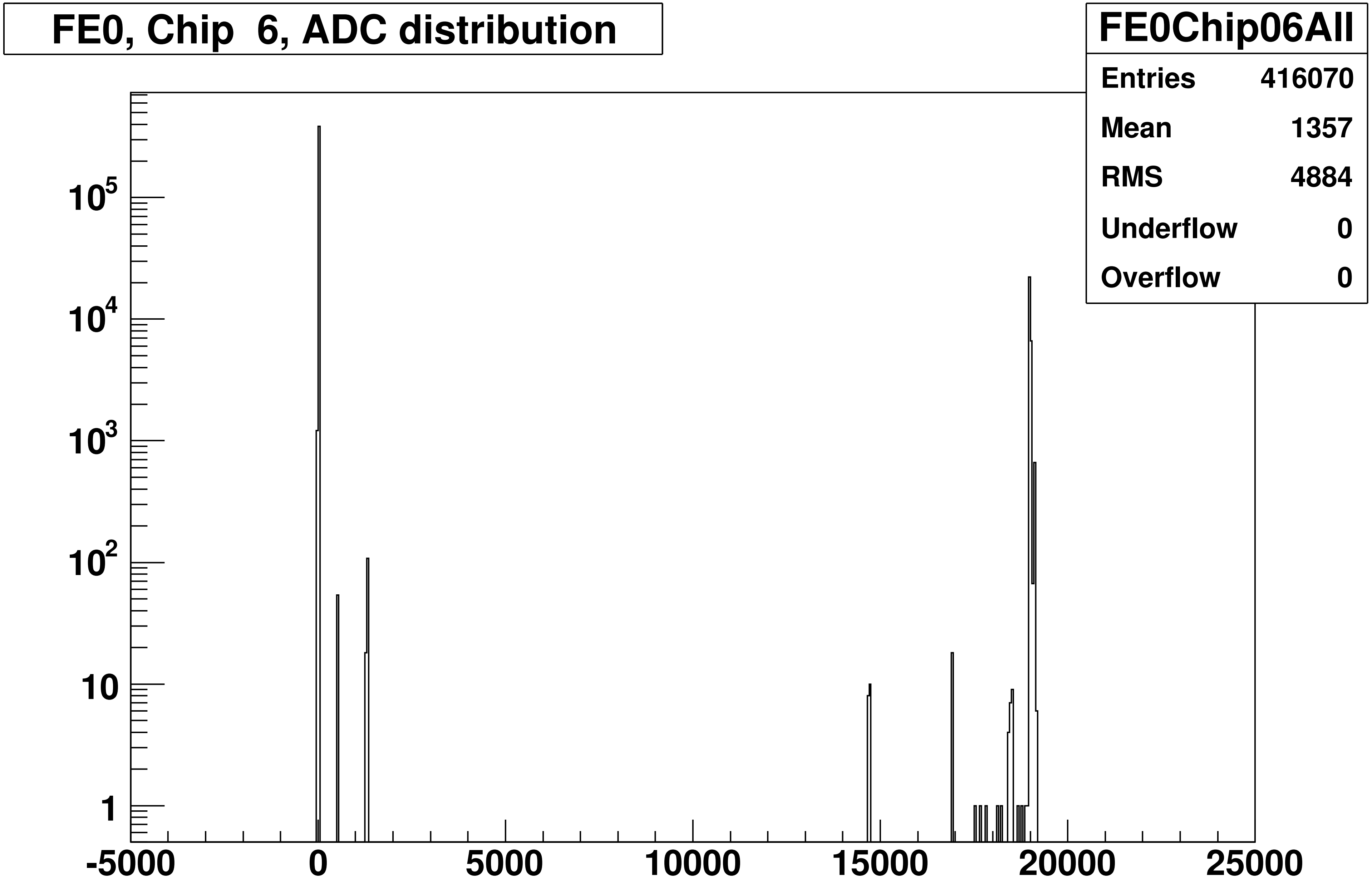
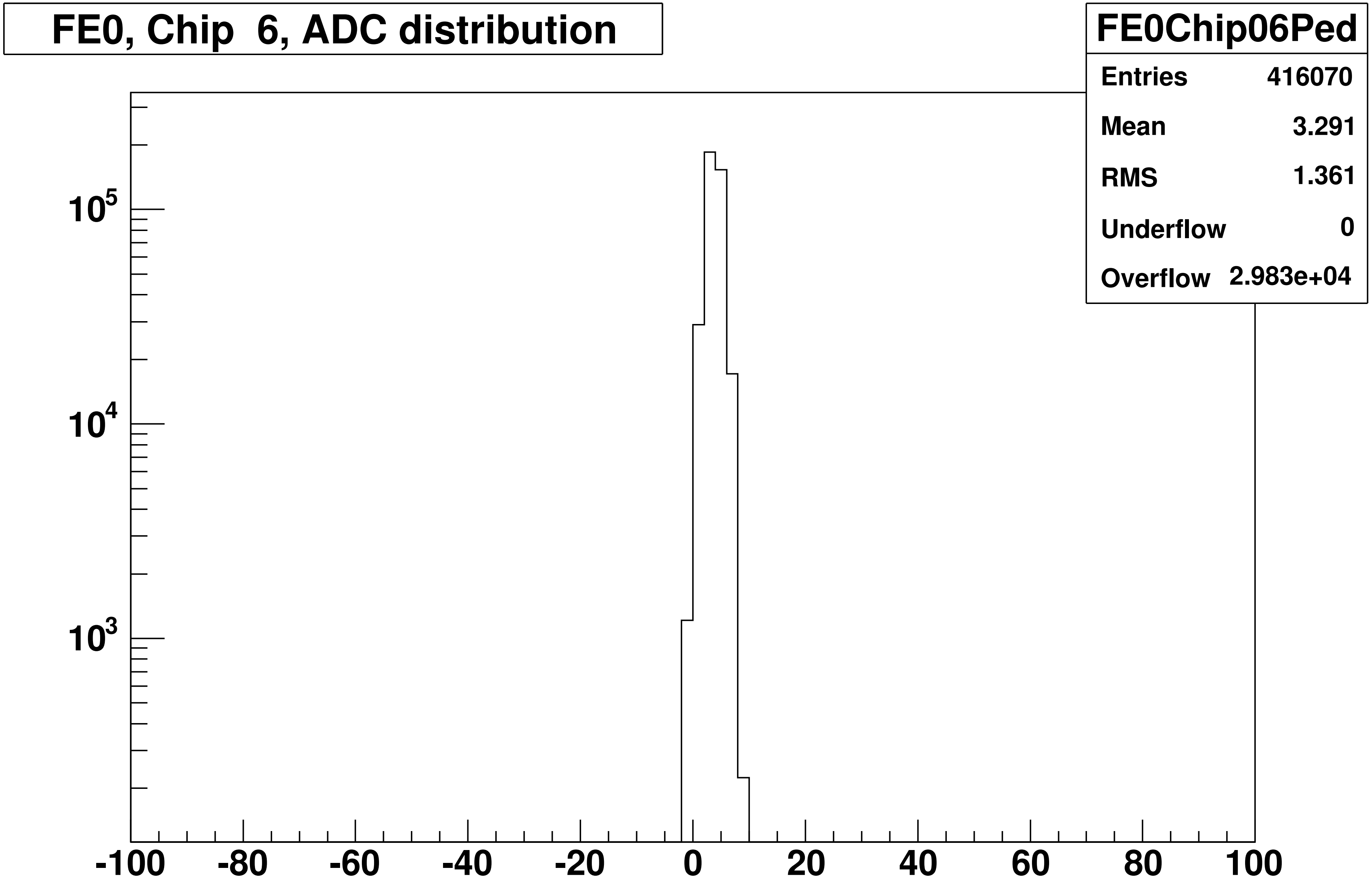
20
40
60
80
100



FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 416070
Mean 1.902e+04
RMS 13.77
Underflow 3.871e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 416070
Mean 1357
RMS 4884
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 416070
Mean 3.291
RMS 1.361
Underflow 0
Overflow 2.983e+04
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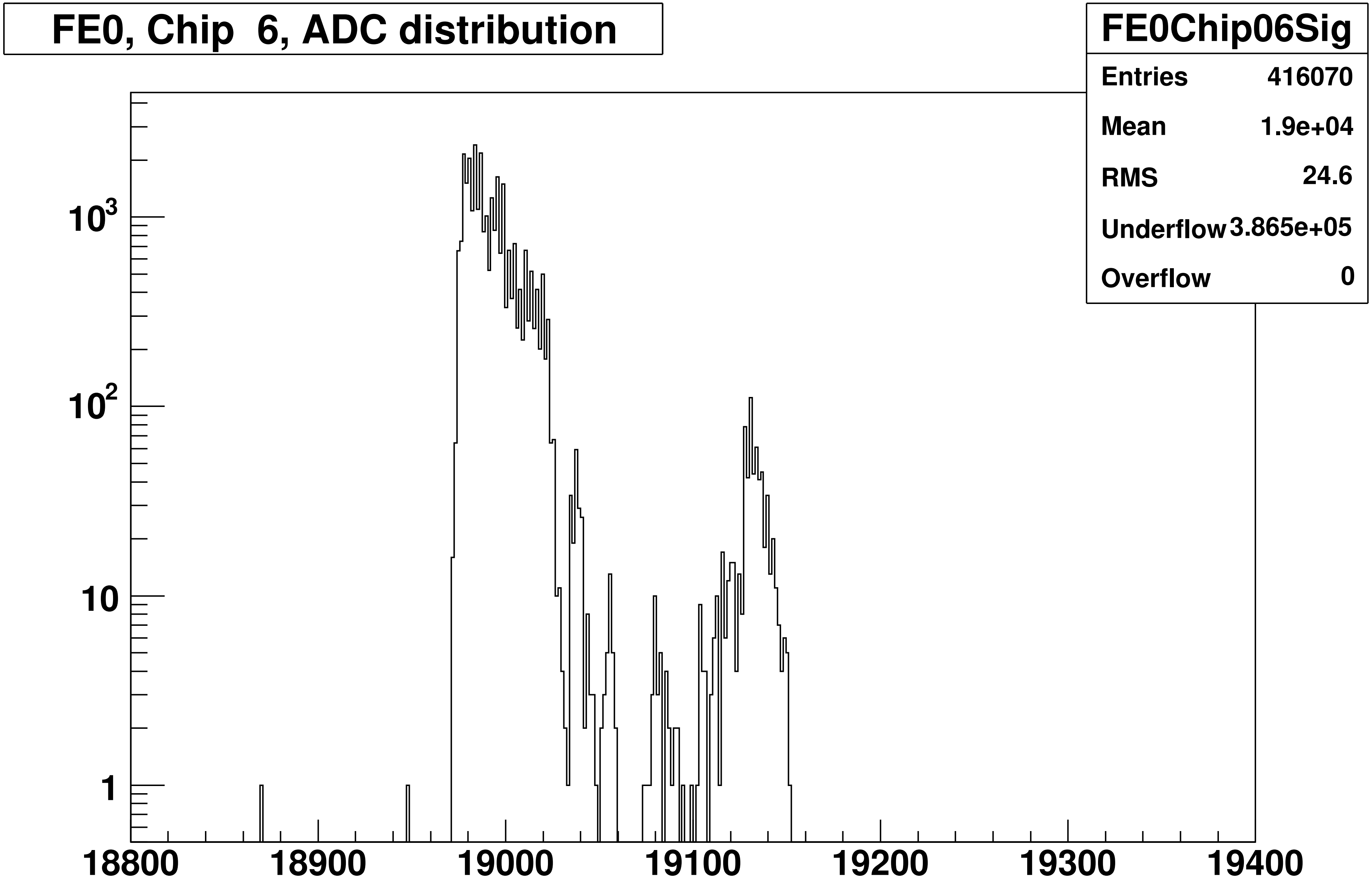
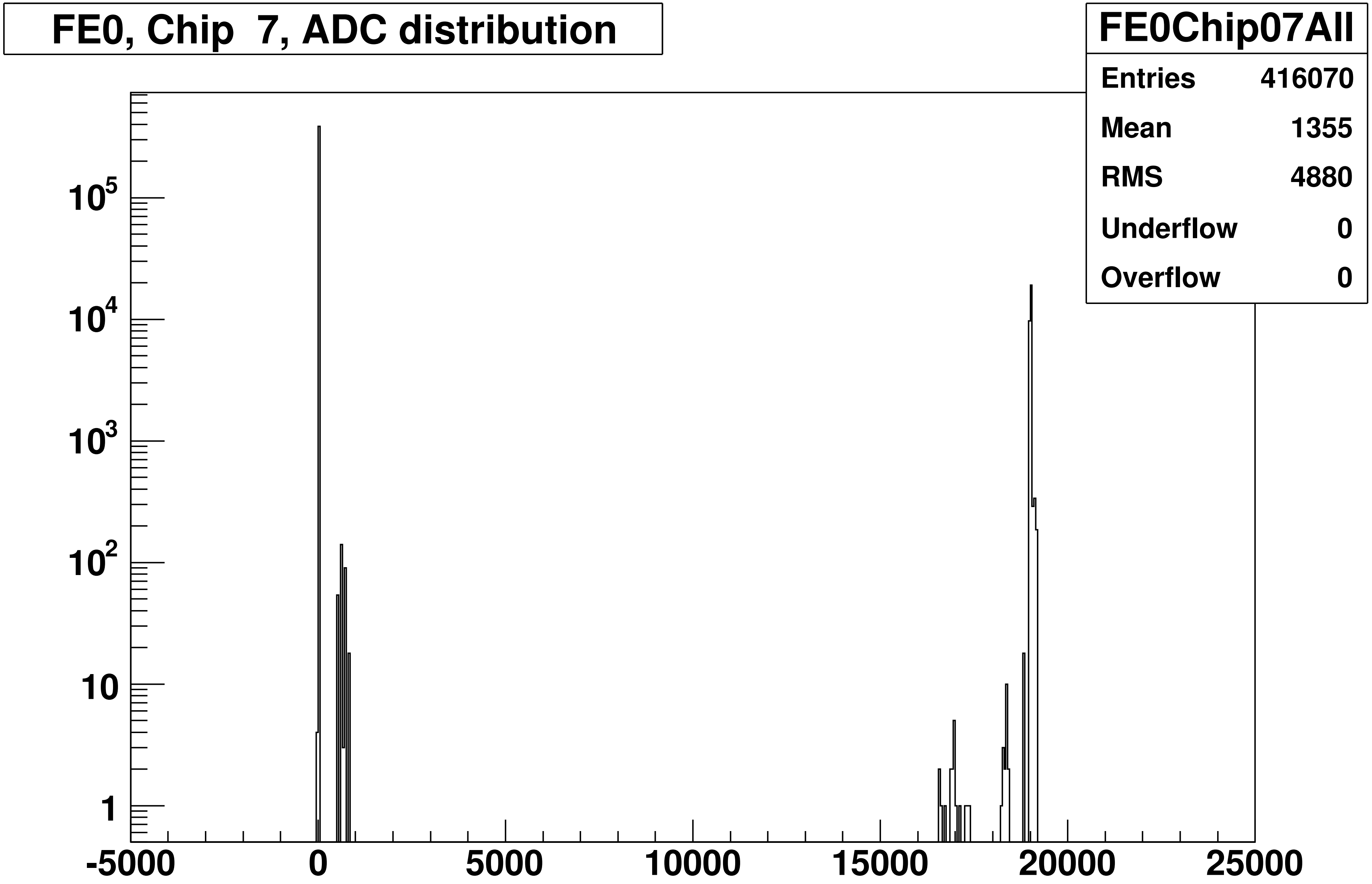
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 416070
Mean 1.9e+04
RMS 24.6
Underflow 3.865e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 416070
Mean 1355
RMS 4880
Underflow 0
Overflow 0
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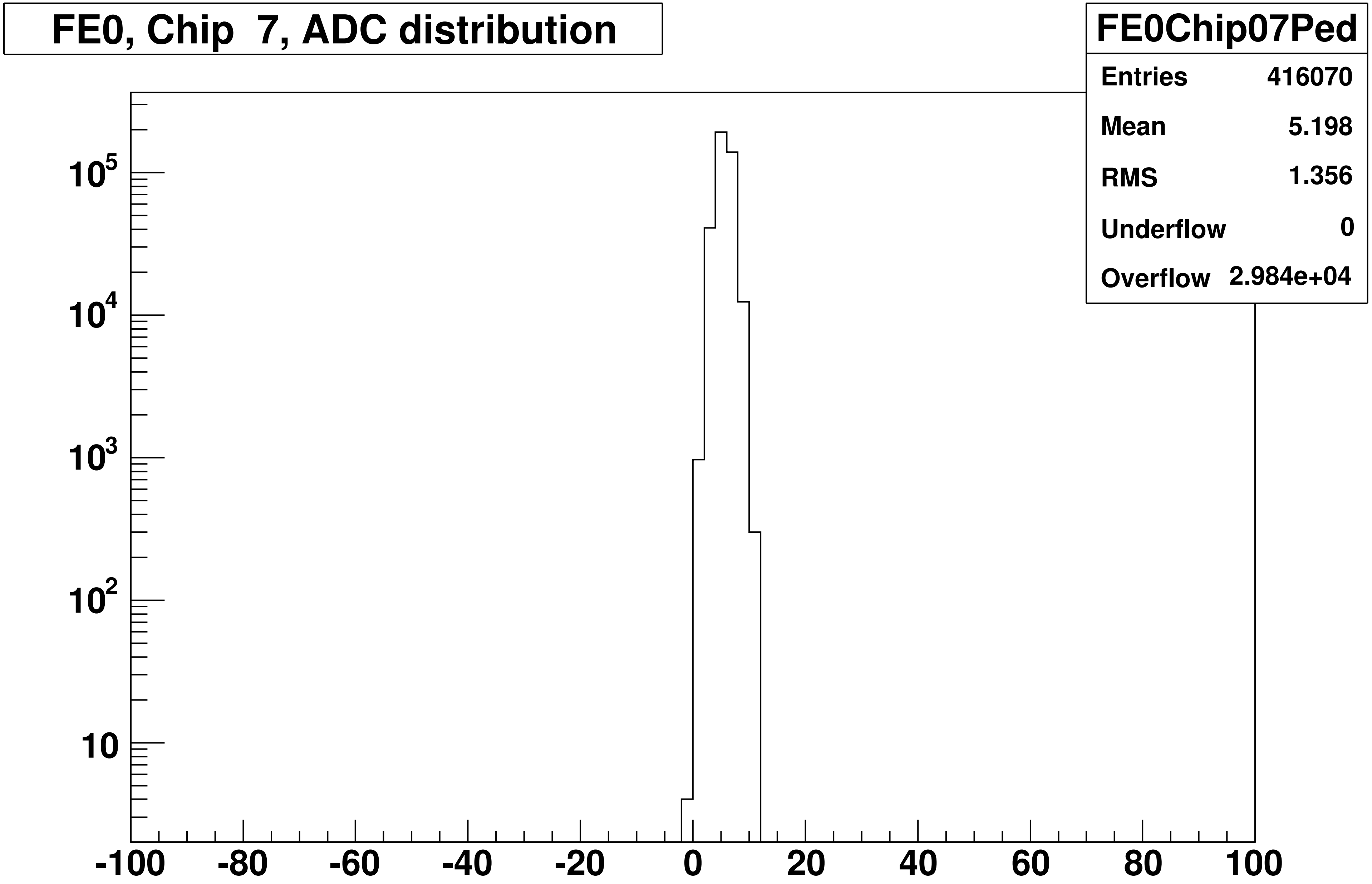
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 416070
Mean 5.198
RMS 1.356
Underflow 0
Overflow 2.984e+04
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10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
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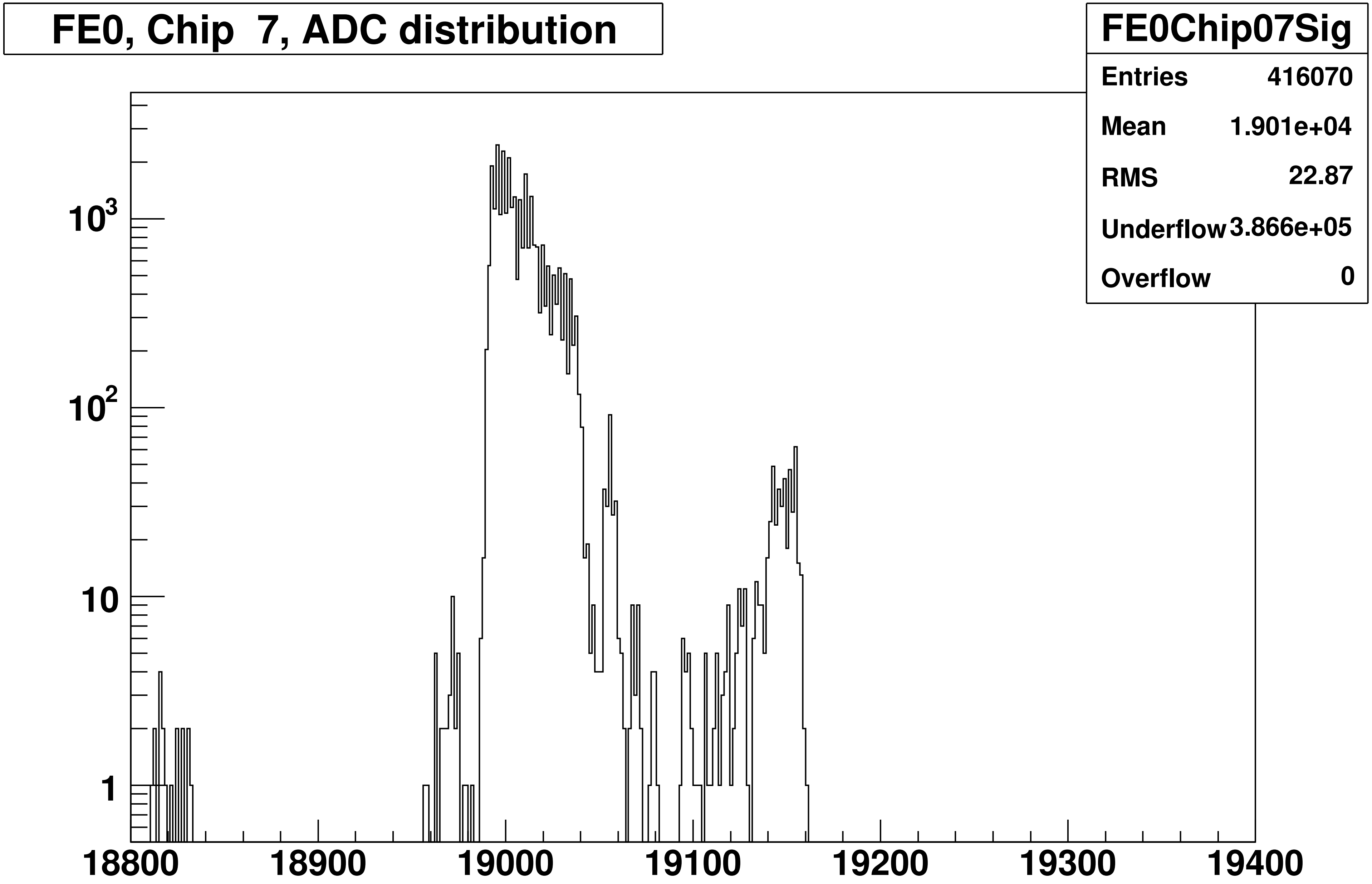
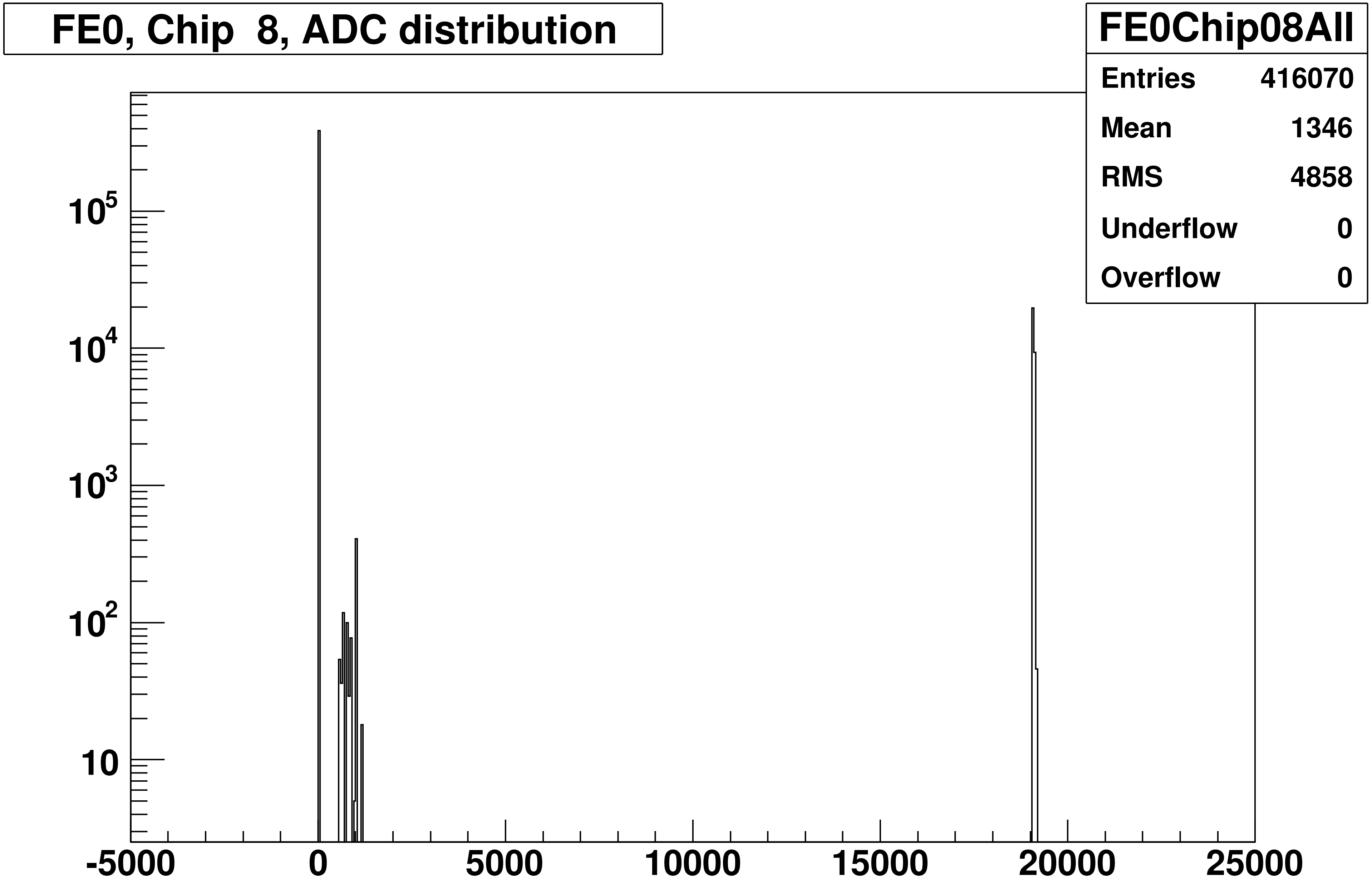
40
60
80
100



FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 416070
Mean 1.901e+04
RMS 22.87
Underflow 3.866e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 416070
Mean 1346
RMS 4858
Underflow 0
Overflow 0
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10^4
10^3
10^2
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-5000
0
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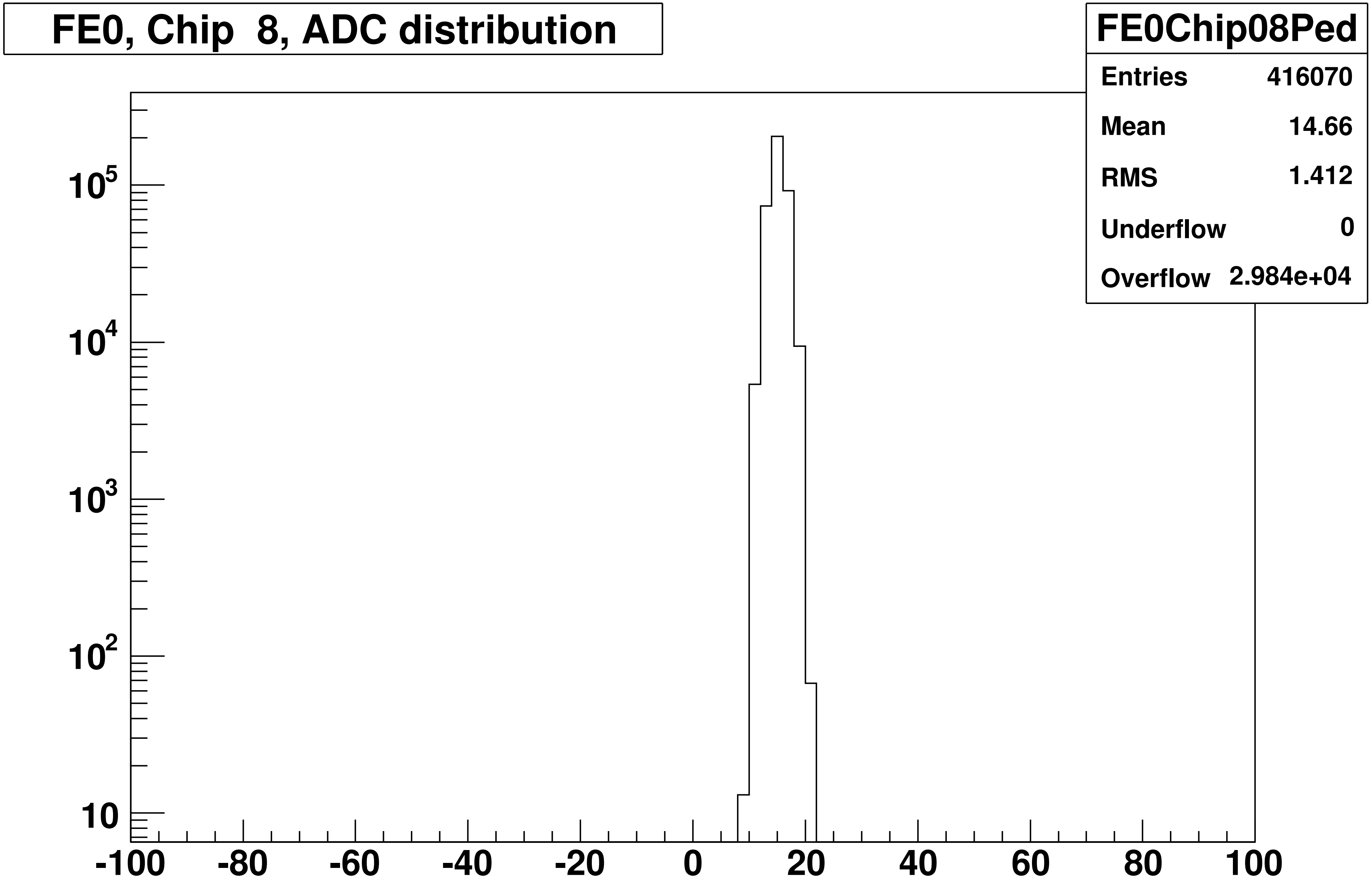
10000
15000
20000
25000



FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 416070
Mean 14.66
RMS 1.412
Underflow 0
Overflow 2.984e+04
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10^4
10^3
10^2
10
-100
-80
-60
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60
80
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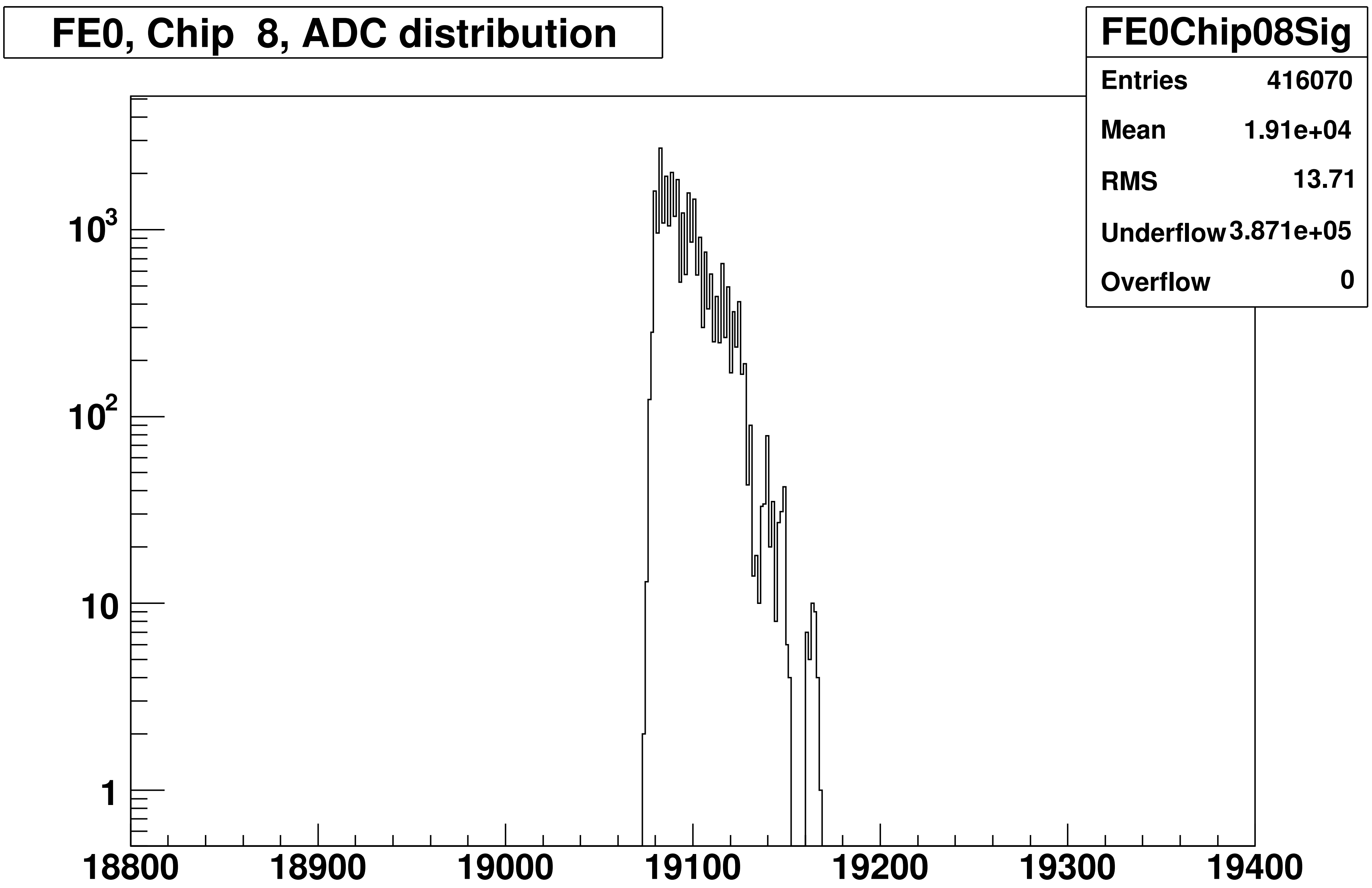
FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 416070
Mean 1.91e+04
RMS 13.71
Underflow 3.871e+05
Overflow 0
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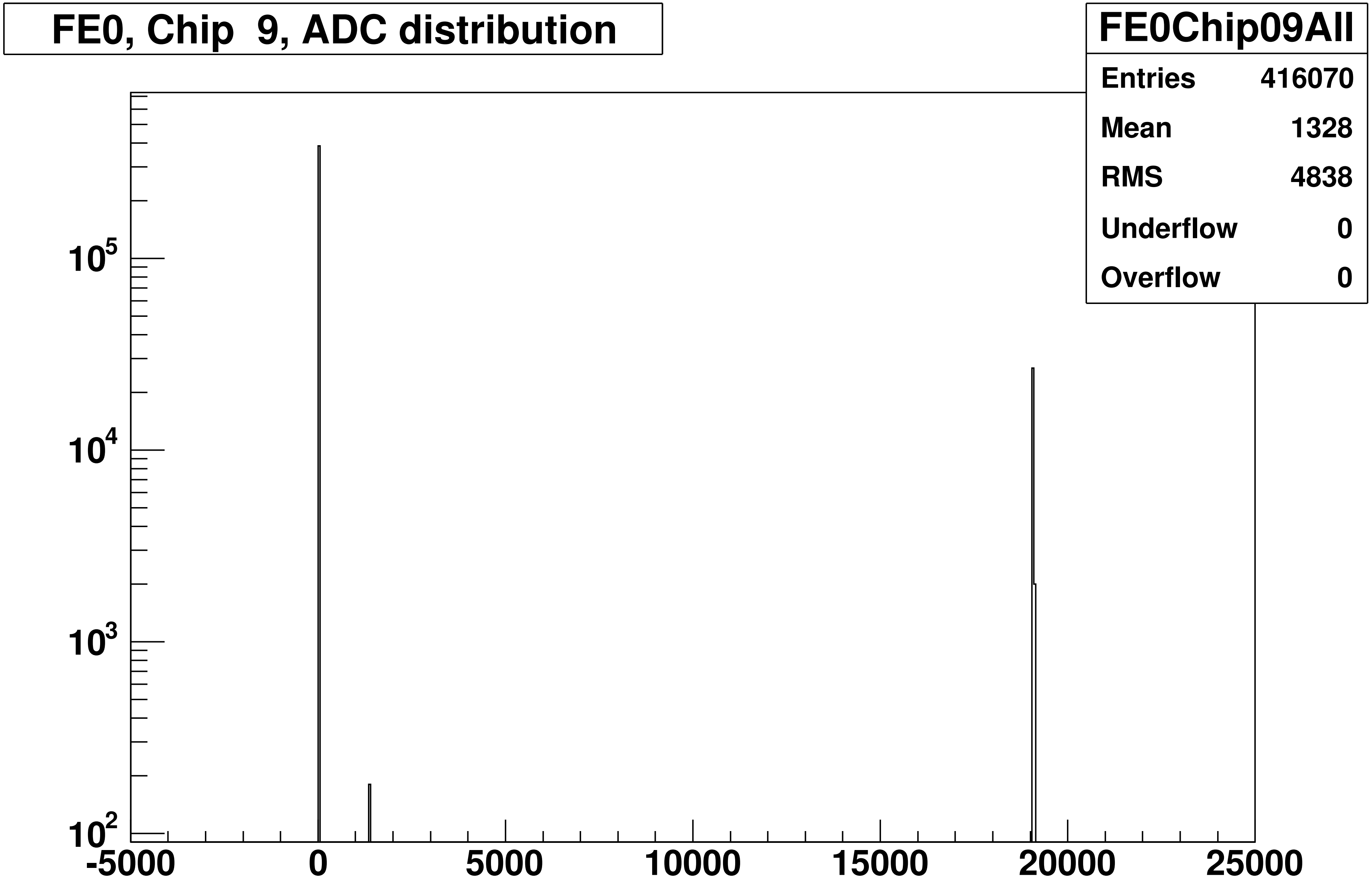
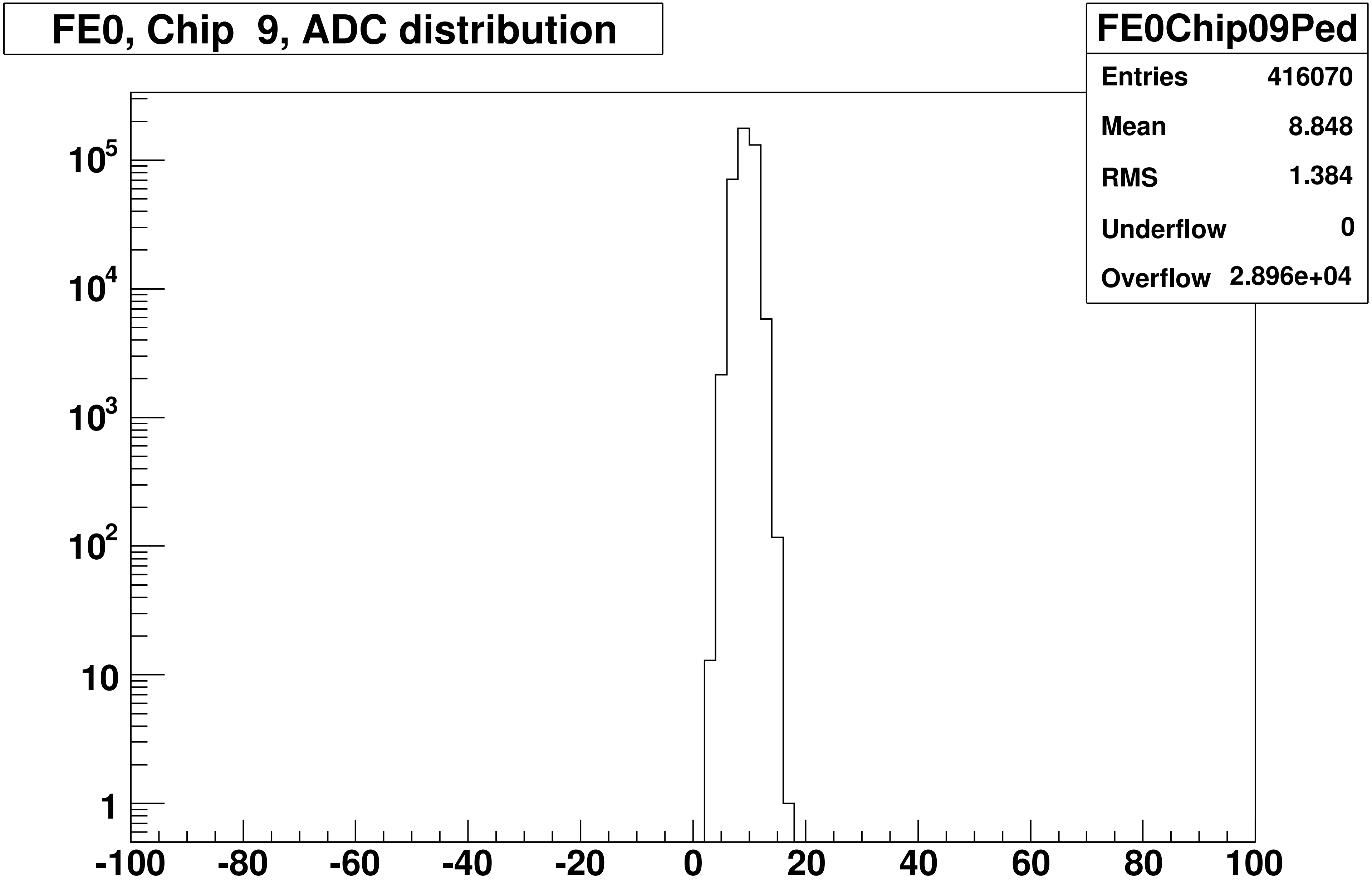
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 416070
Mean 1328
RMS 4838
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 416070
Mean 8.848
RMS 1.384
Underflow 0
Overflow 2.896e+04
10^5
10^4
10^3
10^2
10
1
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-80
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20
40
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80
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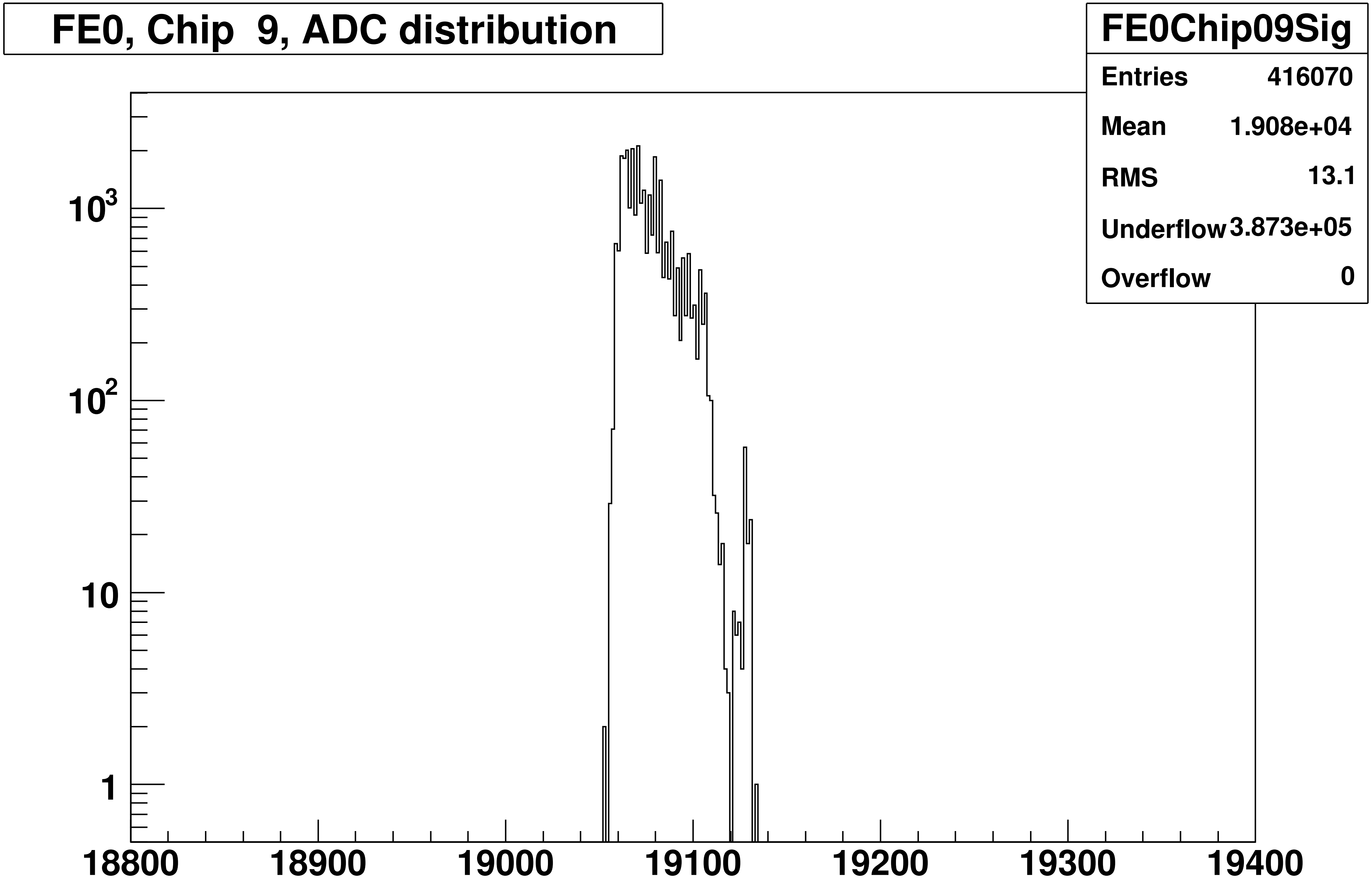
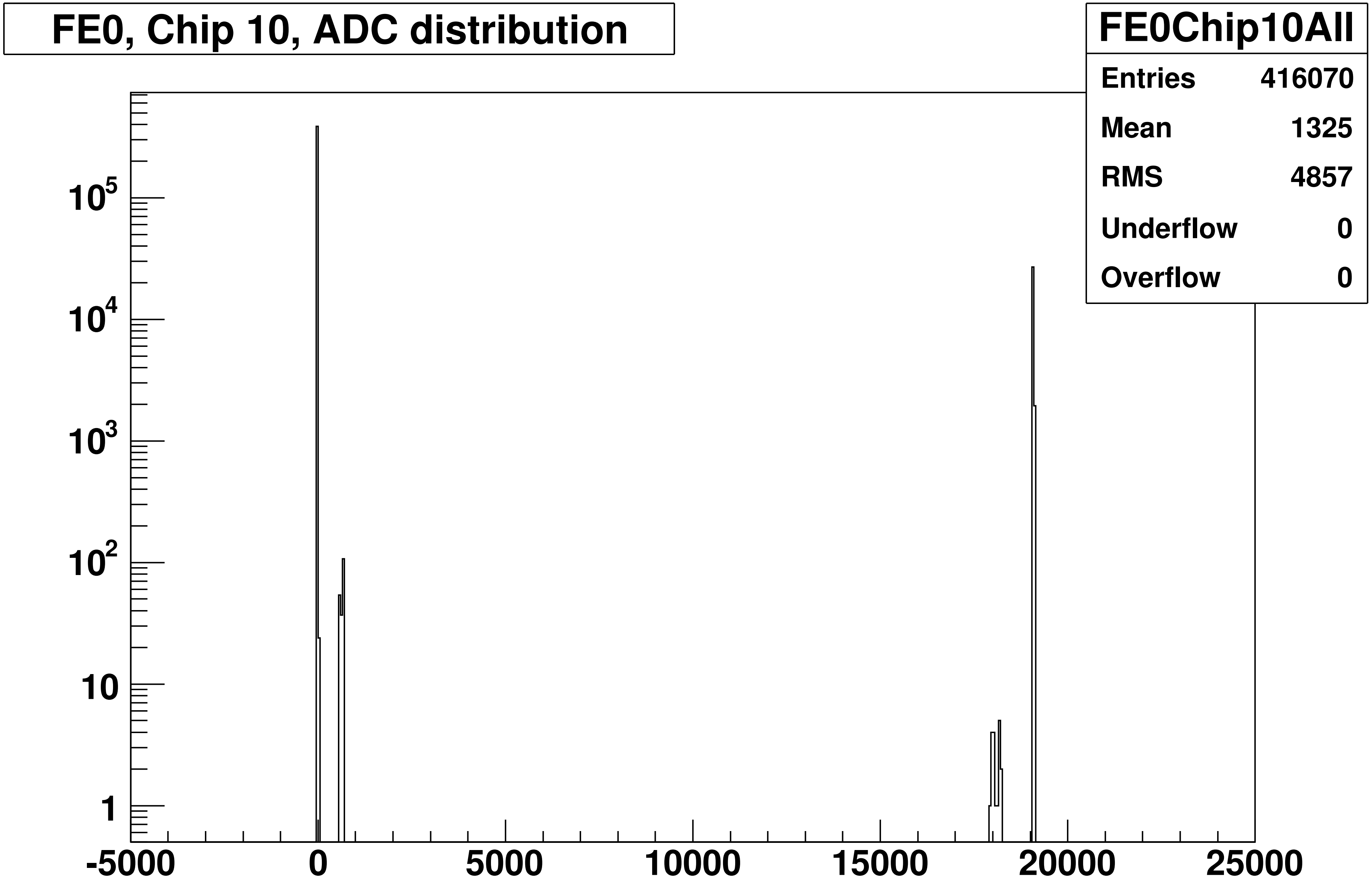

FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 416070
Mean 1.908e+04
RMS 13.1
Underflow 3.873e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 416070
Mean 1325
RMS 4857
Underflow 0
Overflow 0
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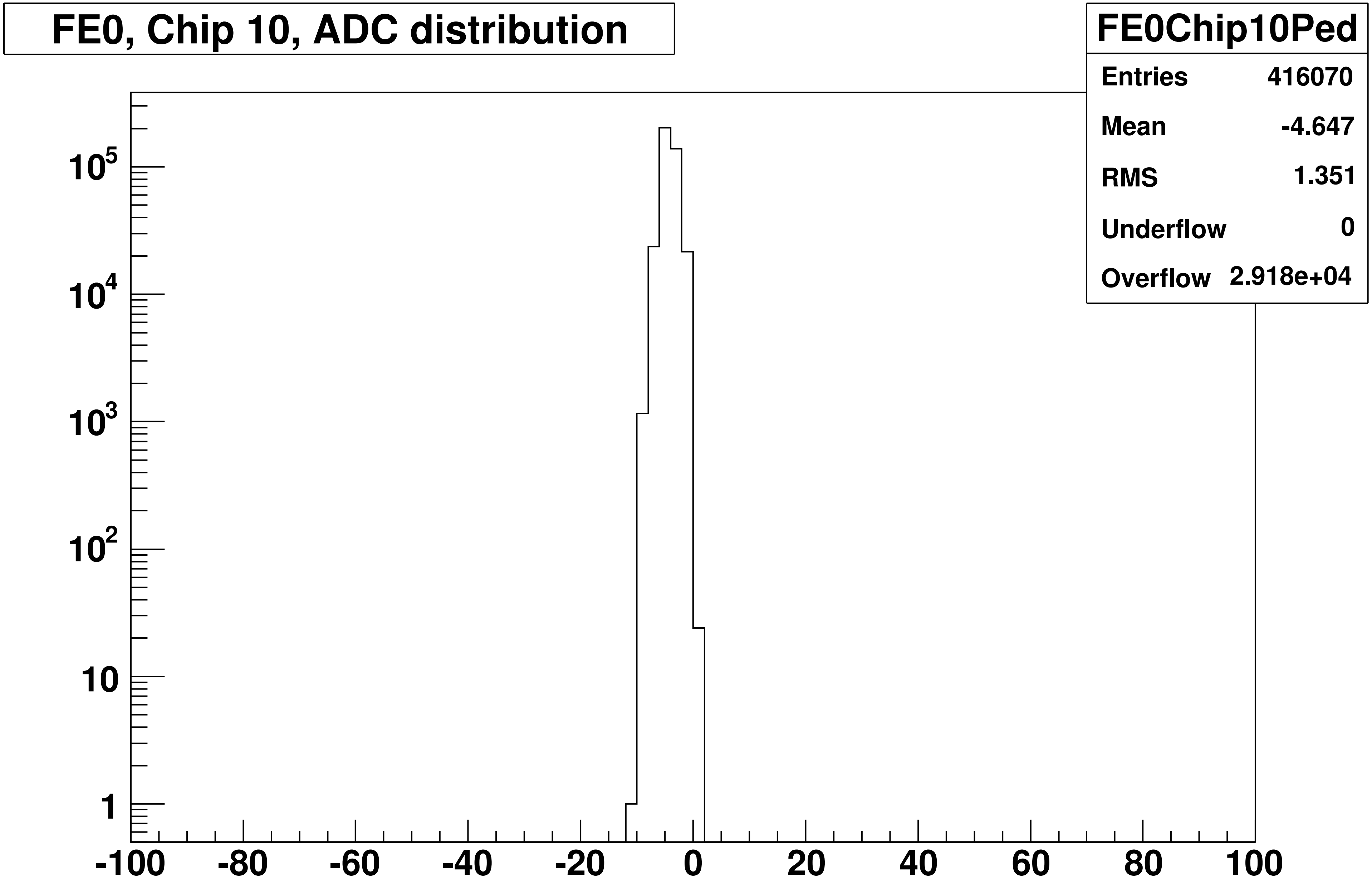
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 416070
Mean -4.647
RMS 1.351
Underflow 0
Overflow 2.918e+04
10^5
10^4
10^3
10^2
10
1
-100
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-20
0
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40
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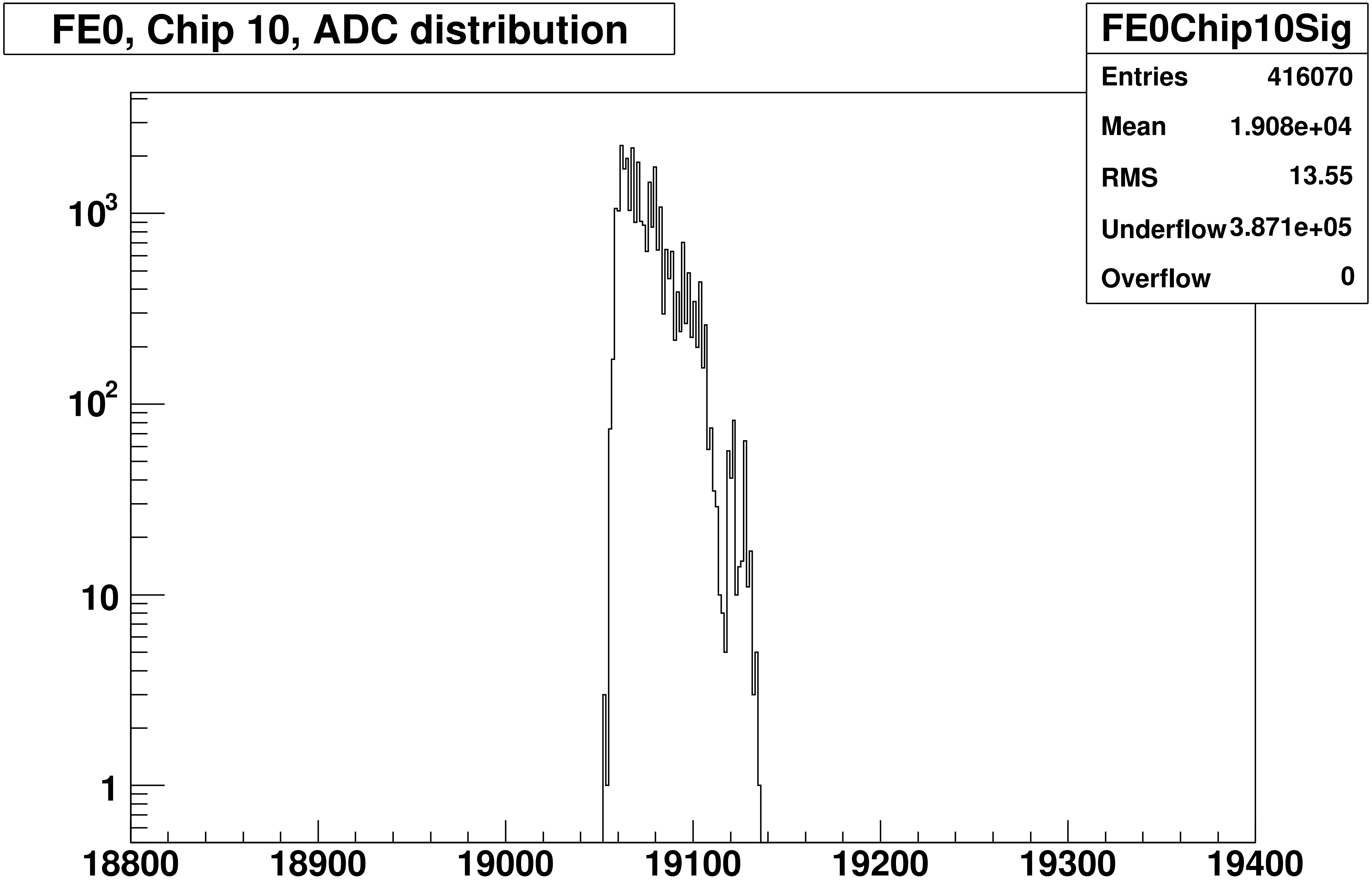
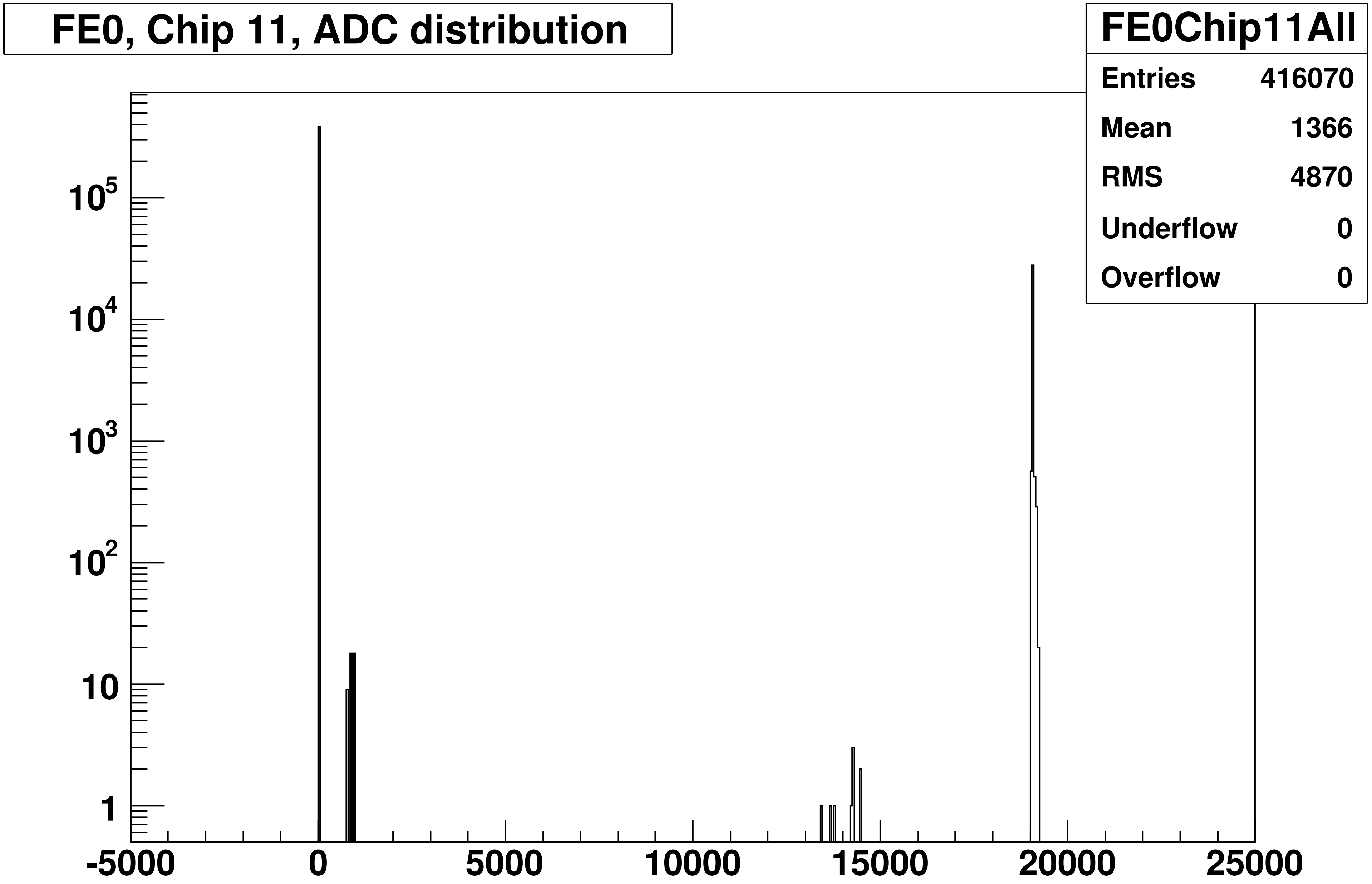
80
100



FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 416070
Mean 1.908e+04
RMS 13.55
Underflow 3.871e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 416070
Mean 1366
RMS 4870
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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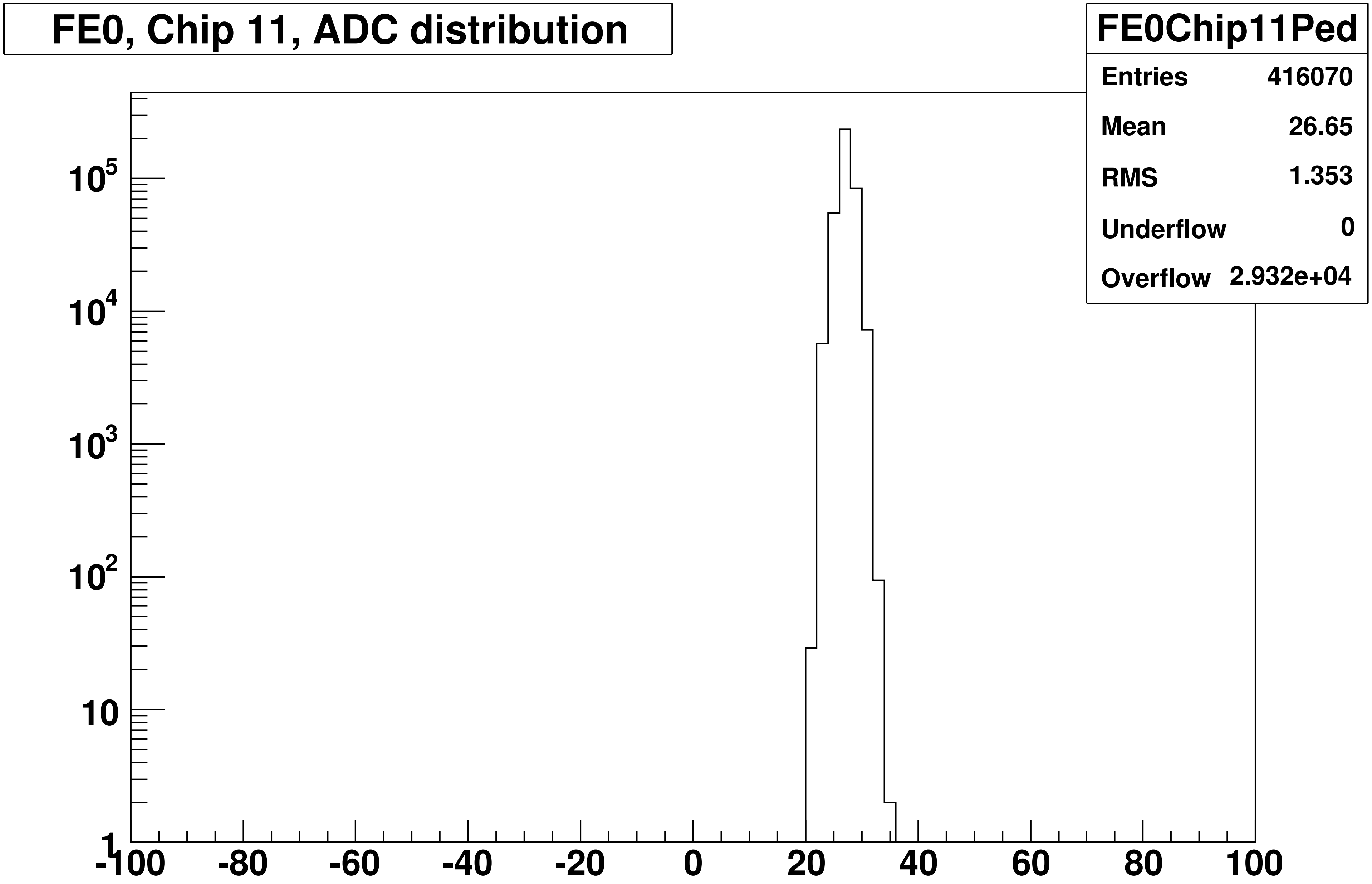
10000
15000
20000
25000



FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 416070
Mean 26.65
RMS 1.353
Underflow 0
Overflow 2.932e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
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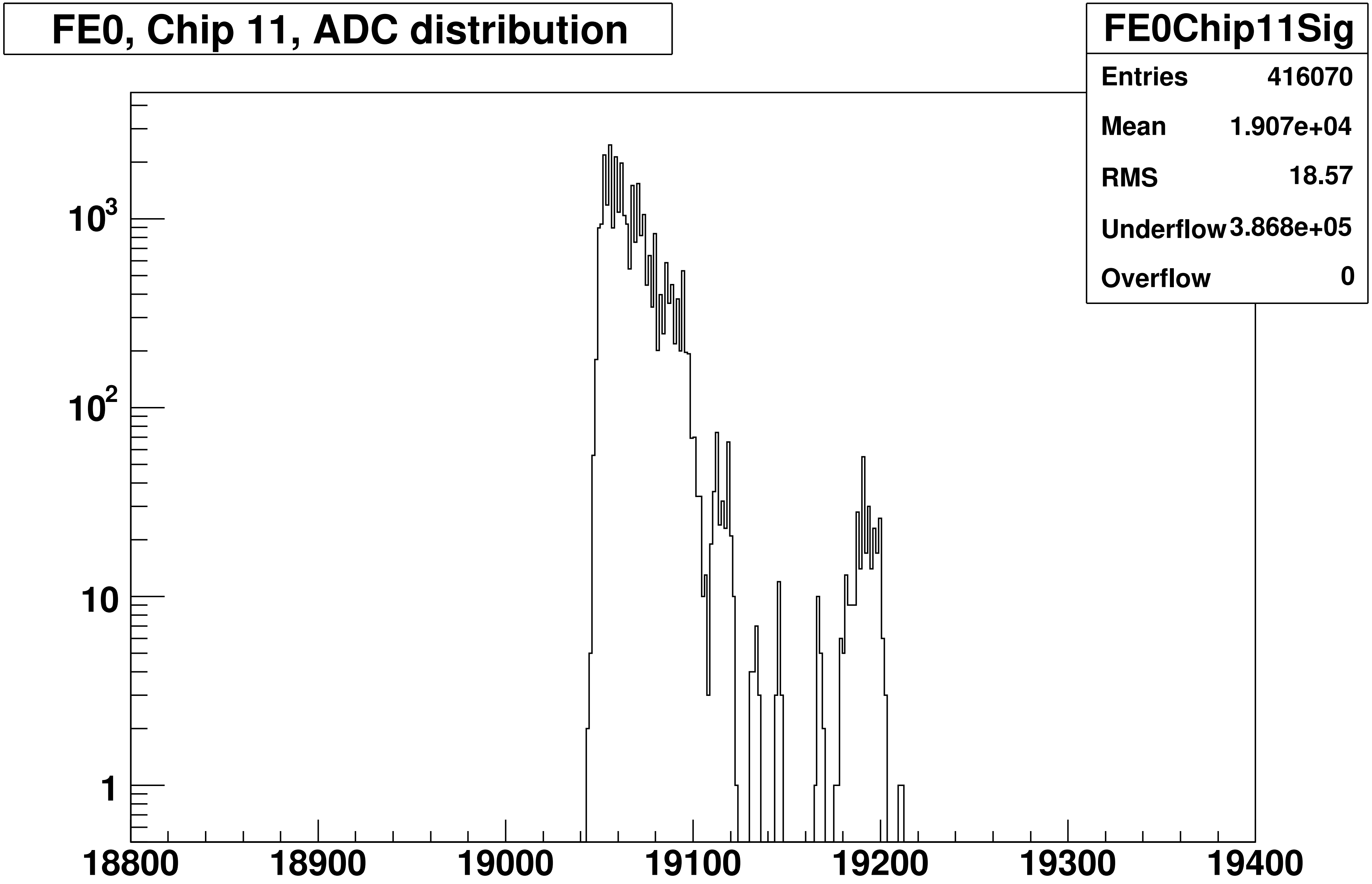
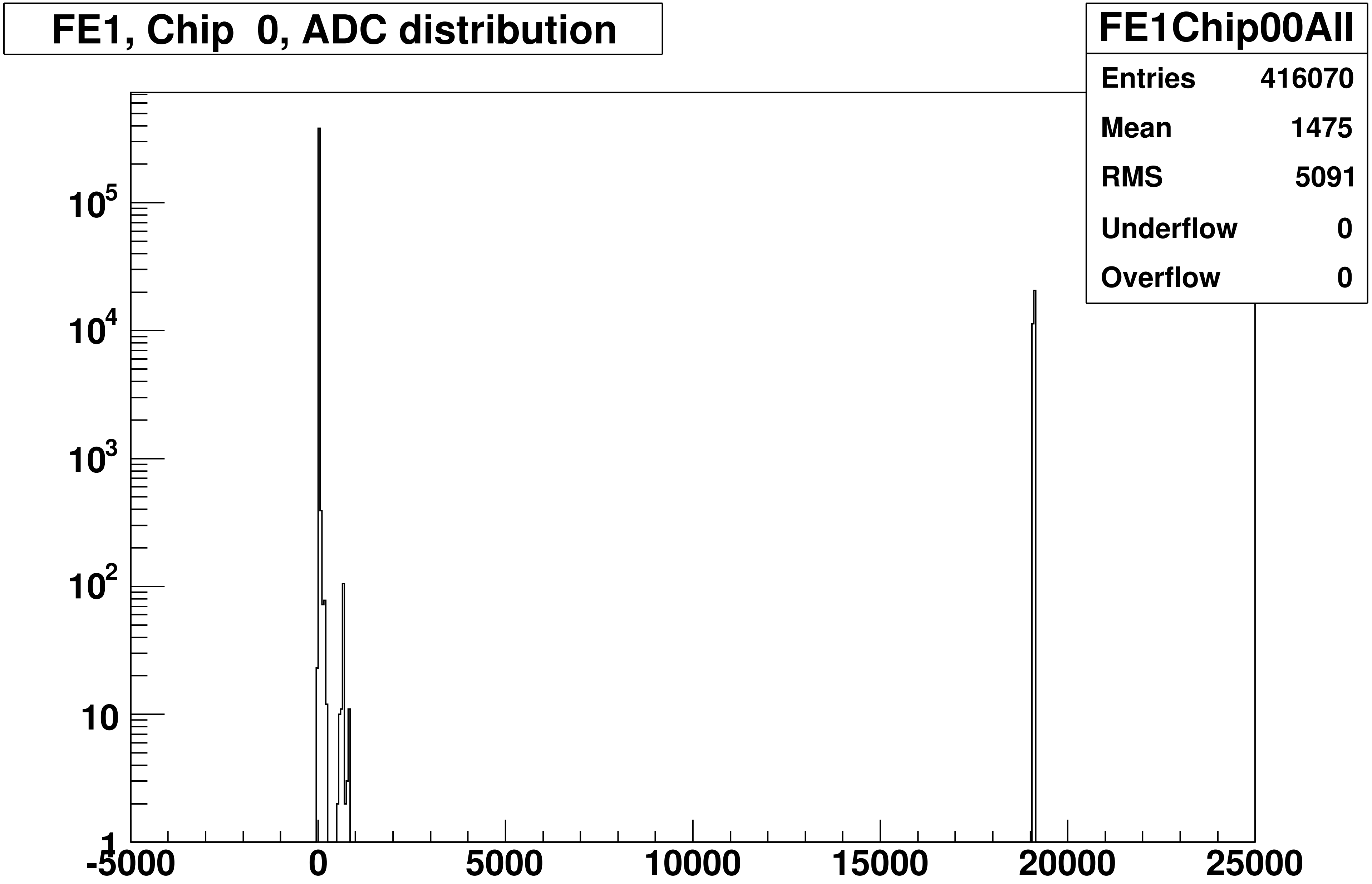
-40
-20
0
20
40
60
80
100



FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 416070
Mean 1.907e+04
RMS 18.57
Underflow 3.868e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 416070
Mean 1475
RMS 5091
Underflow 0
Overflow 0
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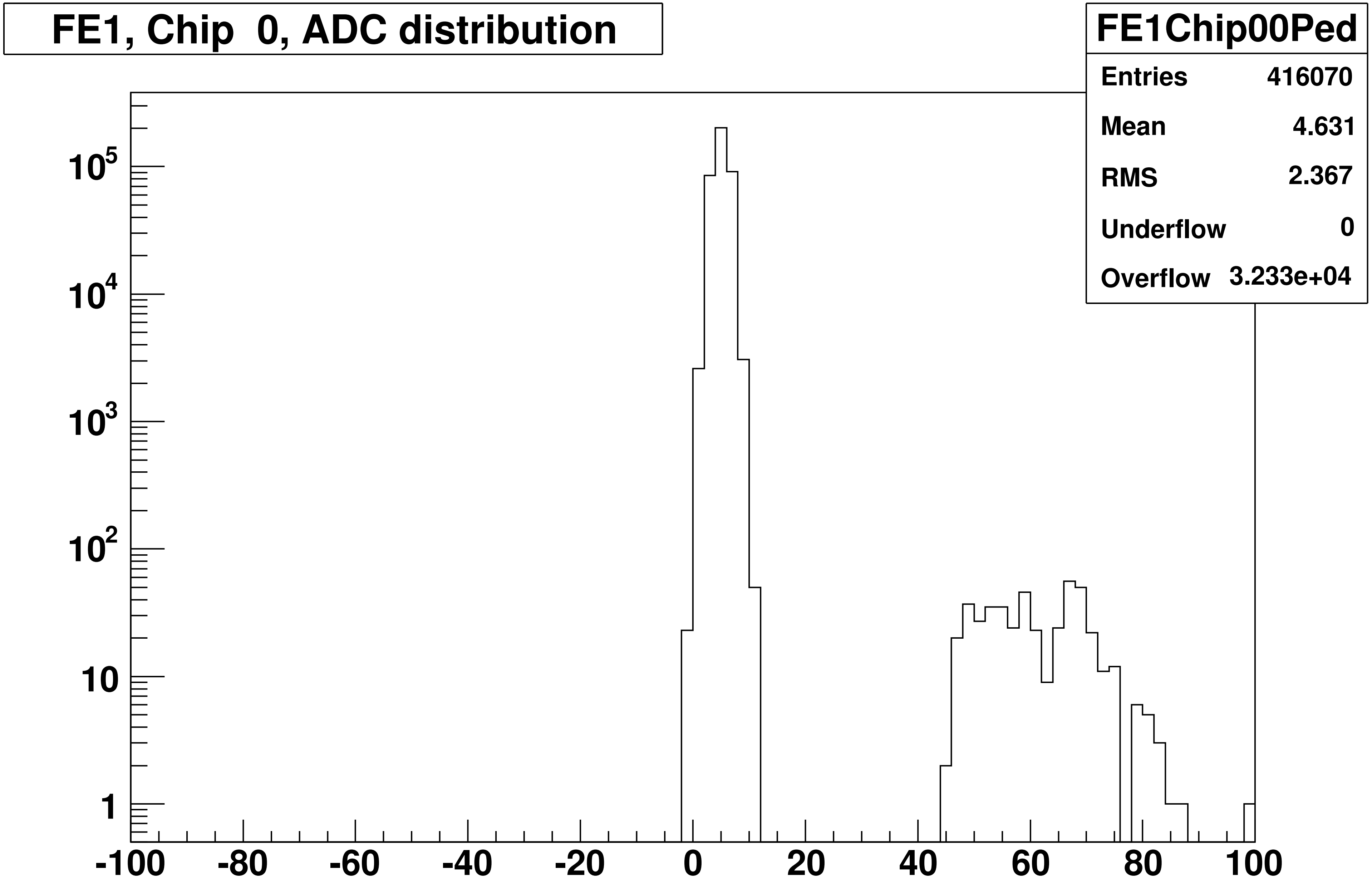
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 416070
Mean 4.631
RMS 2.367
Underflow 0
Overflow 3.233e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
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-20
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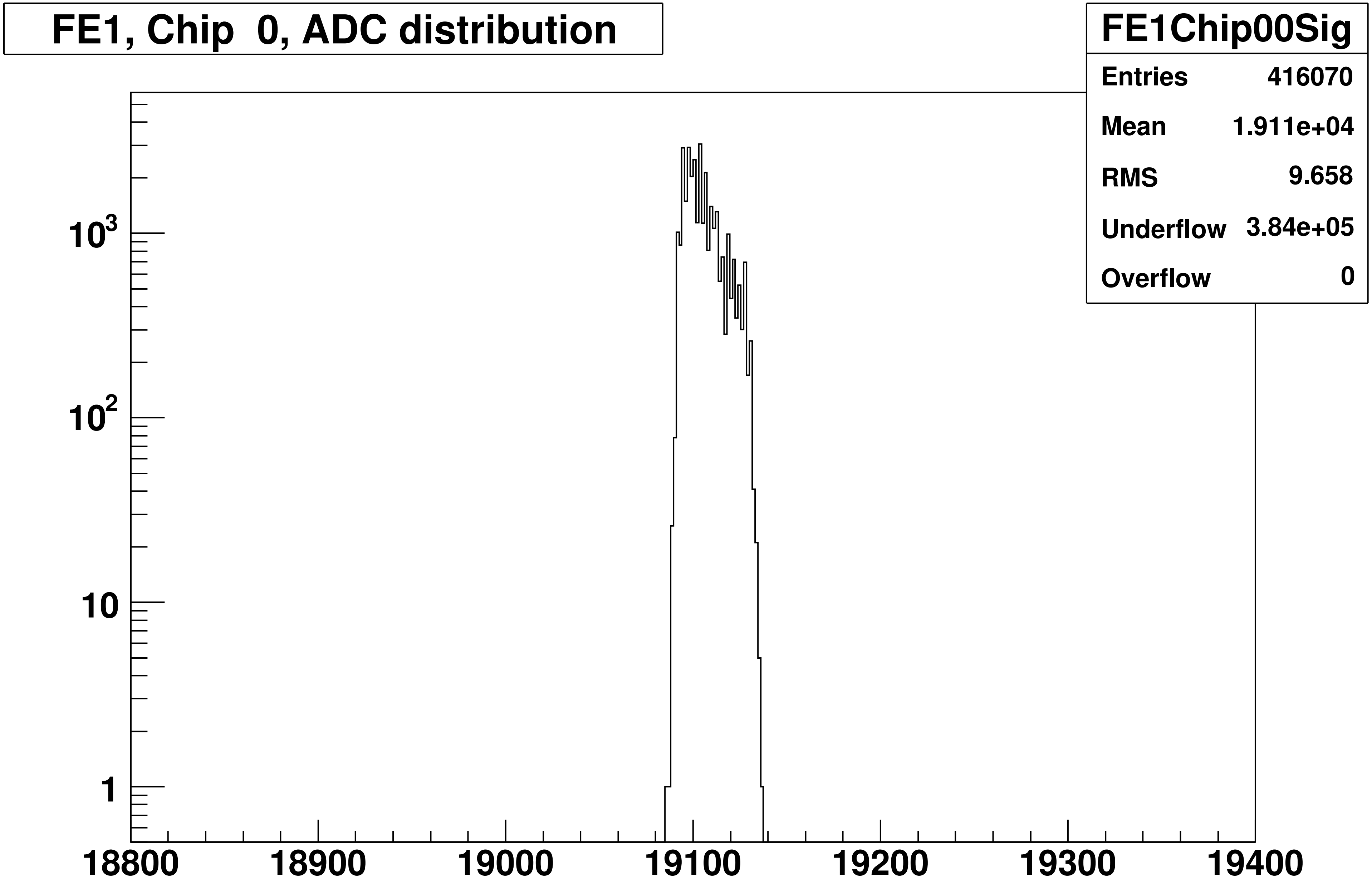
40
60
80
100


FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 416070
Mean 1.911e+04
RMS 9.658
Underflow 3.84e+05
Overflow 0
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10^2
10
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18800
18900
19000
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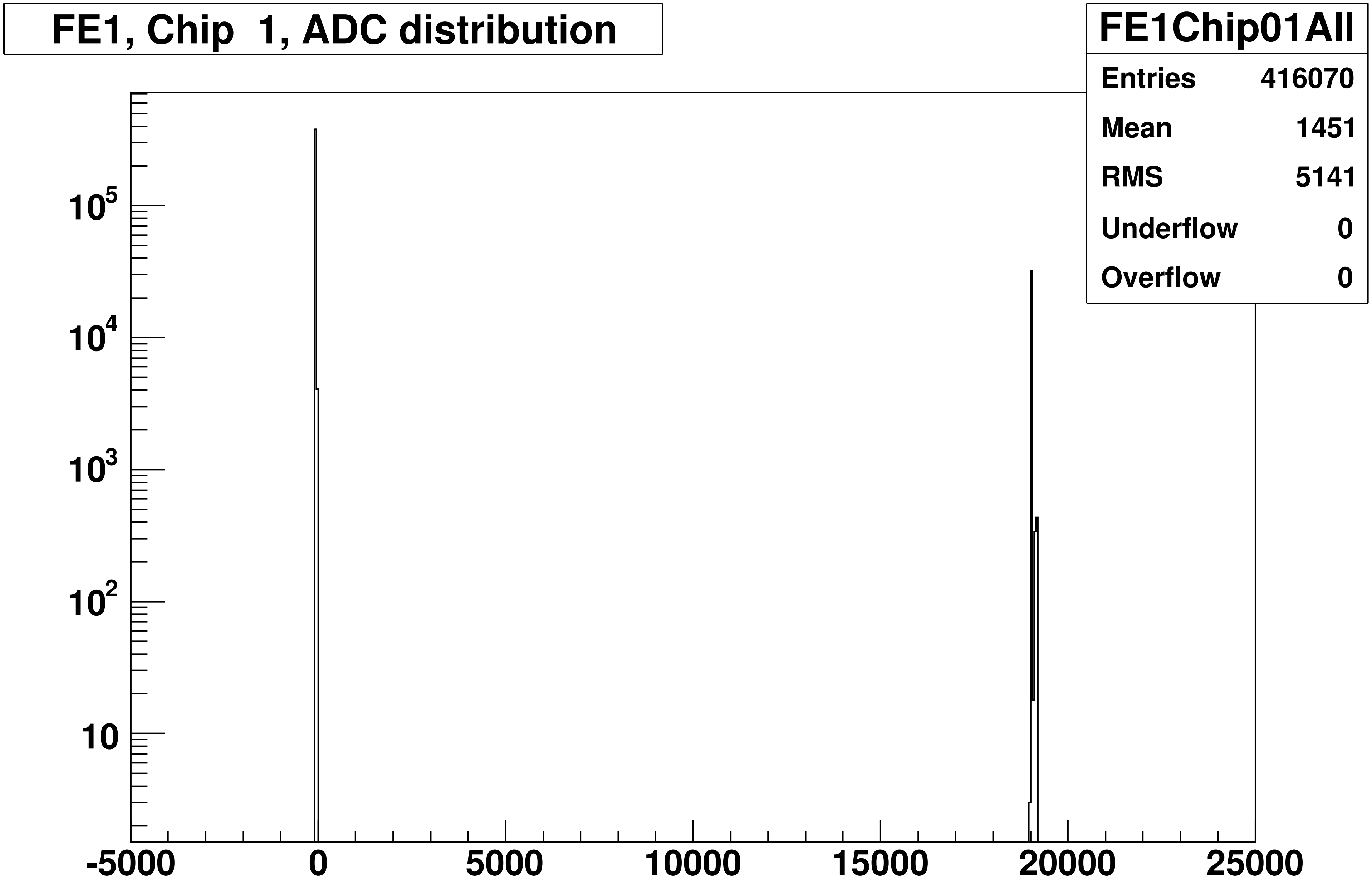
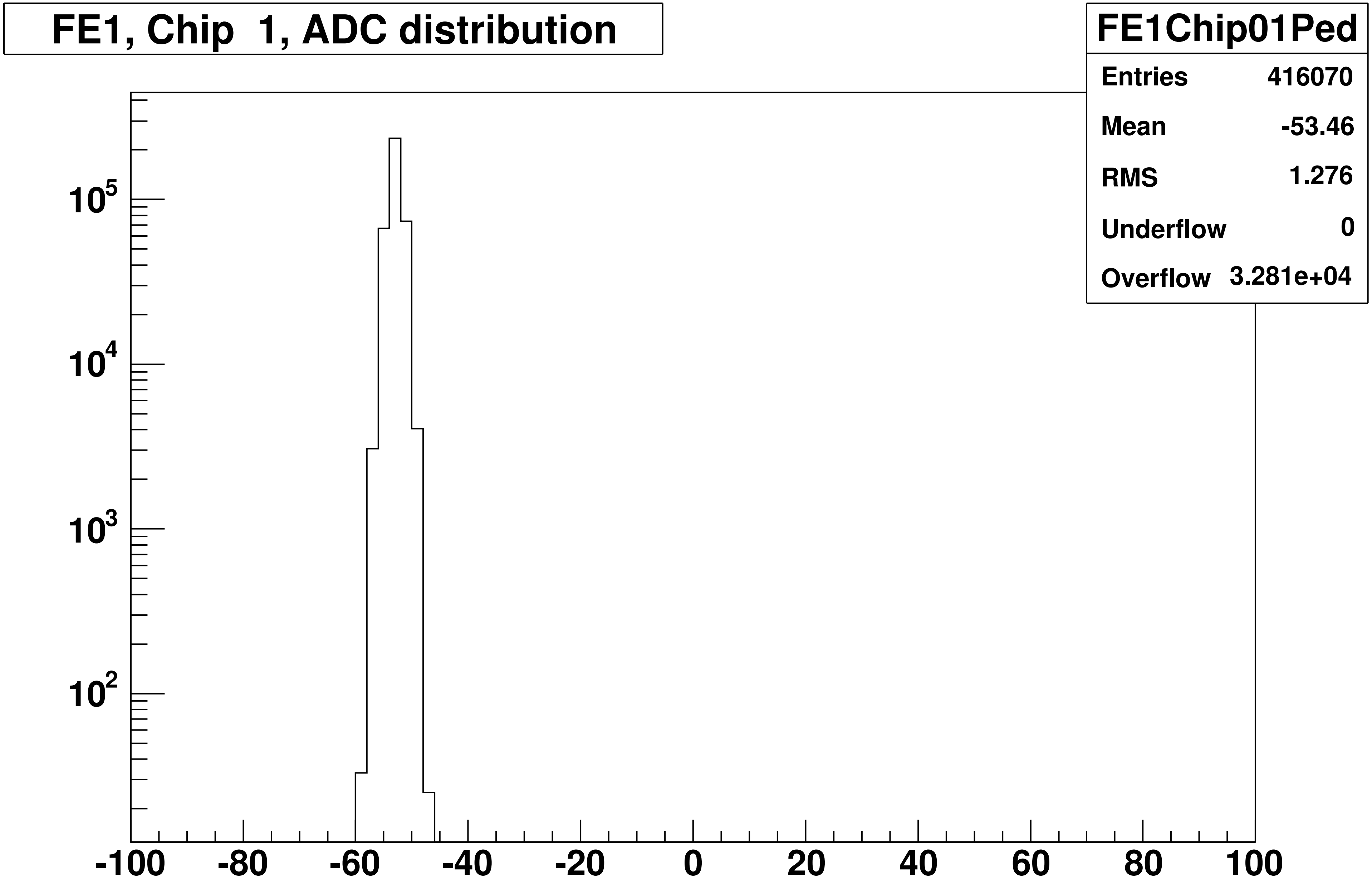
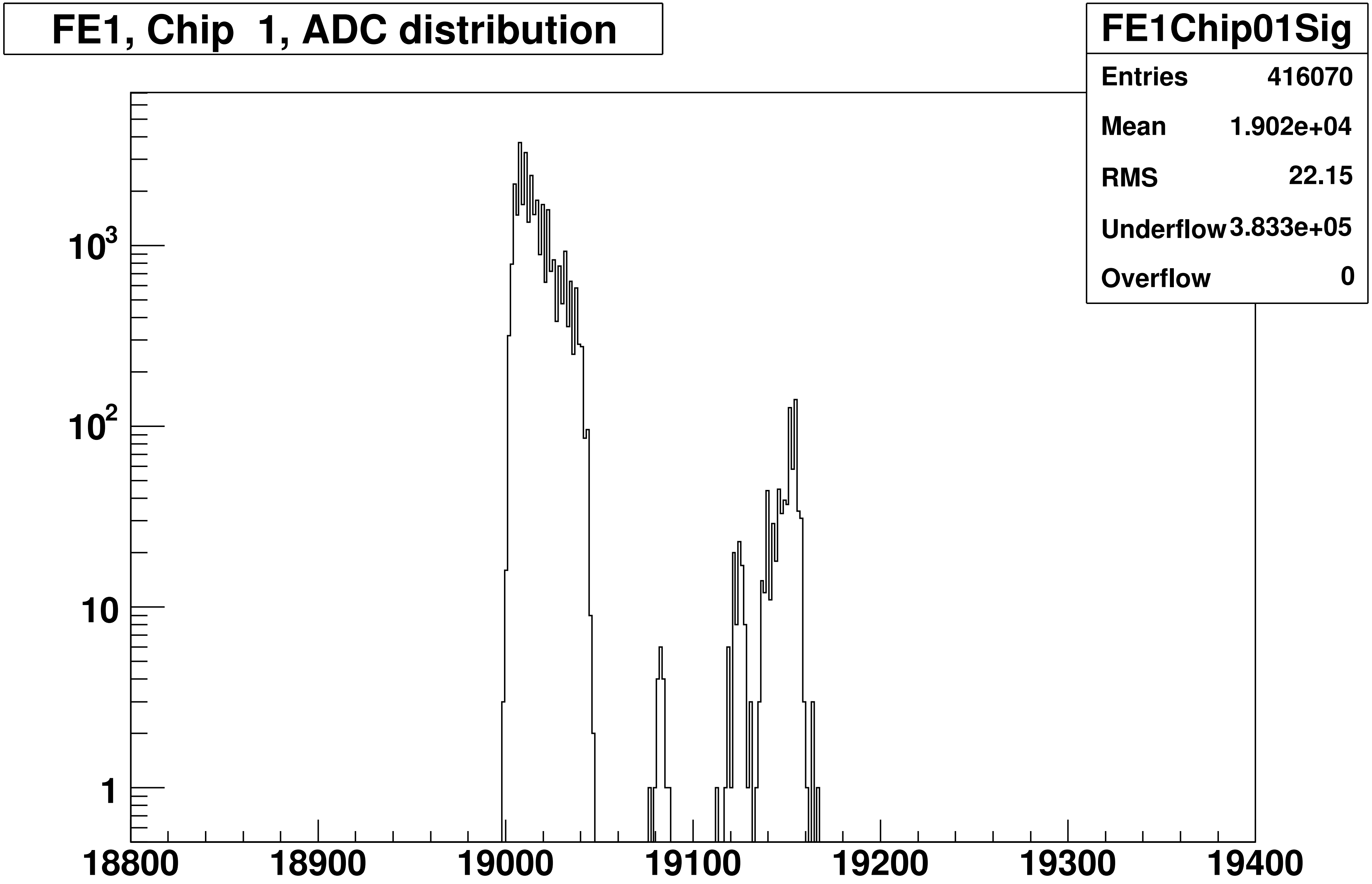
19300
19400

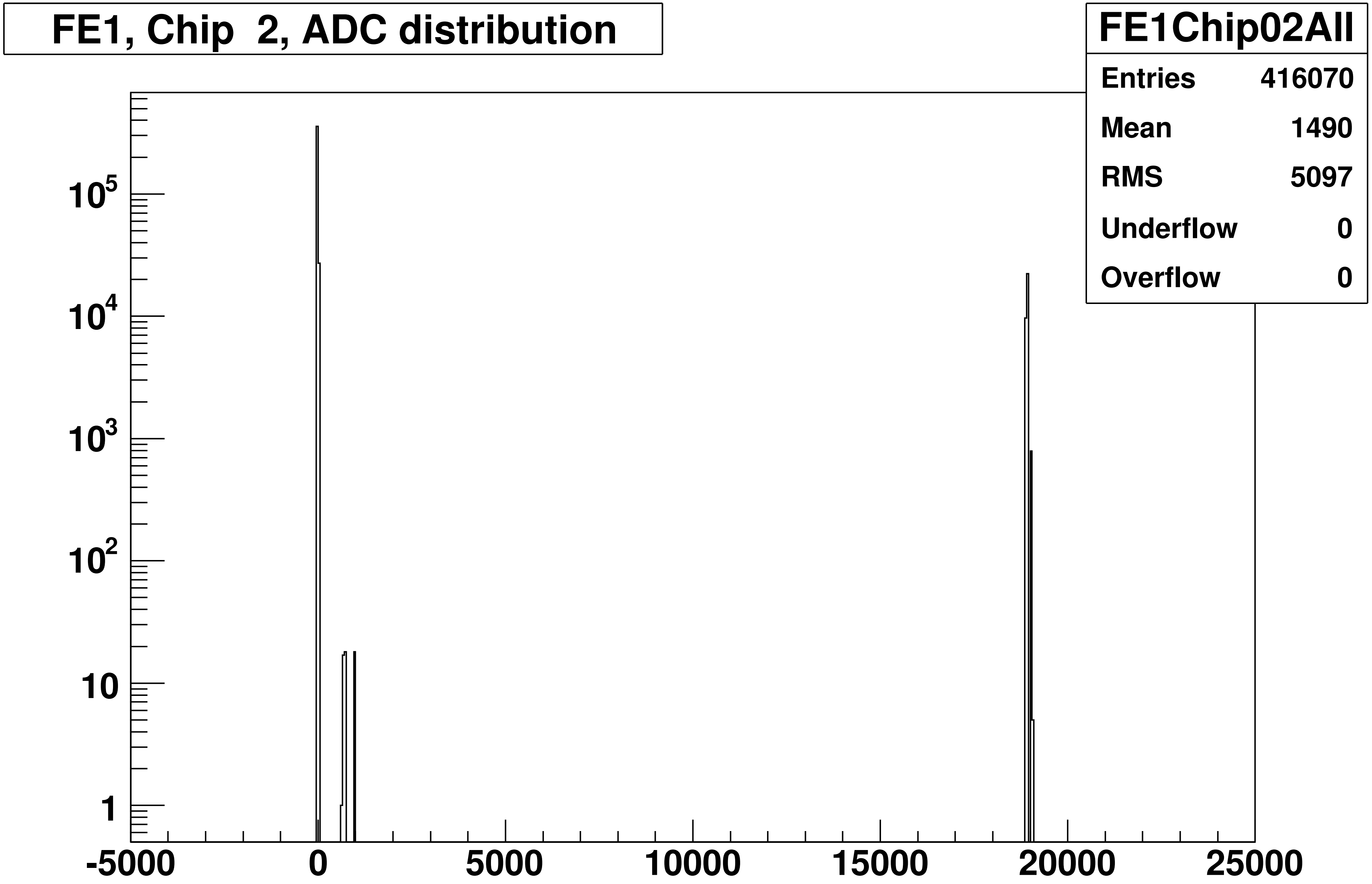


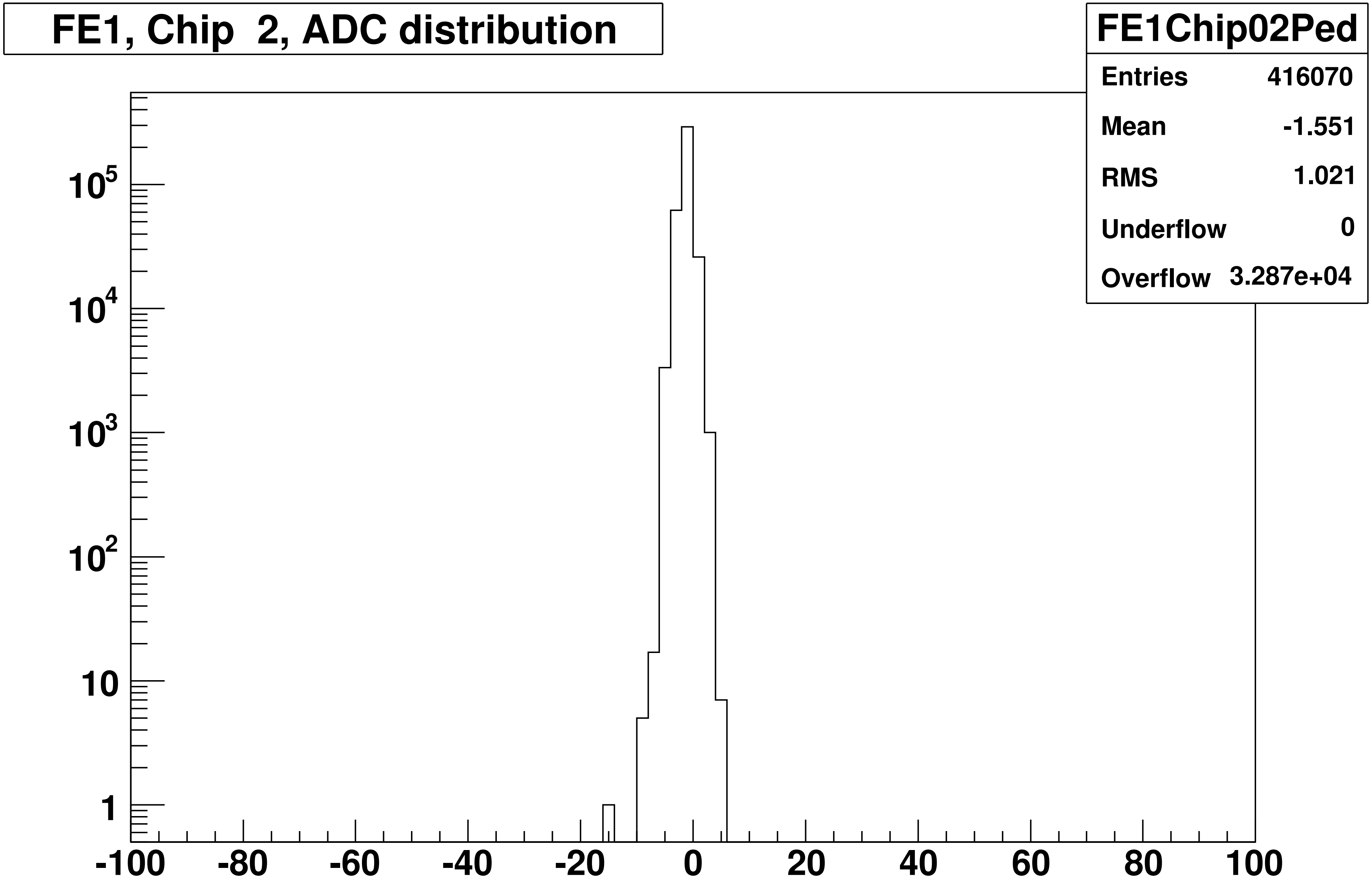
FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 416070
Mean 1451
RMS 5141
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 416070
Mean -53.46
RMS 1.276
Underflow 0
Overflow 3.281e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

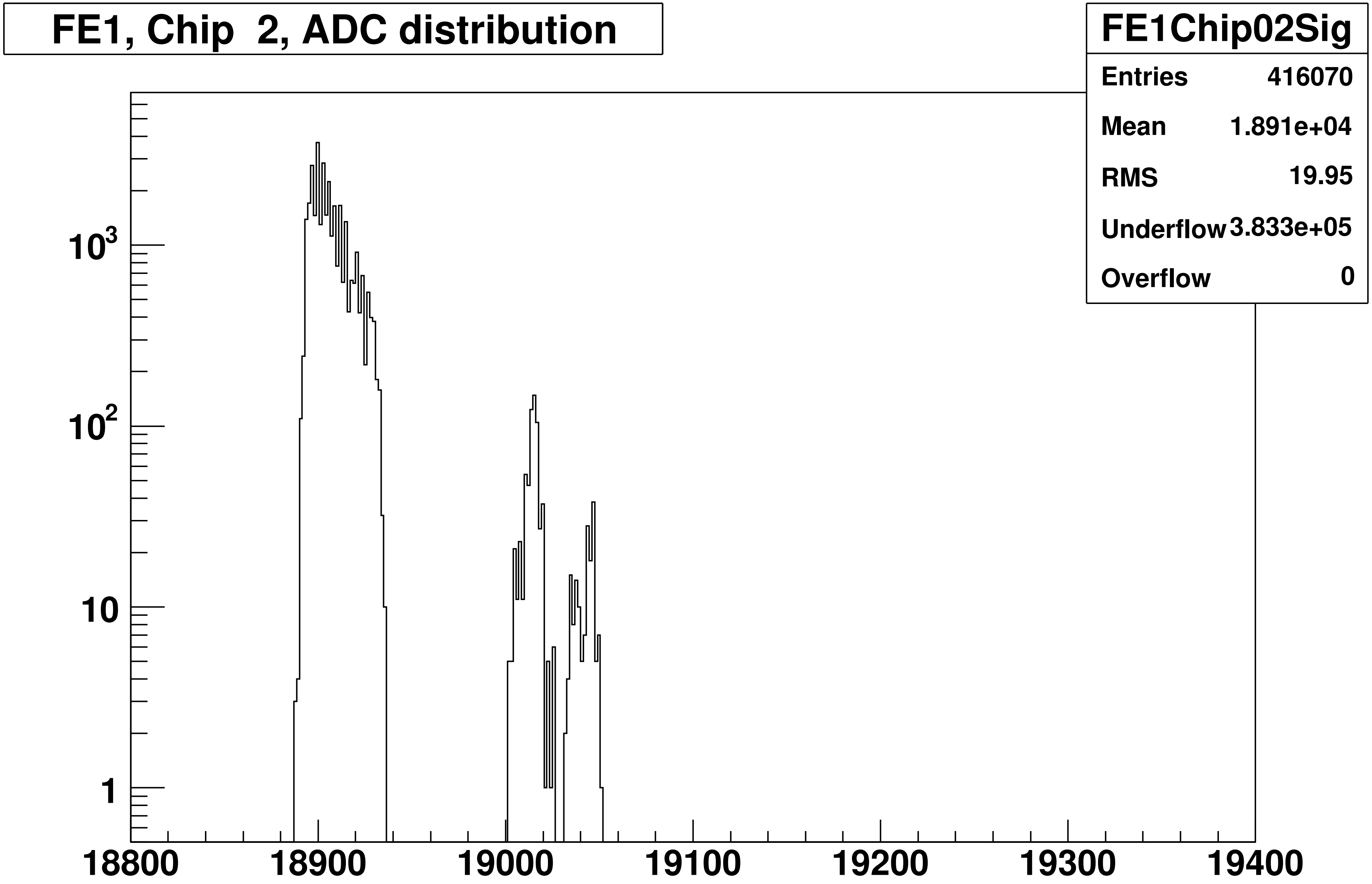


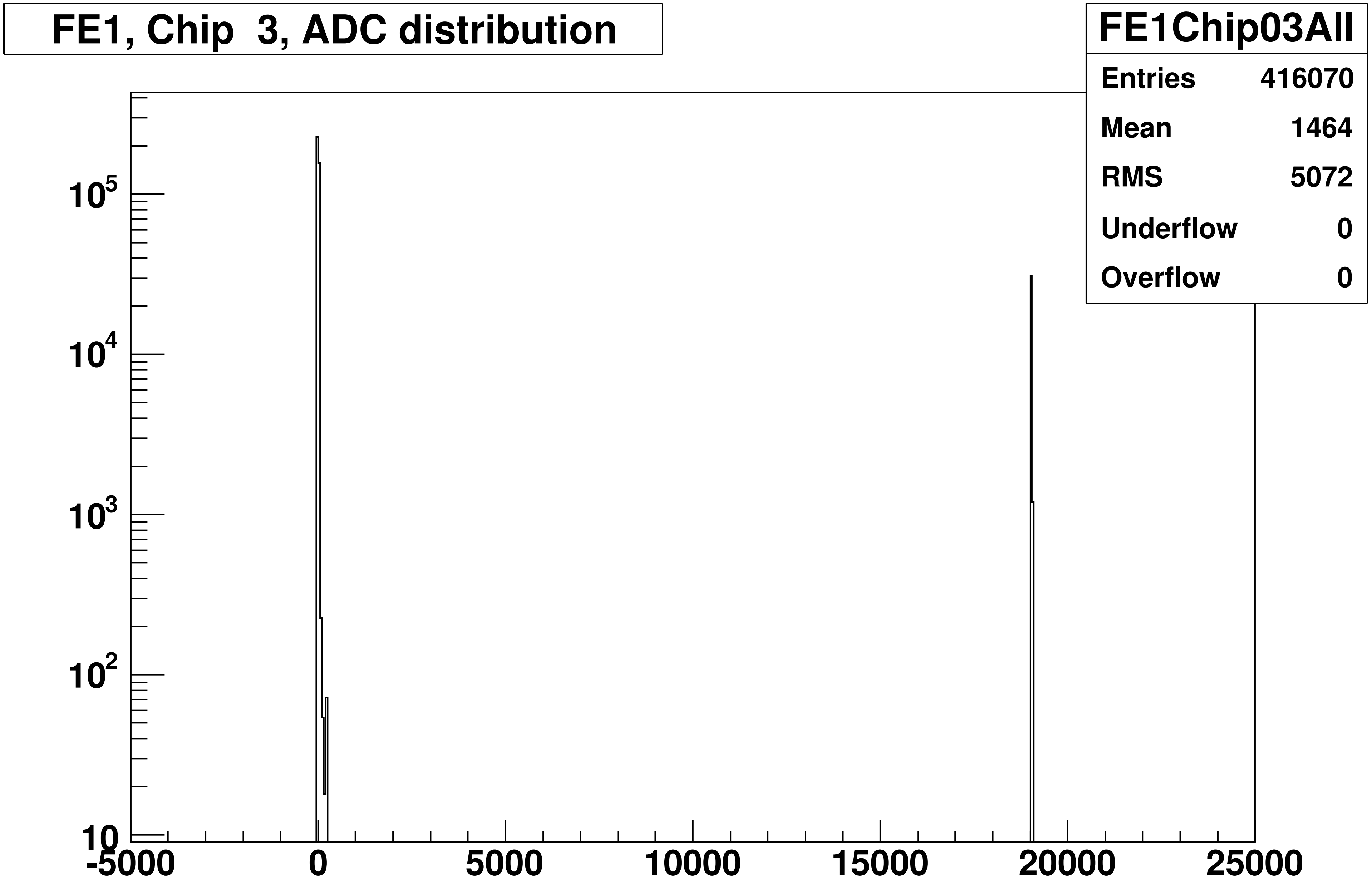
FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 416070
Mean 1.902e+04
RMS 22.15
Underflow 3.833e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 416070
Mean 1490
RMS 5097
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

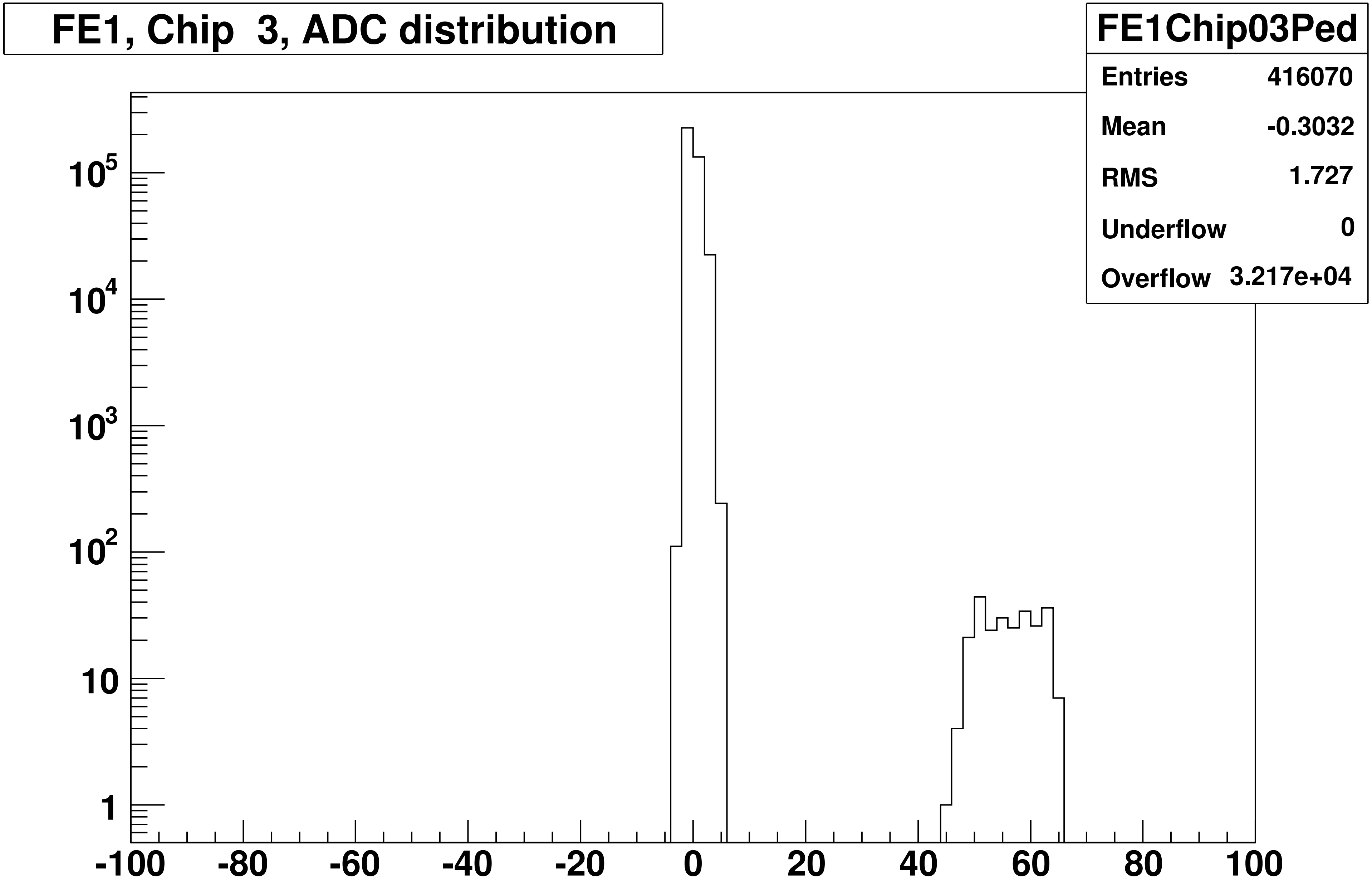


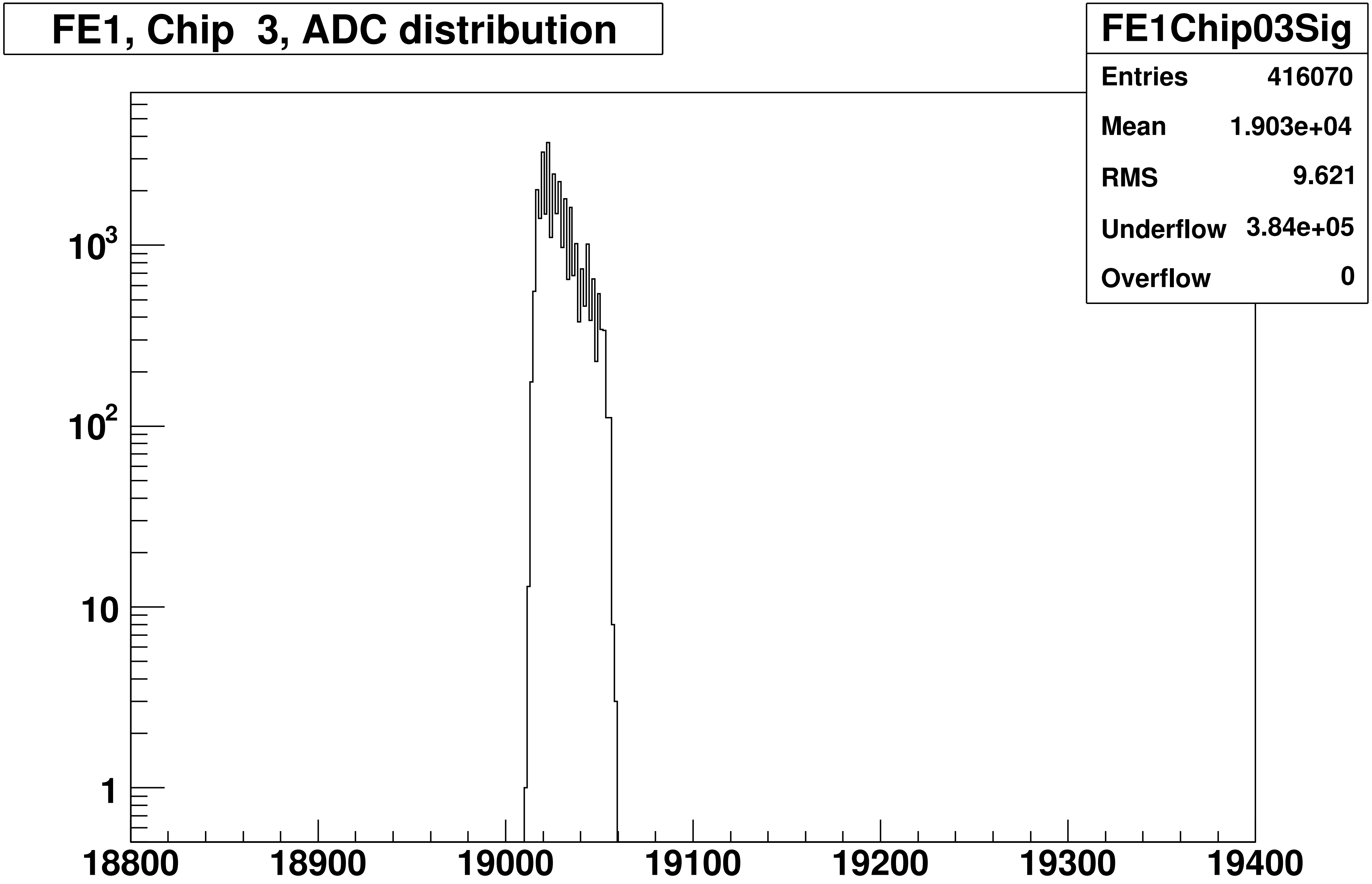
FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 416070
Mean -1.551
RMS 1.021
Underflow 0
Overflow 3.287e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

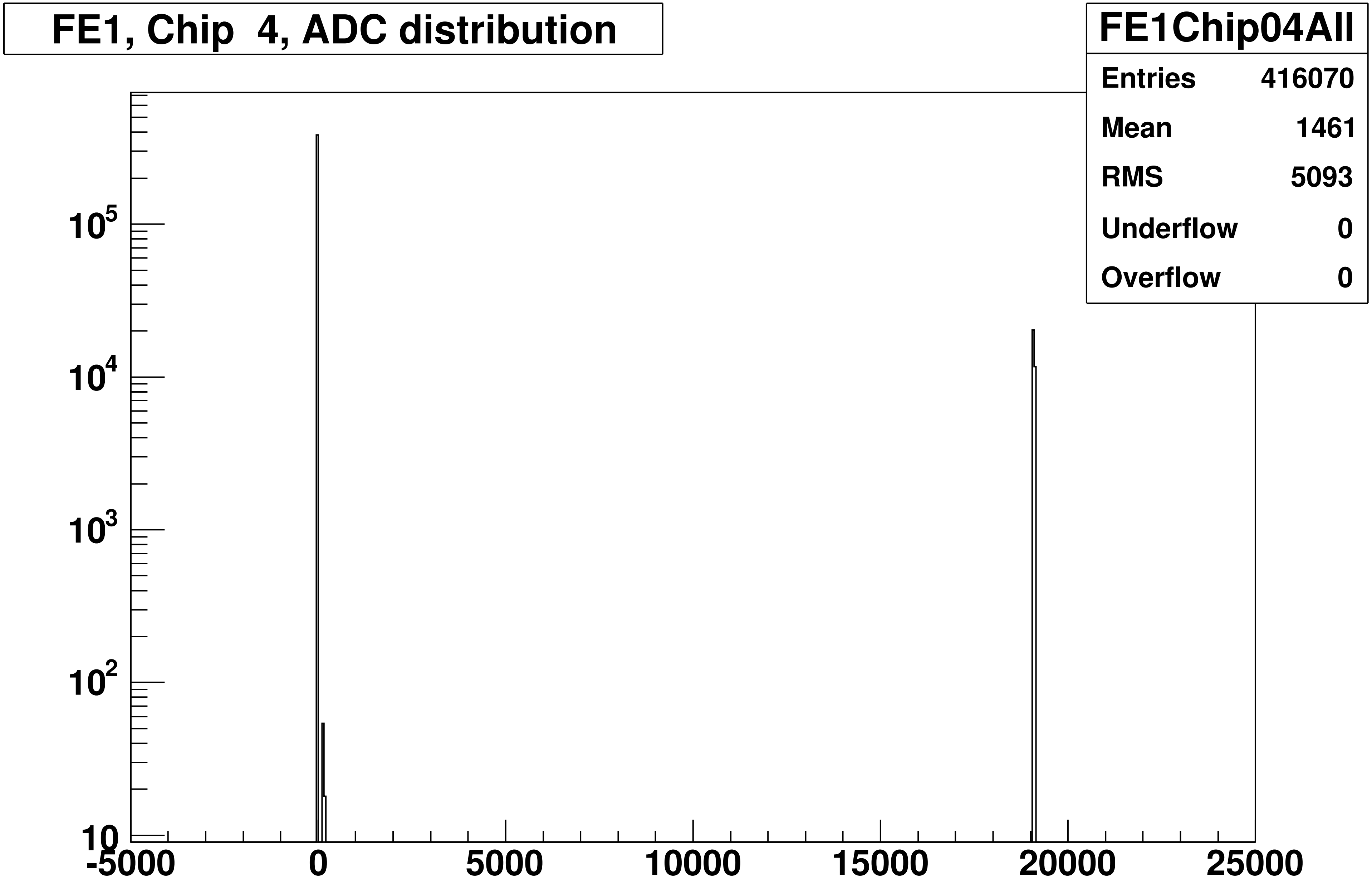

FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 416070
Mean 1.891e+04
RMS 19.95
Underflow 3.833e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

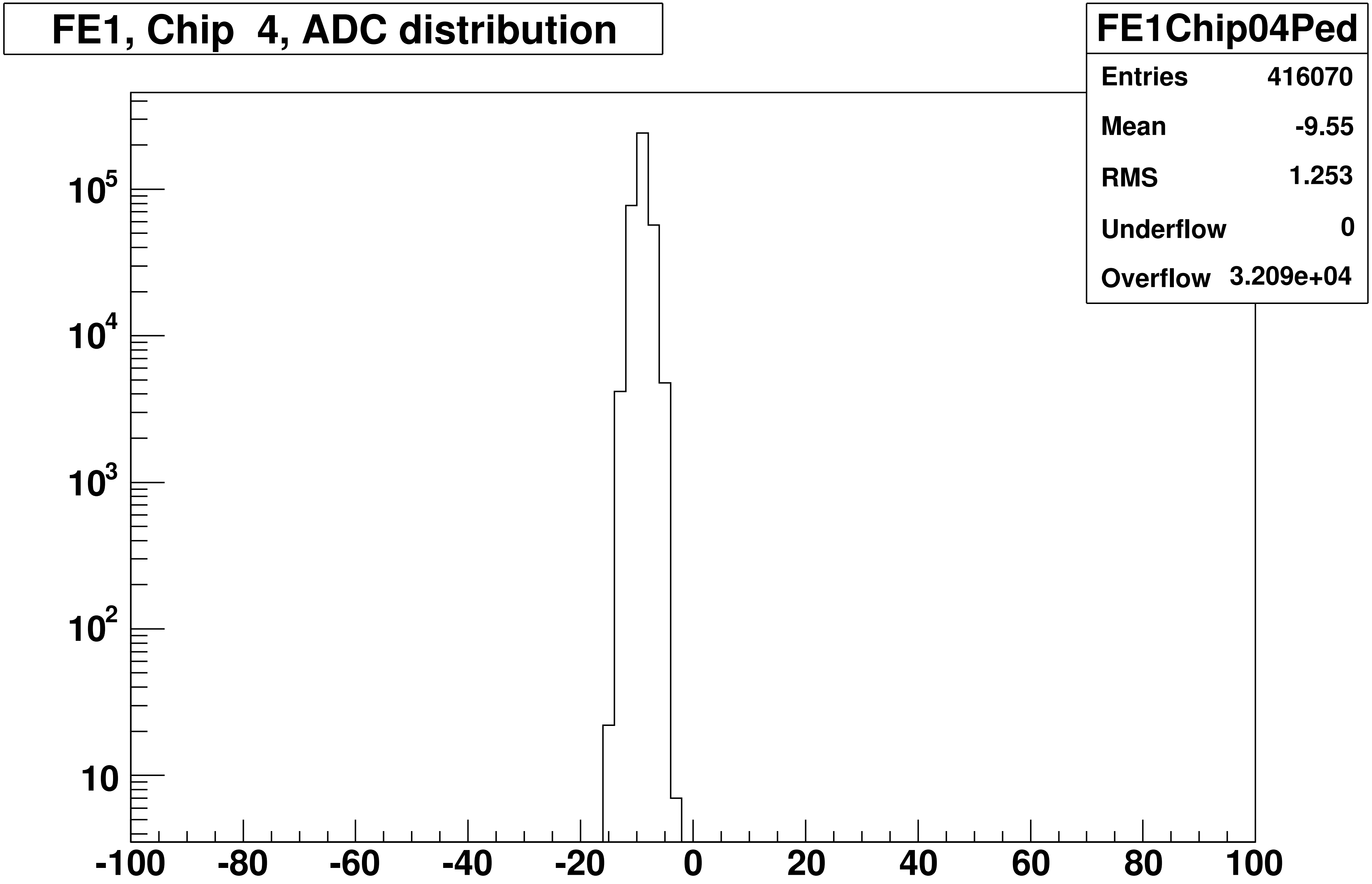


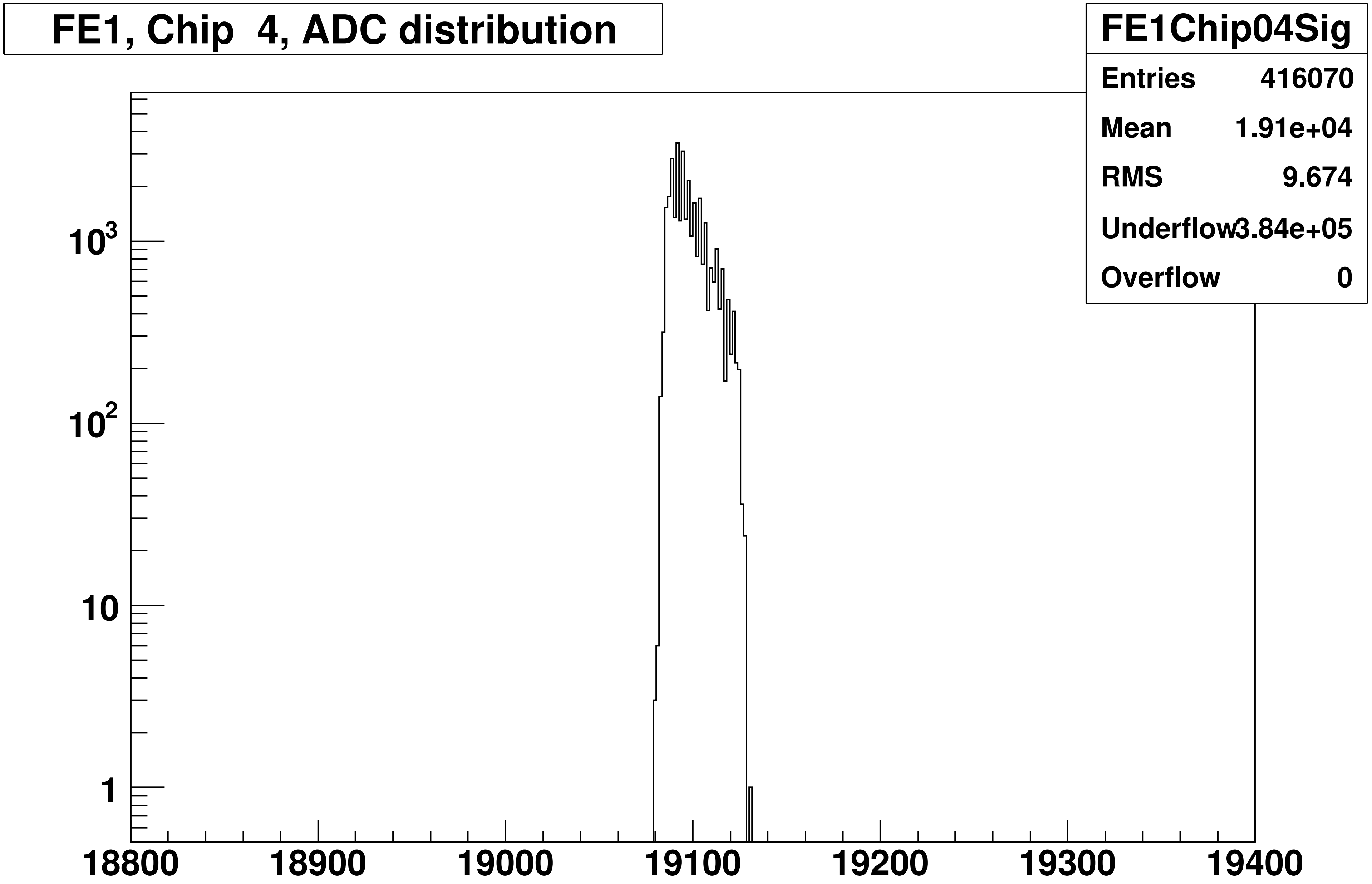
FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 416070
Mean 1464
RMS 5072
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 416070
Mean -0.3032
RMS 1.727
Underflow 0
Overflow 3.217e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

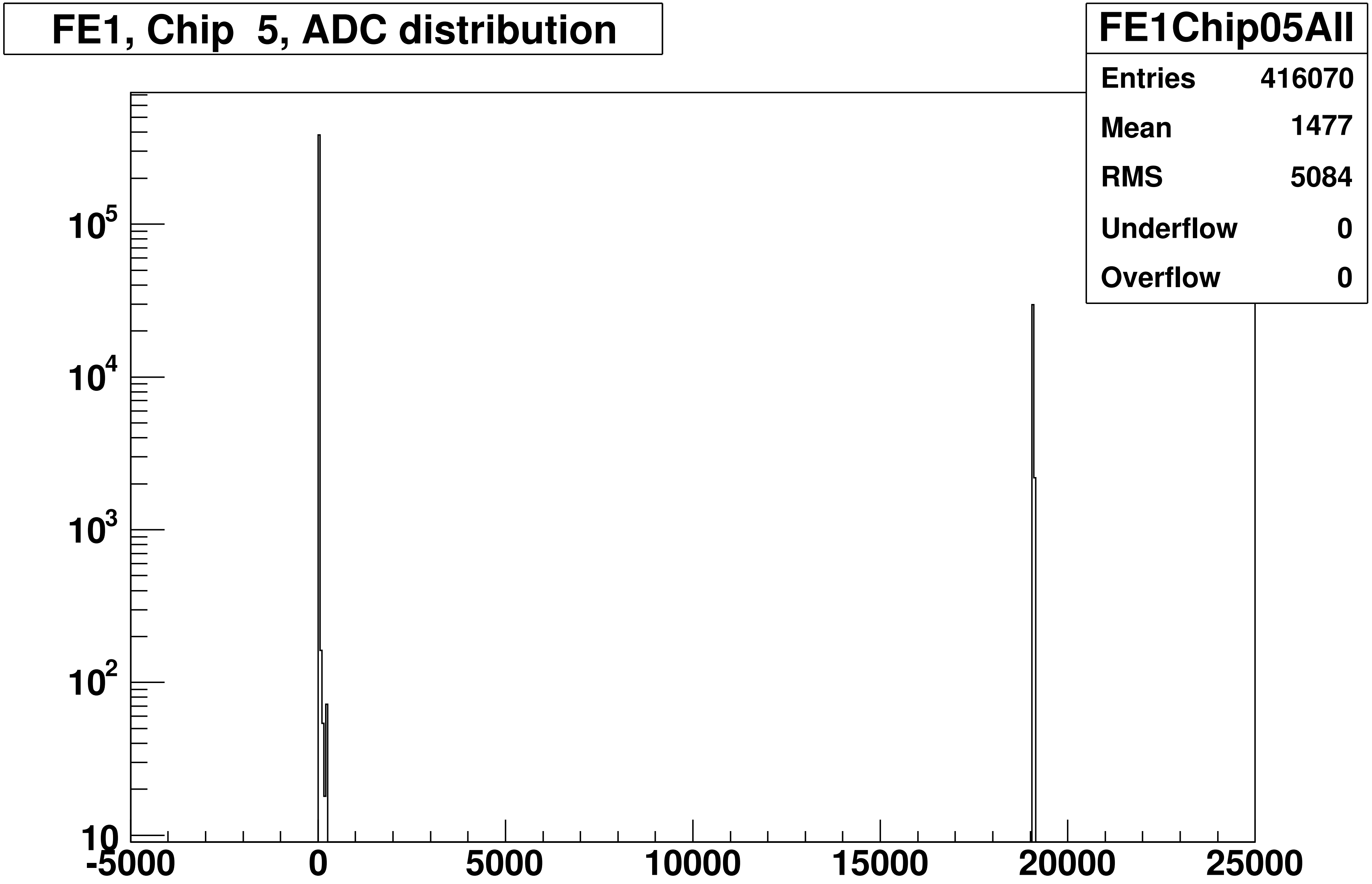

FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 416070
Mean 1.903e+04
RMS 9.621
Underflow 3.84e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

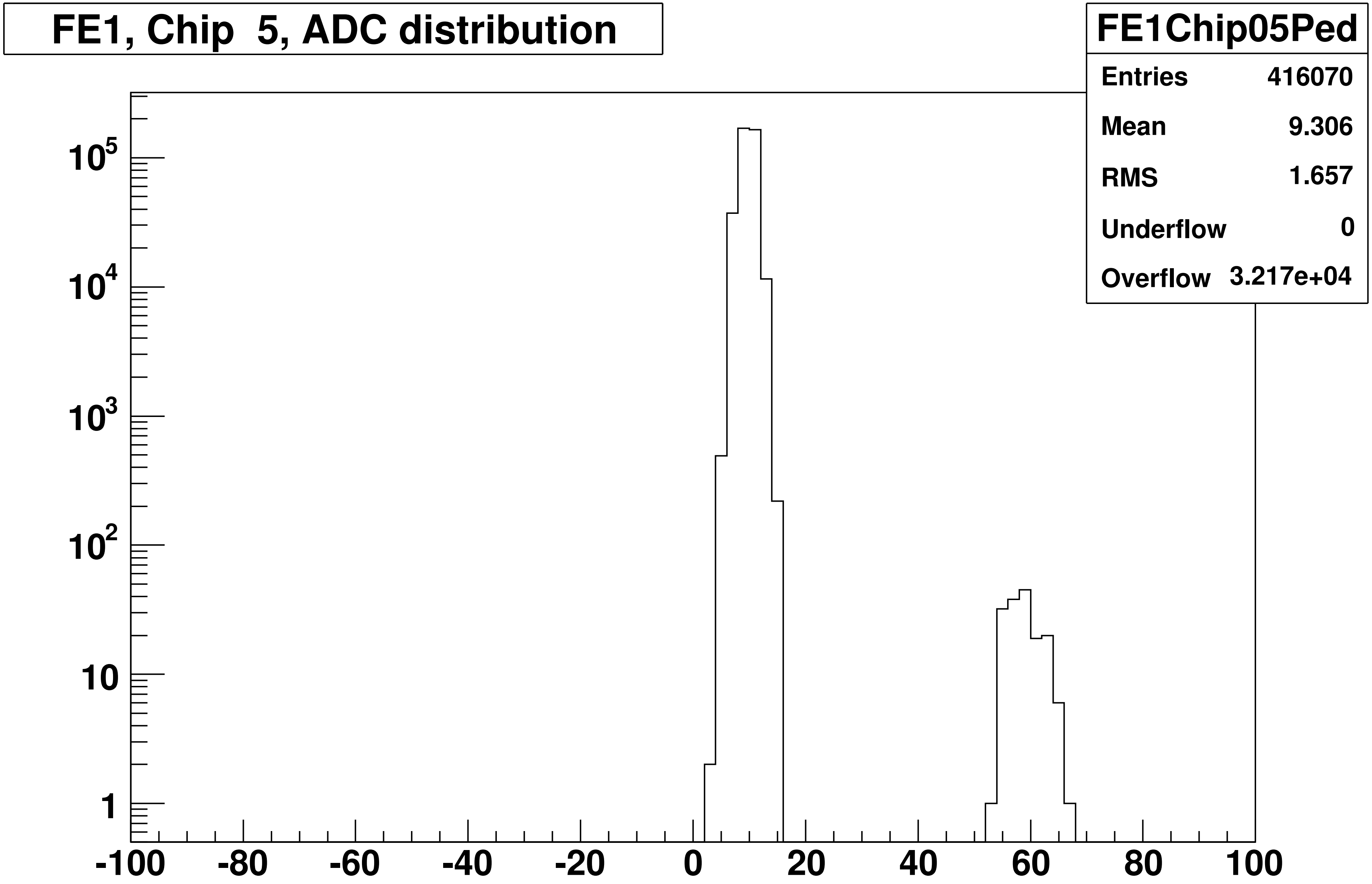

FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 416070
Mean 1461
RMS 5093
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 416070
Mean -9.55
RMS 1.253
Underflow 0
Overflow 3.209e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

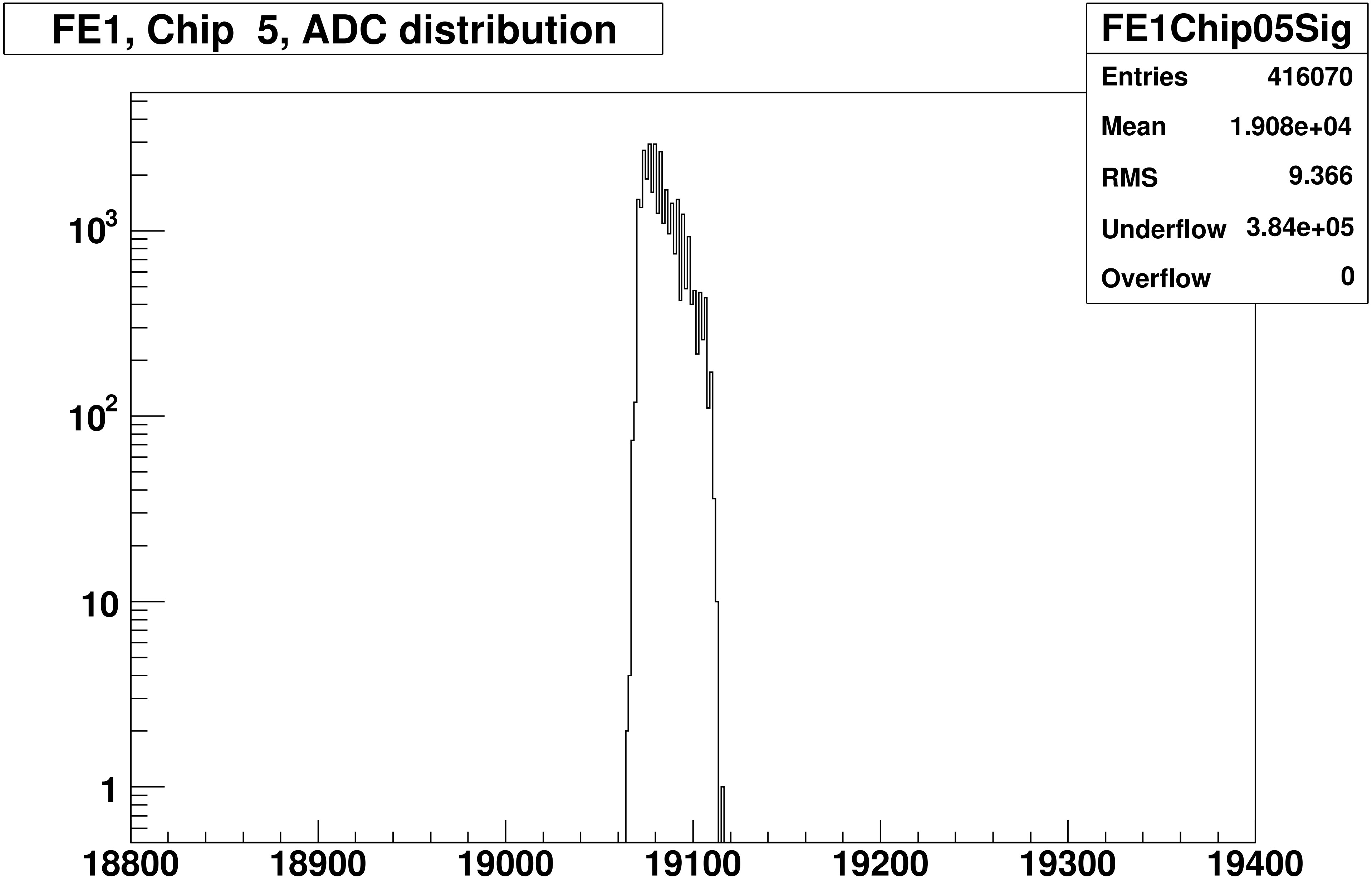


FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 416070
Mean 1.91e+04
RMS 9.674
Underflow 3.84e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

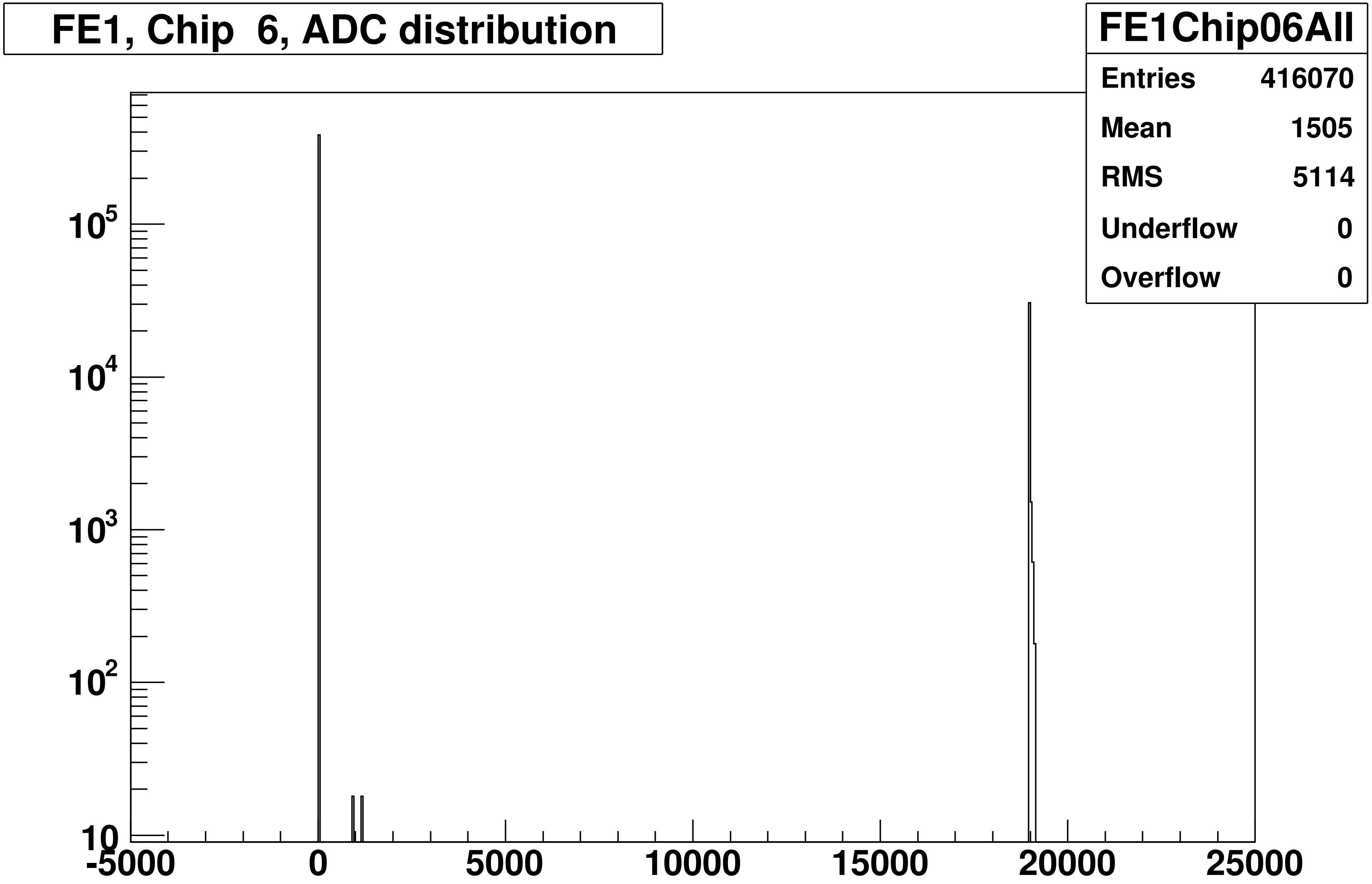

FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 416070
Mean 1477
RMS 5084
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

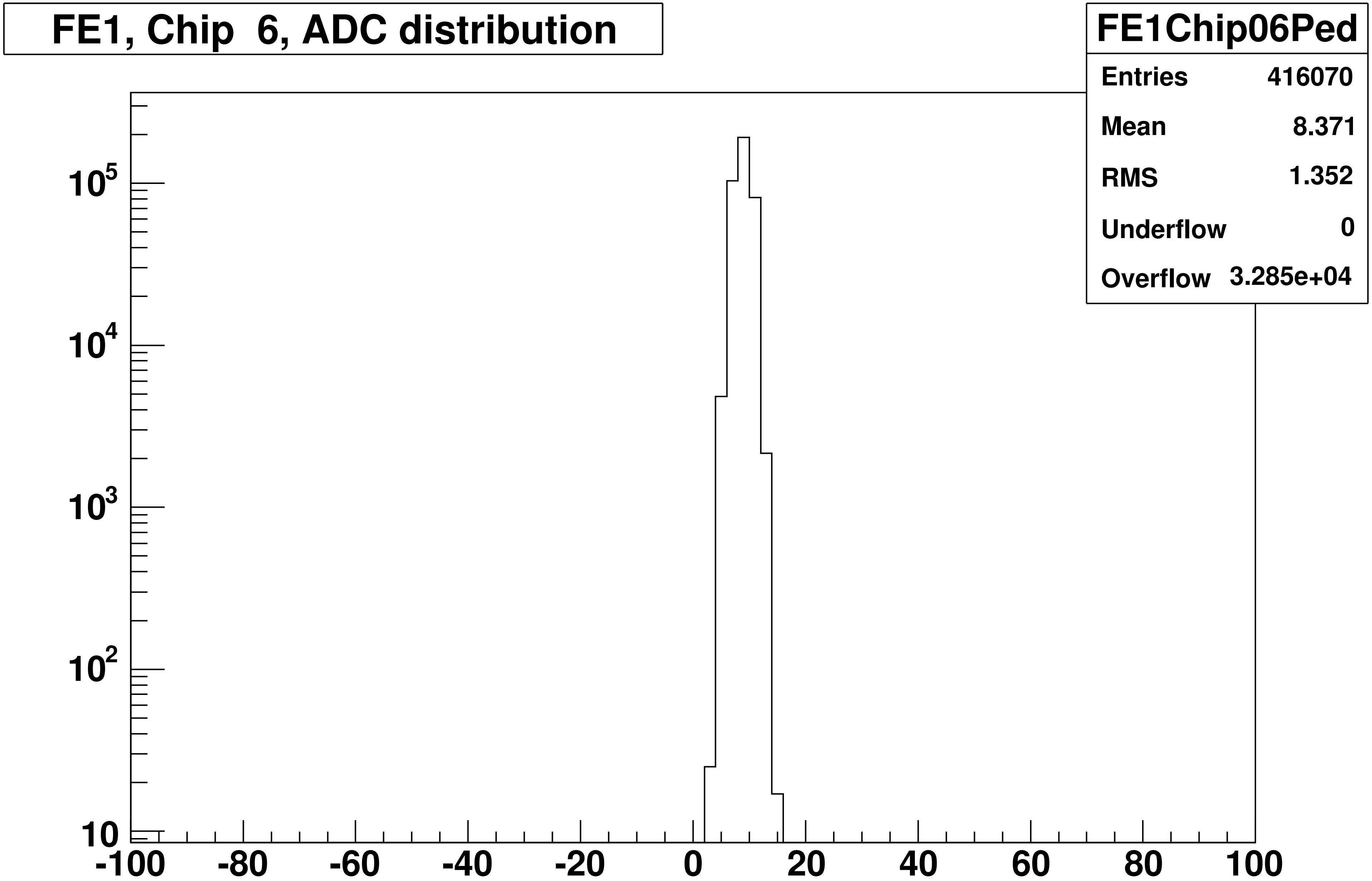


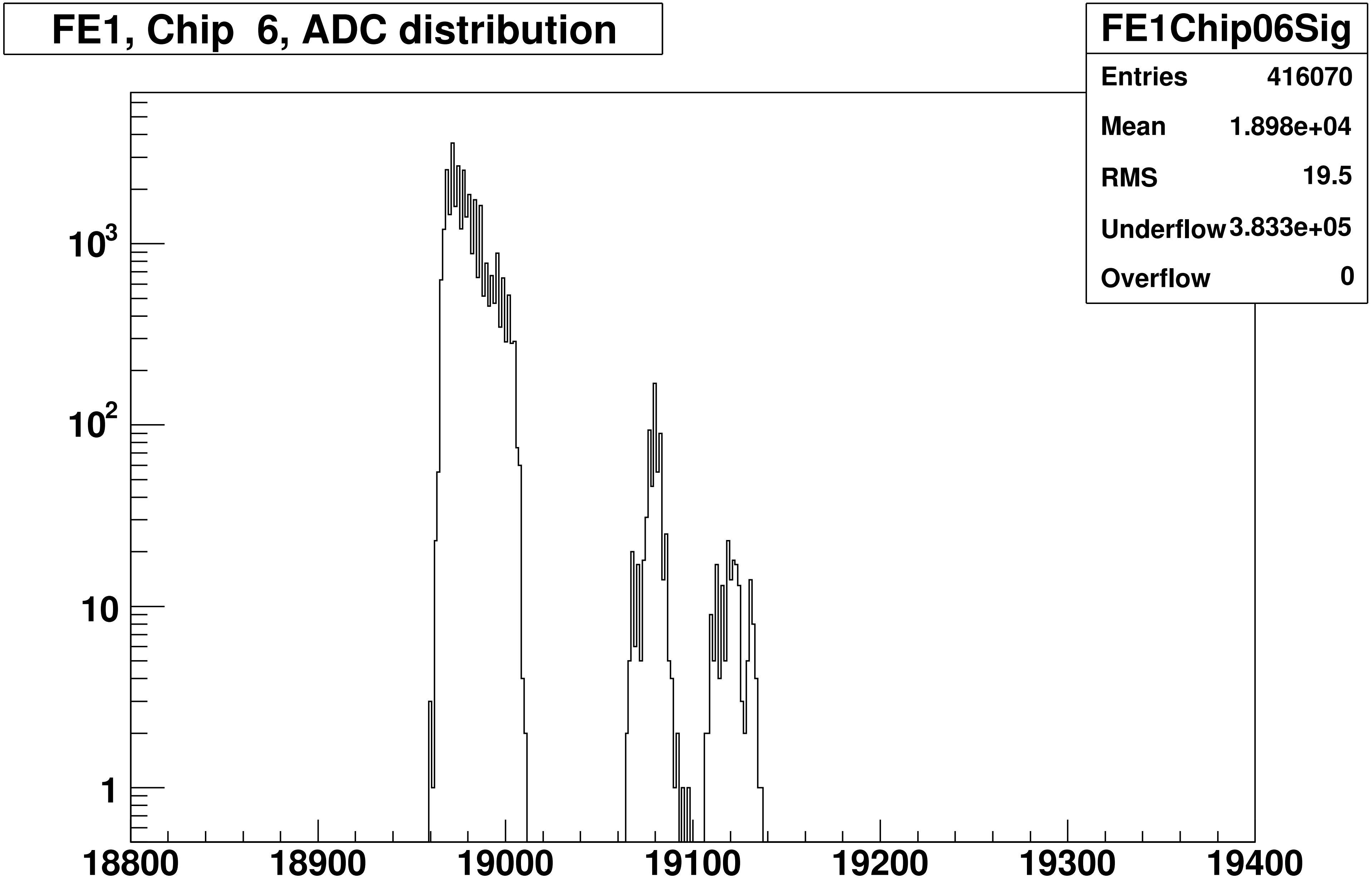
FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 416070
Mean 9.306
RMS 1.657
Underflow 0
Overflow 3.217e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

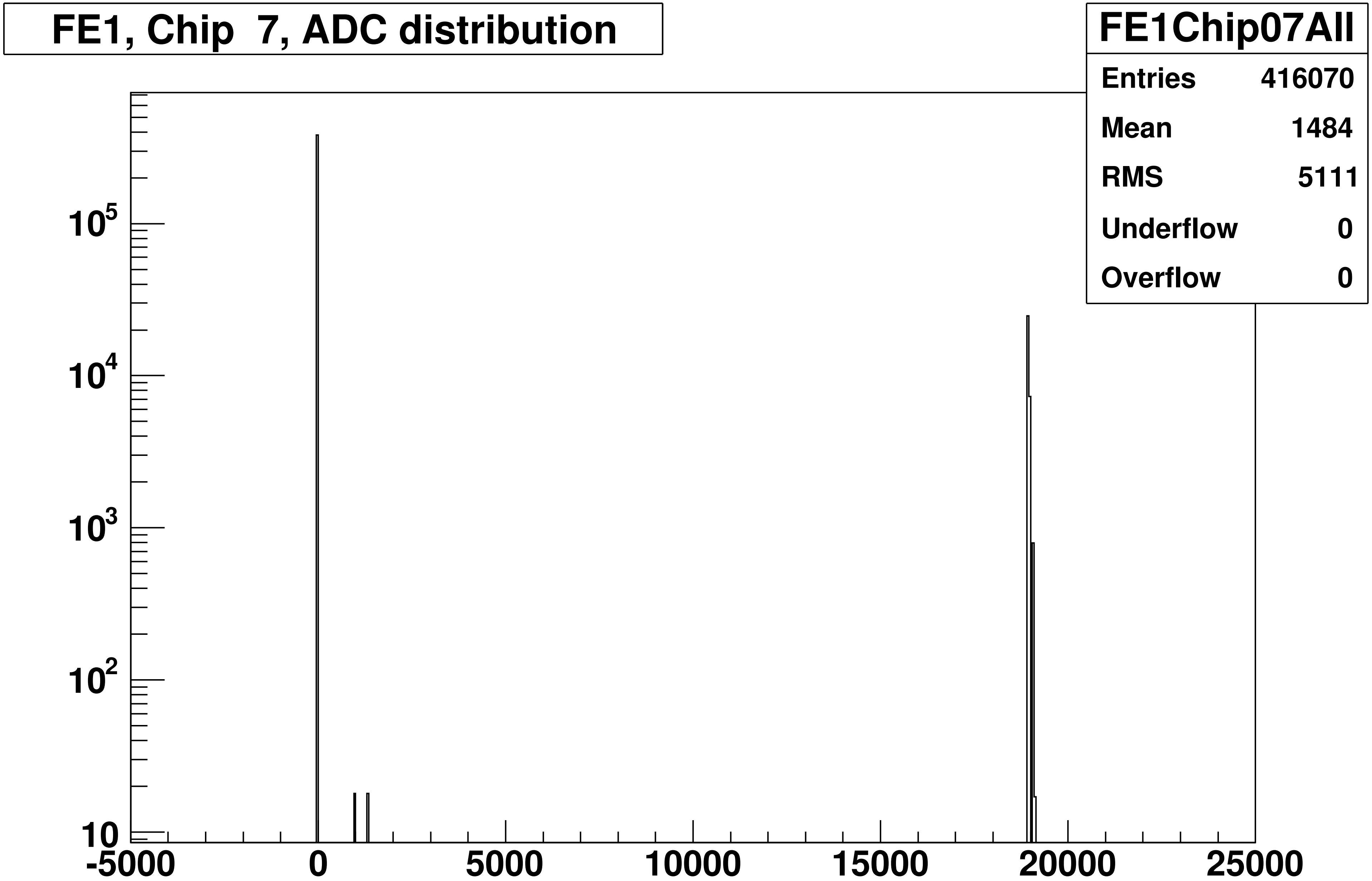


FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 416070
Mean 1.908e+04
RMS 9.366
Underflow 3.84e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

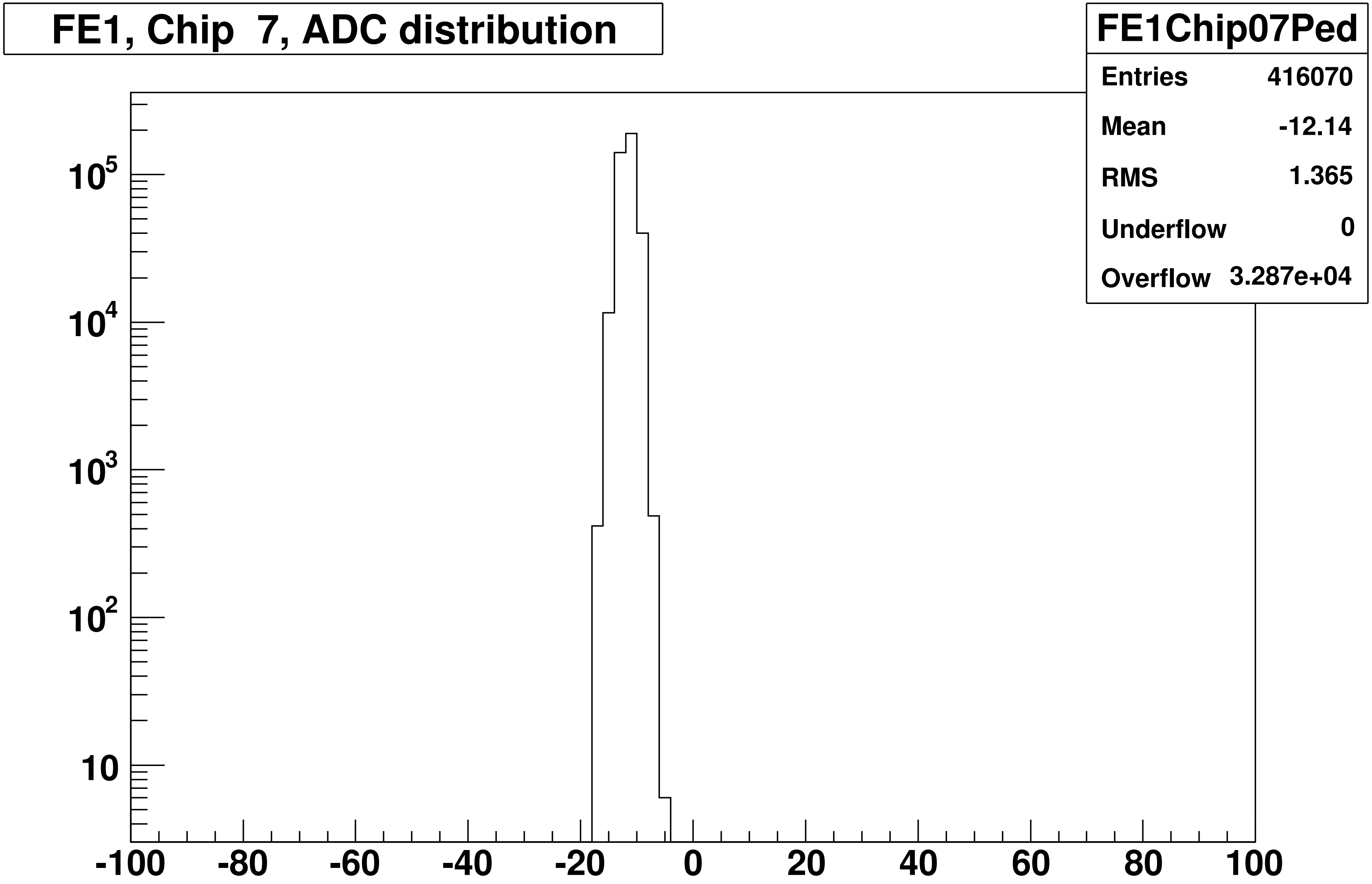

FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 416070
Mean 1505
RMS 5114
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 416070
Mean 8.371
RMS 1.352
Underflow 0
Overflow 3.285e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

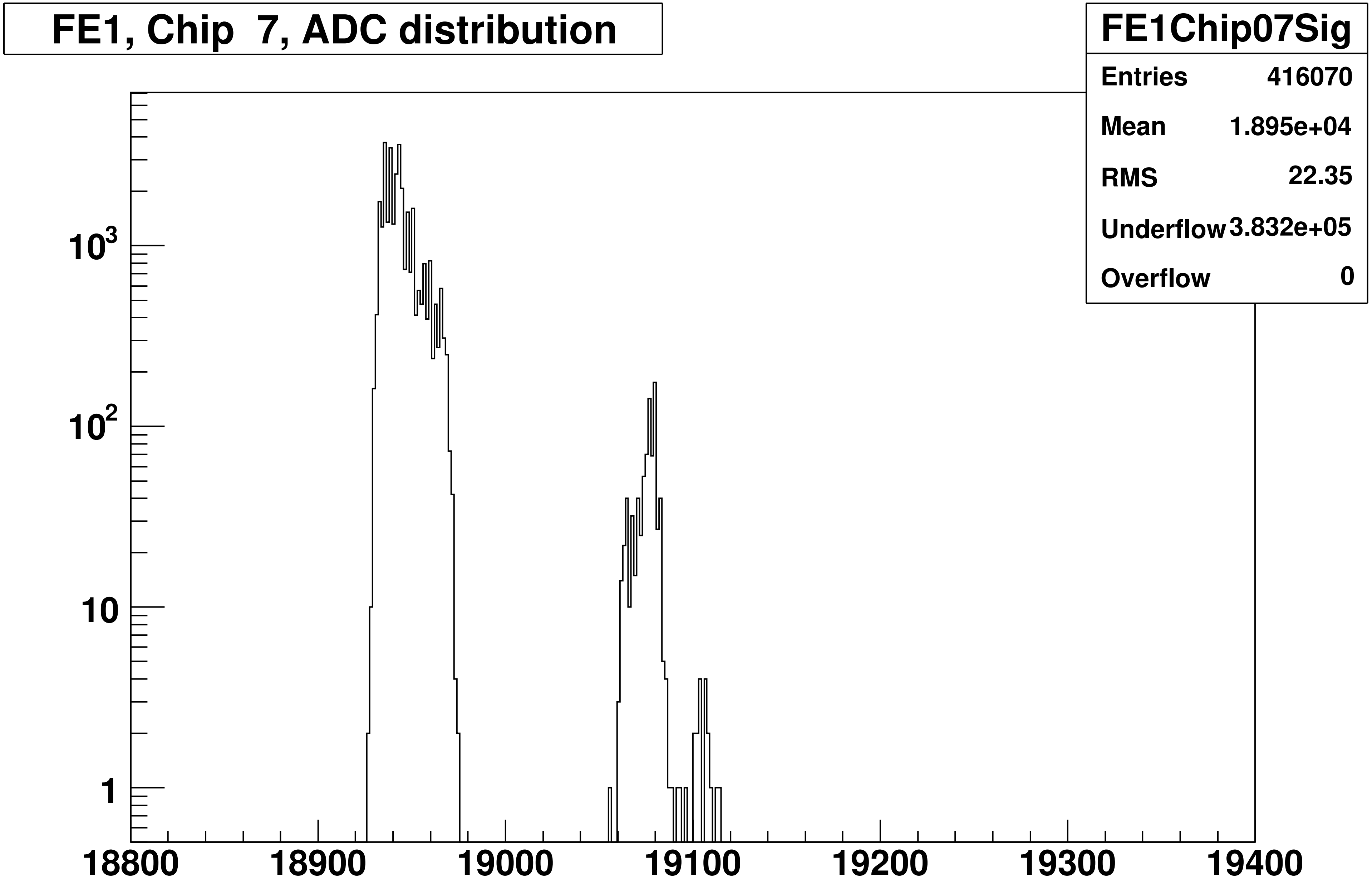

FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 416070
Mean 1.898e+04
RMS 19.5
Underflow 3.833e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



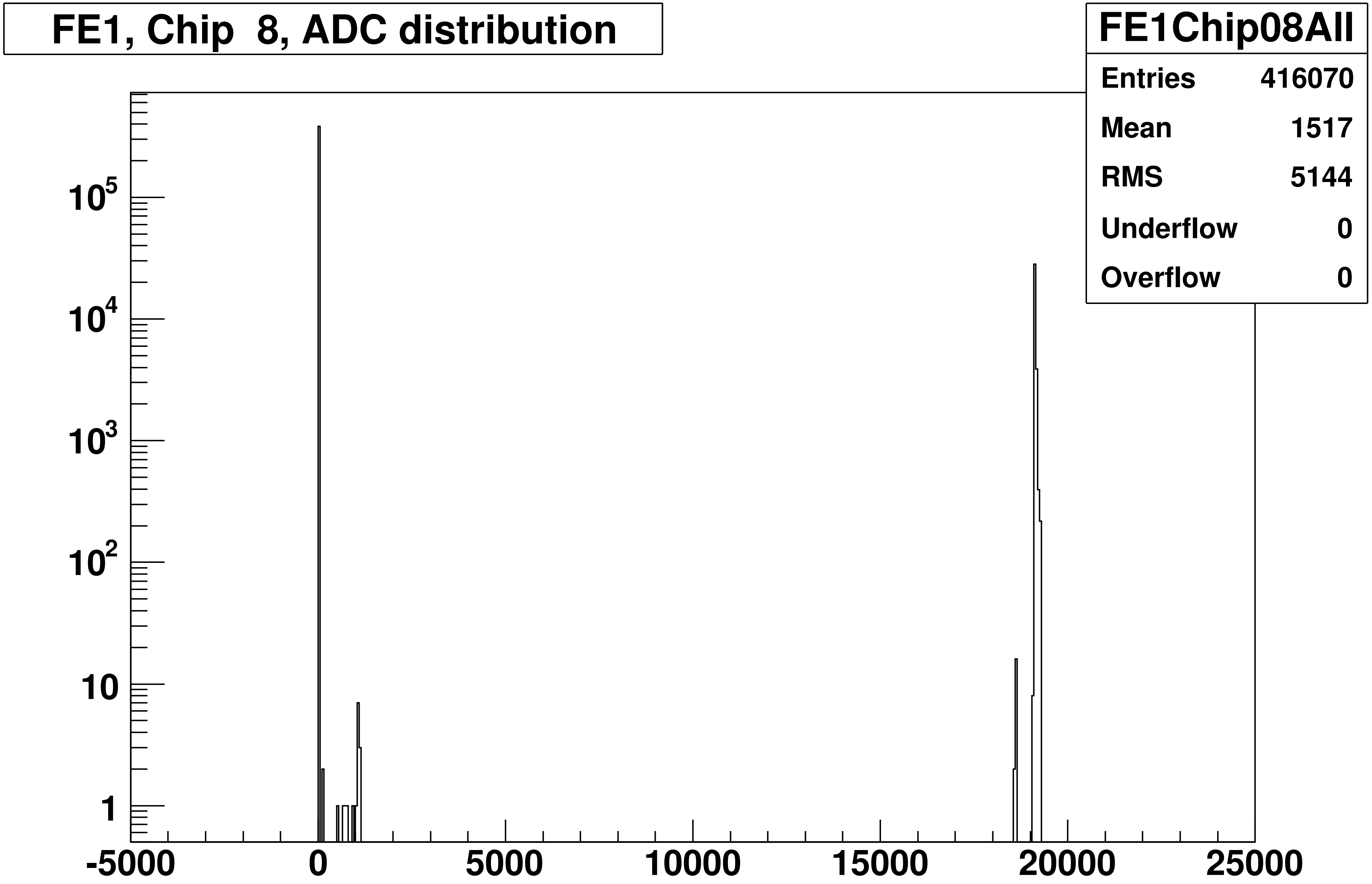
FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 416070
Mean 1484
RMS 5111
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



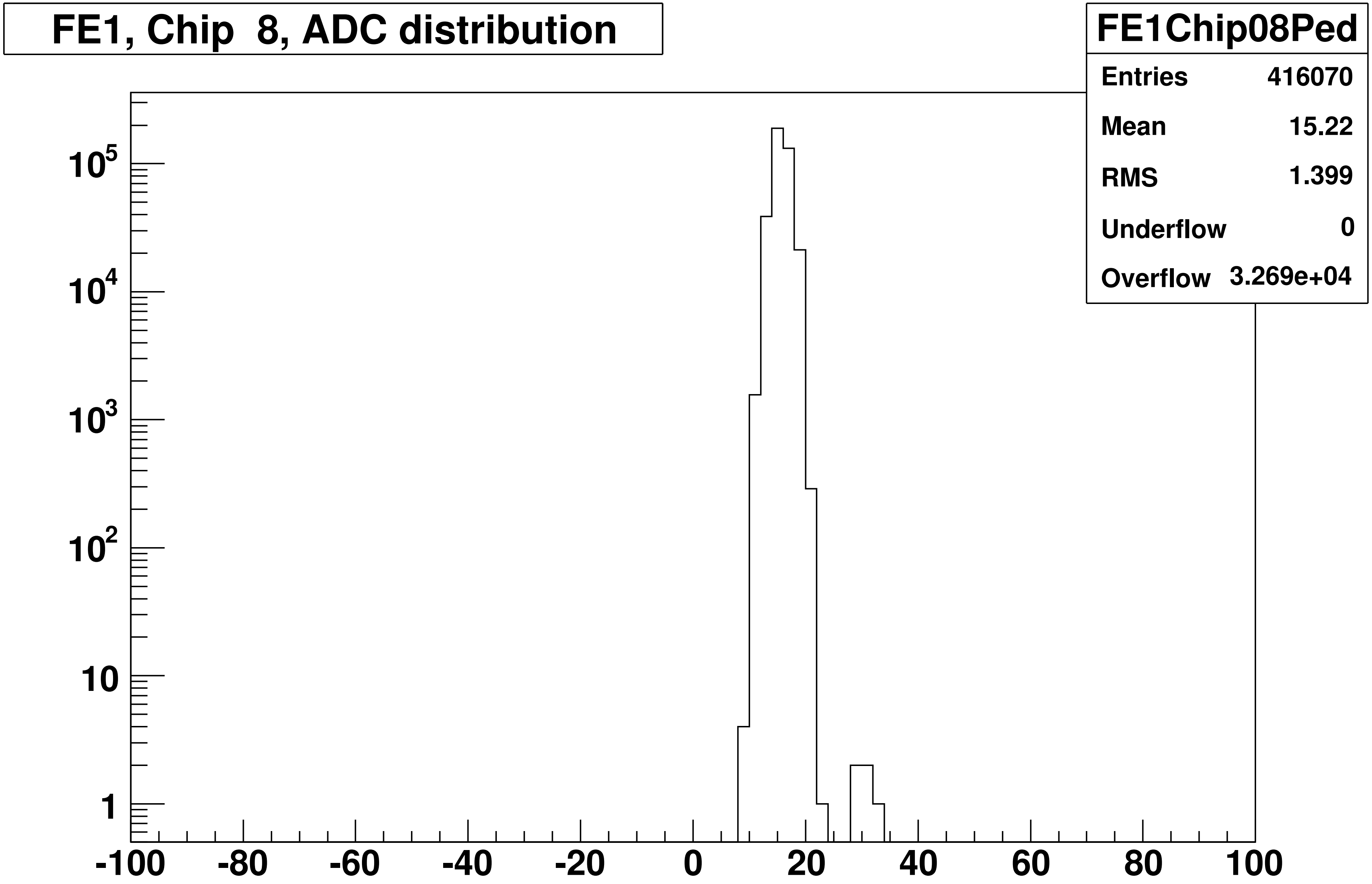
FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 416070
Mean -12.14
RMS 1.365
Underflow 0
Overflow 3.287e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



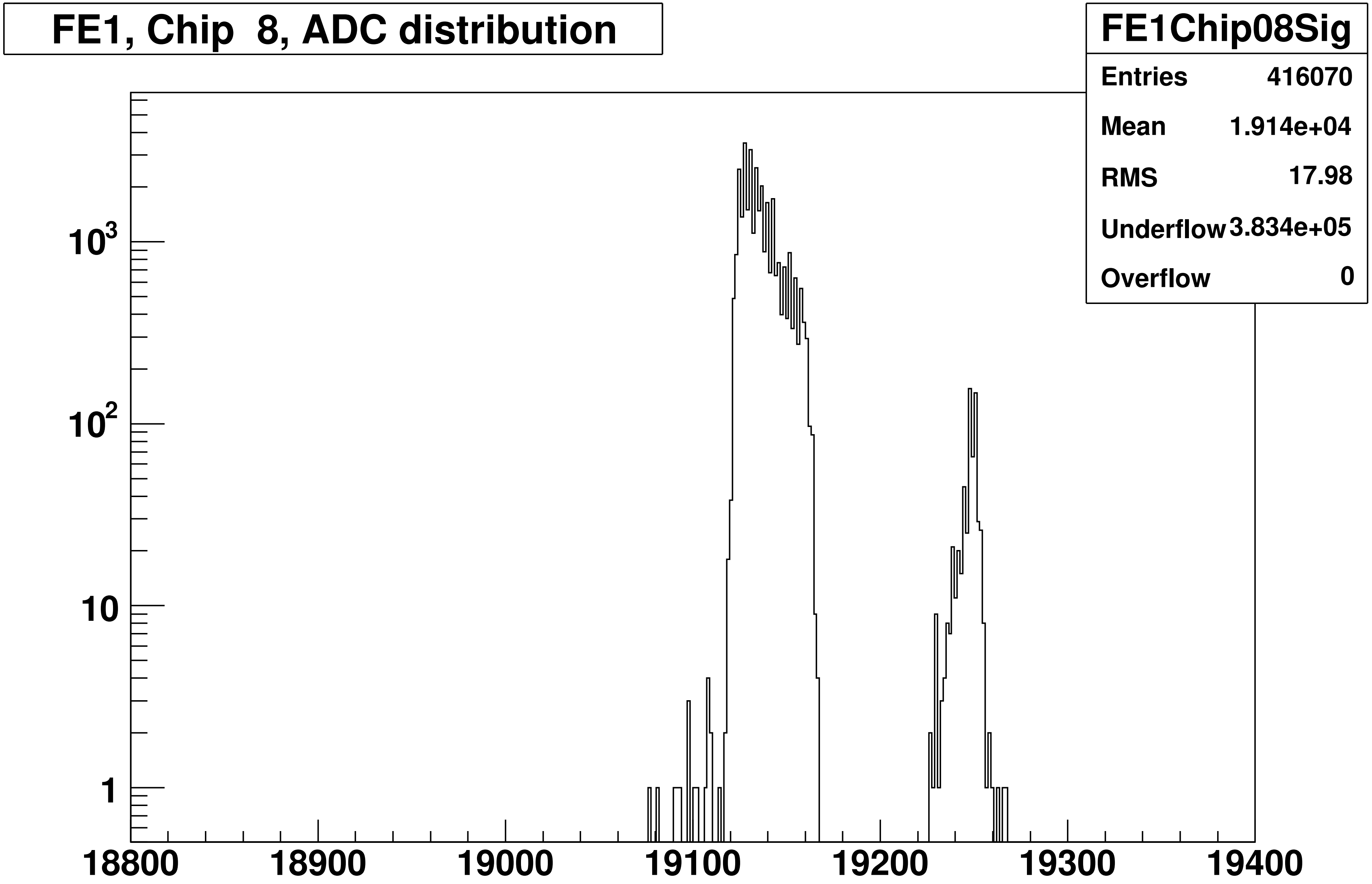
FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 416070
Mean 1.895e+04
RMS 22.35
Underflow 3.832e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

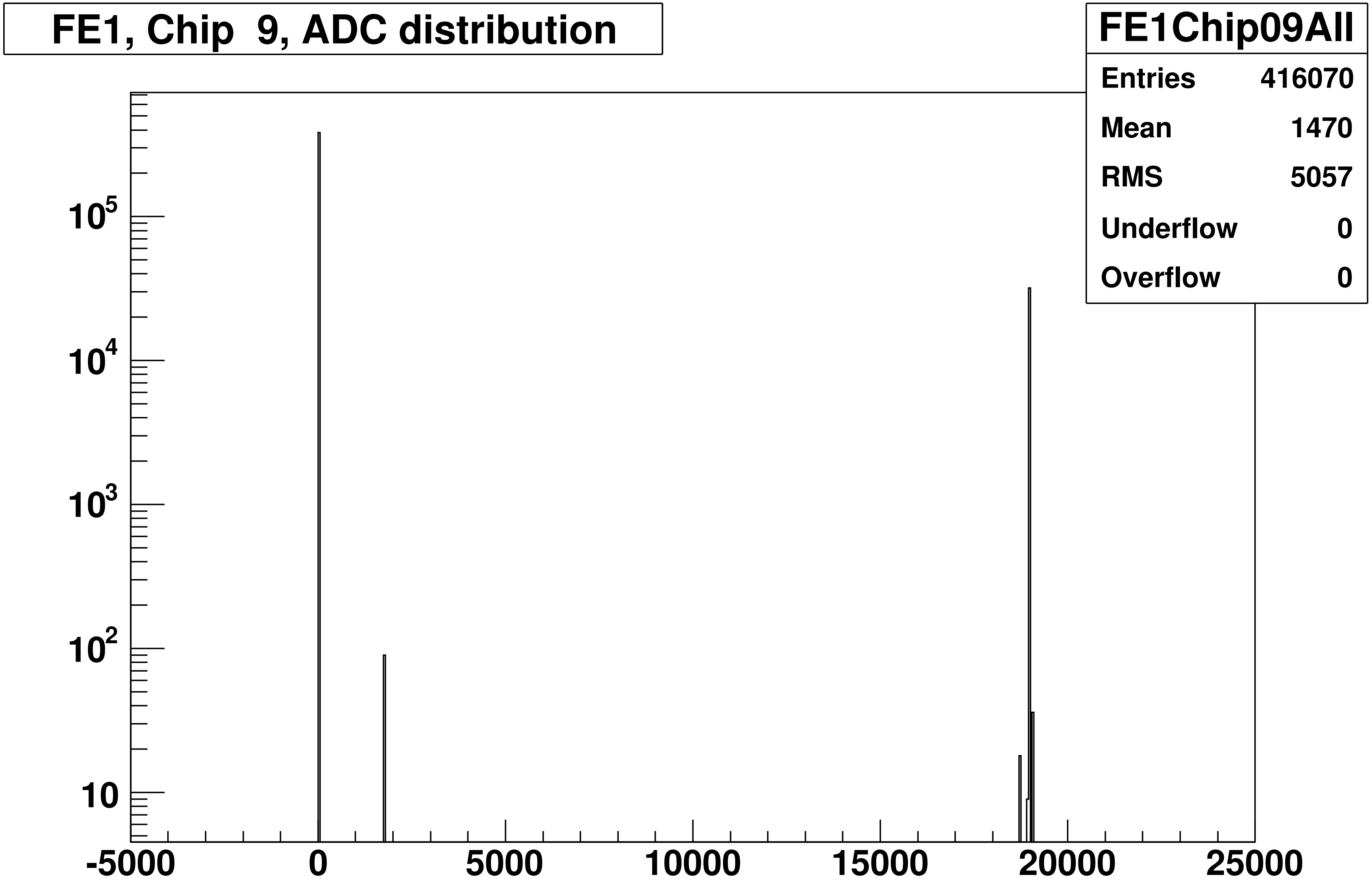


FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 416070
Mean 1517
RMS 5144
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

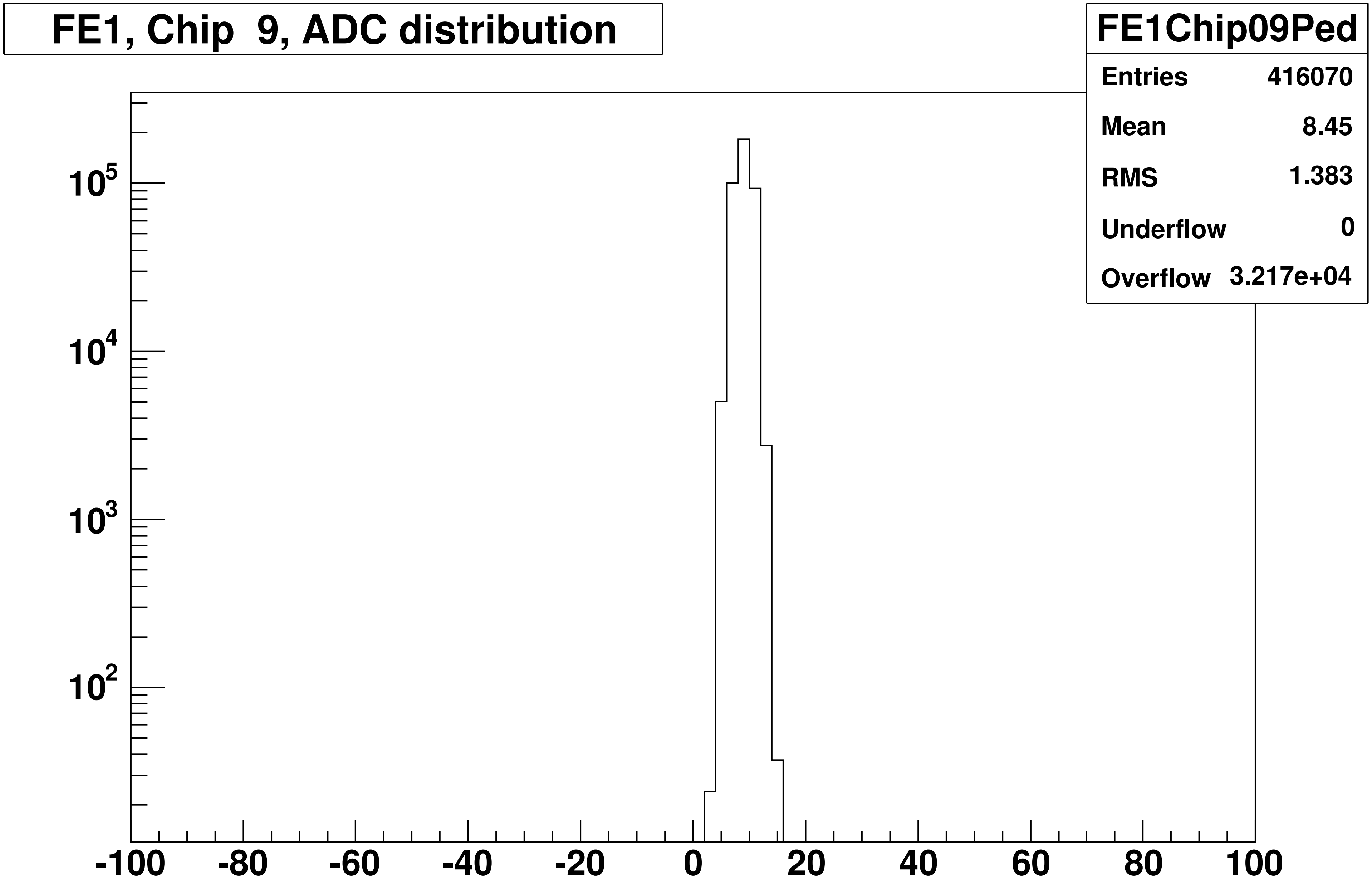


FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 416070
Mean 15.22
RMS 1.399
Underflow 0
Overflow 3.269e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

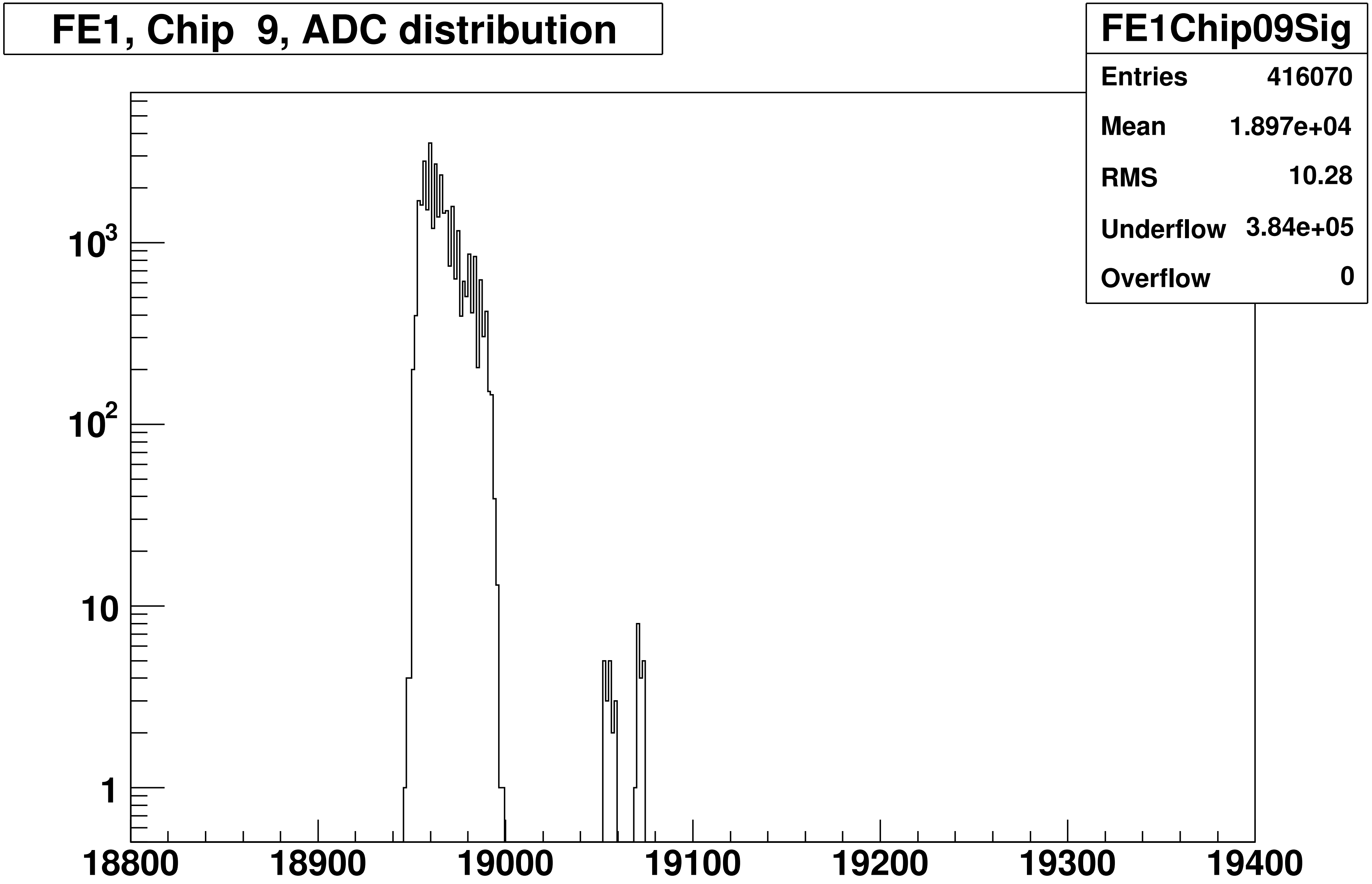

FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 416070
Mean 1.914e+04
RMS 17.98
Underflow 3.834e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



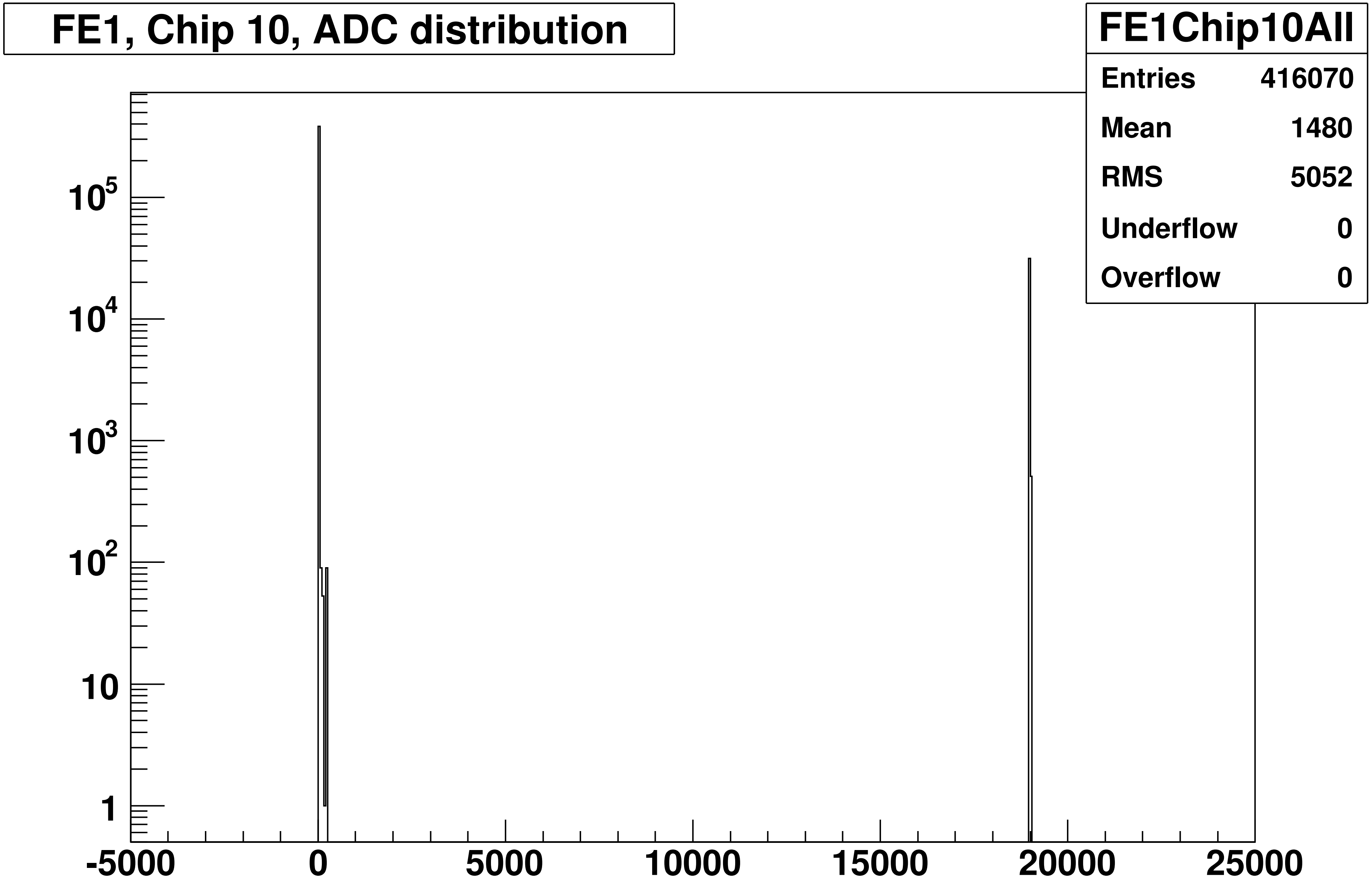
FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 416070
Mean 1470
RMS 5057
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



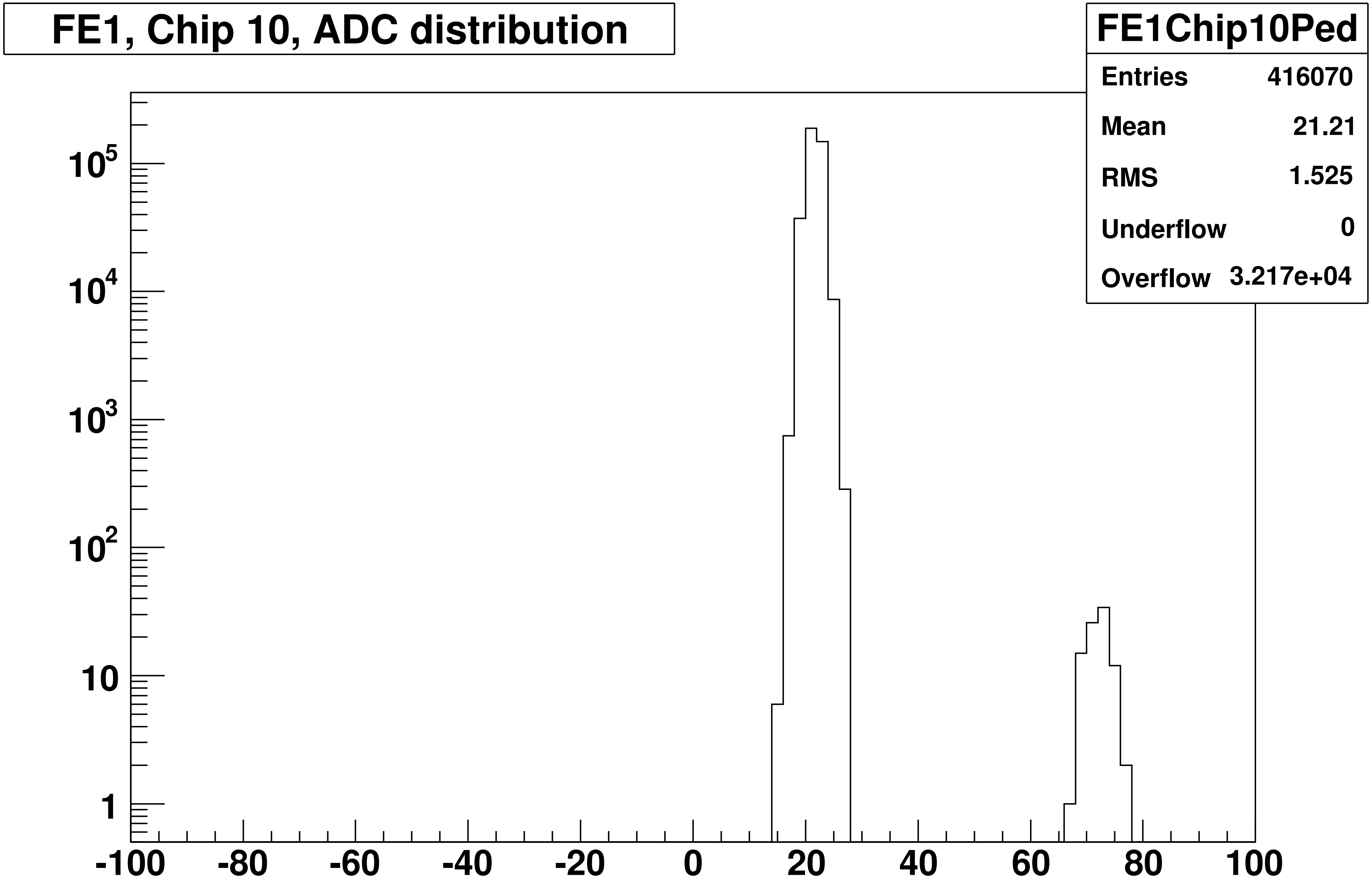
FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 416070
Mean 8.45
RMS 1.383
Underflow 0
Overflow 3.217e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

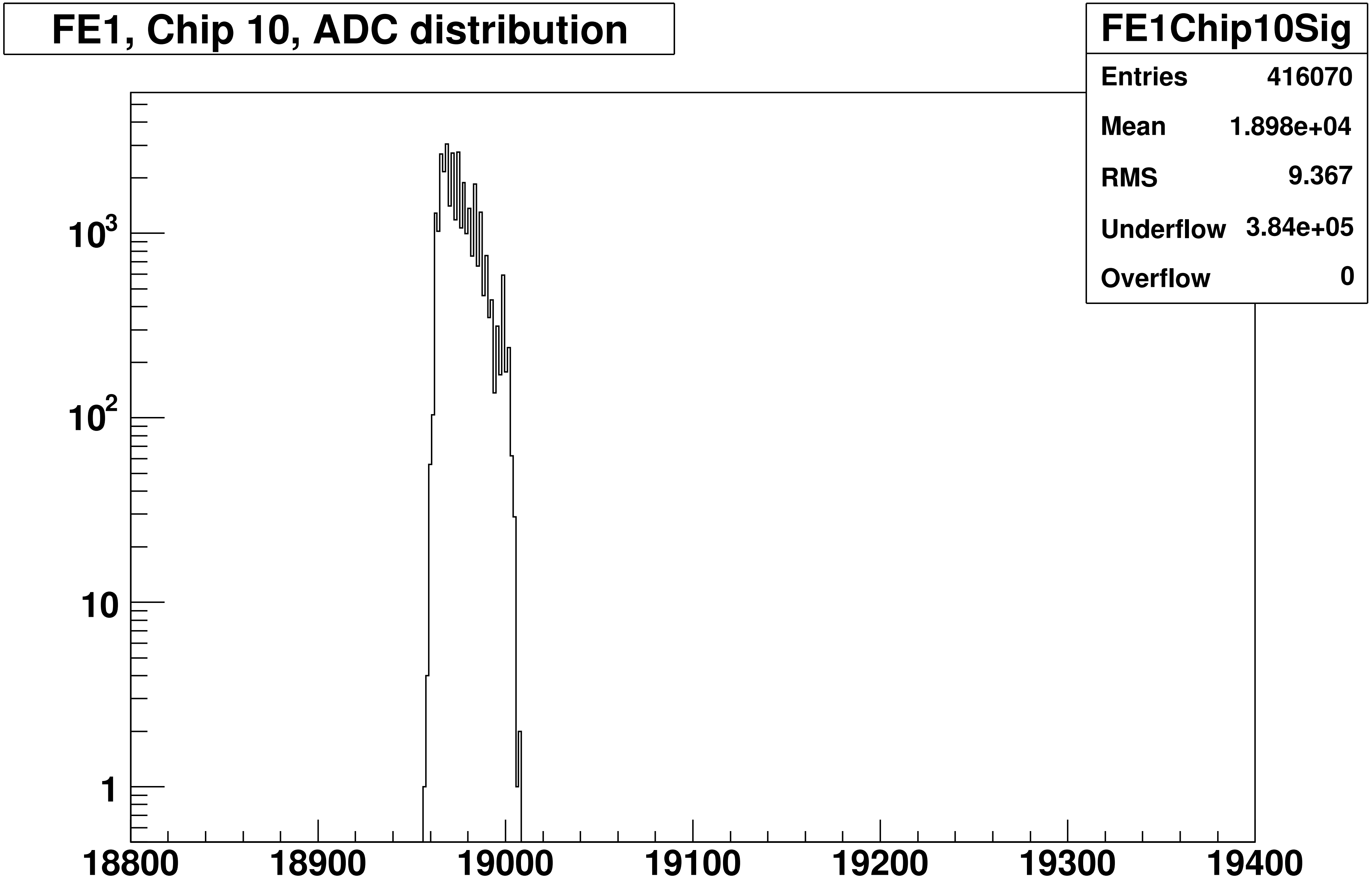


FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 416070
Mean 1.897e+04
RMS 10.28
Underflow 3.84e+05
Overflow 0
10³
10²
10
1
18800
18900
19000
19100
19200
19300
19400

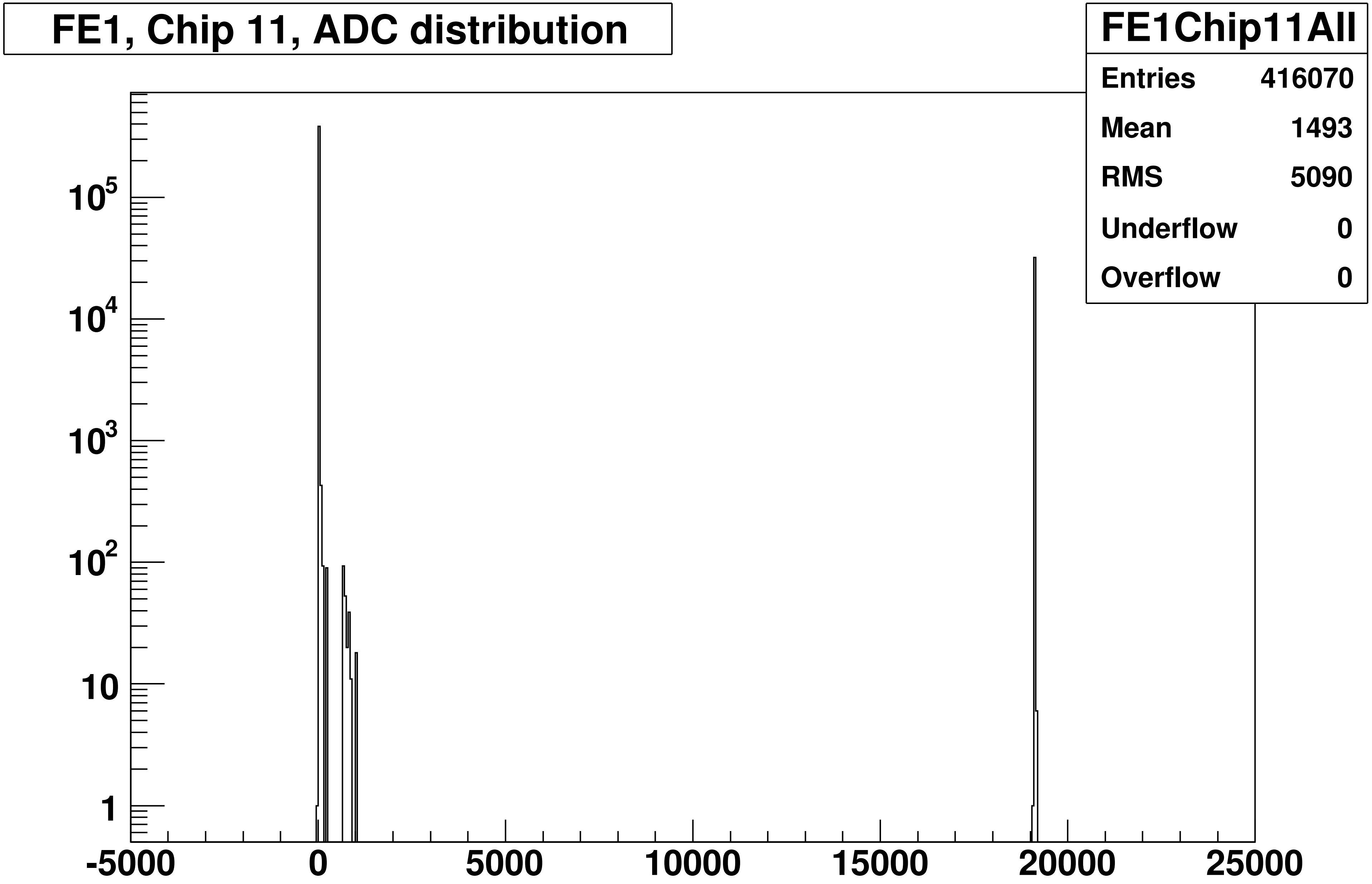


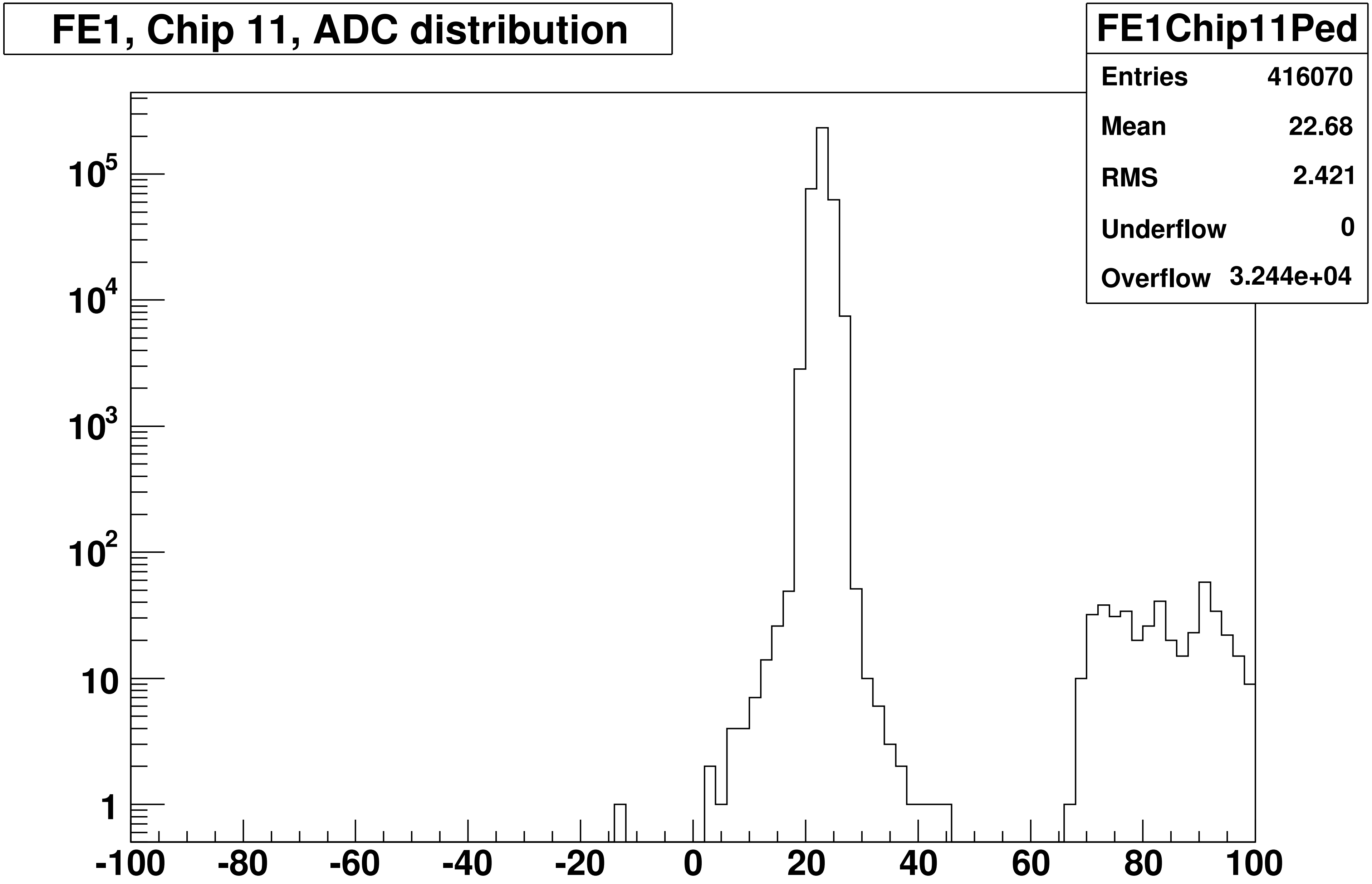
FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 416070
Mean 1480
RMS 5052
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

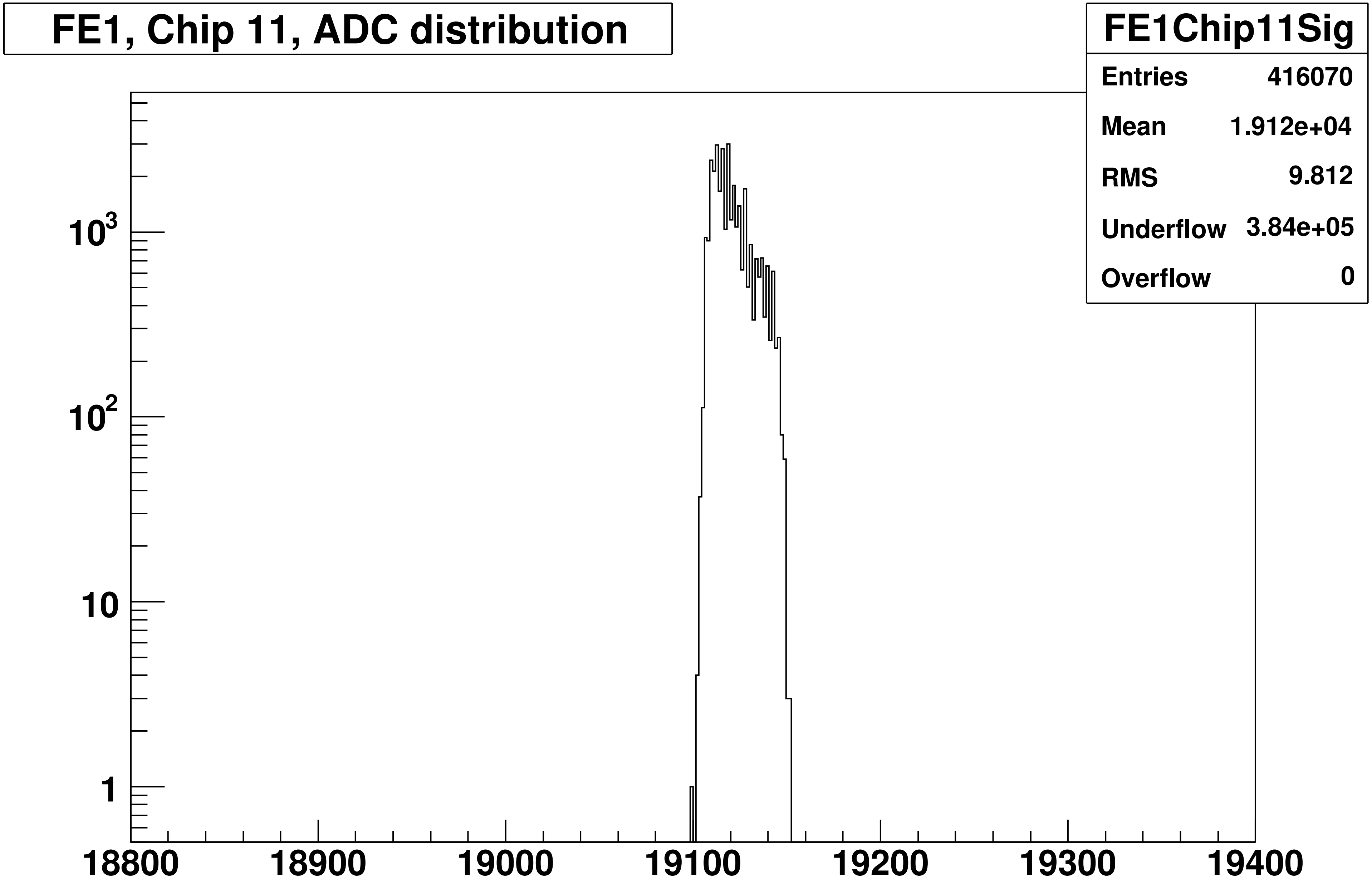

FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 416070
Mean 21.21
RMS 1.525
Underflow 0
Overflow 3.217e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

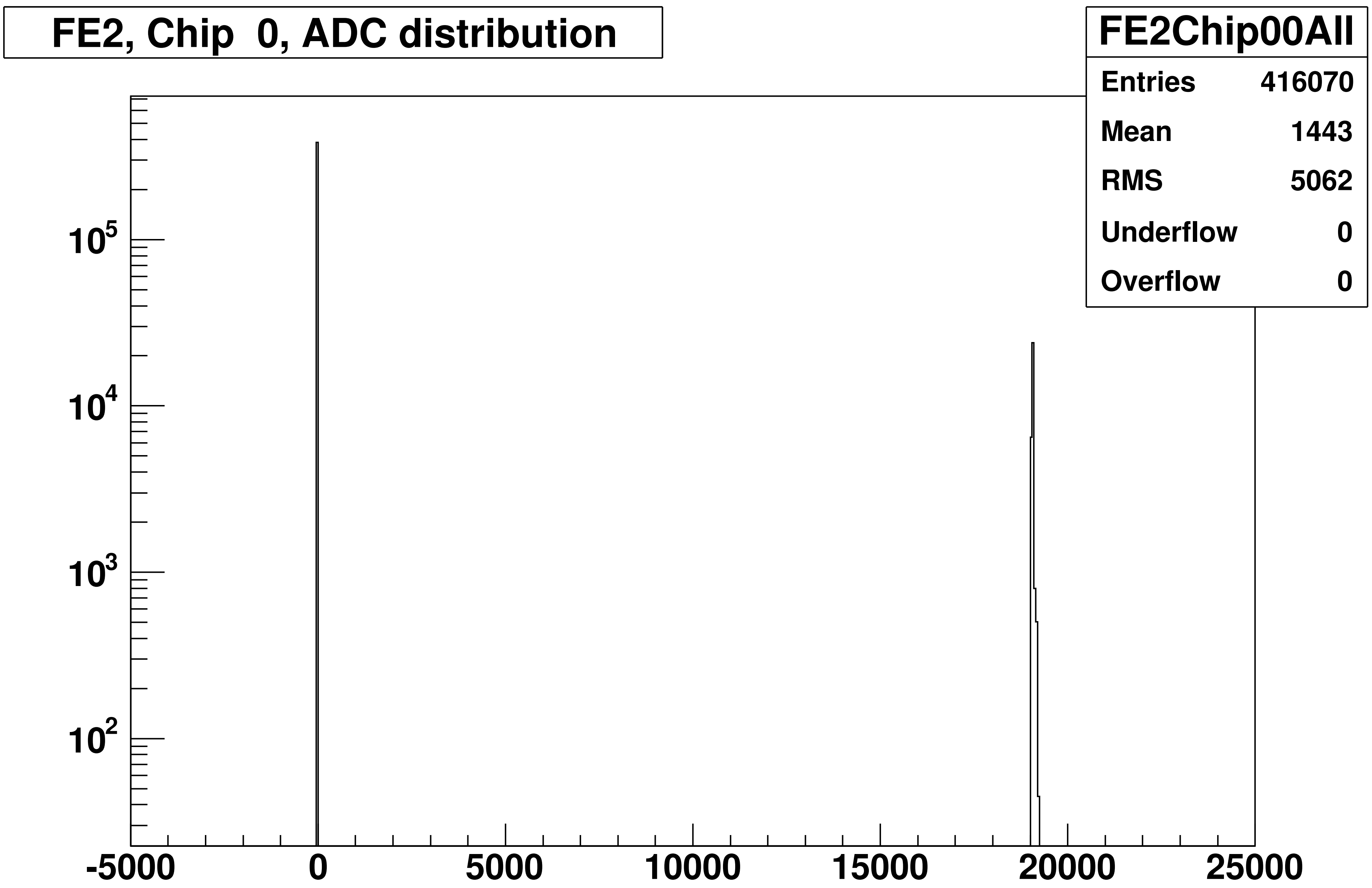


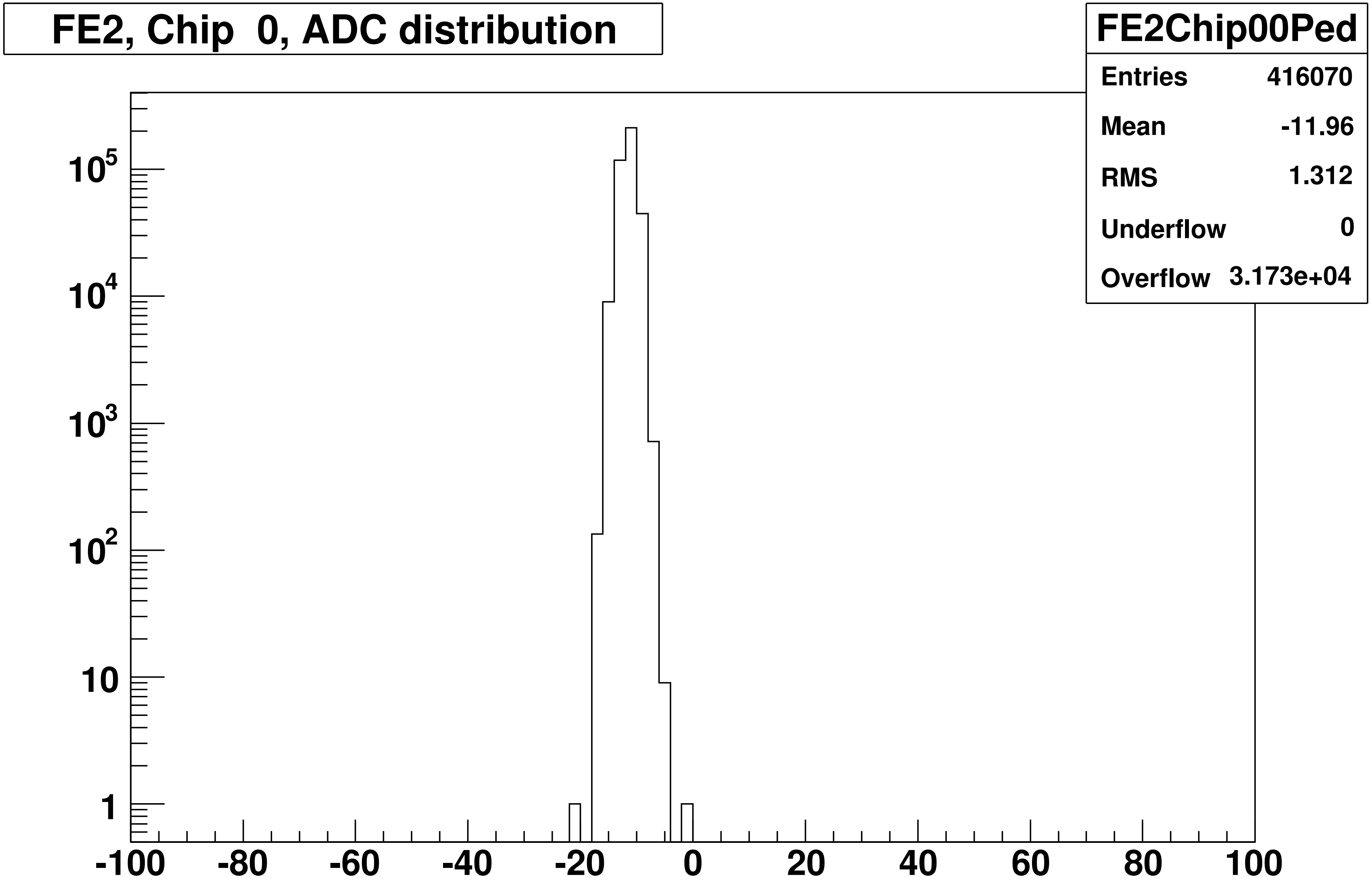
FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 416070
Mean 1.898e+04
RMS 9.367
Underflow 3.84e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

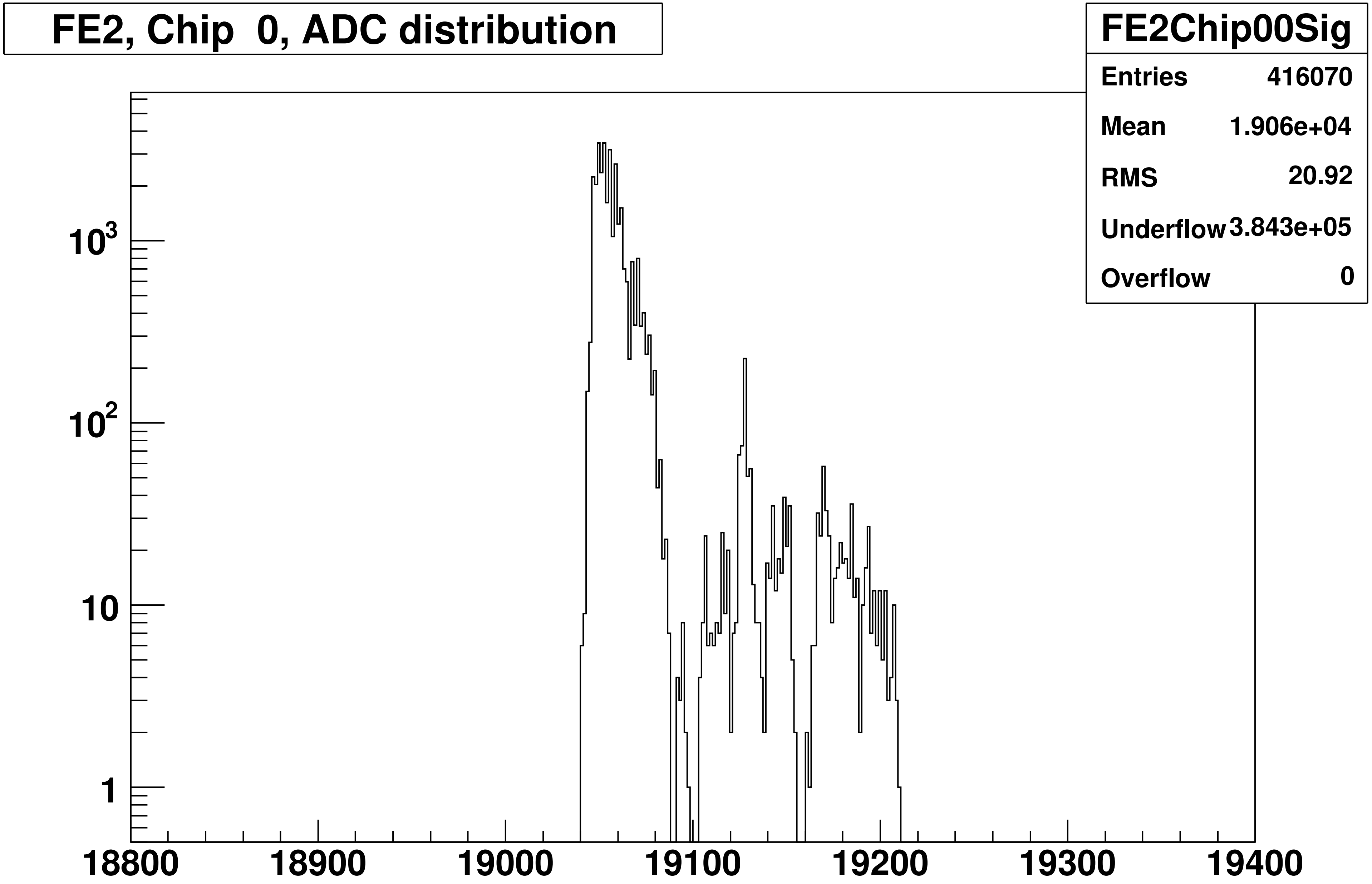

FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 416070
Mean 1493
RMS 5090
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

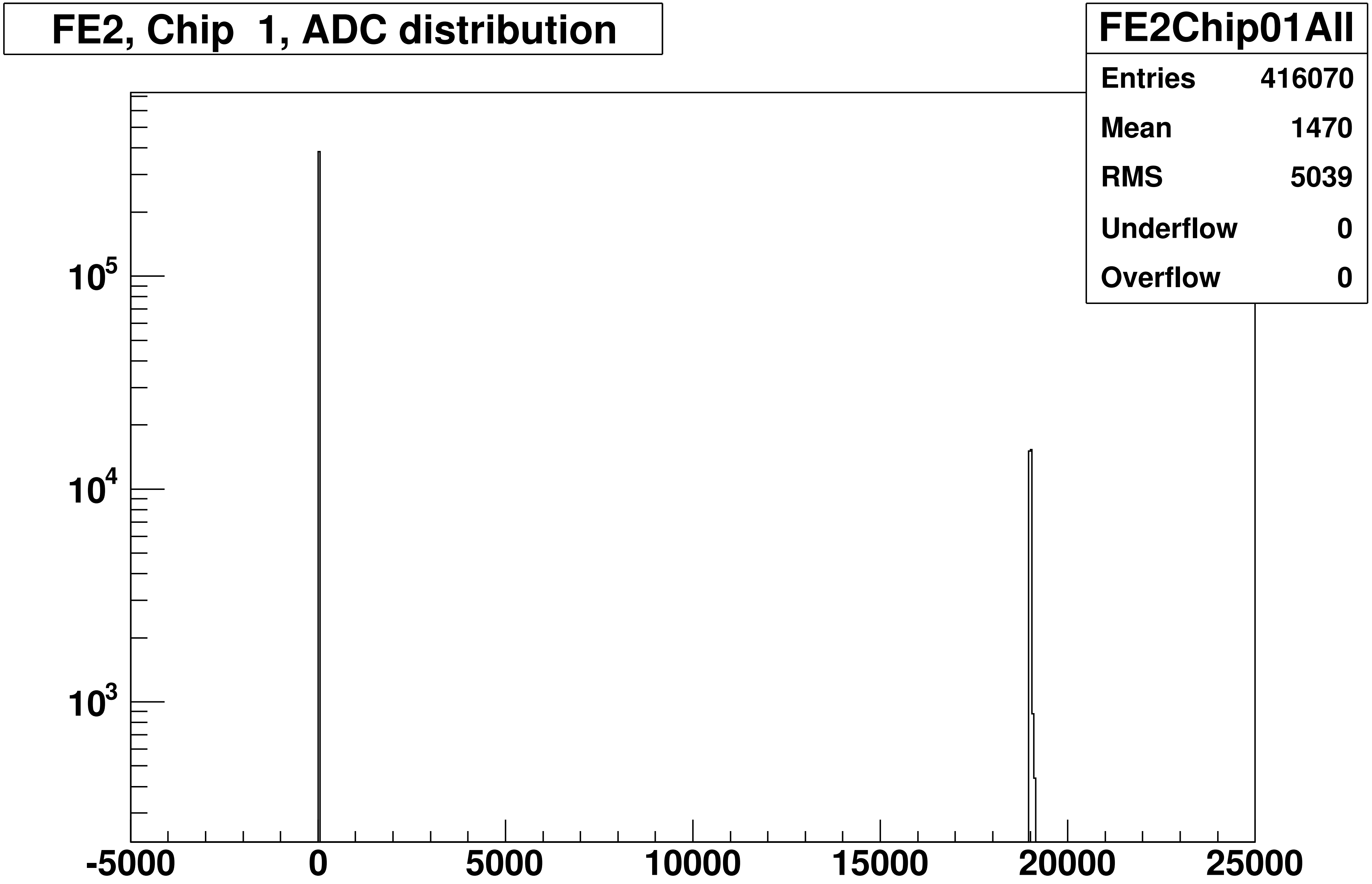

FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 416070
Mean 22.68
RMS 2.421
Underflow 0
Overflow 3.244e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 416070
Mean 1.912e+04
RMS 9.812
Underflow 3.84e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

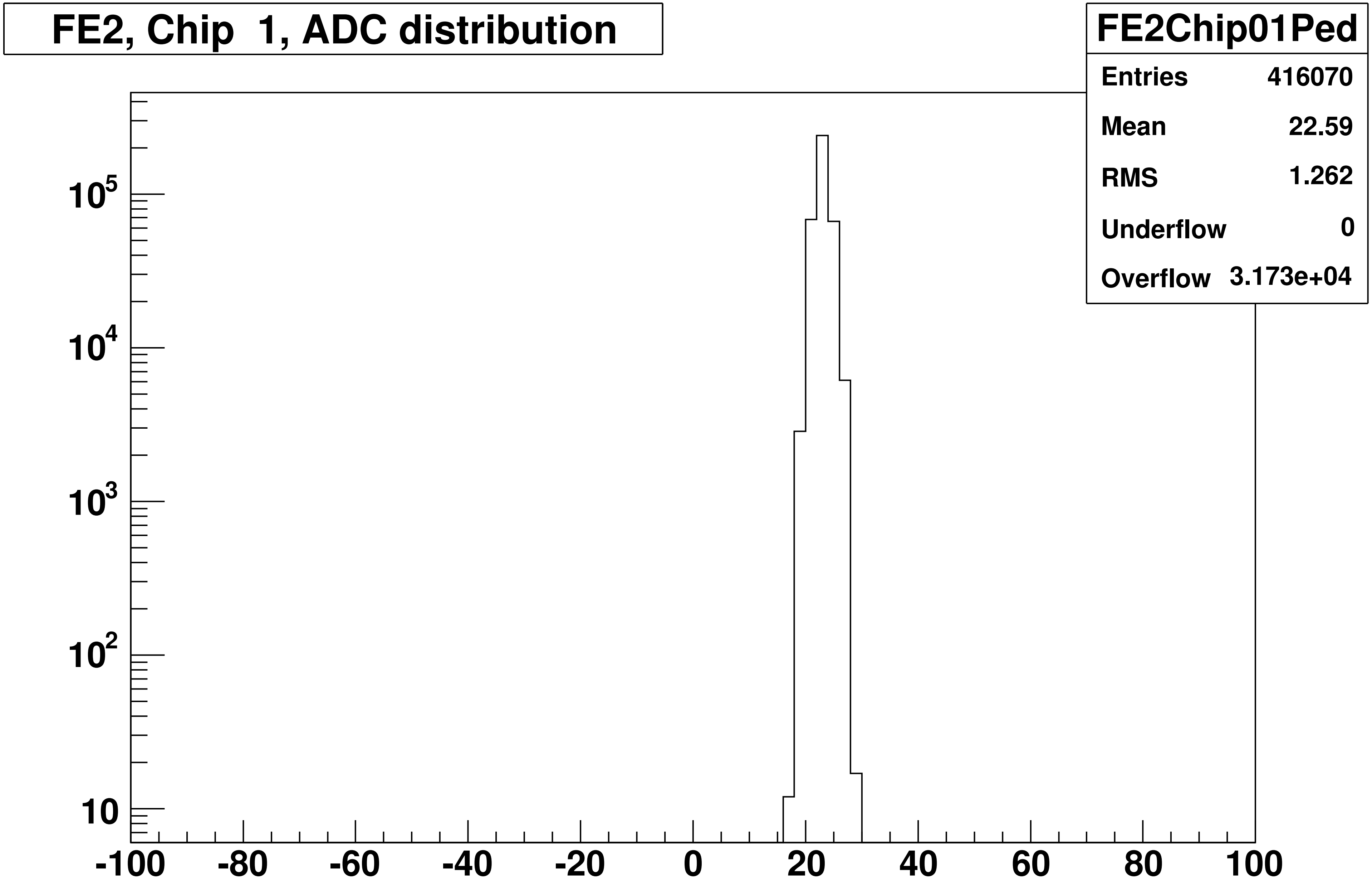

FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 416070
Mean 1443
RMS 5062
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

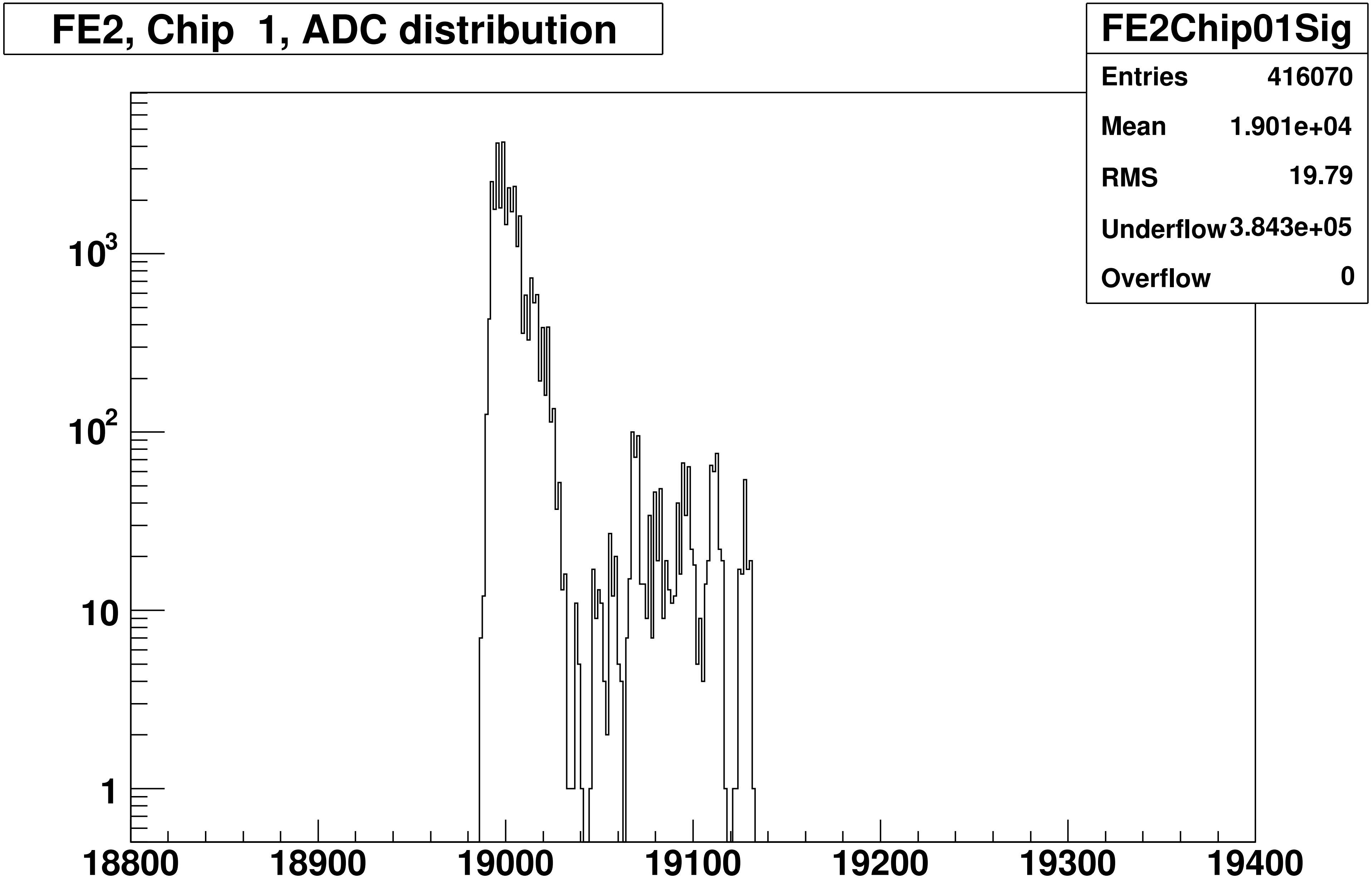

FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 416070
Mean -11.96
RMS 1.312
Underflow 0
Overflow 3.173e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

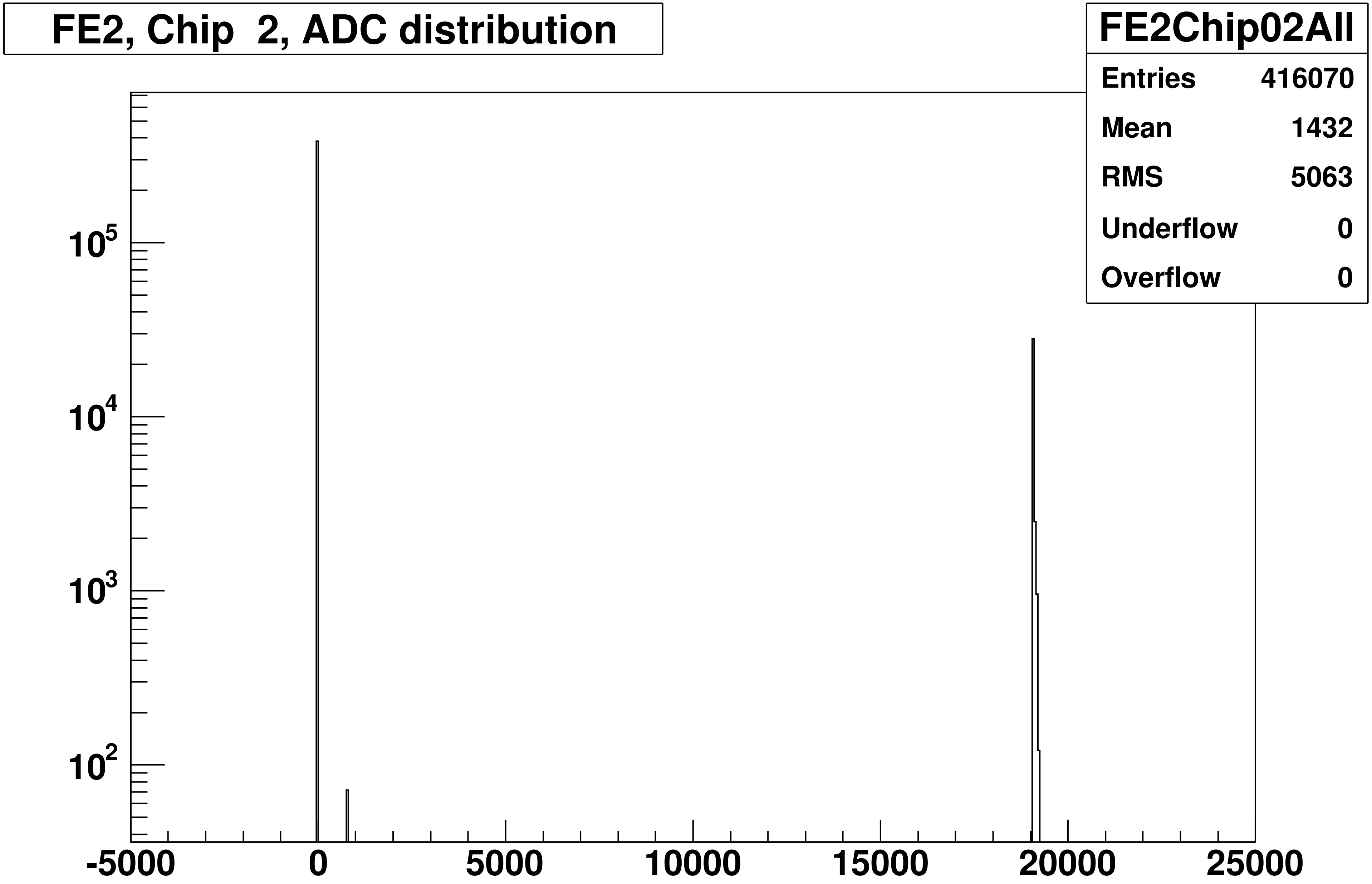

FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 416070
Mean 1.906e+04
RMS 20.92
Underflow 3.843e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

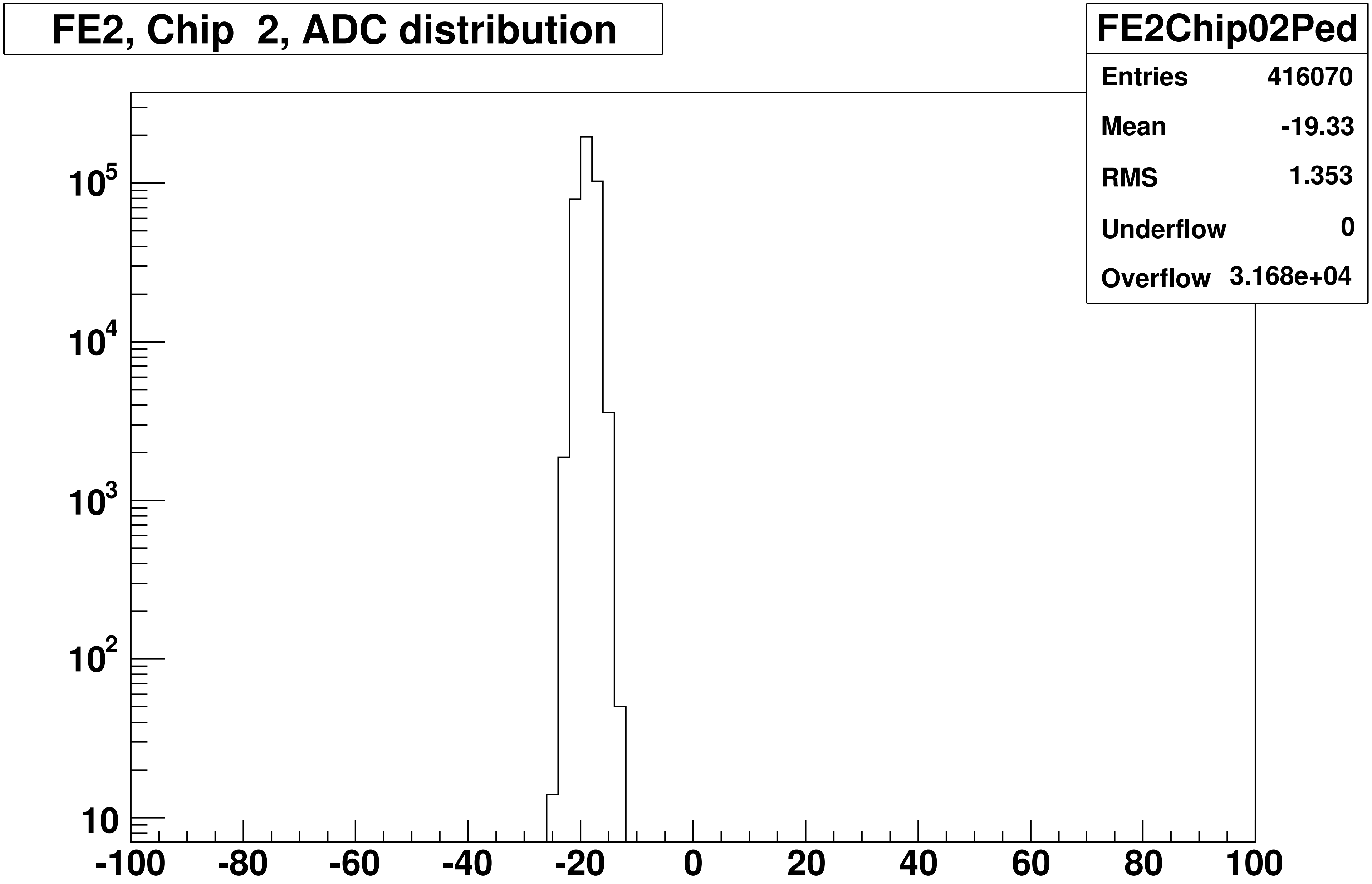


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 416070
Mean 1470
RMS 5039
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

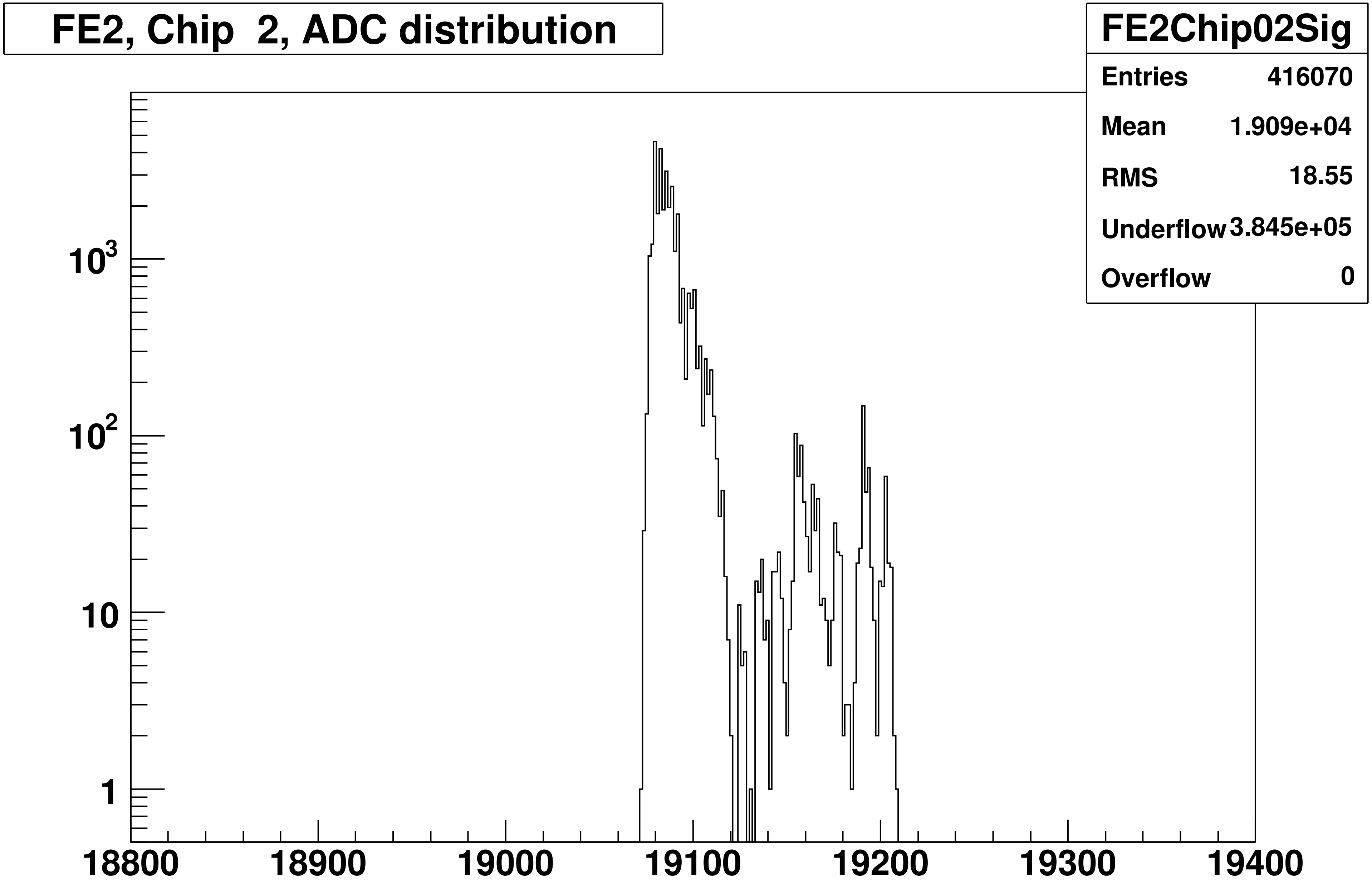

FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 416070
Mean 22.59
RMS 1.262
Underflow 0
Overflow 3.173e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

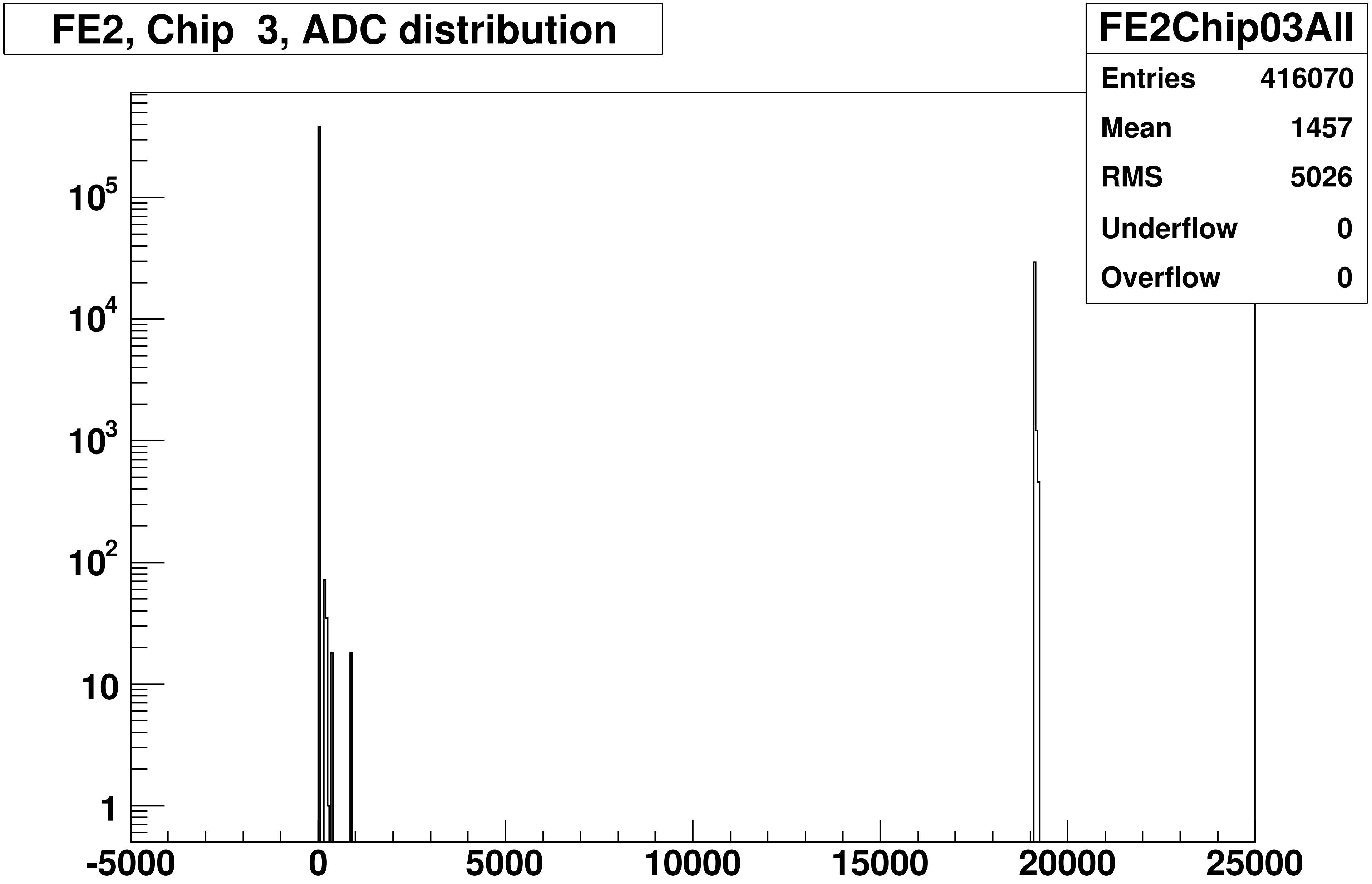

FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 416070
Mean 1.901e+04
RMS 19.79
Underflow 3.843e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 416070
Mean 1432
RMS 5063
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 416070
Mean -19.33
RMS 1.353
Underflow 0
Overflow 3.168e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 416070
Mean 1.909e+04
RMS 18.55
Underflow 3.845e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 416070
Mean 1457
RMS 5026
Underflow 0
Overflow 0
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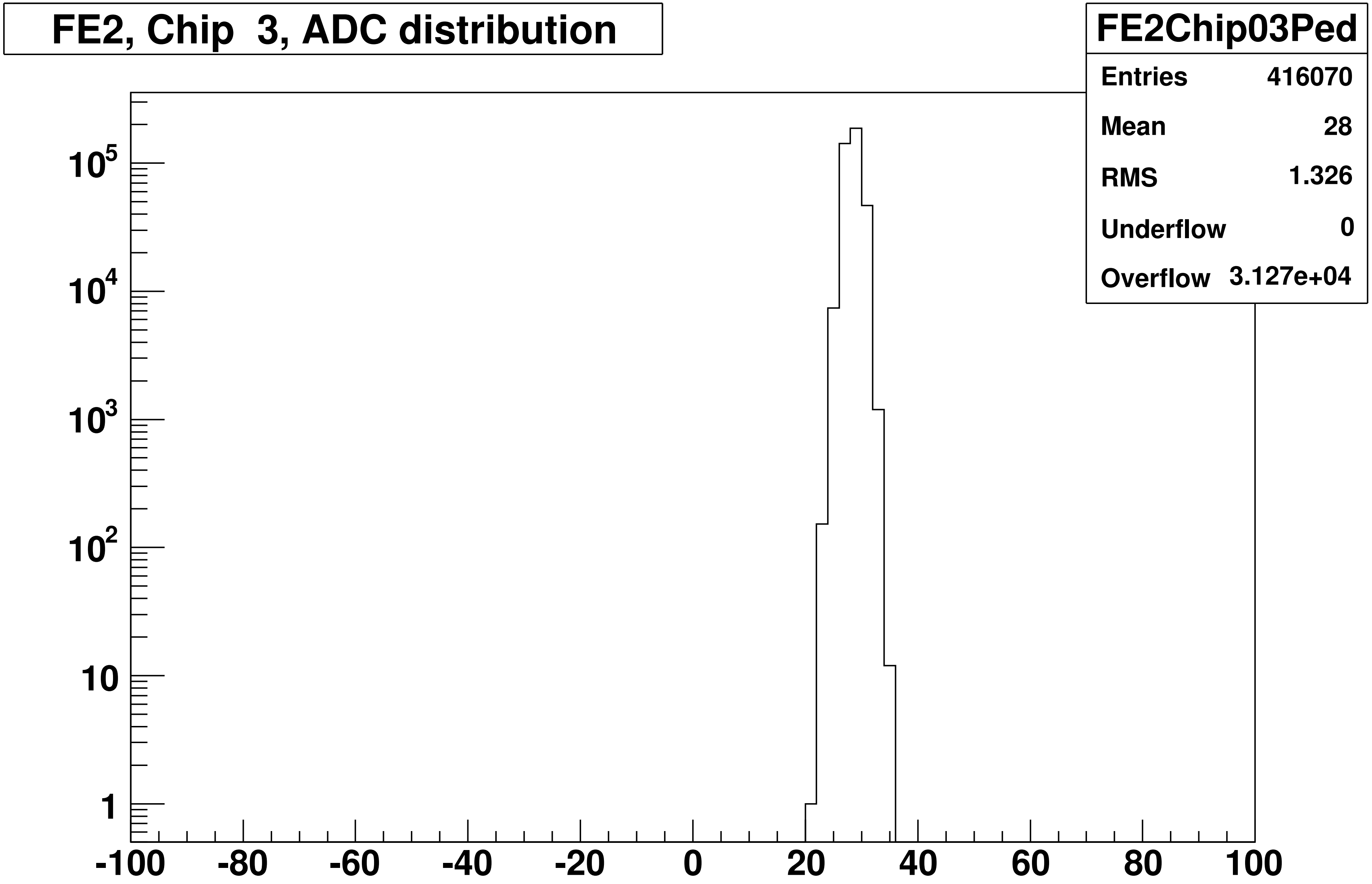
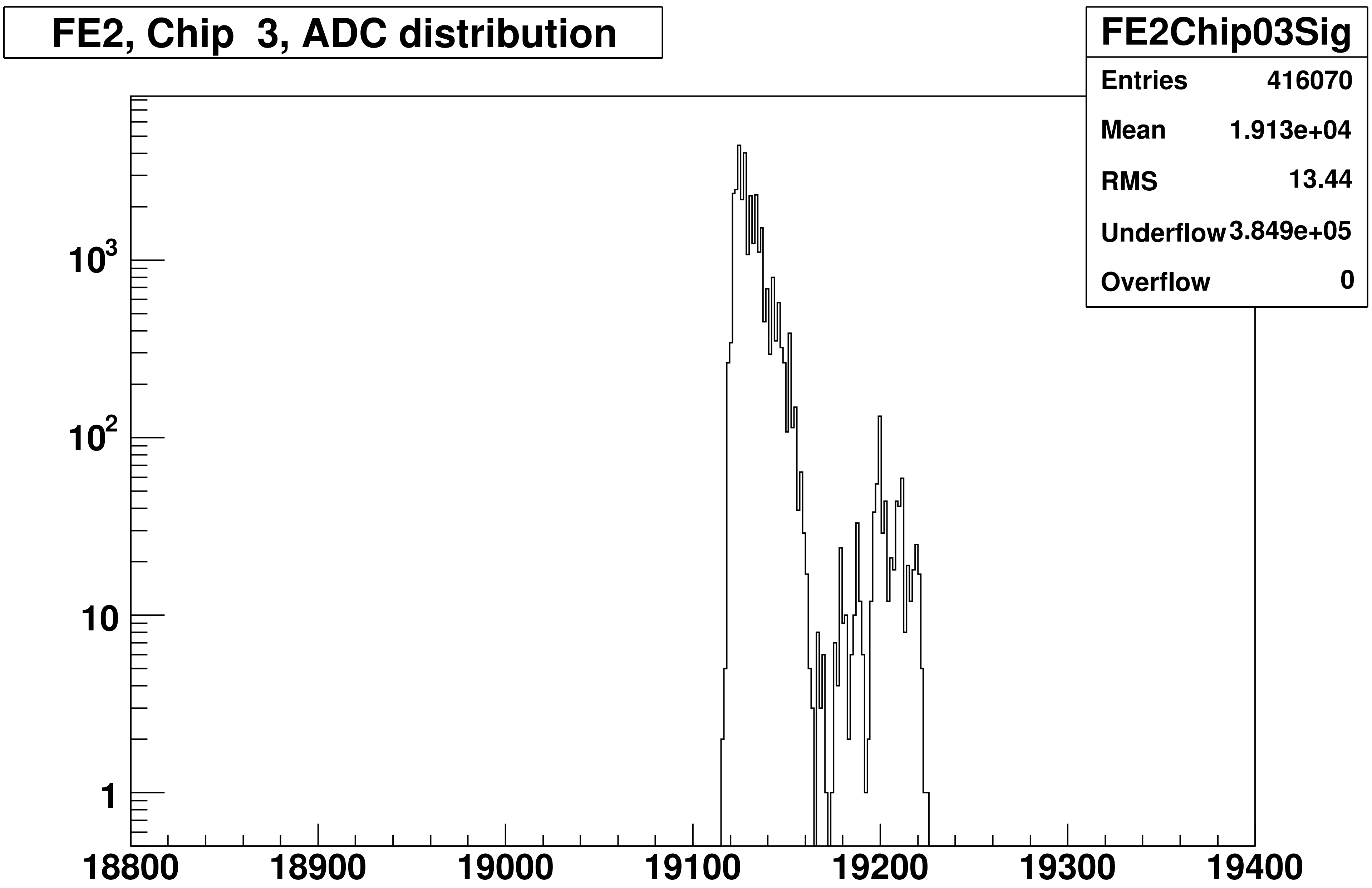
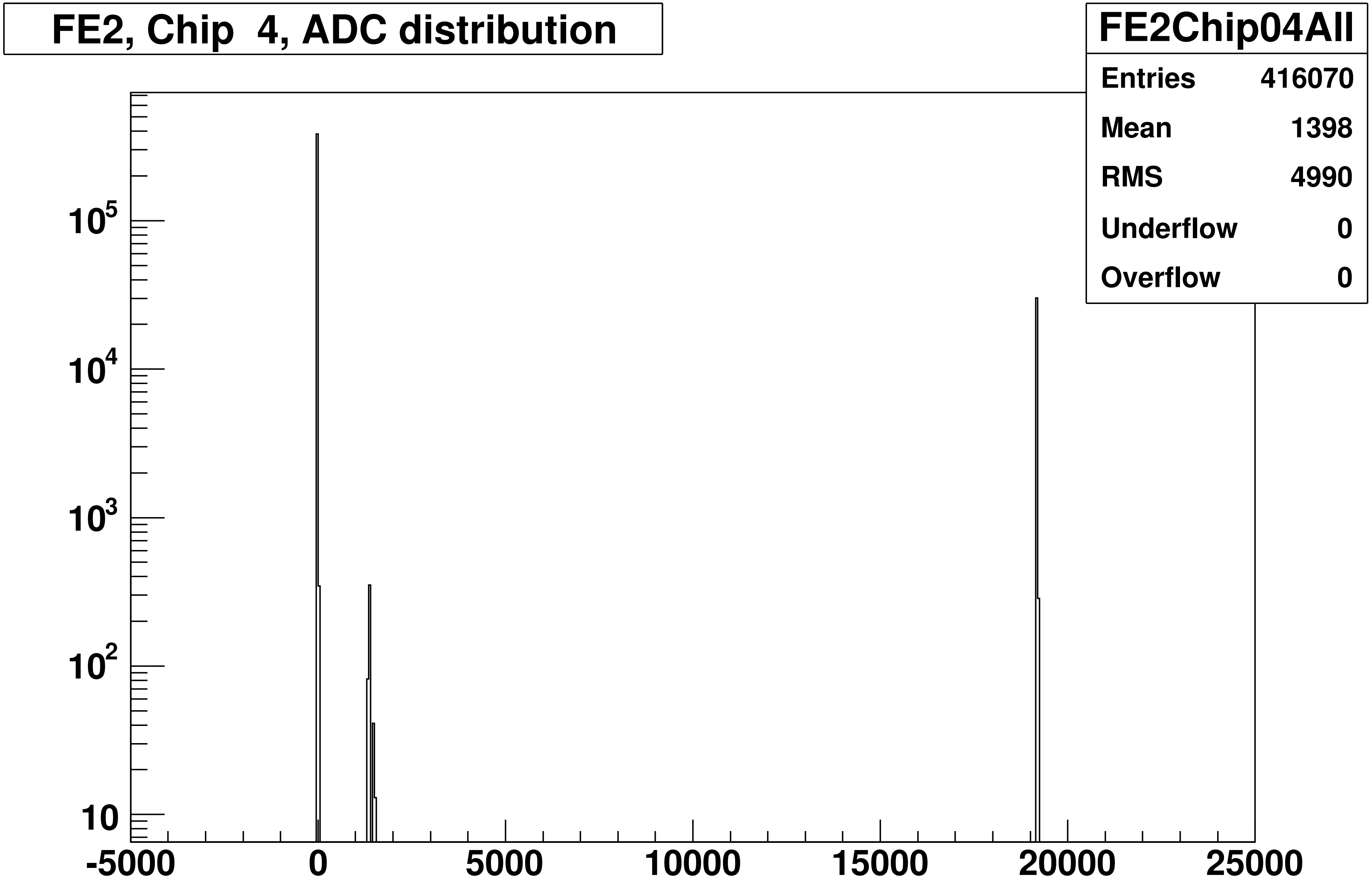
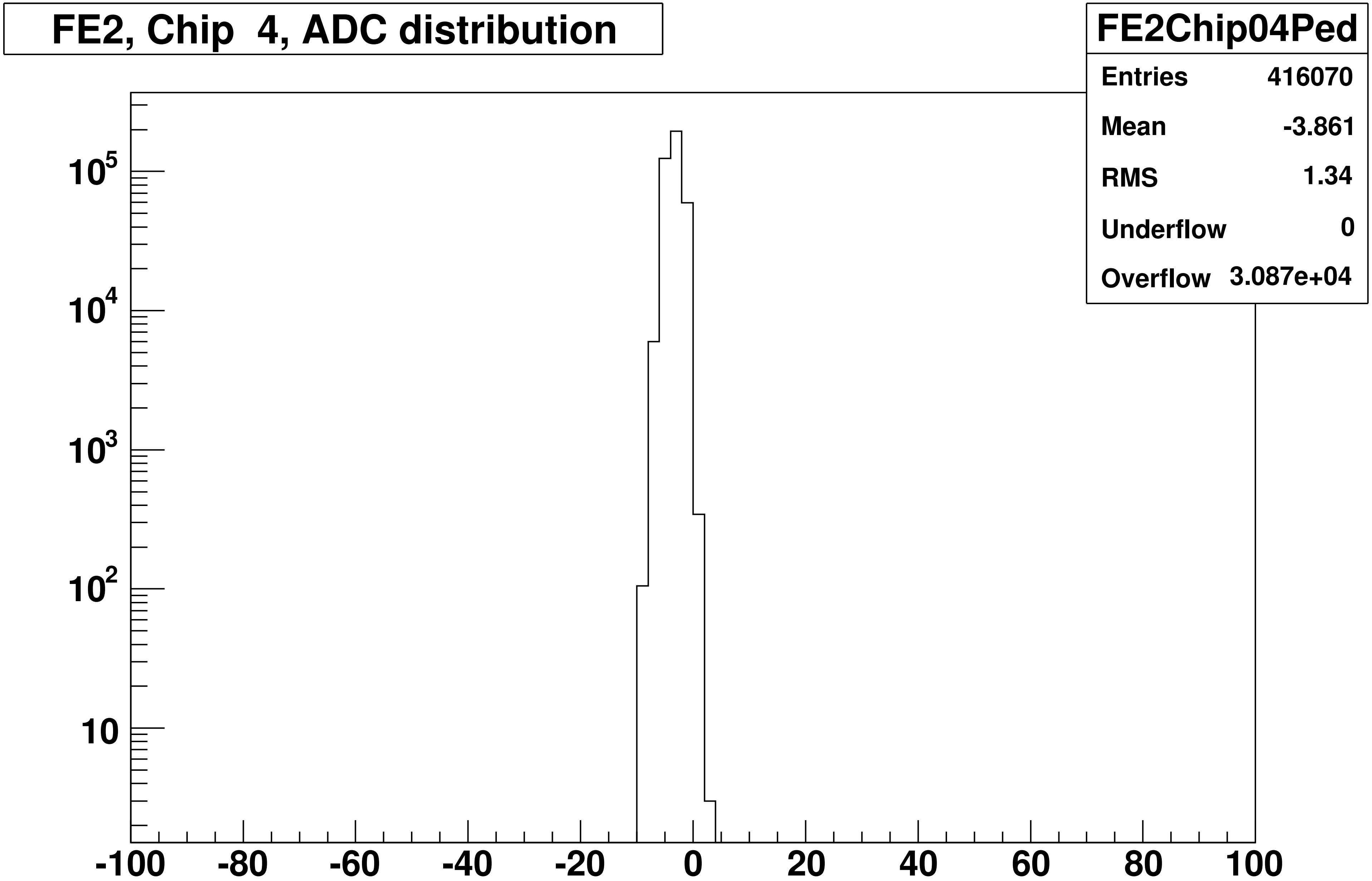
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 416070
Mean 28
RMS 1.326
Underflow 0
Overflow 3.127e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 416070
Mean 1.913e+04
RMS 13.44
Underflow 3.849e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 416070
Mean 1398
RMS 4990
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 416070
Mean -3.861
RMS 1.34
Underflow 0
Overflow 3.087e+04
10
10^2
10^3
10^4
10^5
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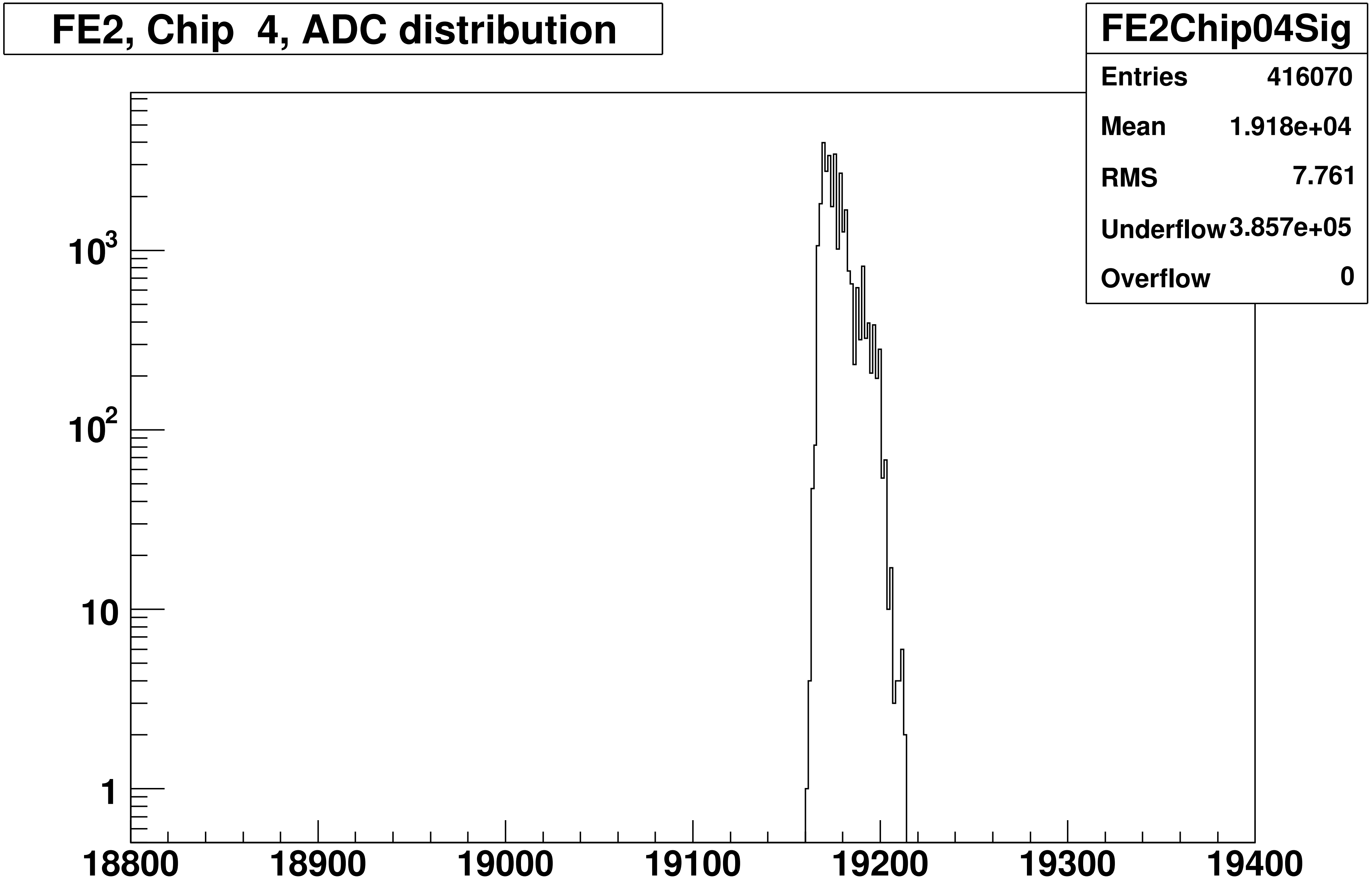
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 416070
Mean 1.918e+04
RMS 7.761
Underflow 3.857e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
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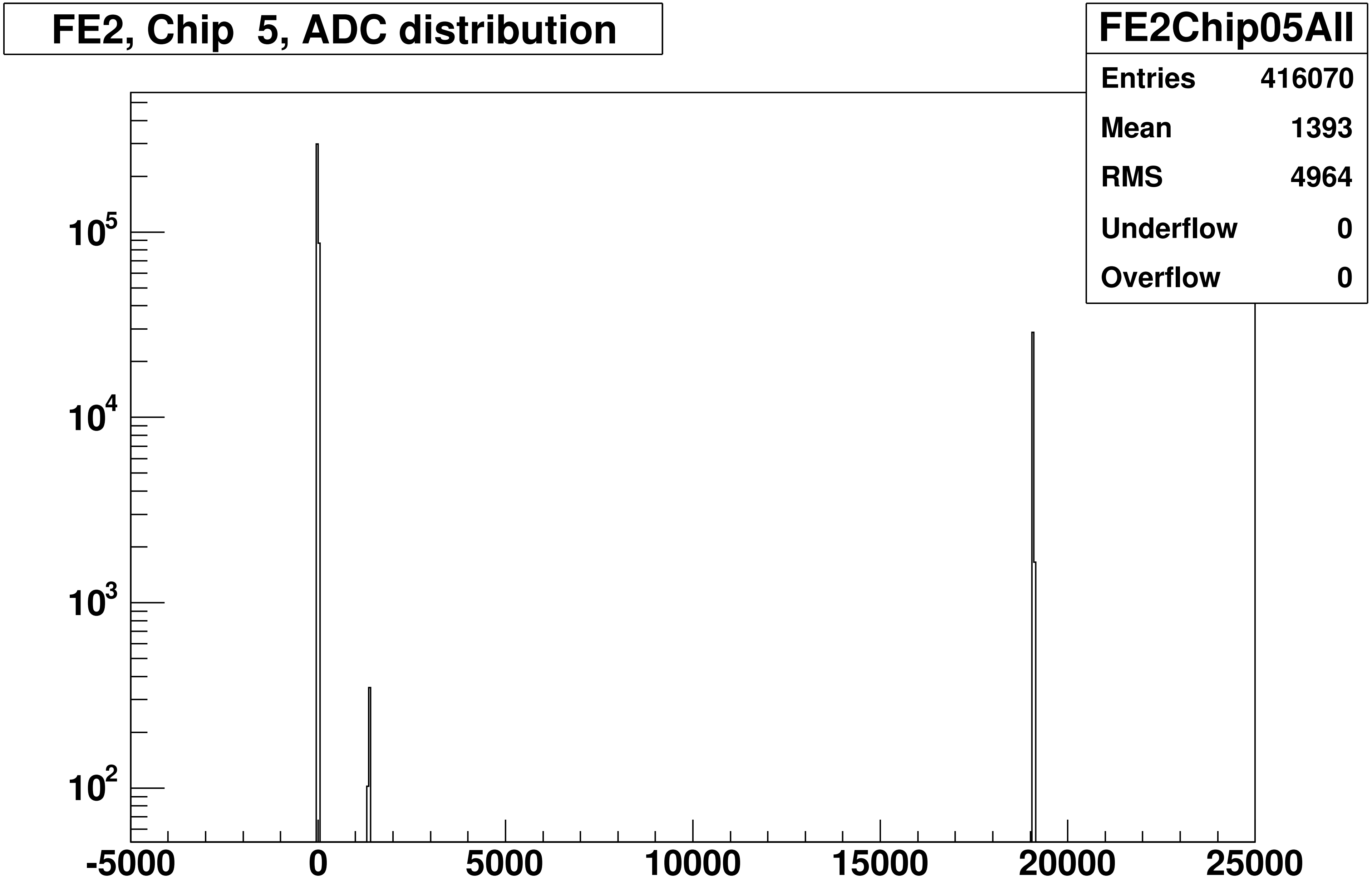
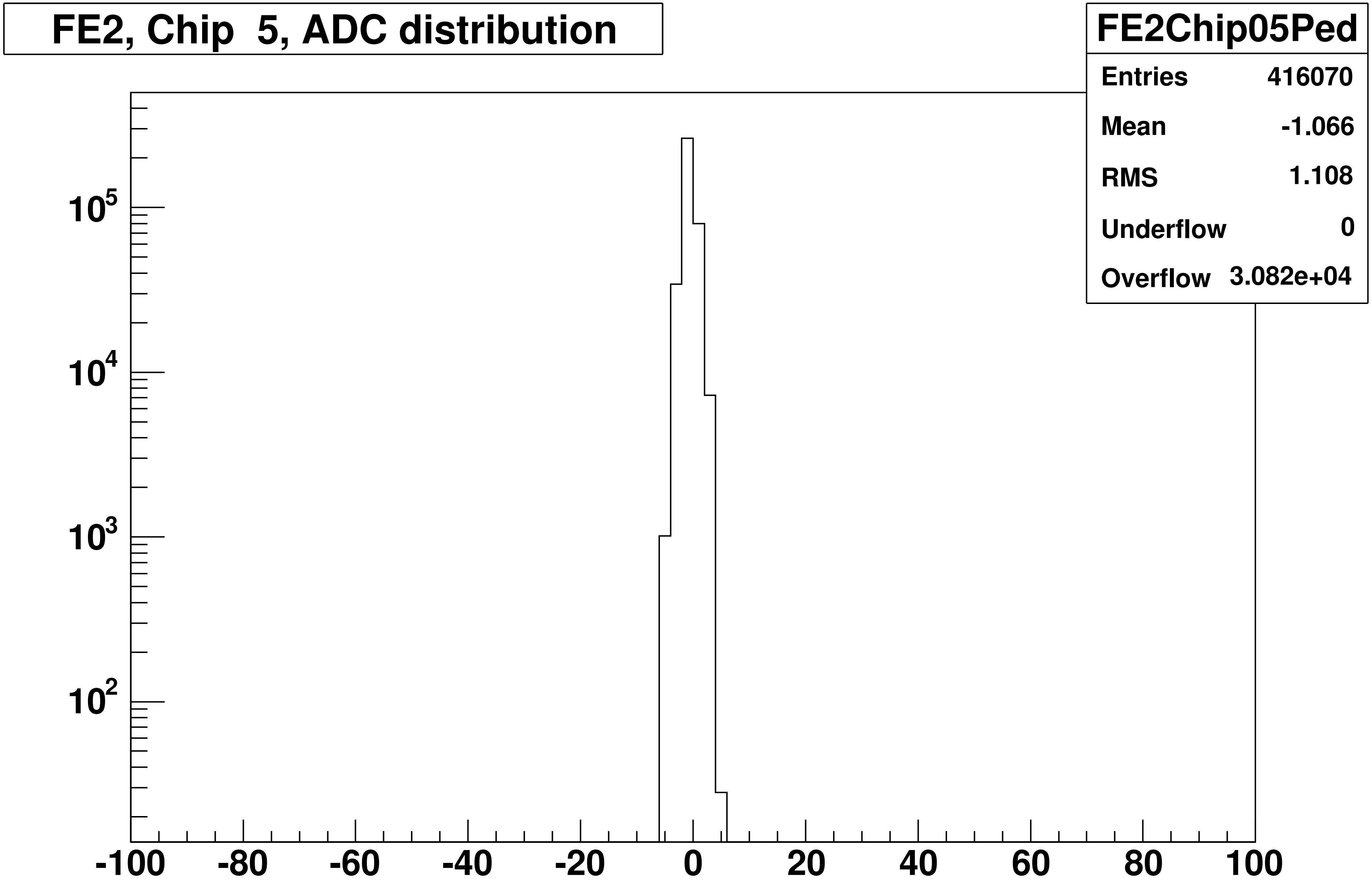
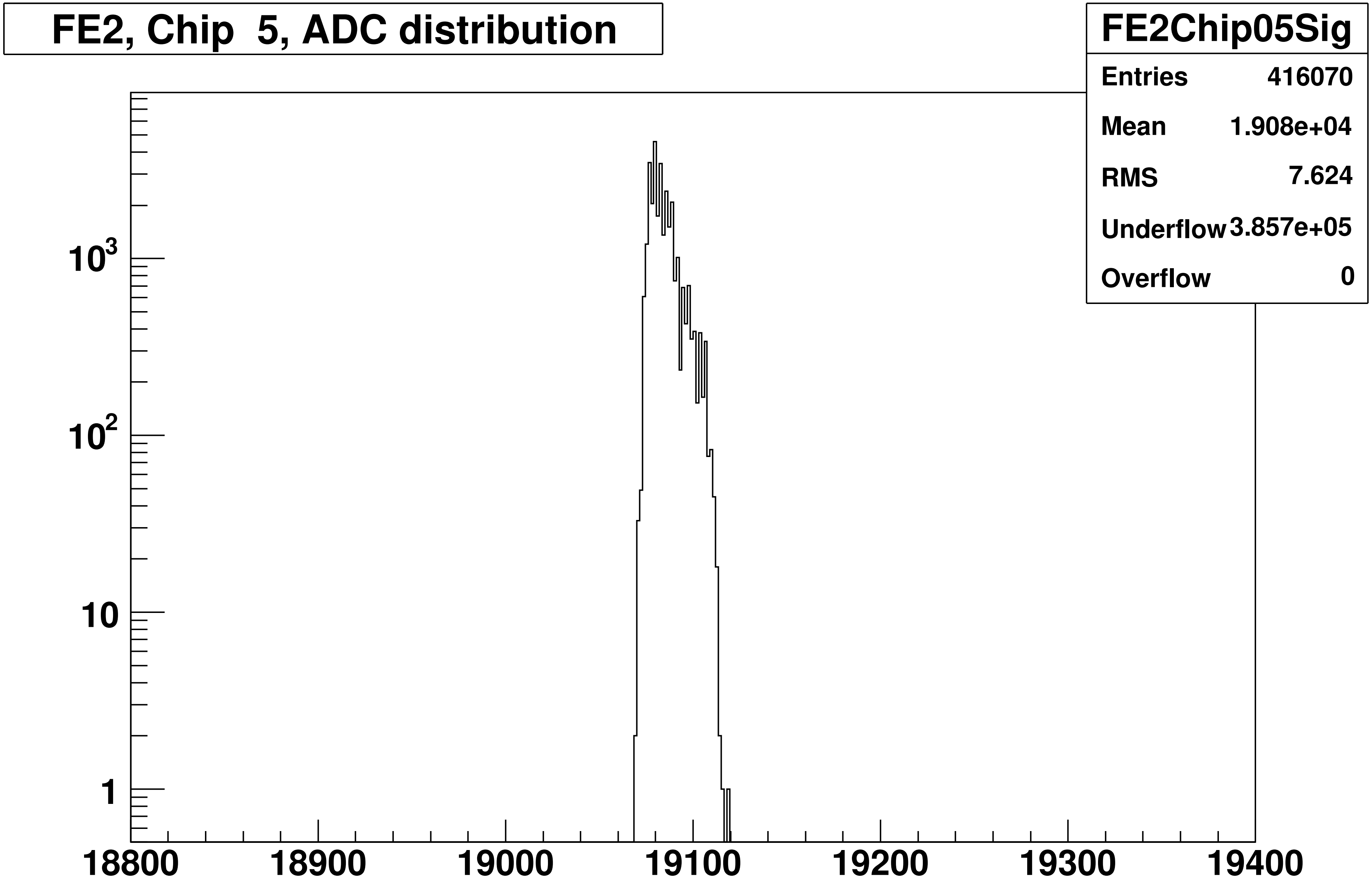
19200
19300
19400



FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 416070
Mean 1393
RMS 4964
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 416070
Mean -1.066
RMS 1.108
Underflow 0
Overflow 3.082e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 416070
Mean 1.908e+04
RMS 7.624
Underflow 3.857e+05
Overflow 0
10^3
10^2
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1
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18900
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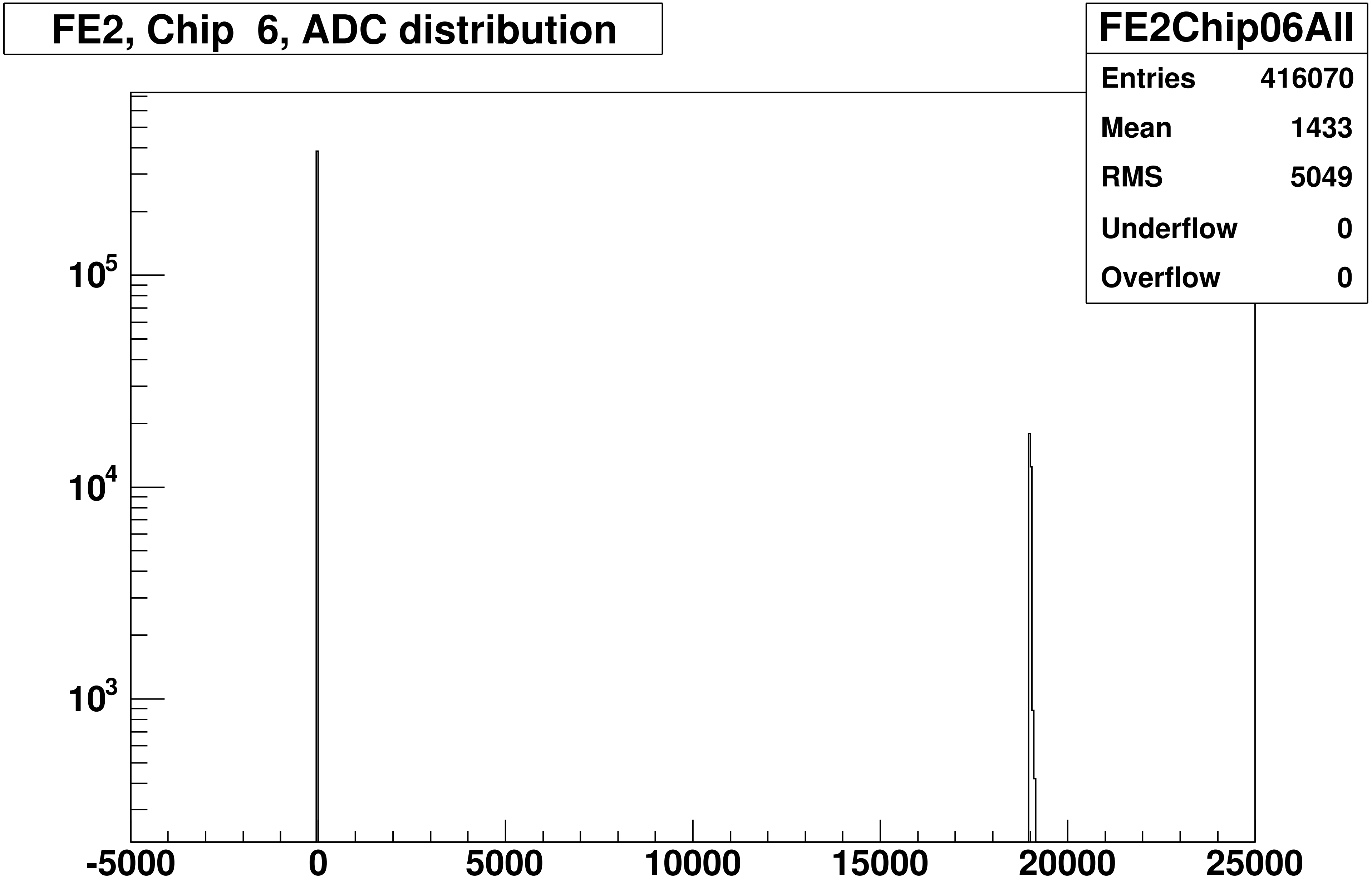
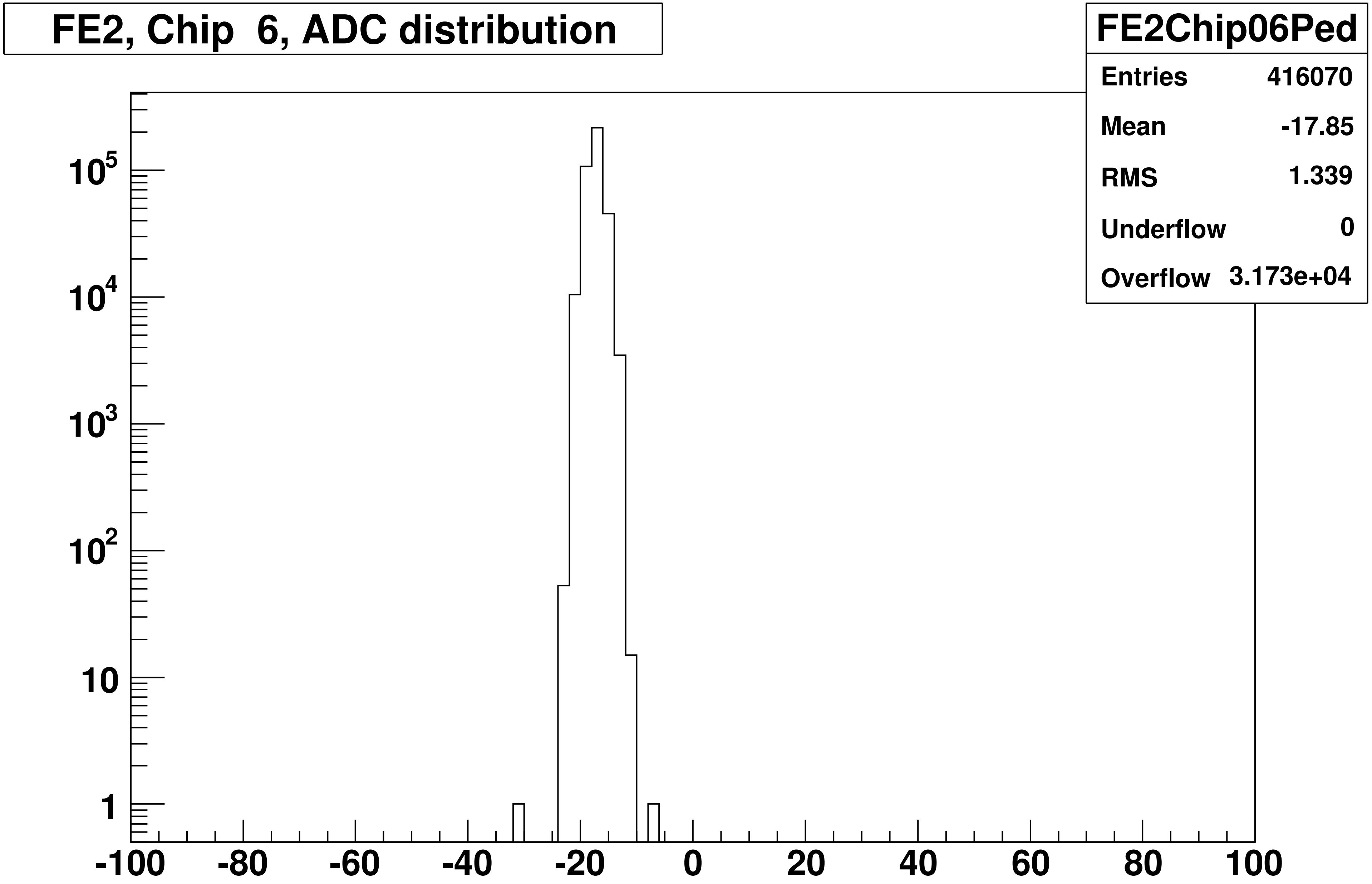
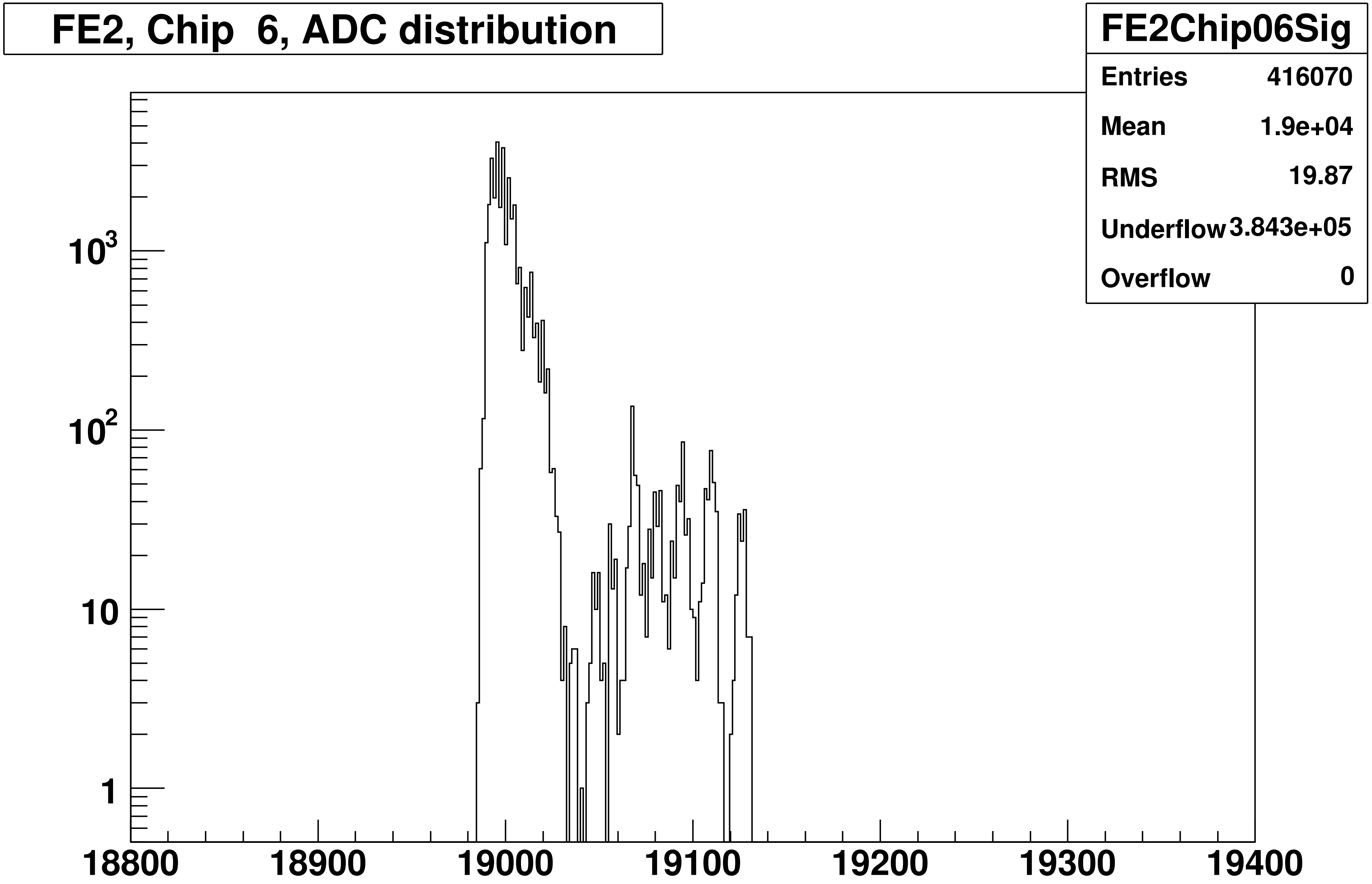
19200
19300
19400


FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 416070
Mean 1433
RMS 5049
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 416070
Mean -17.85
RMS 1.339
Underflow 0
Overflow 3.173e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 416070
Mean 1.9e+04
RMS 19.87
Underflow 3.843e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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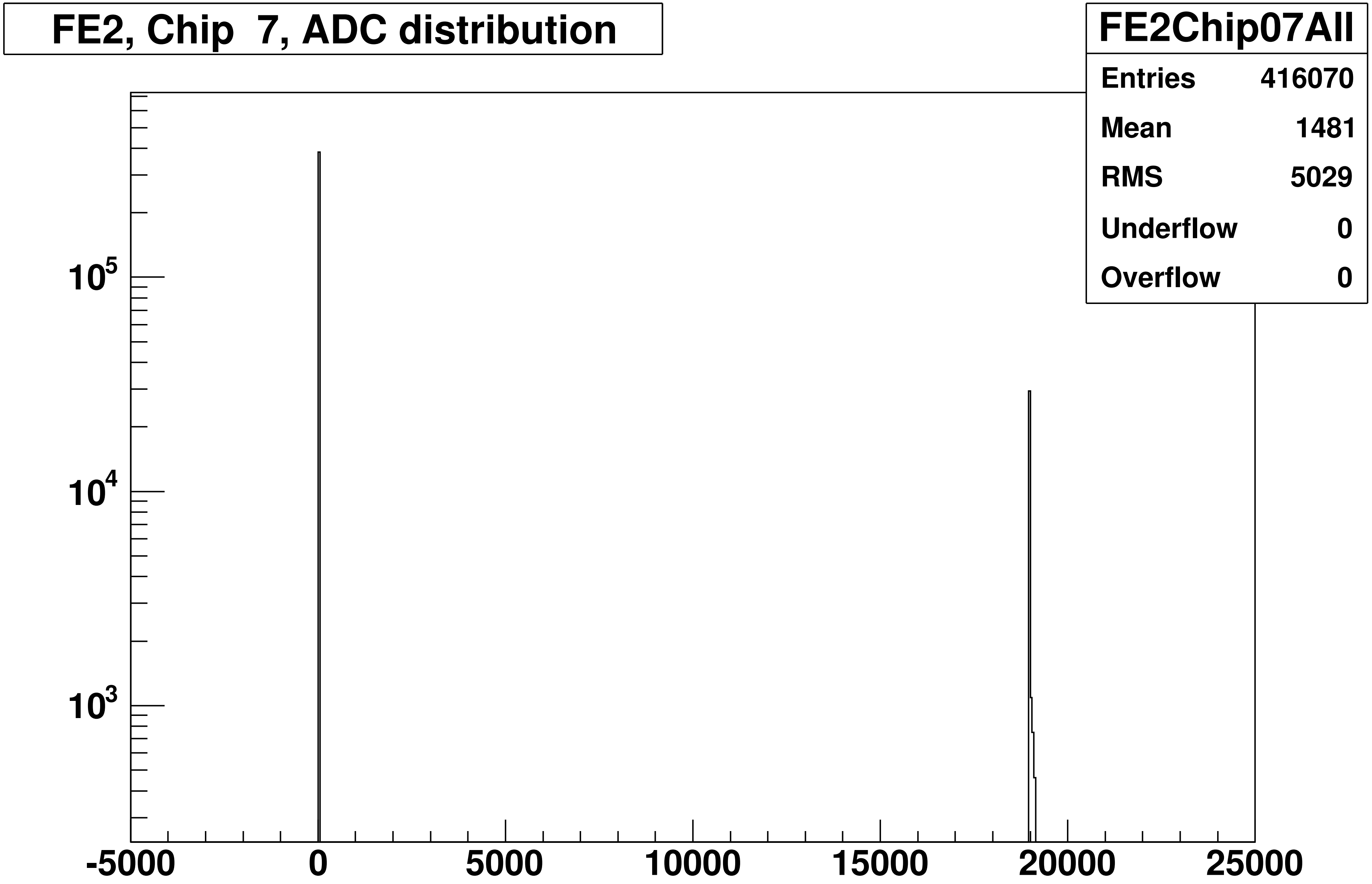
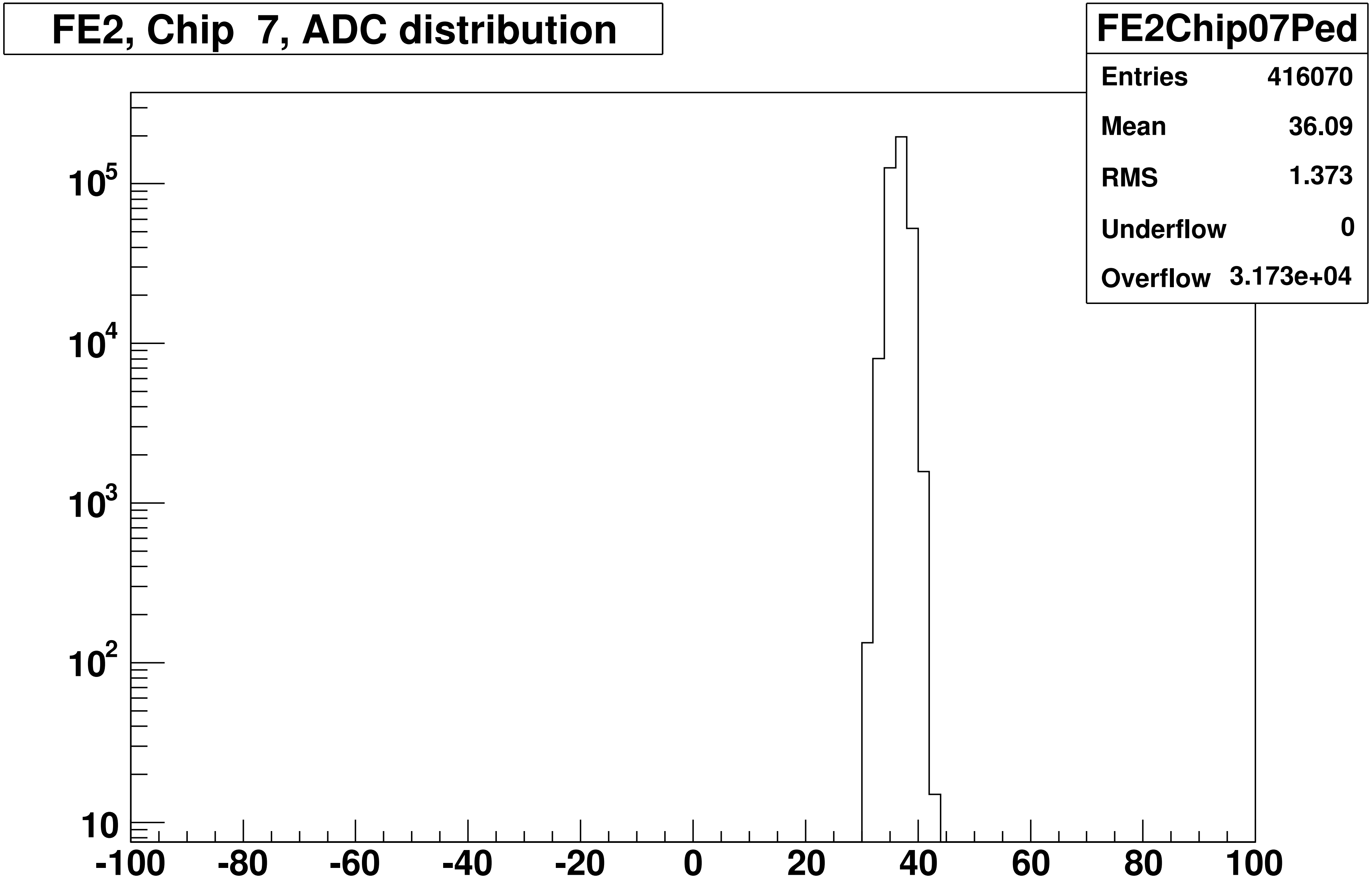
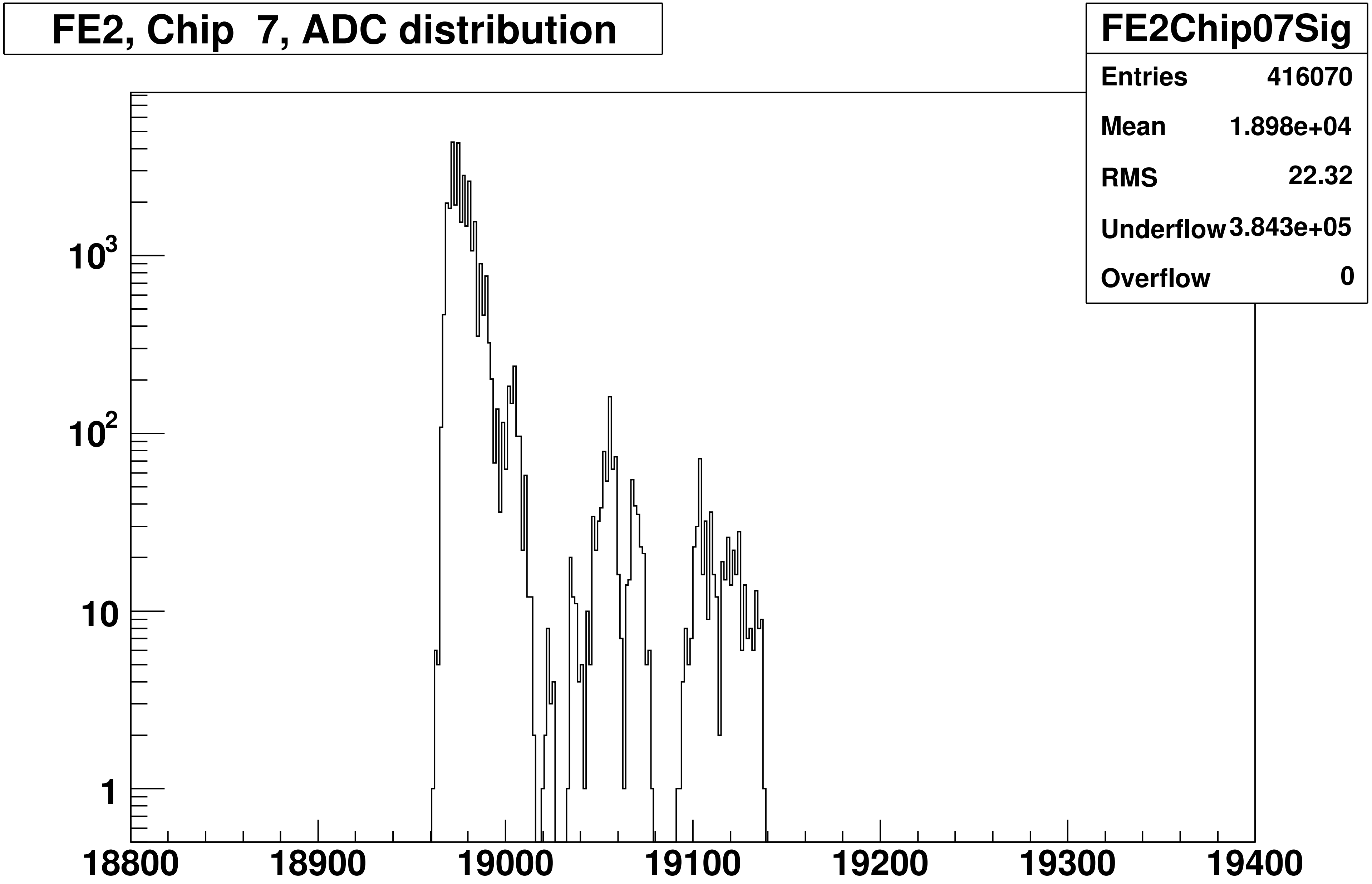
19100
19200
19300
19400

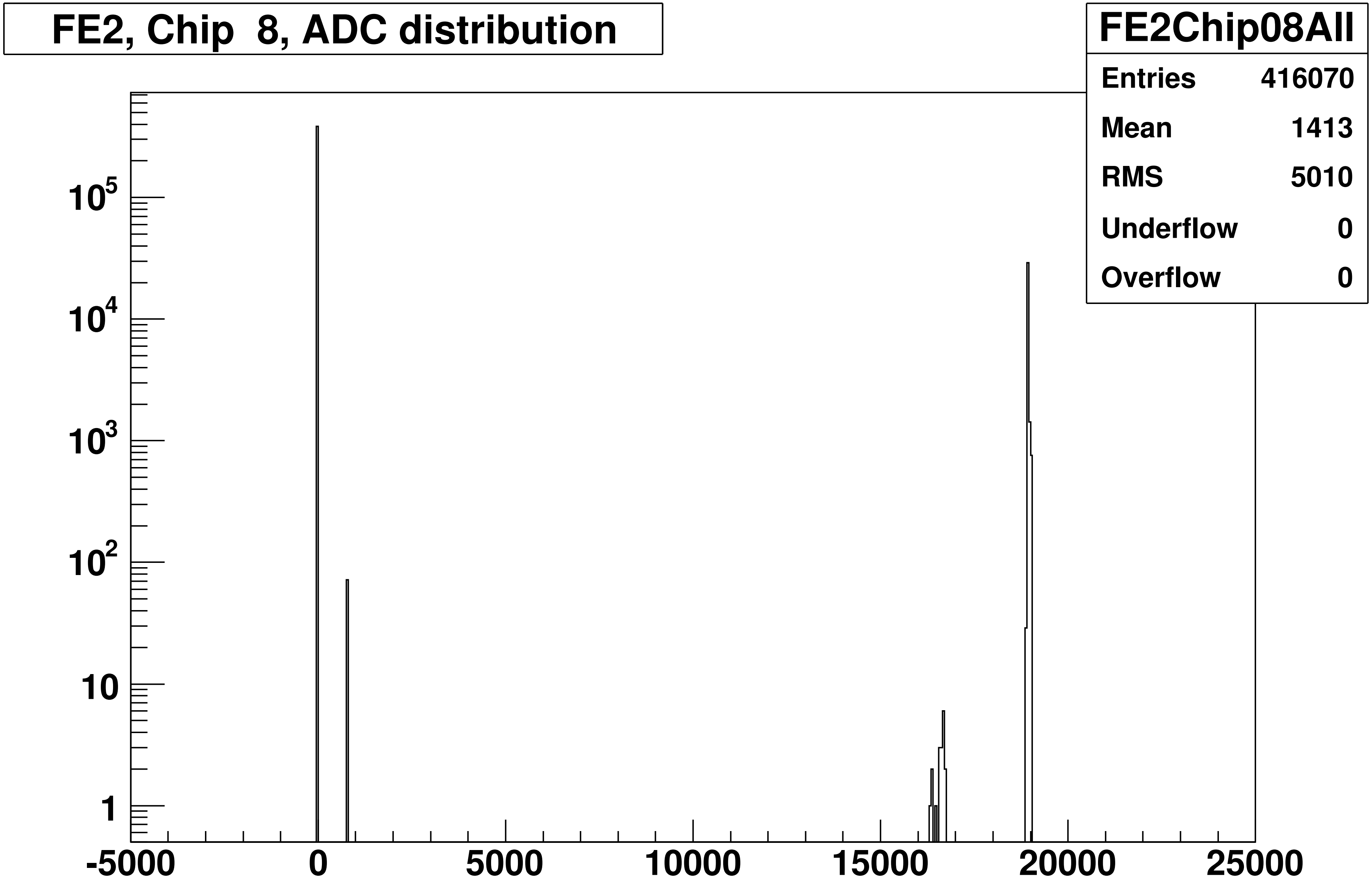


FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 416070
Mean 1481
RMS 5029
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 416070
Mean 36.09
RMS 1.373
Underflow 0
Overflow 3.173e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 416070
Mean 1.898e+04
RMS 22.32
Underflow 3.843e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 416070
Mean 1413
RMS 5010
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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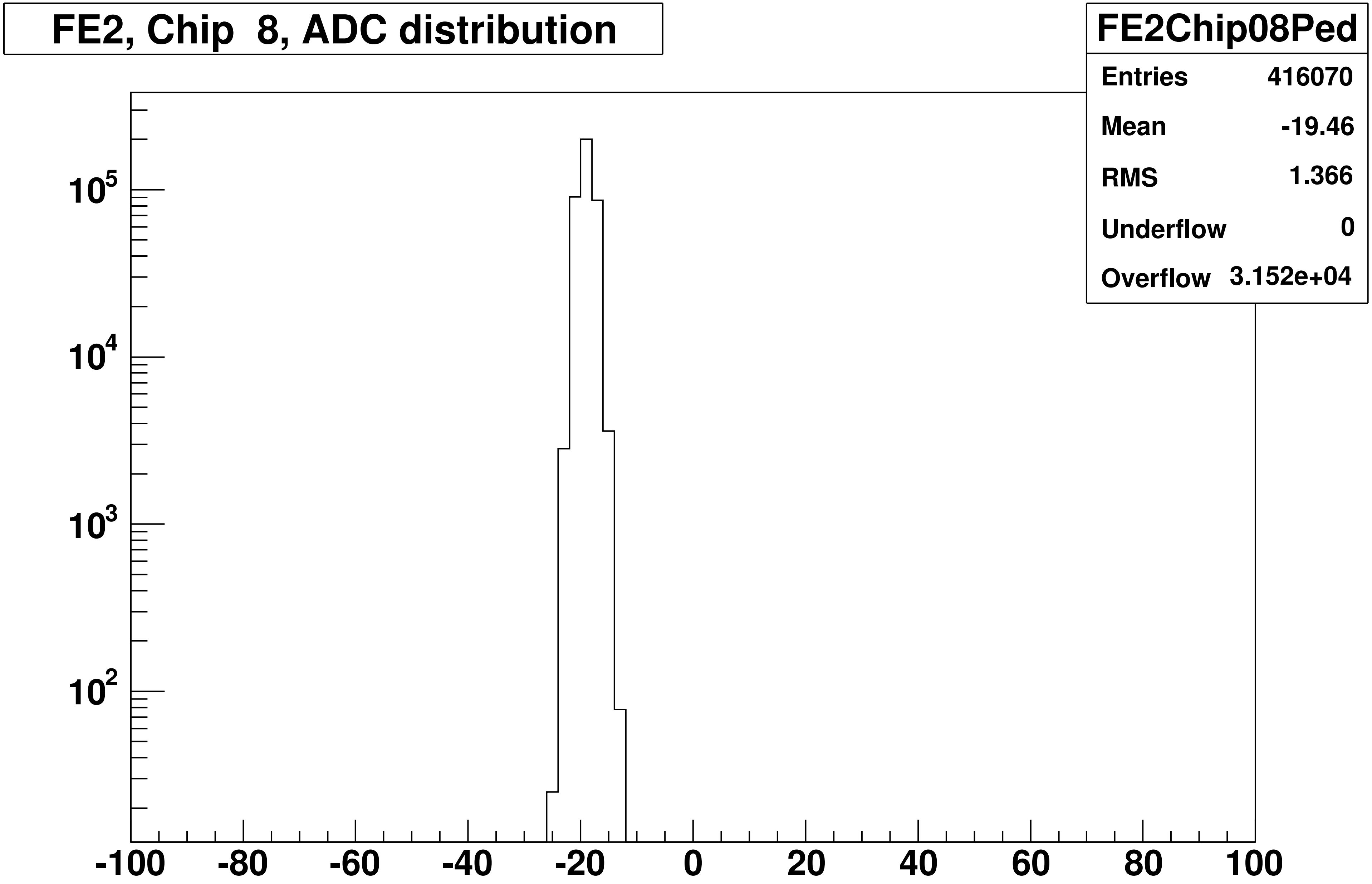
10000
15000
20000
25000


FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 416070
Mean -19.46
RMS 1.366
Underflow 0
Overflow 3.152e+04
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10^4
10^3
10^2
-100
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-60
-40
-20
0
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40
60
80
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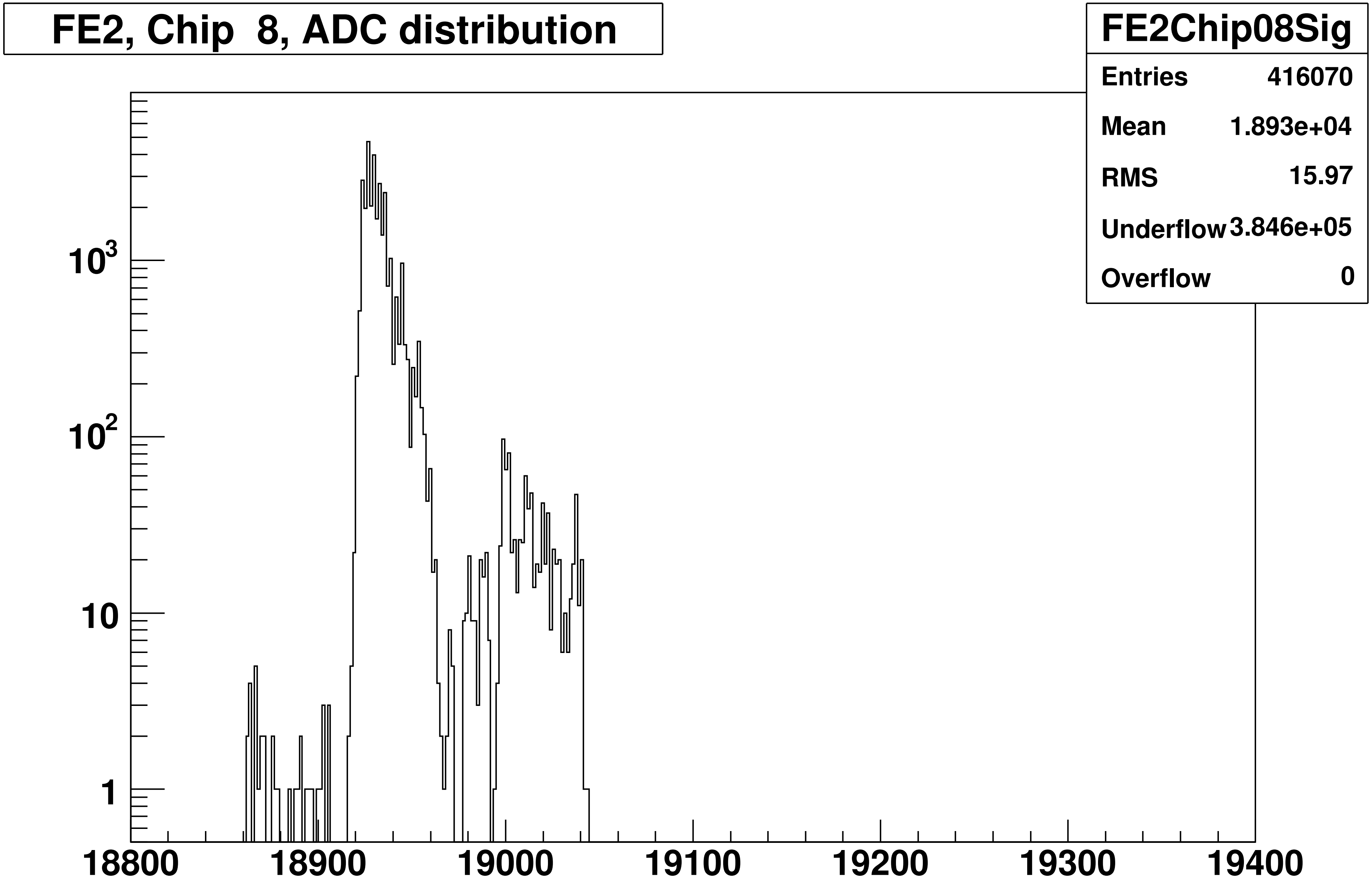
FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 416070
Mean 1.893e+04
RMS 15.97
Underflow 3.846e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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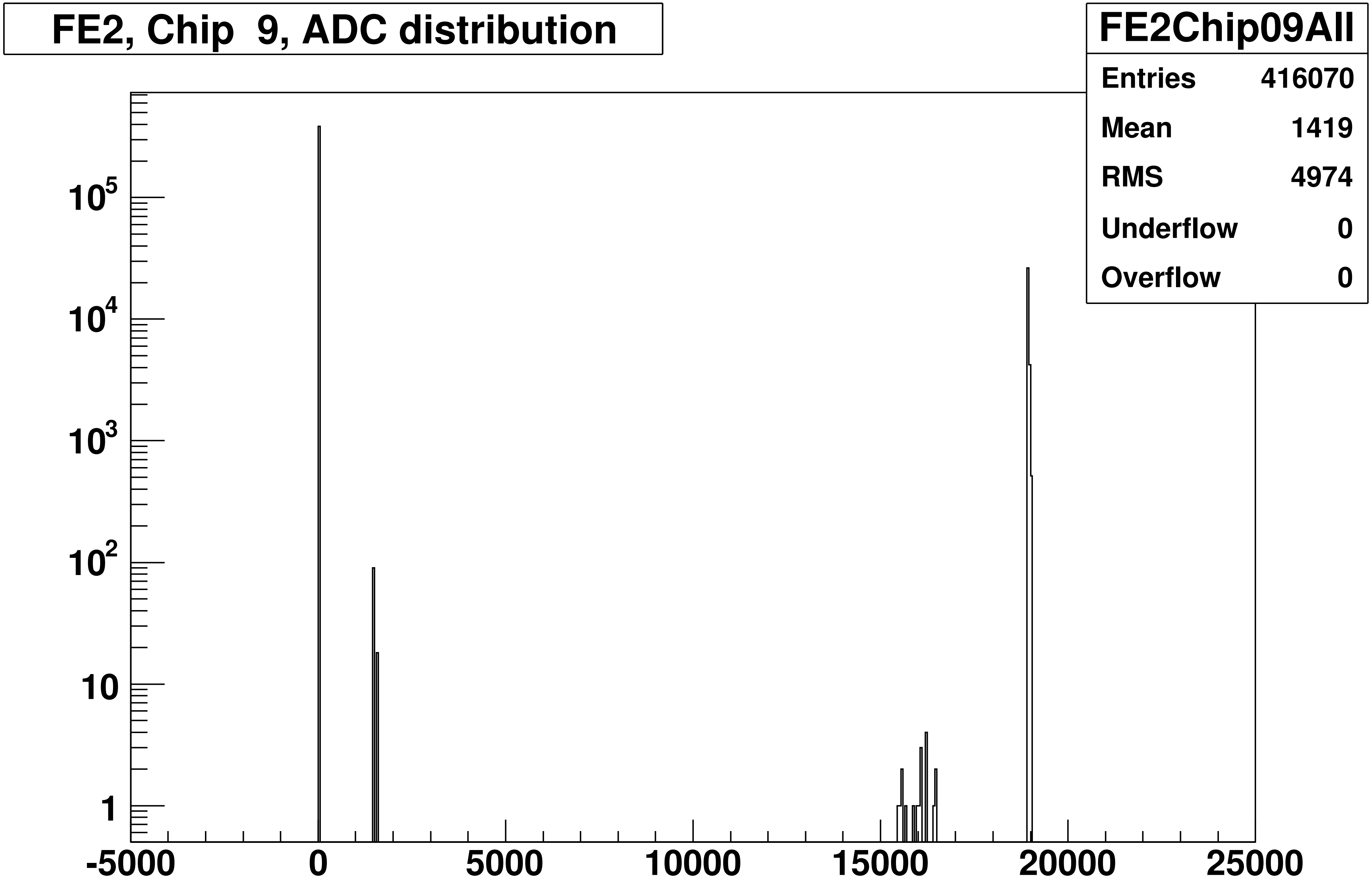
19200
19300
19400


FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 416070
Mean 1419
RMS 4974
Underflow 0
Overflow 0
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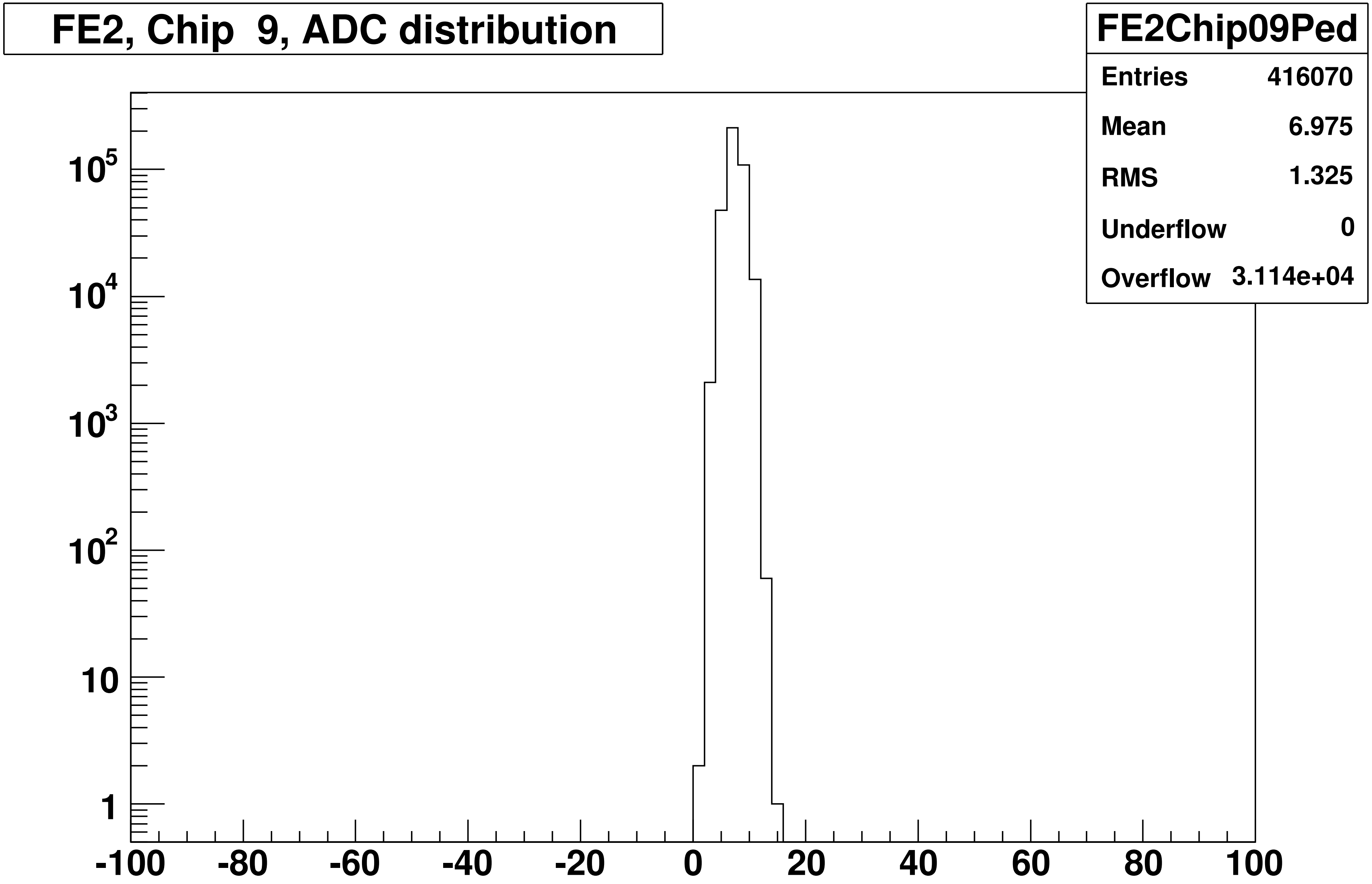
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 416070
Mean 6.975
RMS 1.325
Underflow 0
Overflow 3.114e+04
10^5
10^4
10^3
10^2
10
1
-100
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-60
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-20
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80
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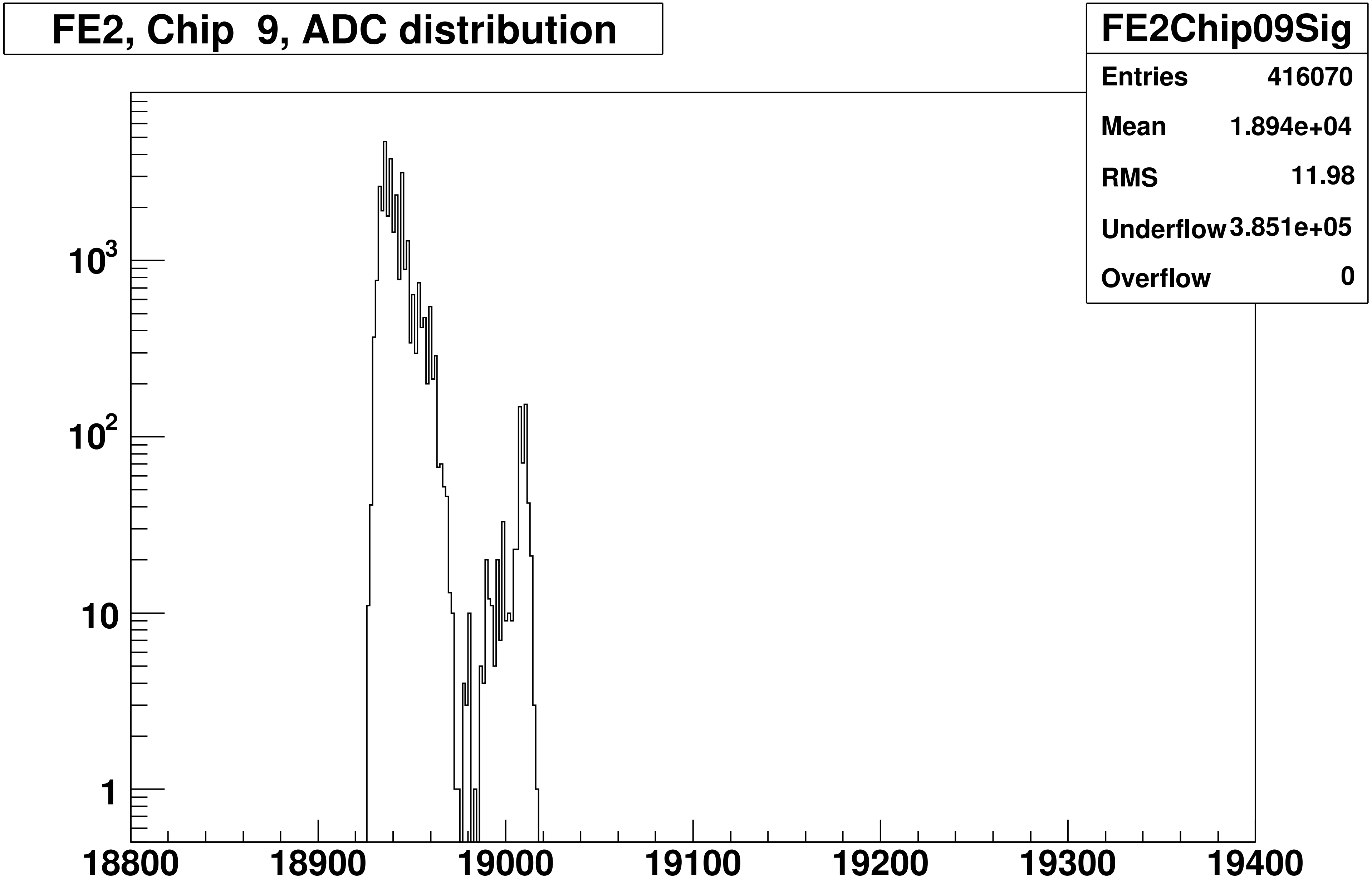

FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 416070
Mean 1.894e+04
RMS 11.98
Underflow 3.851e+05
Overflow 0
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10^2
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1
18800
18900
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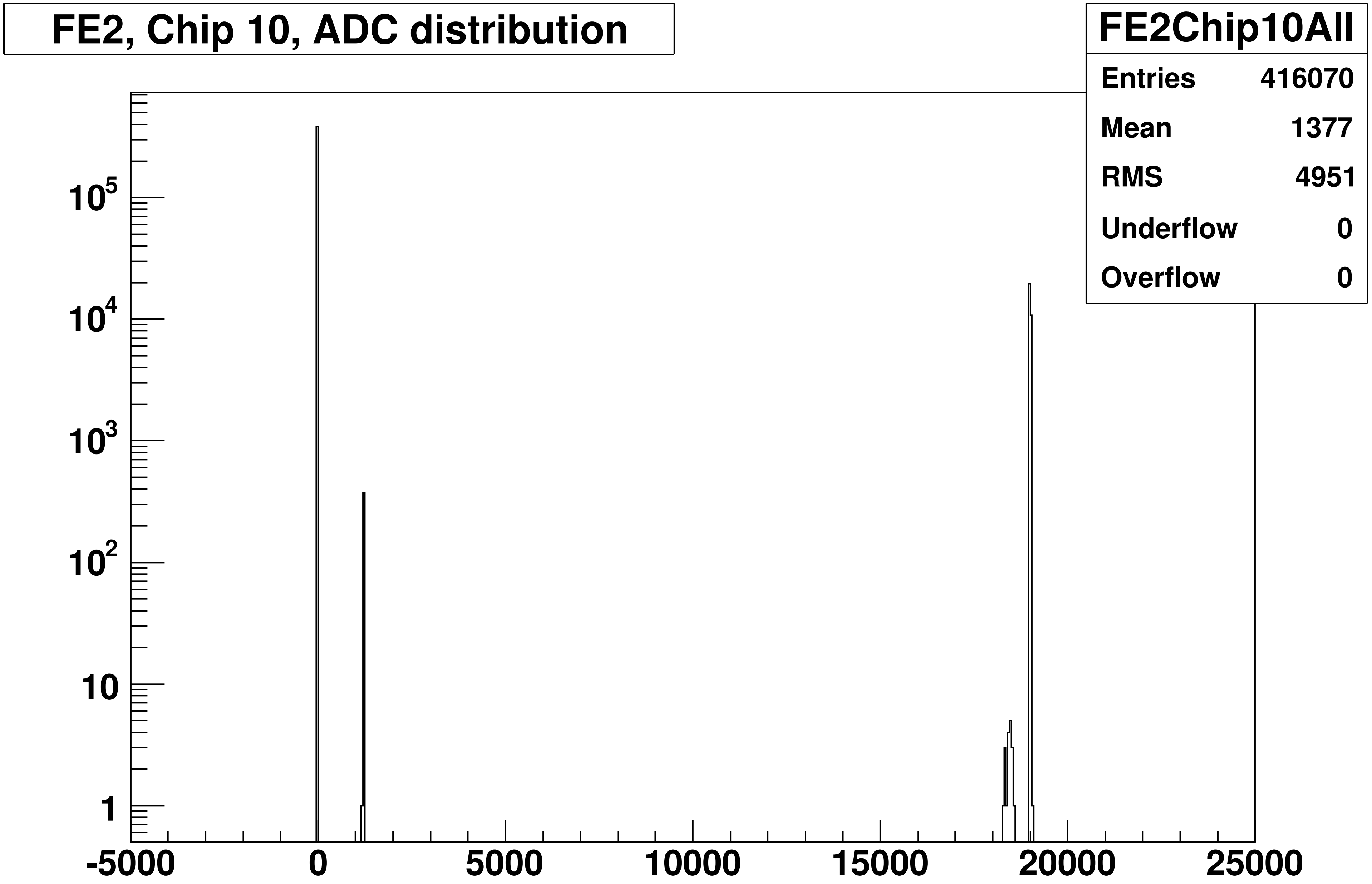
19200
19300
19400


FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 416070
Mean 1377
RMS 4951
Underflow 0
Overflow 0
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10
10^2
10^3
10^4
10^5
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0
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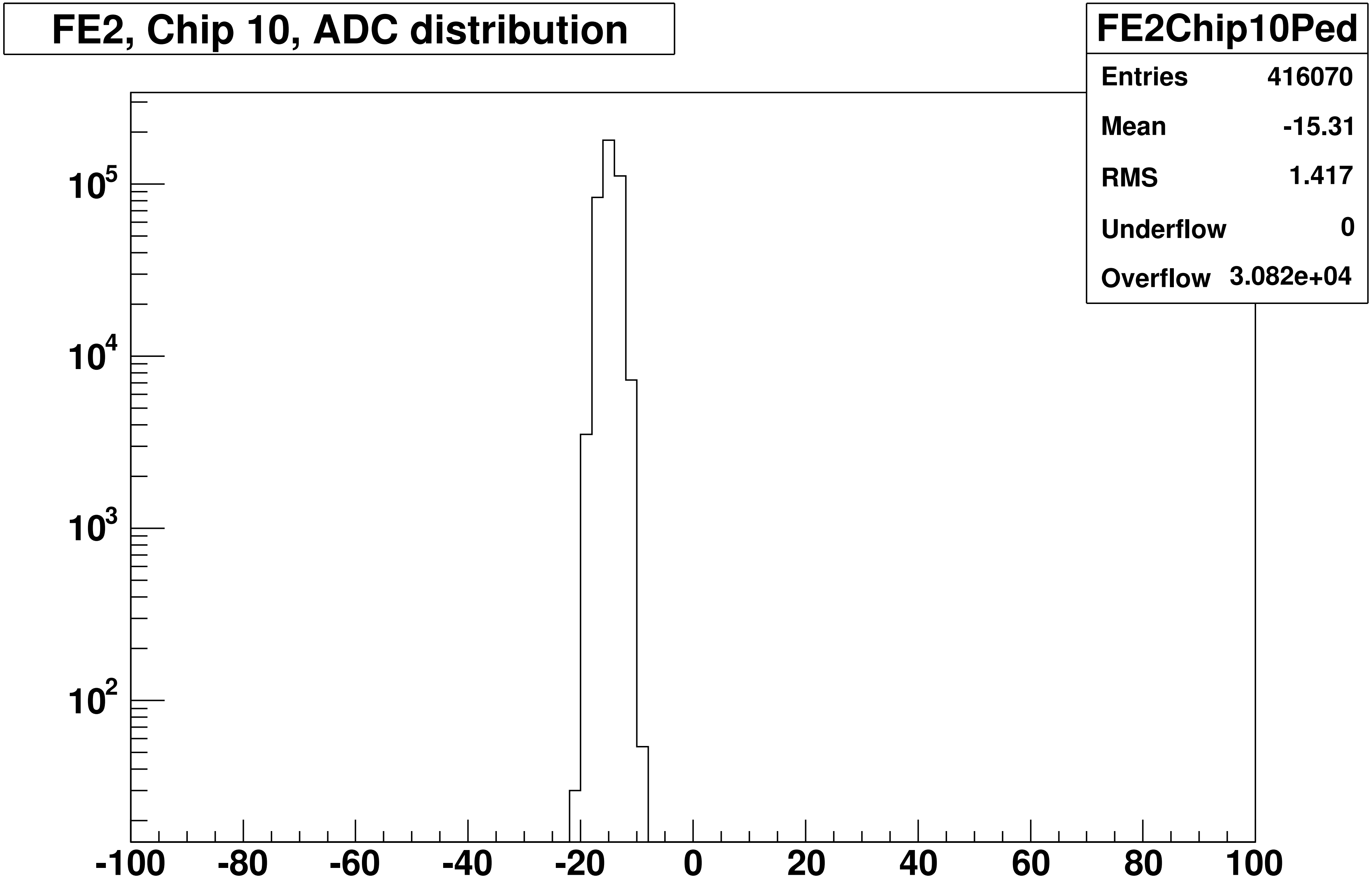
10000
15000
20000
25000


FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 416070
Mean -15.31
RMS 1.417
Underflow 0
Overflow 3.082e+04
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10^4
10^3
10^2
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-60
-40
-20
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80
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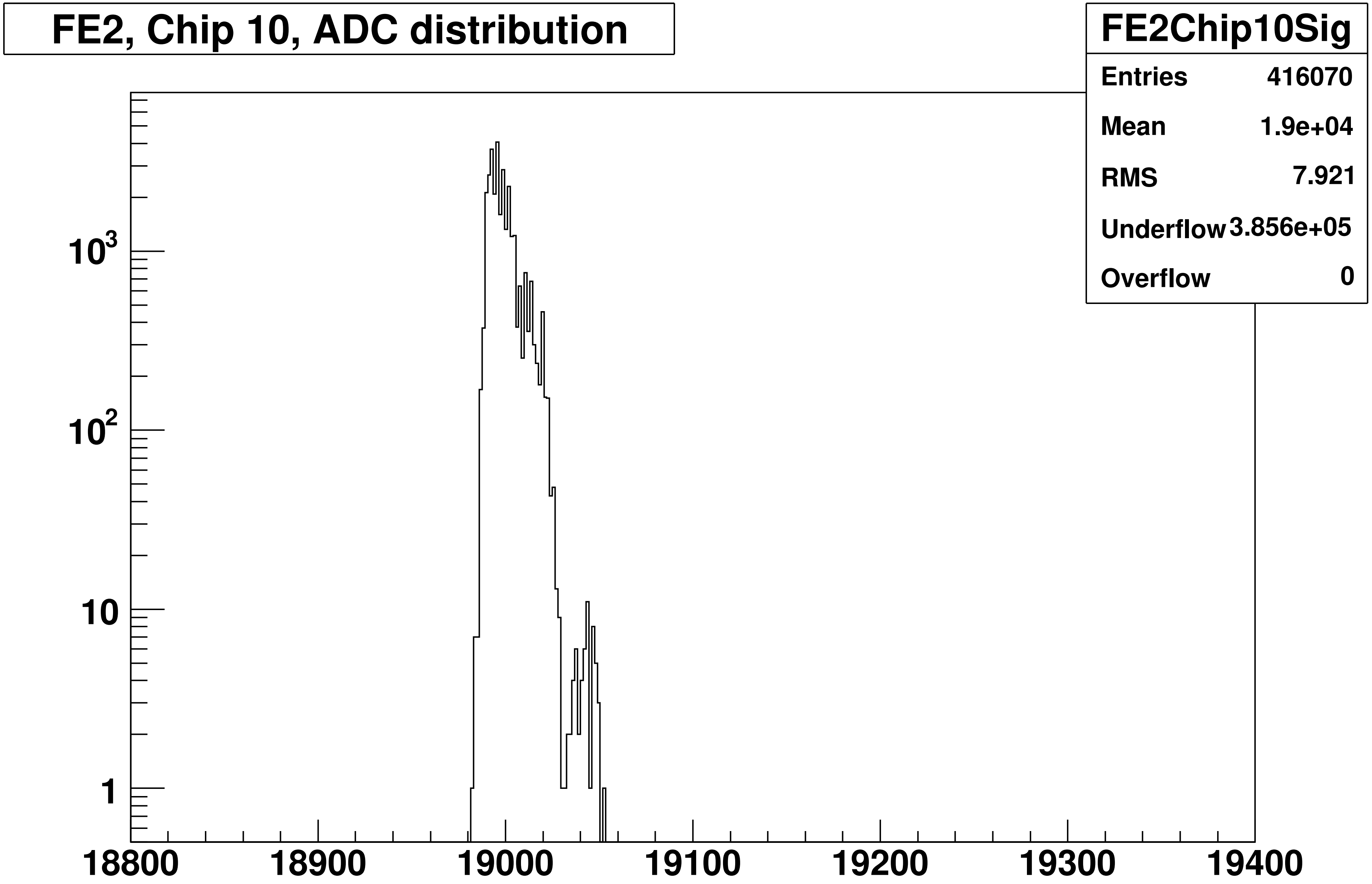
FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 416070
Mean 1.9e+04
RMS 7.921
Underflow 3.856e+05
Overflow 0
10^3
10^2
10
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18800
18900
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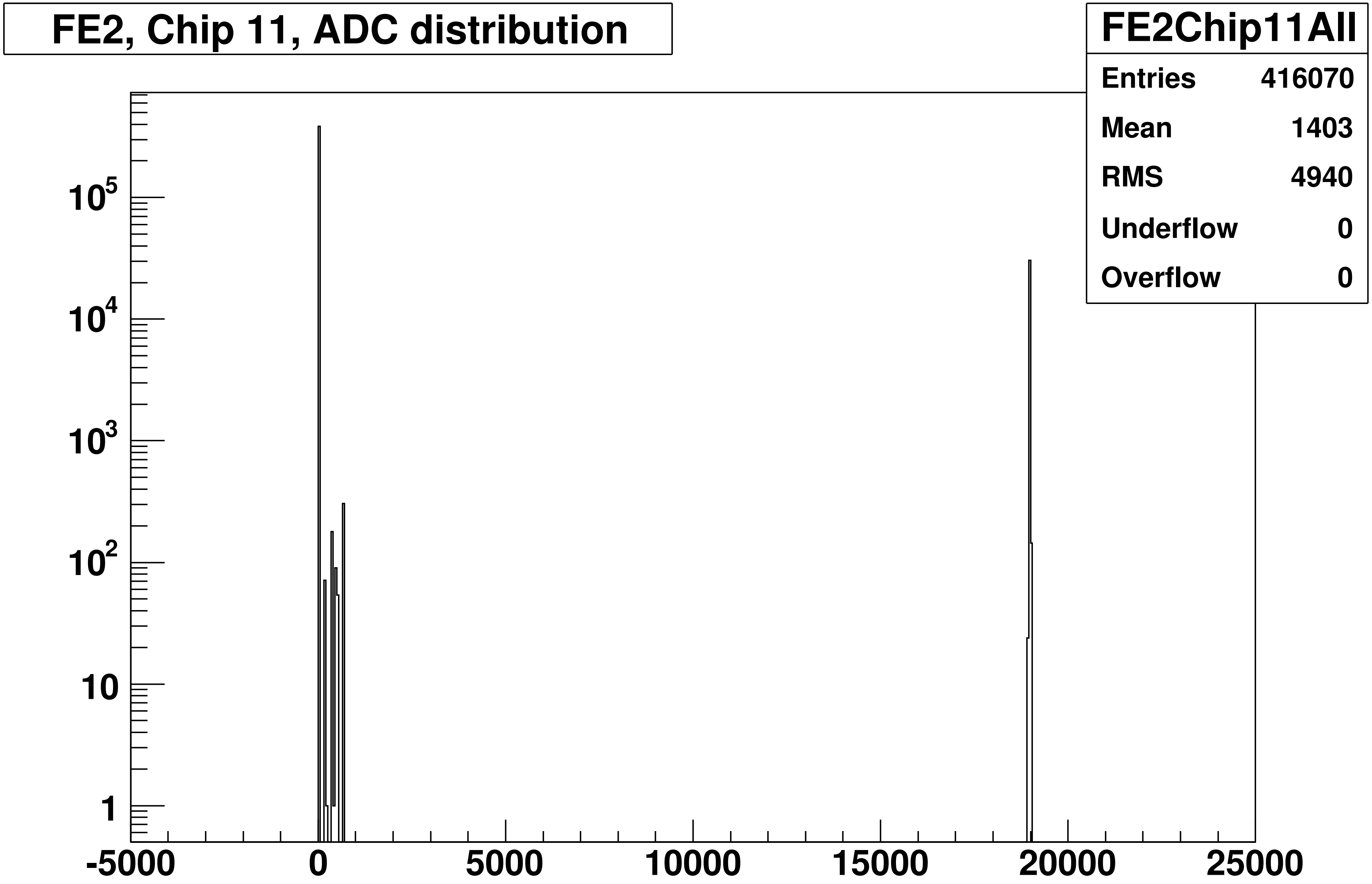
19400



FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 416070
Mean 1403
RMS 4940
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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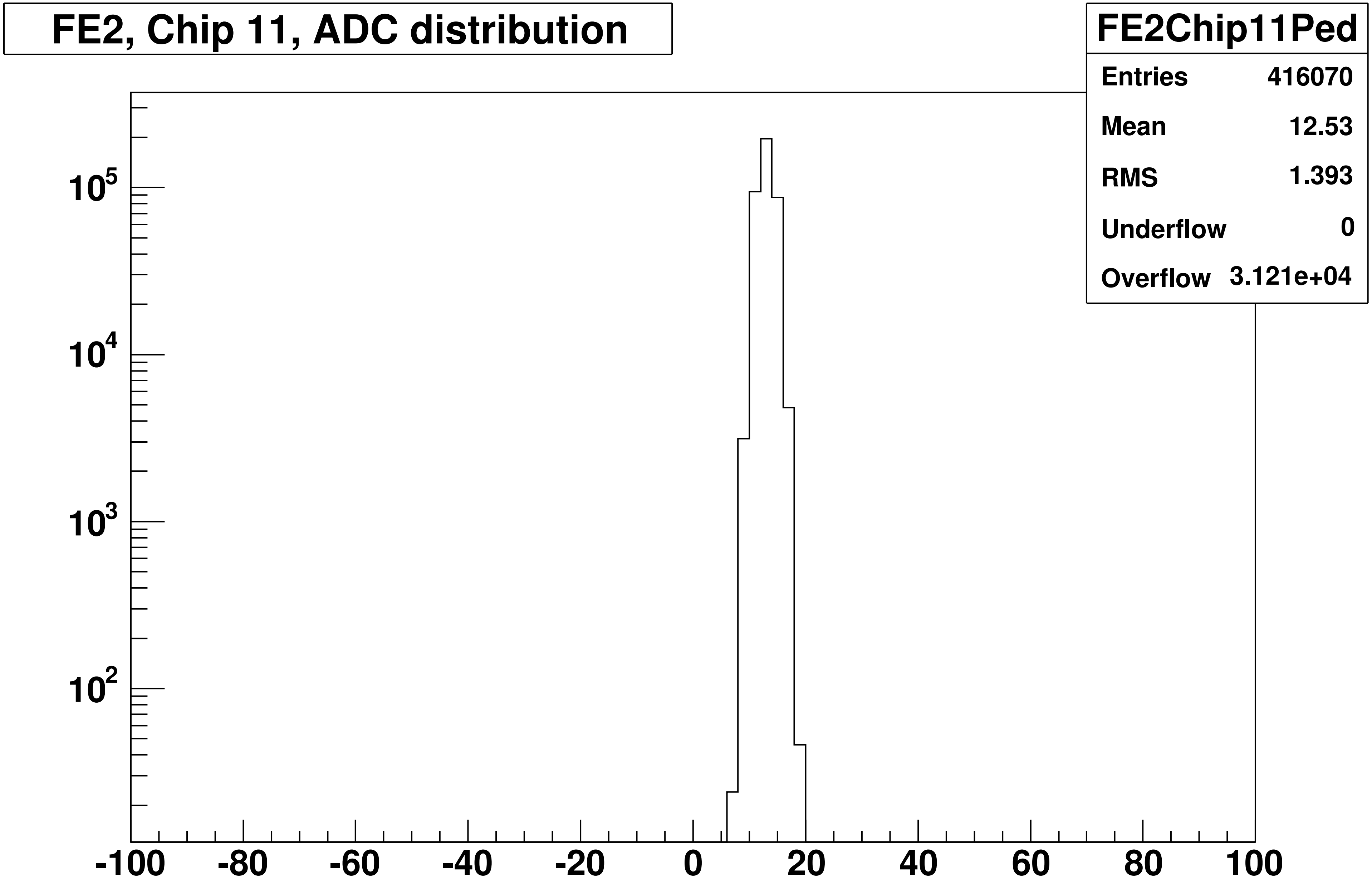

FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 416070
Mean 12.53
RMS 1.393
Underflow 0
Overflow 3.121e+04
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10^4
10^3
10^2
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-60
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-20
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40
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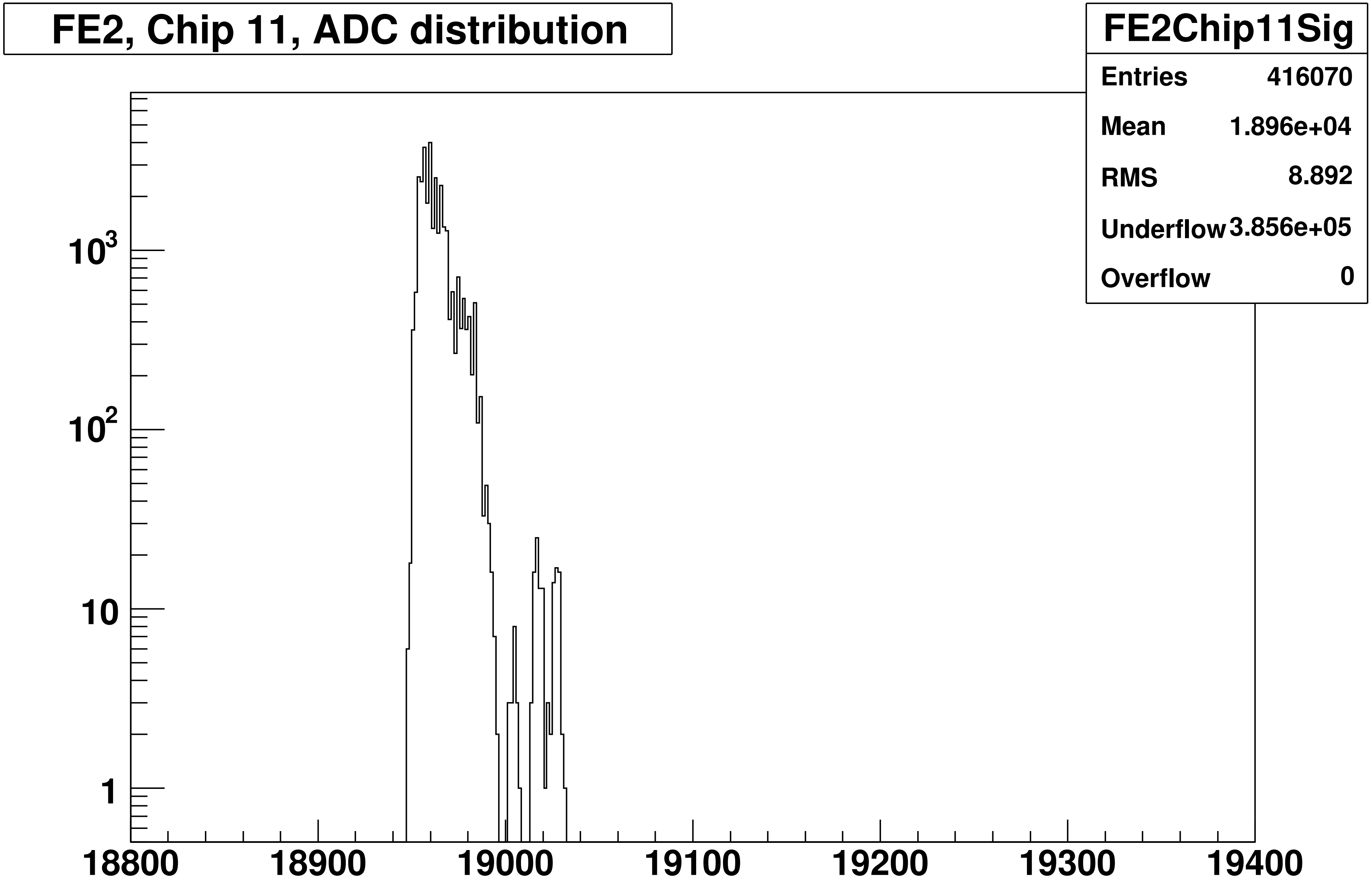
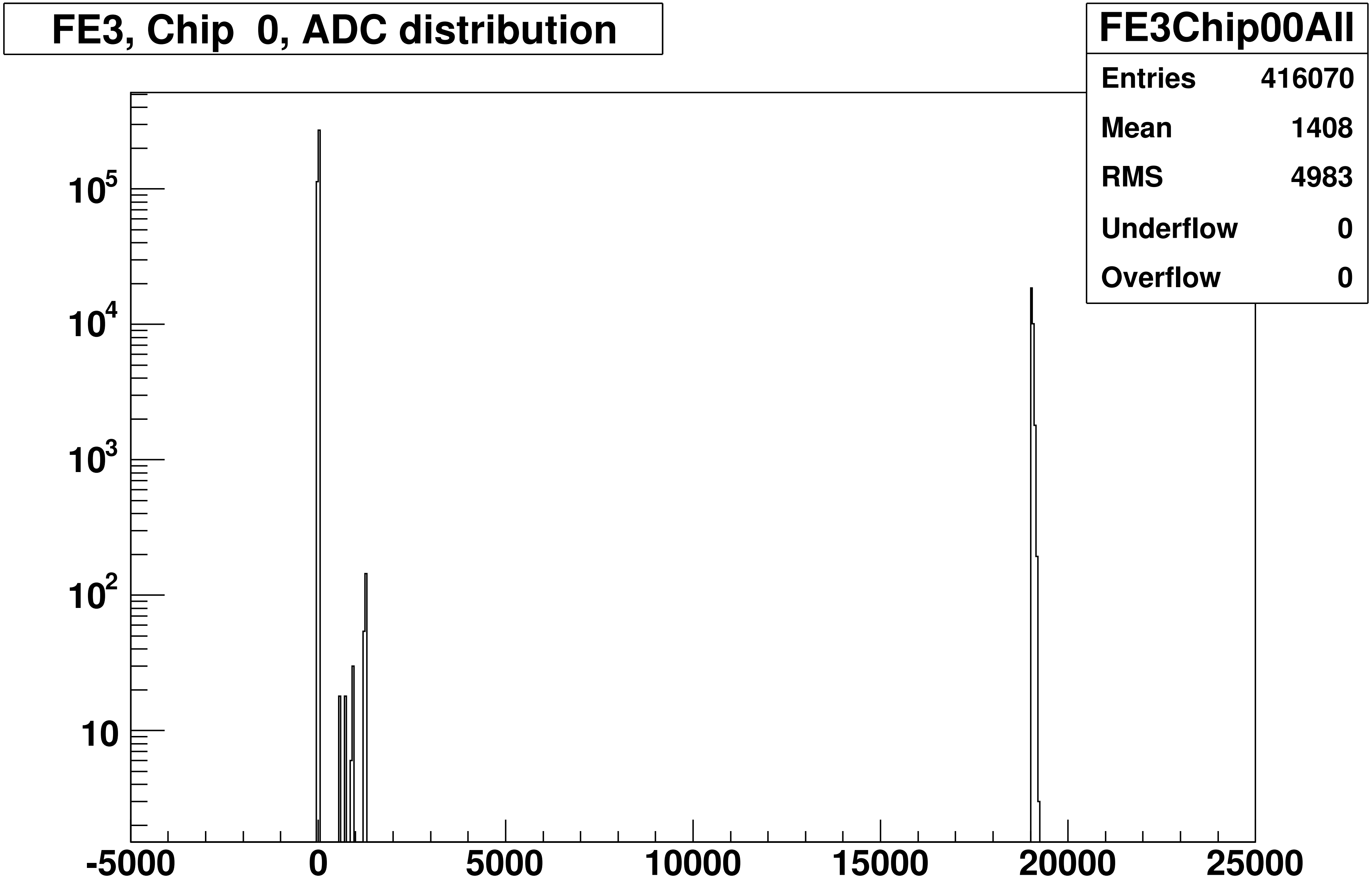

FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 416070
Mean 1.896e+04
RMS 8.892
Underflow 3.856e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 416070
Mean 1408
RMS 4983
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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0
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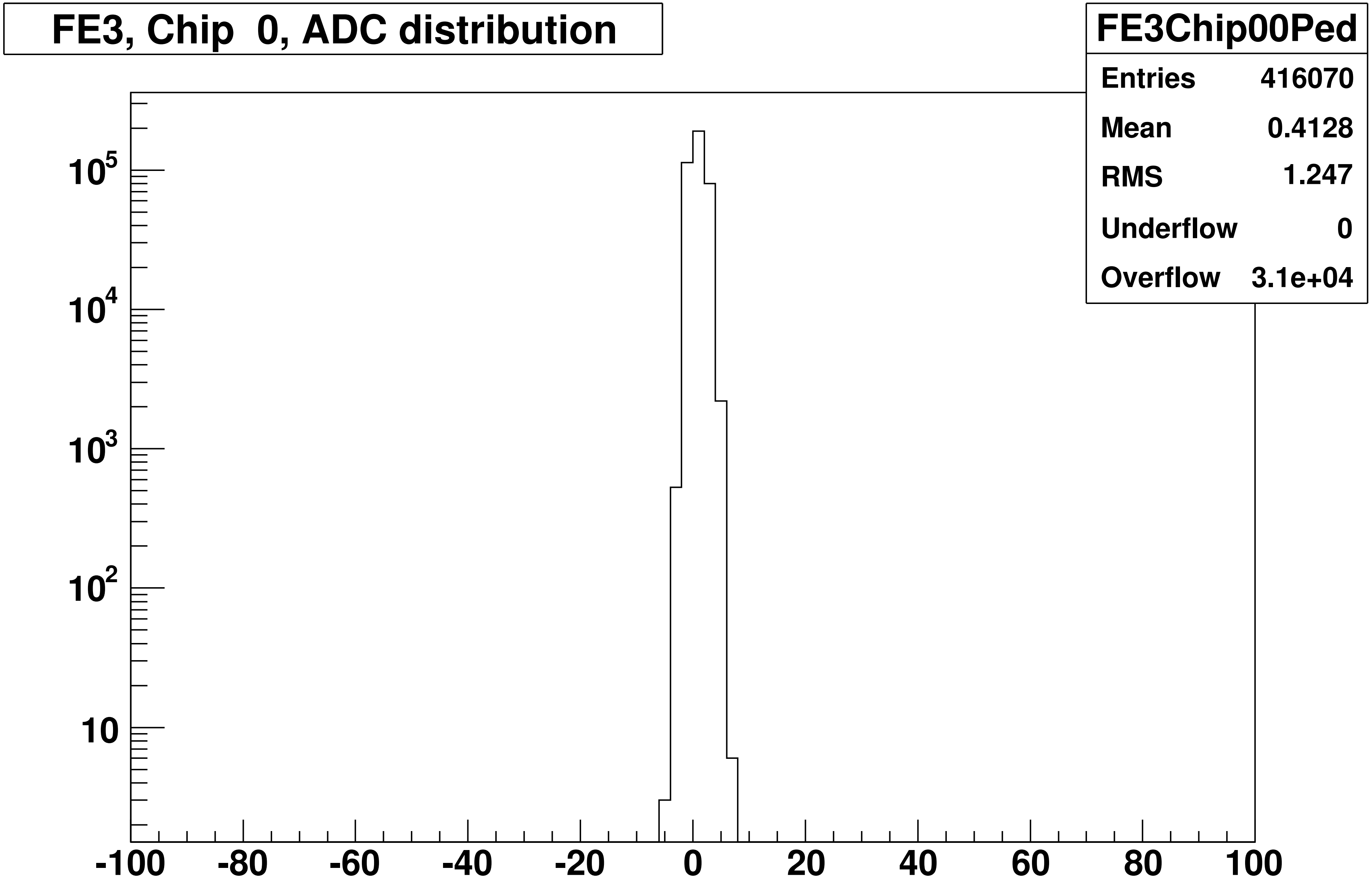
15000
20000
25000


FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 416070
Mean 0.4128
RMS 1.247
Underflow 0
Overflow 3.1e+04
10^5
10^4
10^3
10^2
10
-100
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-60
-40
-20
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40
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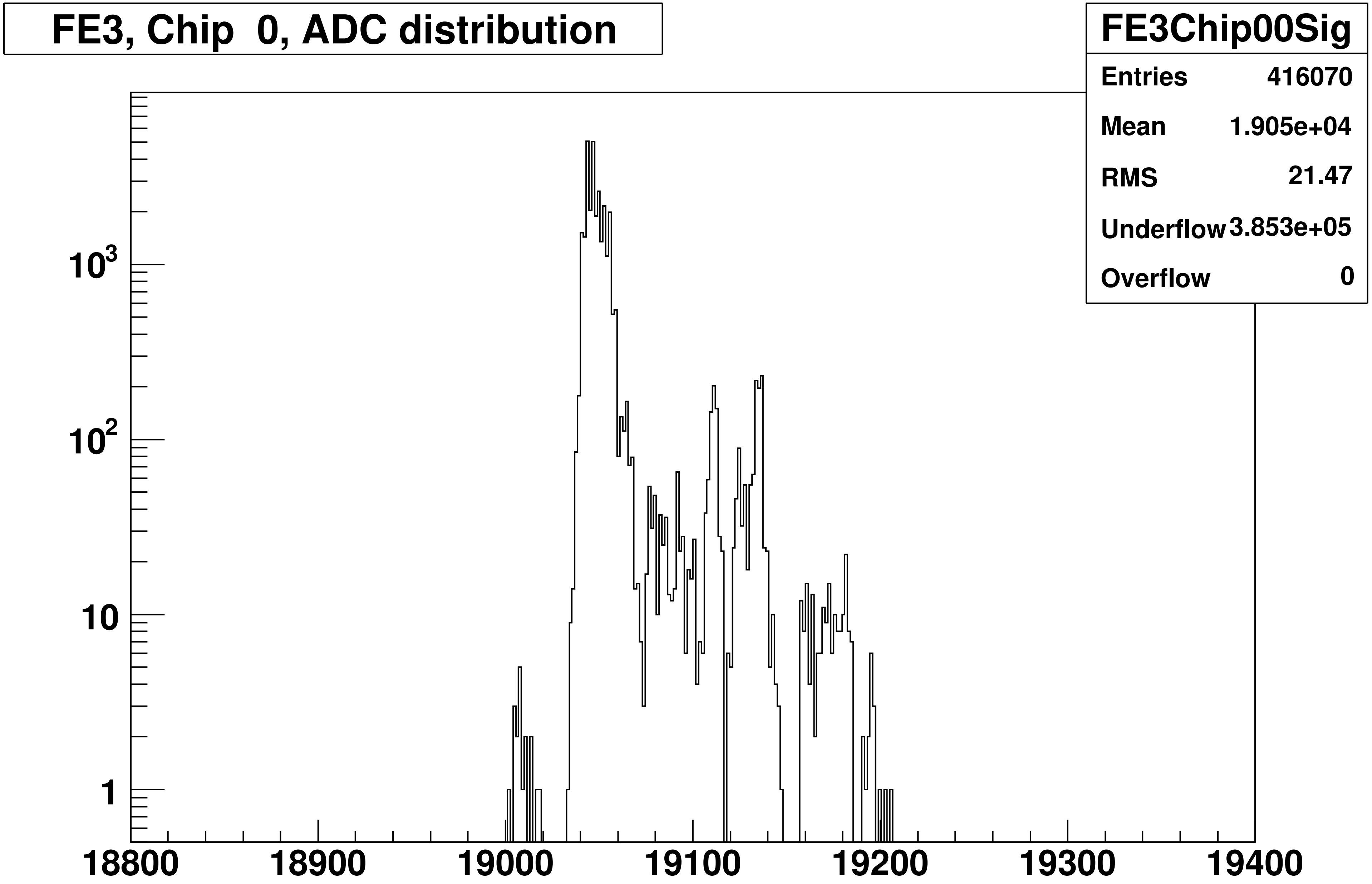
80
100



FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 416070
Mean 1.905e+04
RMS 21.47
Underflow 3.853e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
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19200
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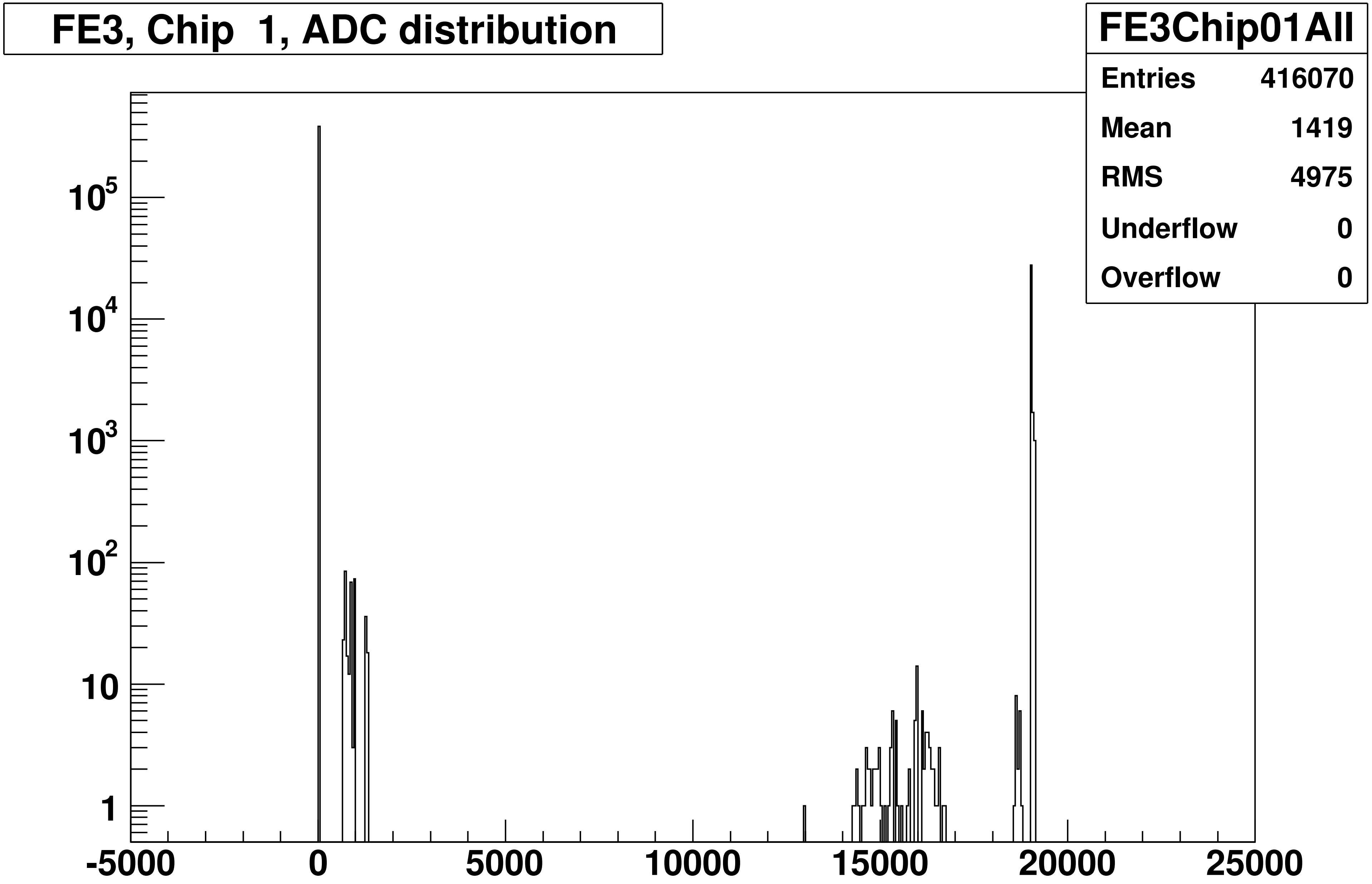
19400


FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 416070
Mean 1419
RMS 4975
Underflow 0
Overflow 0
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10^4
10^3
10^2
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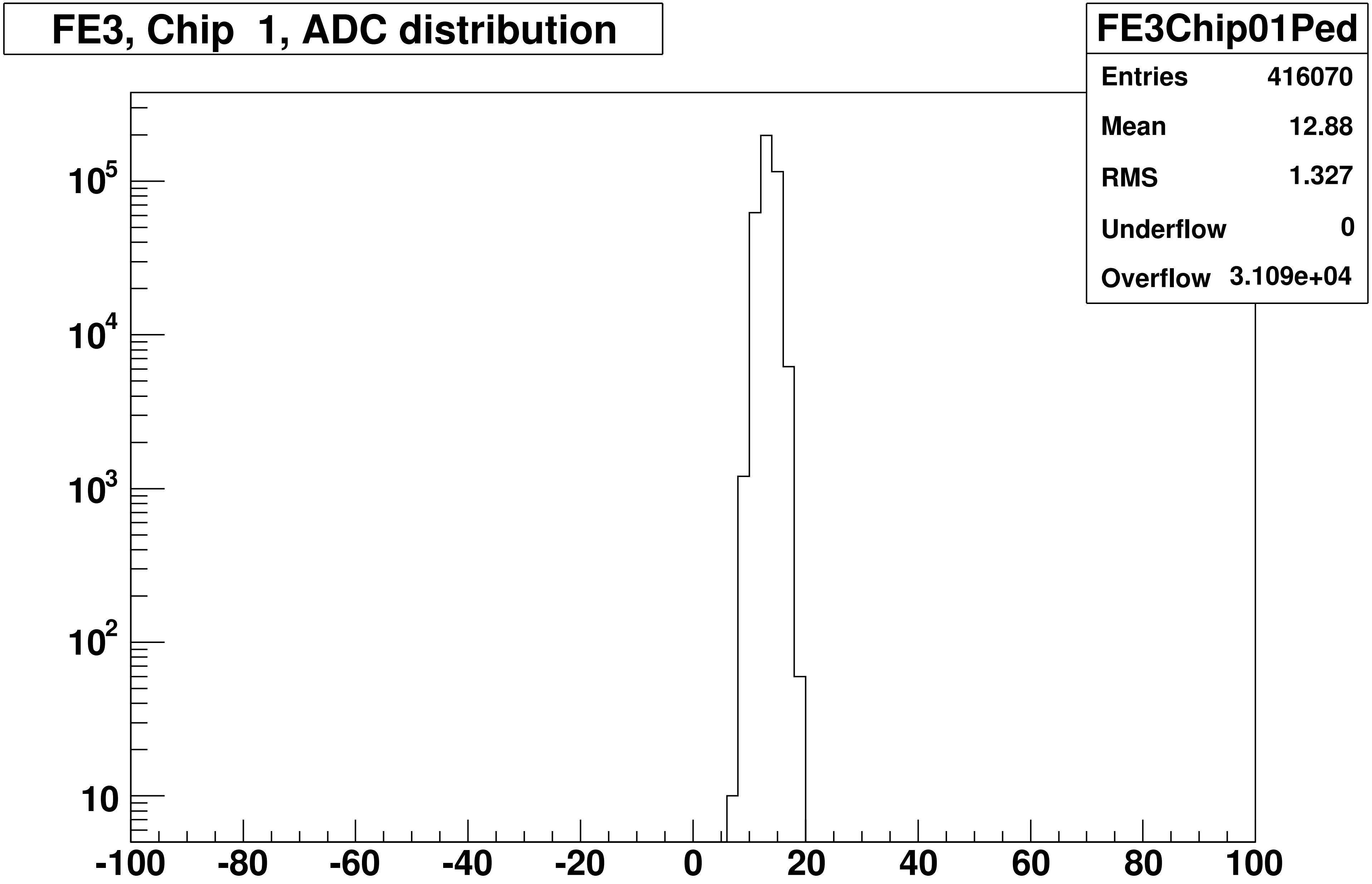
20000
25000



FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 416070
Mean 12.88
RMS 1.327
Underflow 0
Overflow 3.109e+04
10
10^2
10^3
10^4
10^5
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-80
-60
-40
-20
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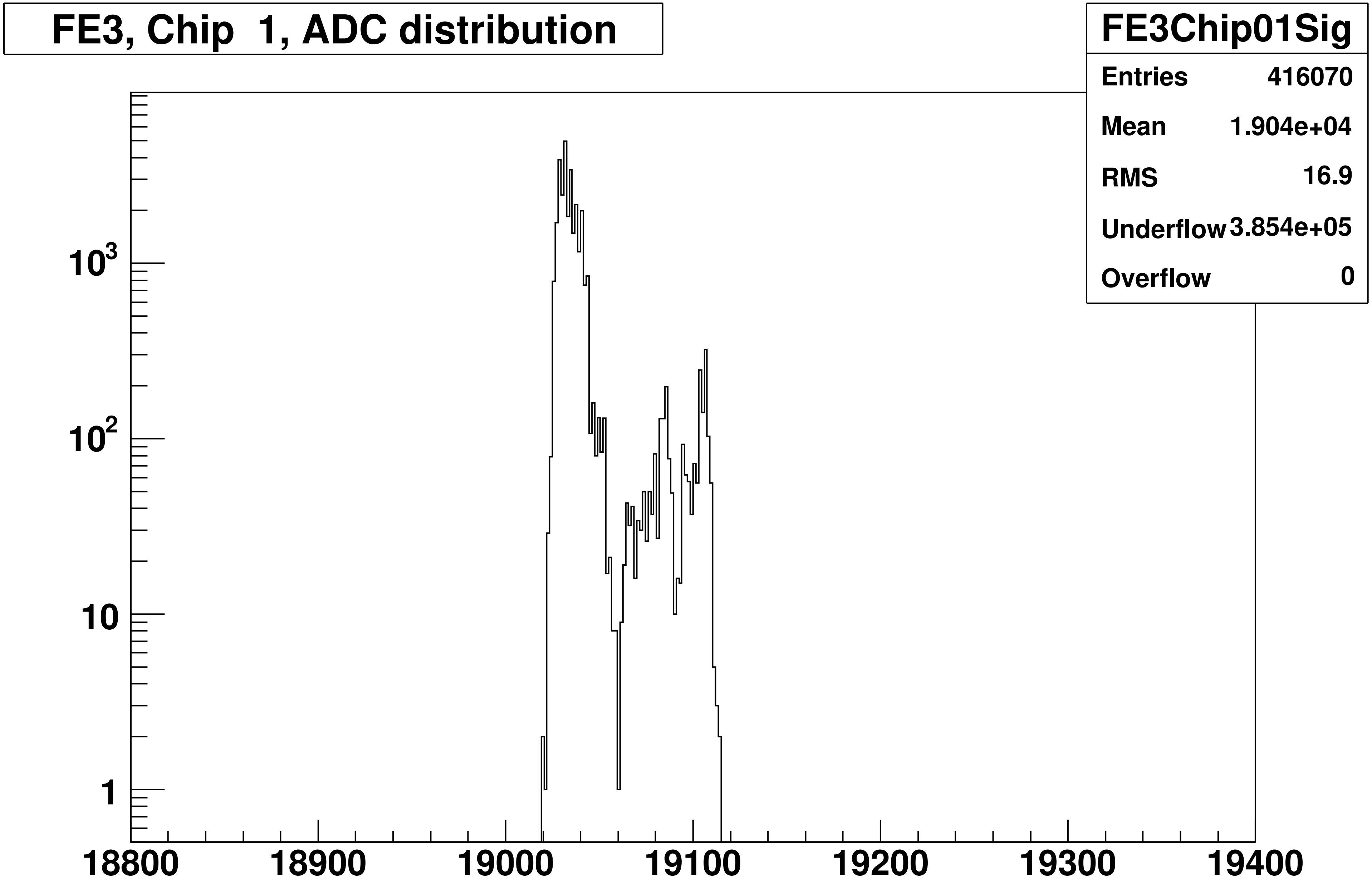
20
40
60
80
100


FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 416070
Mean 1.904e+04
RMS 16.9
Underflow 3.854e+05
Overflow 0
10^3
10^2
10
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18800
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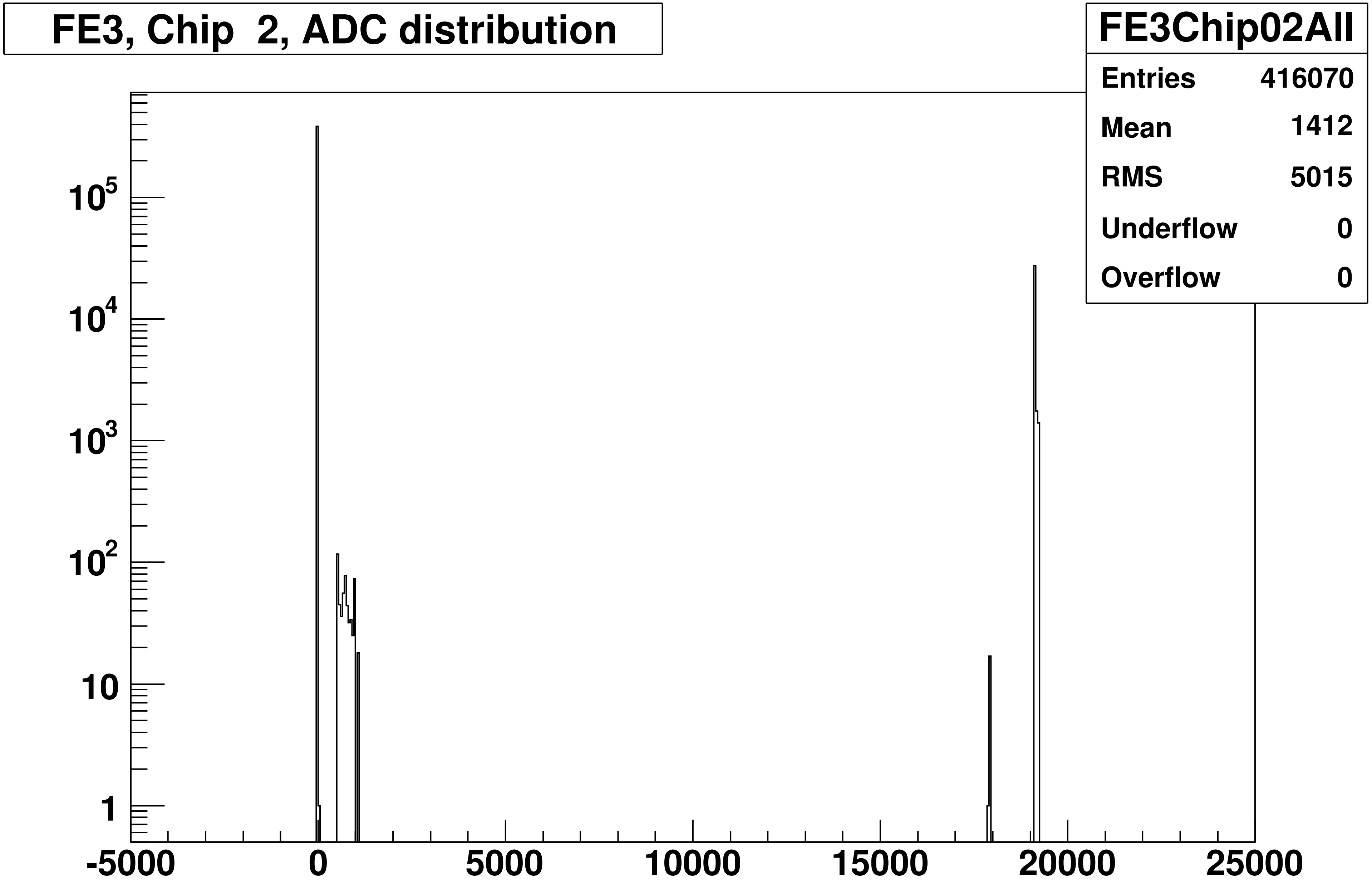
19400


FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 416070
Mean 1412
RMS 5015
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
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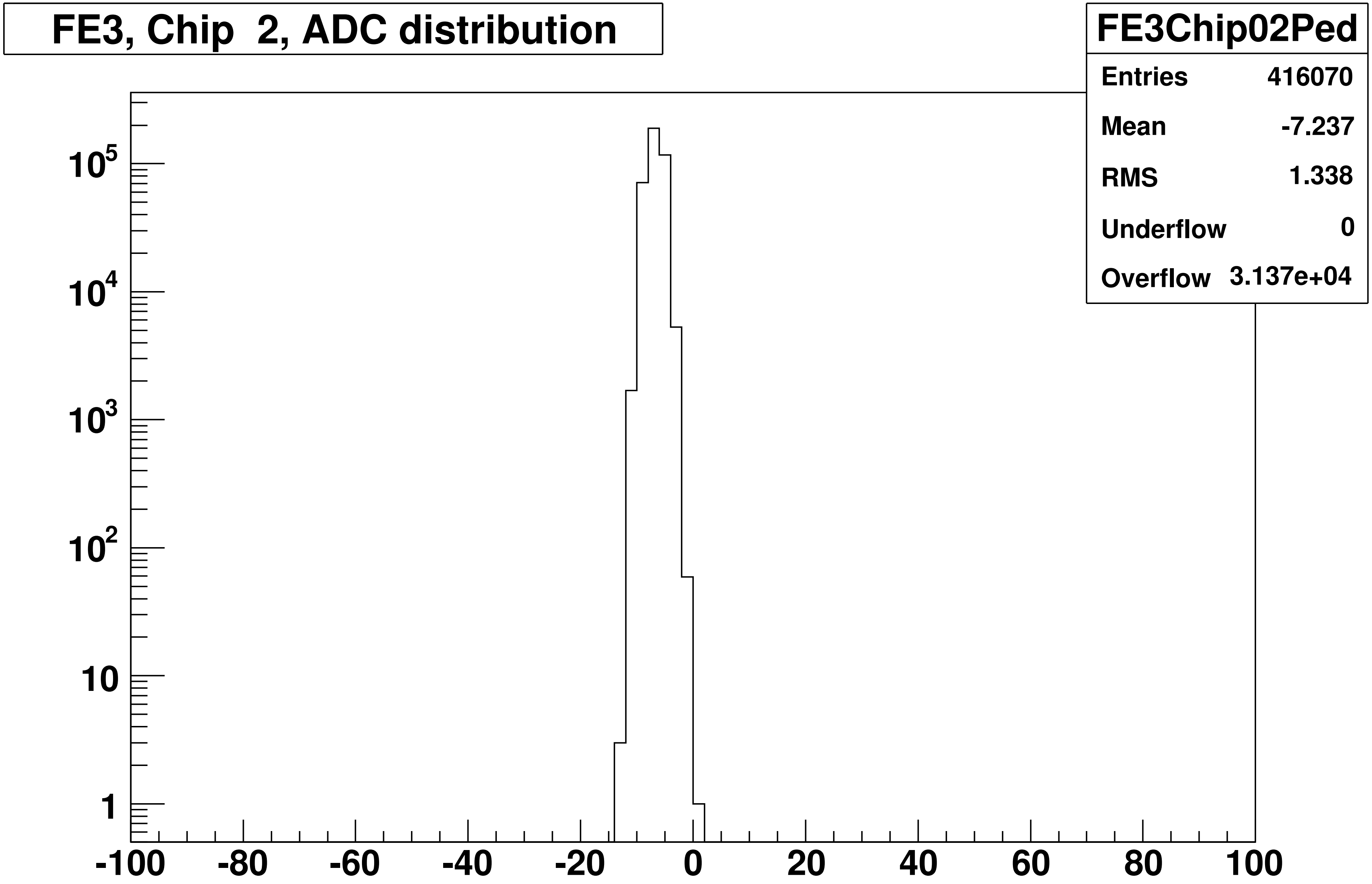
20000
25000



FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 416070
Mean -7.237
RMS 1.338
Underflow 0
Overflow 3.137e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
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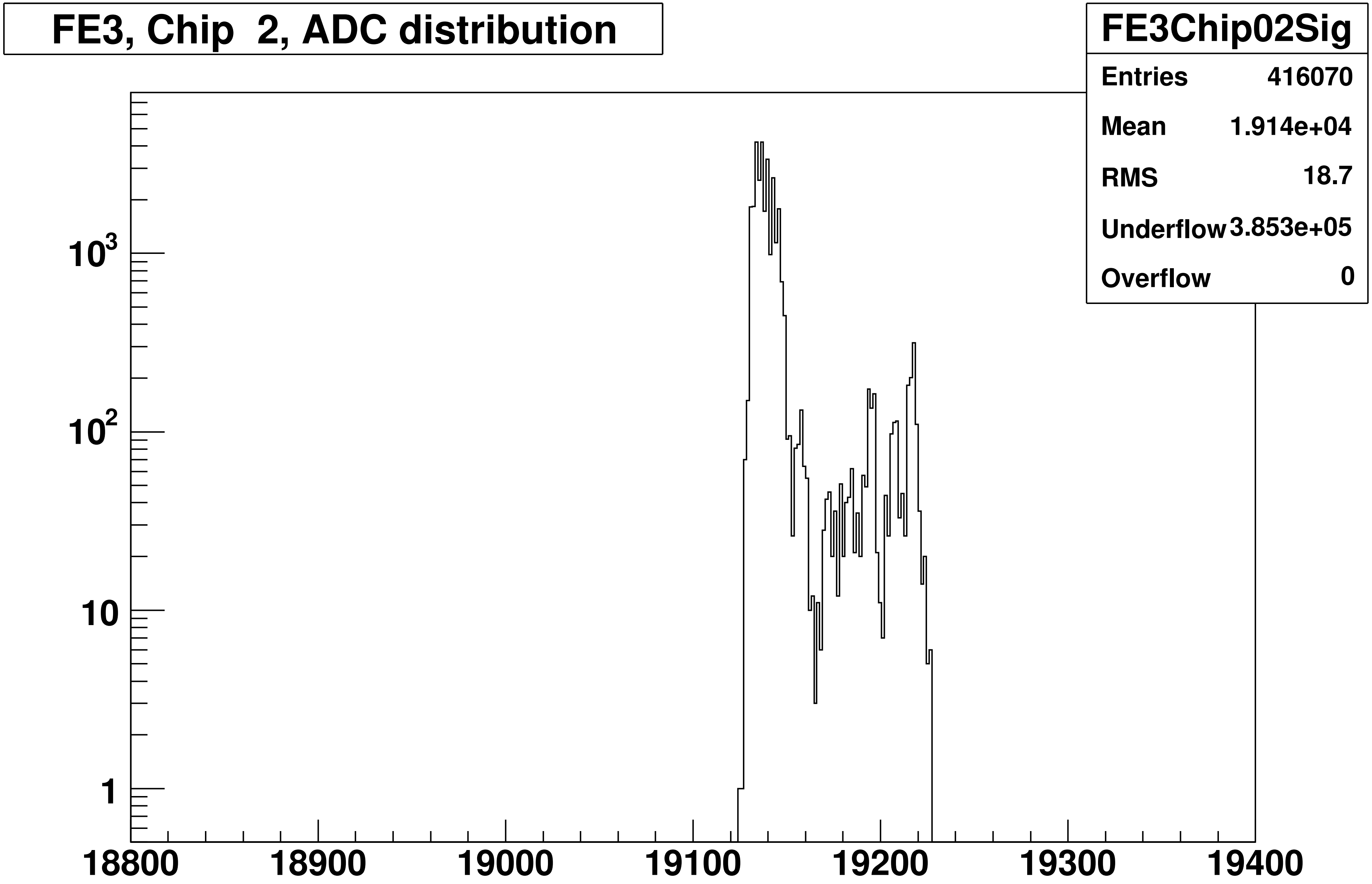
60
80
100


FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 416070
Mean 1.914e+04
RMS 18.7
Underflow 3.853e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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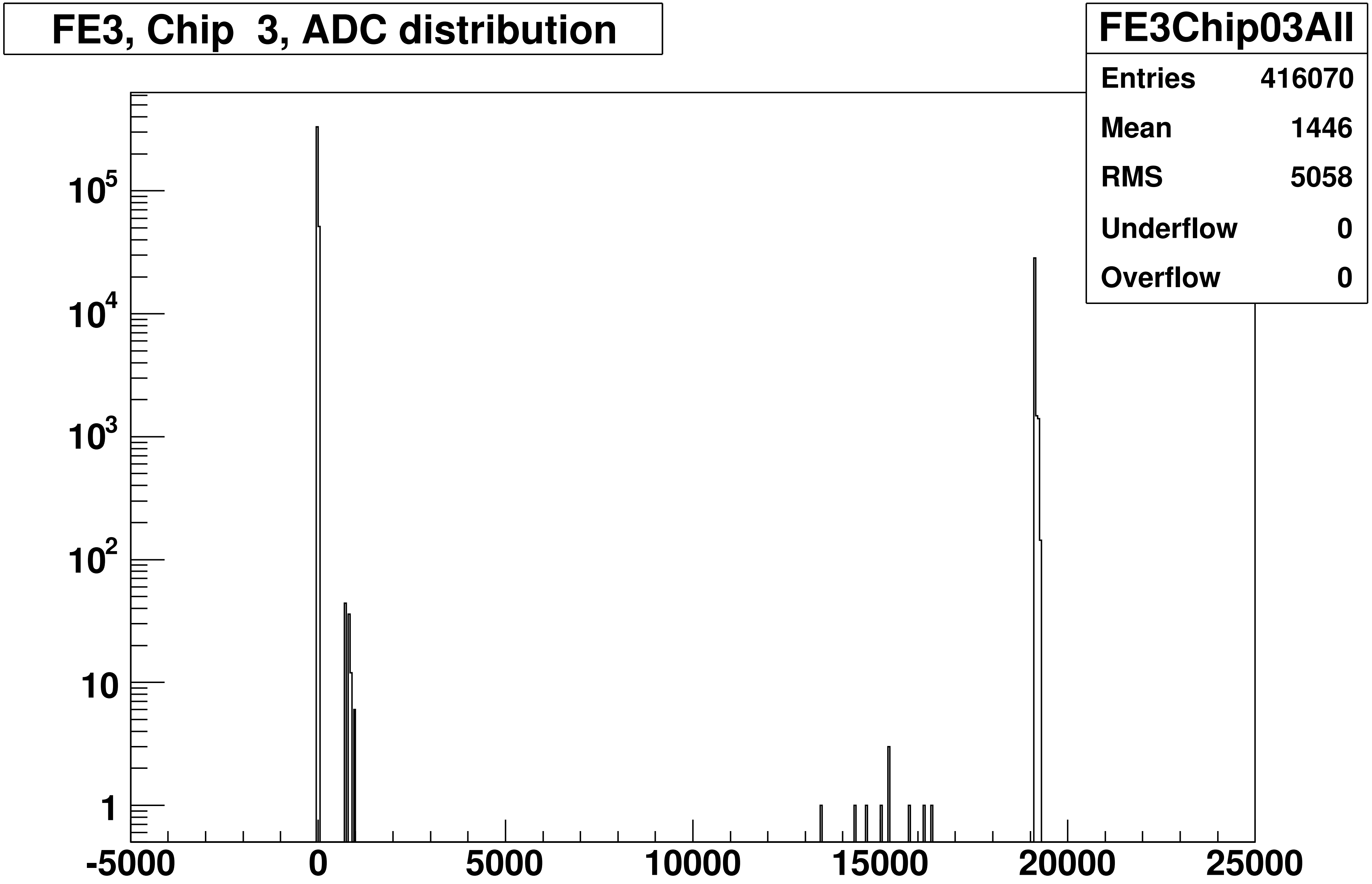
19200
19300
19400


FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 416070
Mean 1446
RMS 5058
Underflow 0
Overflow 0
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10^4
10^3
10^2
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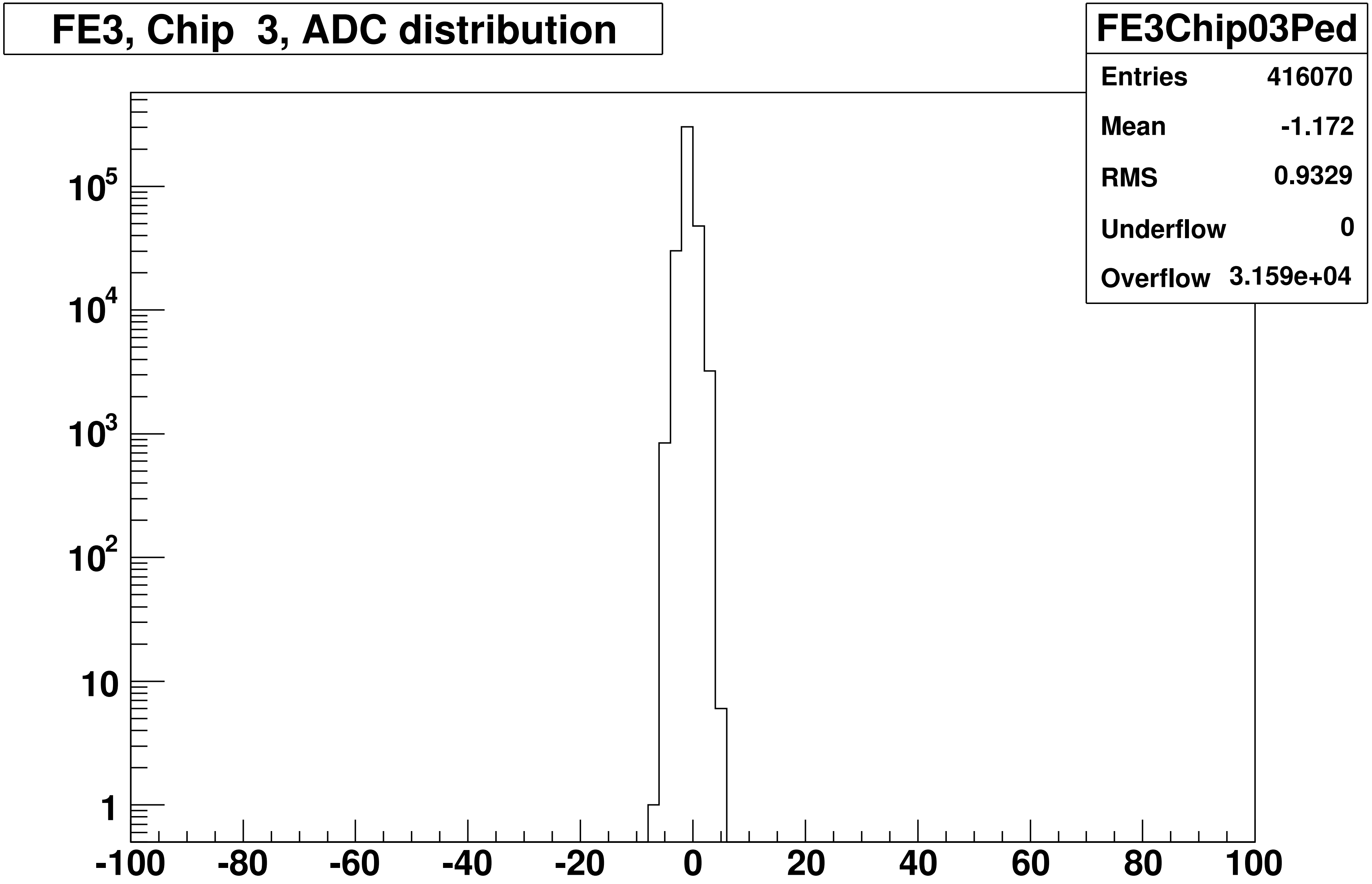
FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 416070
Mean -1.172
RMS 0.9329
Underflow 0
Overflow 3.159e+04
10^5
10^4
10^3
10^2
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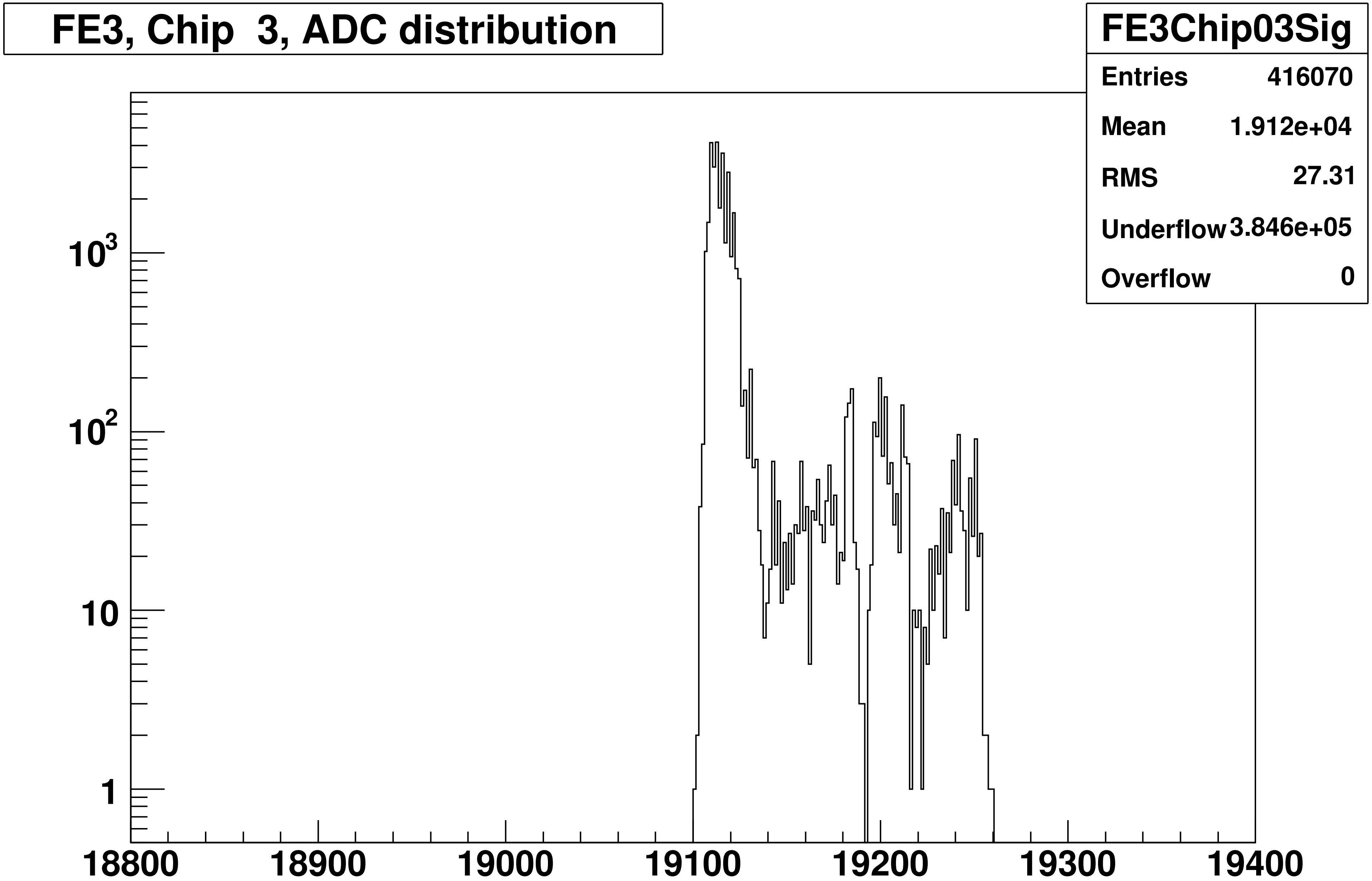
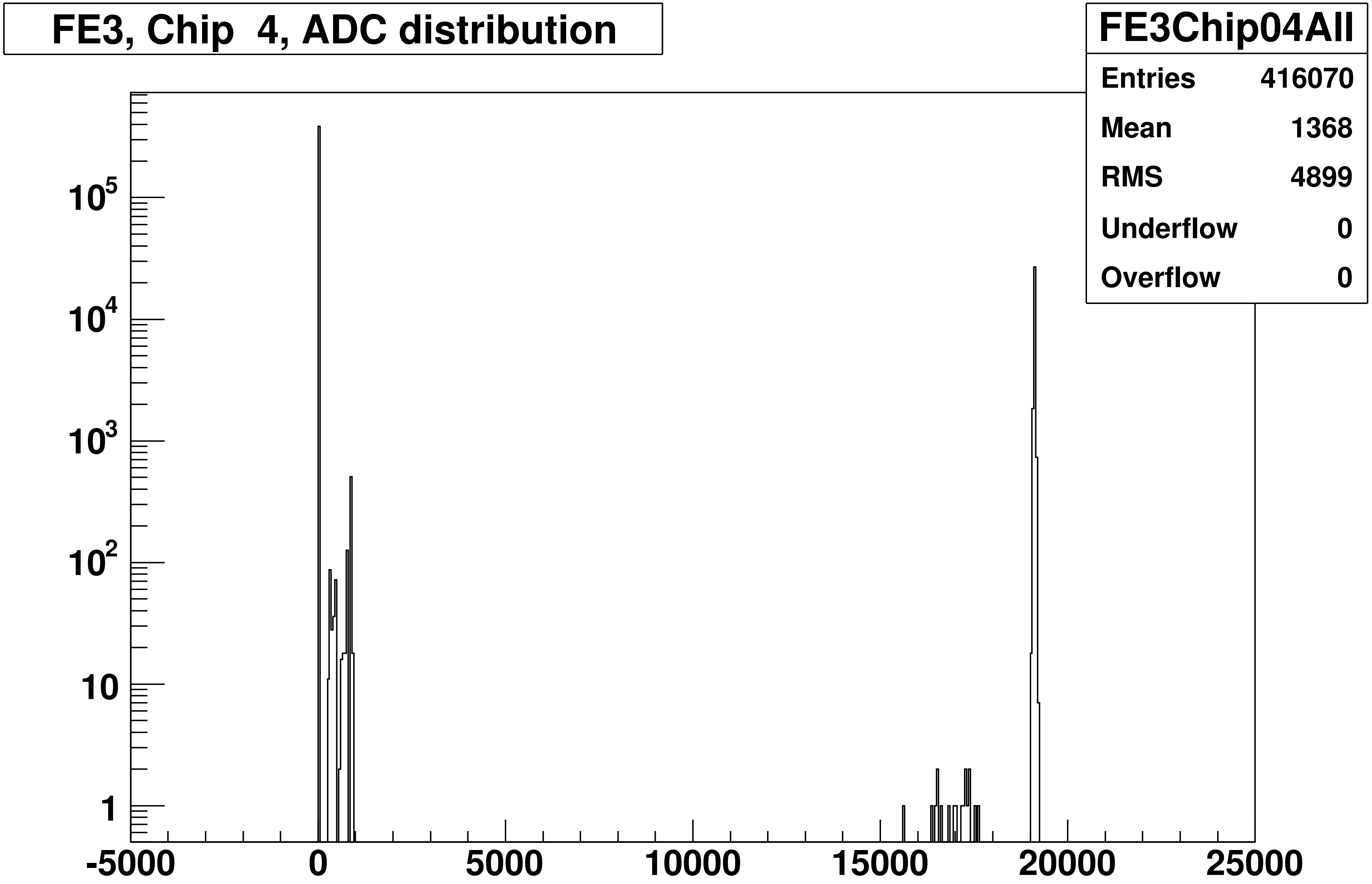
80
100


FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 416070
Mean 1.912e+04
RMS 27.31
Underflow 3.846e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 416070
Mean 1368
RMS 4899
Underflow 0
Overflow 0
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10^4
10^3
10^2
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-5000
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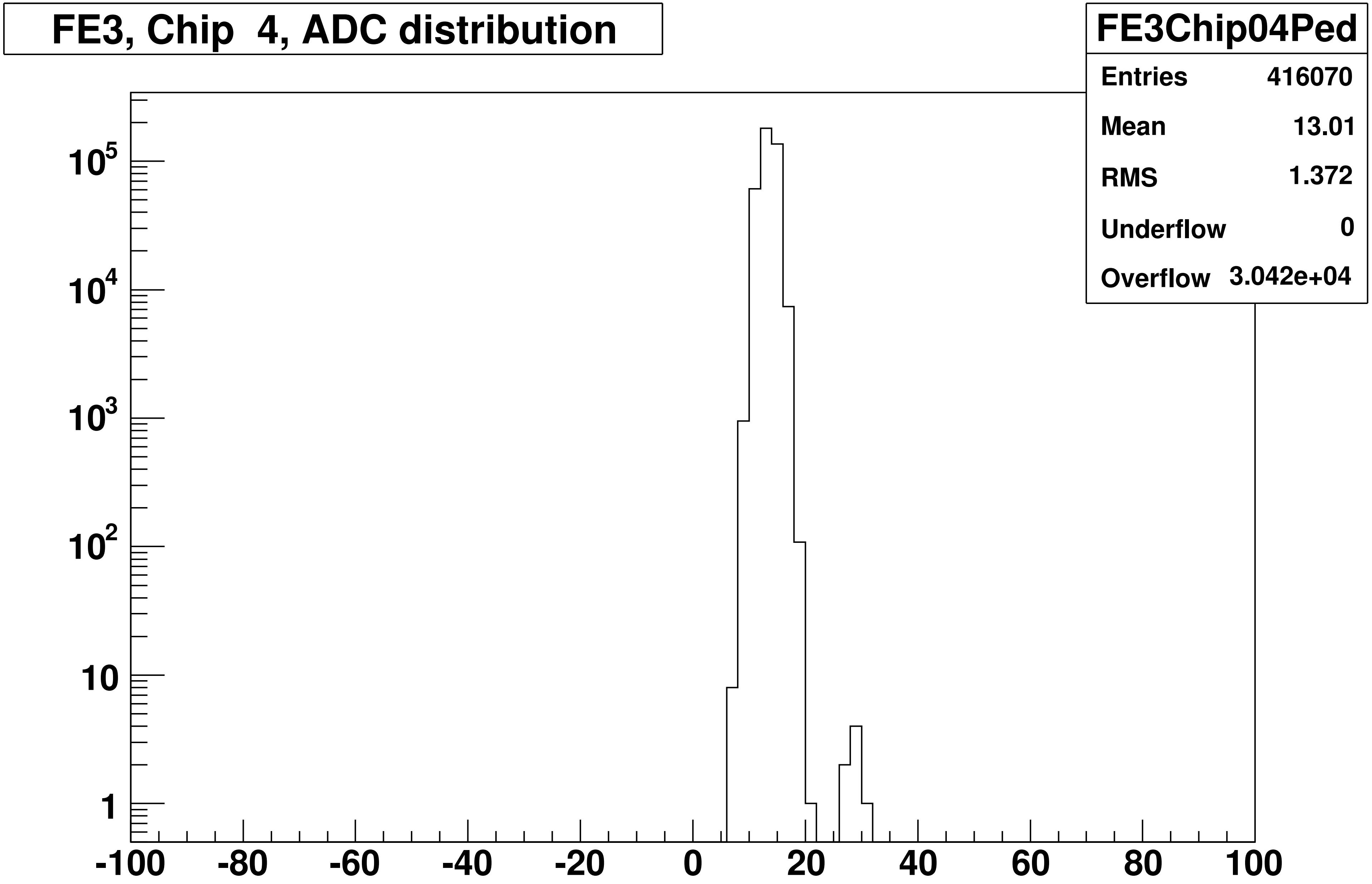
10000
15000
20000
25000



FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 416070
Mean 13.01
RMS 1.372
Underflow 0
Overflow 3.042e+04
10^5
10^4
10^3
10^2
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-20
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20
40
60
80
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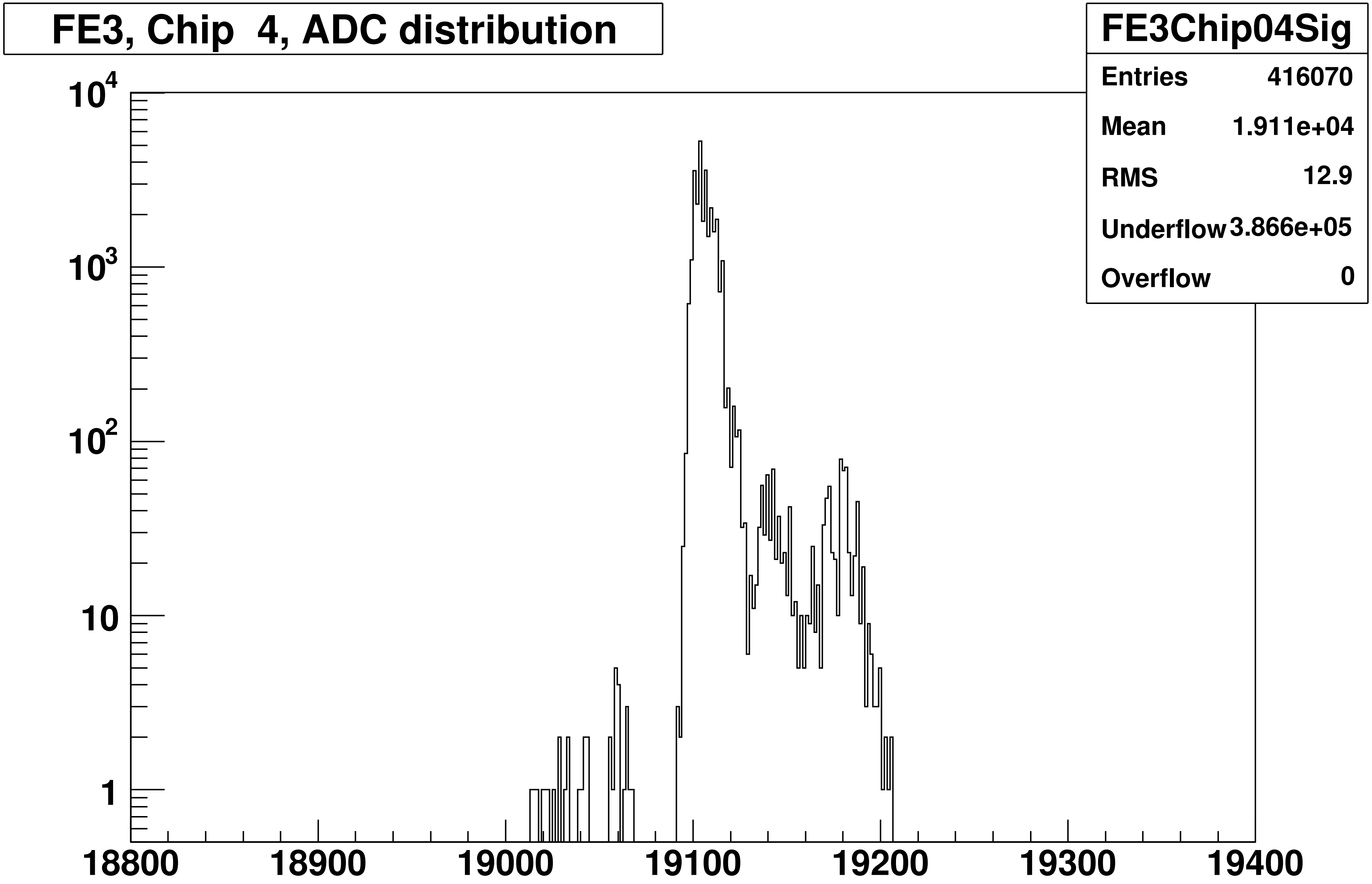
FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 416070
Mean 1.911e+04
RMS 12.9
Underflow 3.866e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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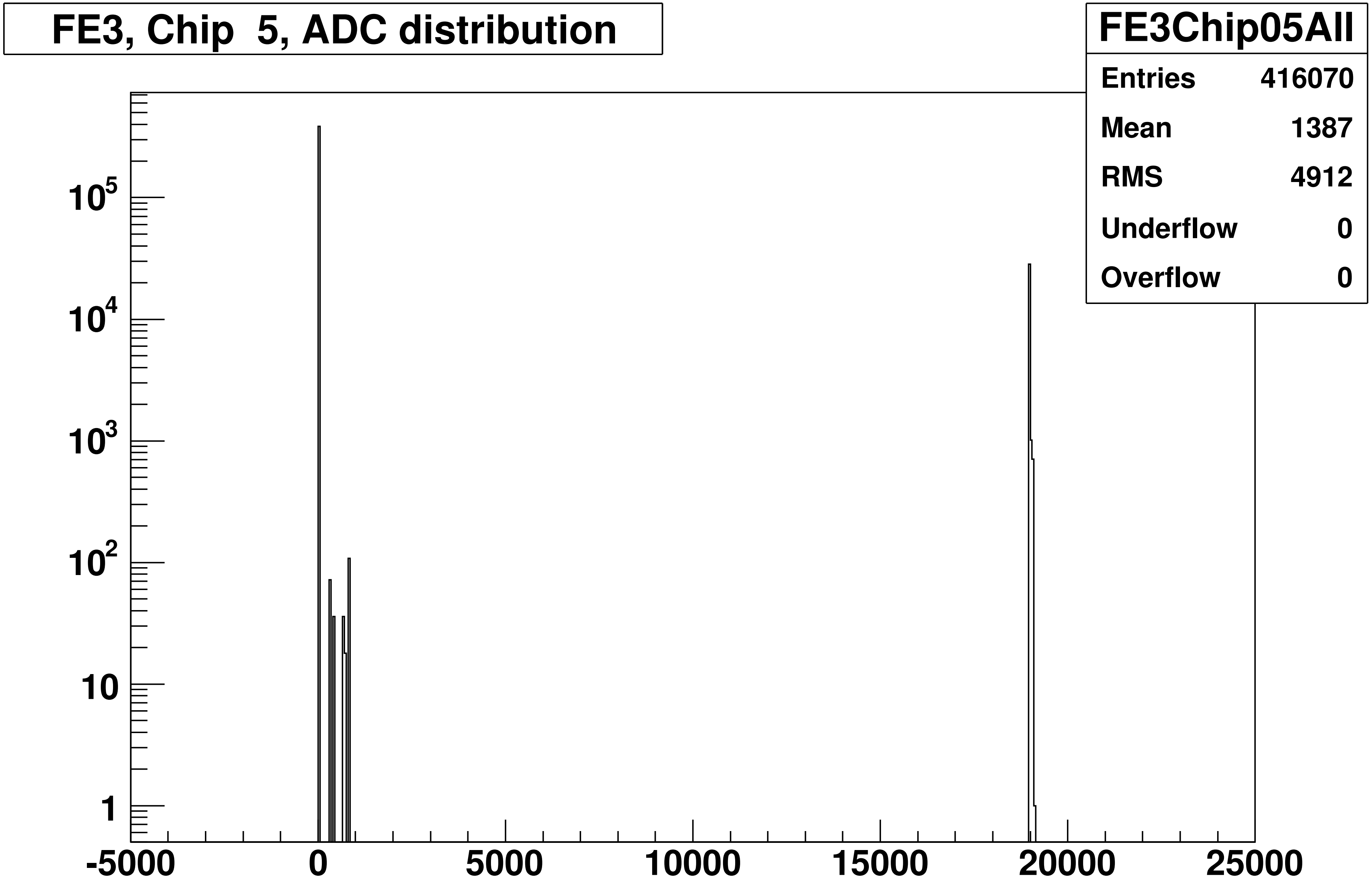
19400


FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 416070
Mean 1387
RMS 4912
Underflow 0
Overflow 0
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10^4
10^3
10^2
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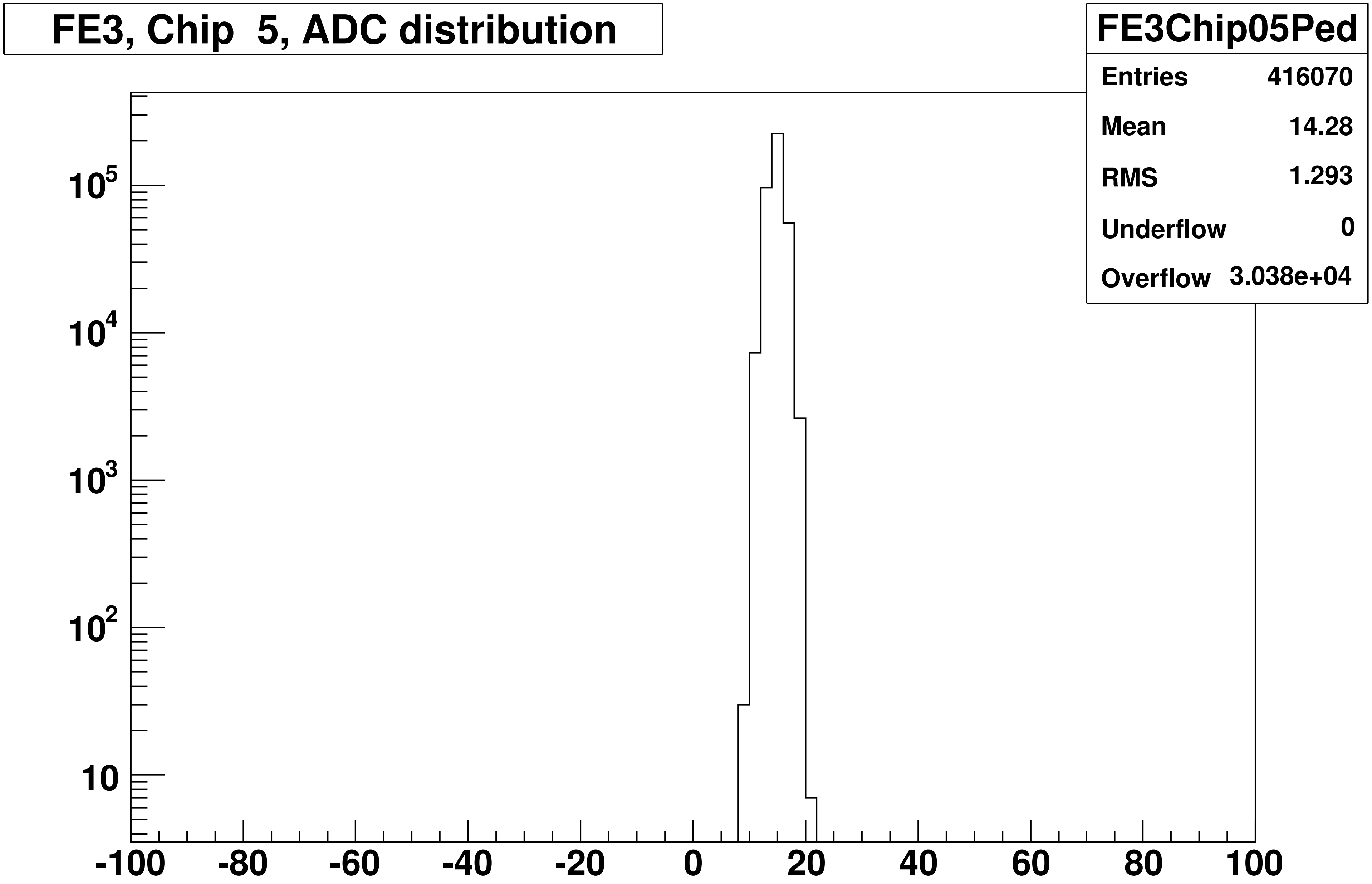
FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 416070
Mean 14.28
RMS 1.293
Underflow 0
Overflow 3.038e+04
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10^4
10^3
10^2
10
-100
-80
-60
-40
-20
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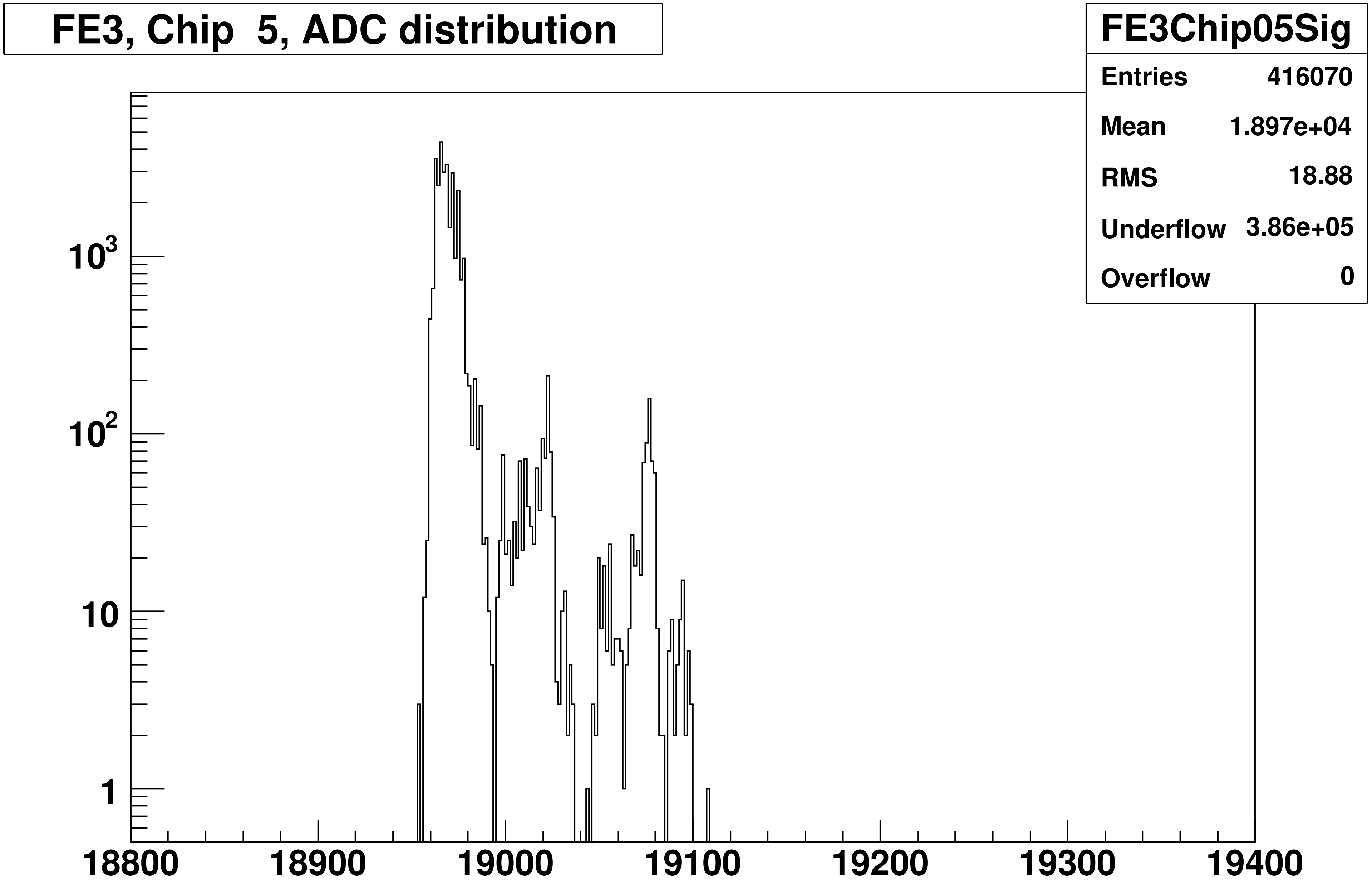
40
60
80
100


FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 416070
Mean 1.897e+04
RMS 18.88
Underflow 3.86e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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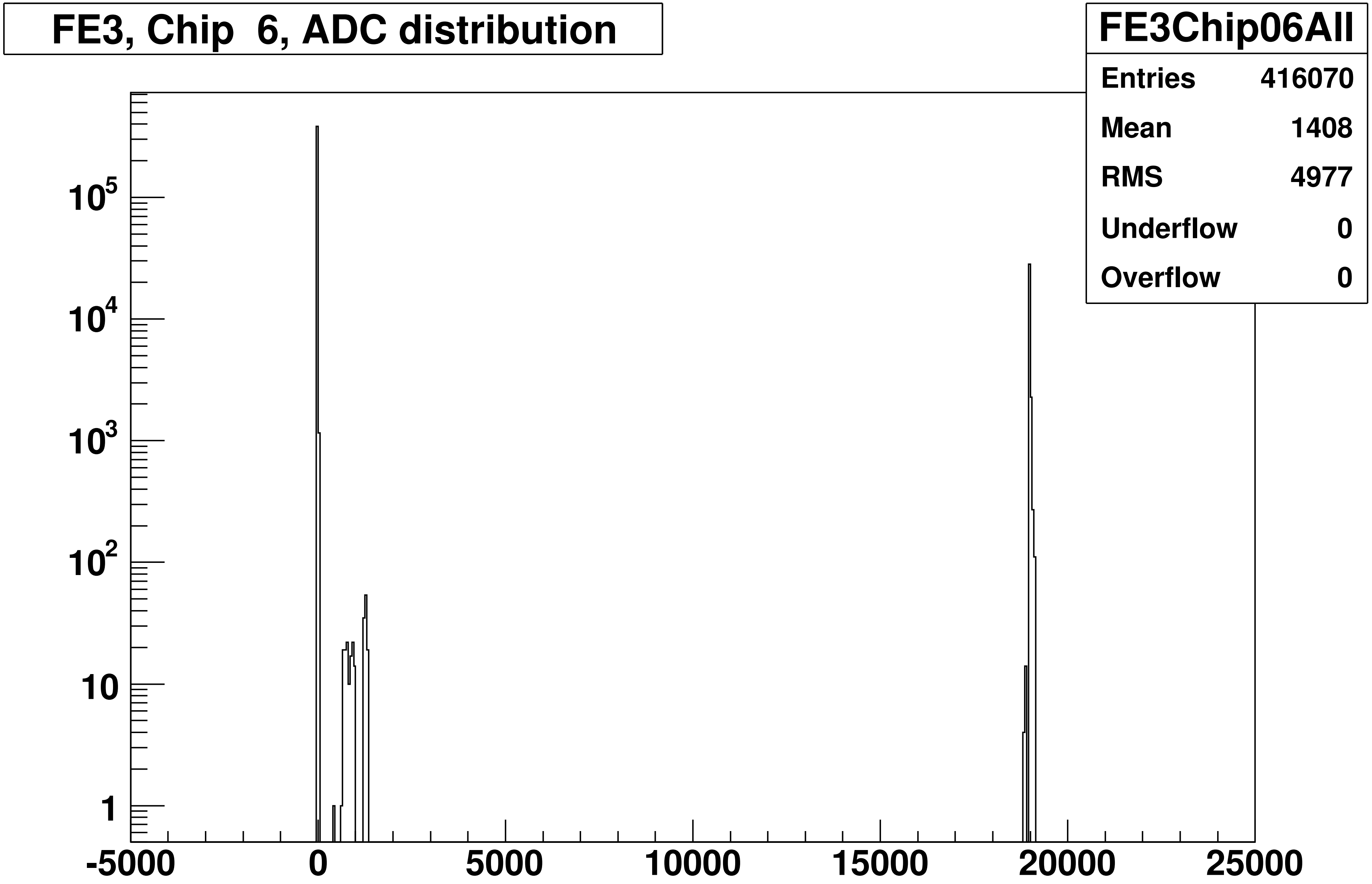
19400



FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 416070
Mean 1408
RMS 4977
Underflow 0
Overflow 0
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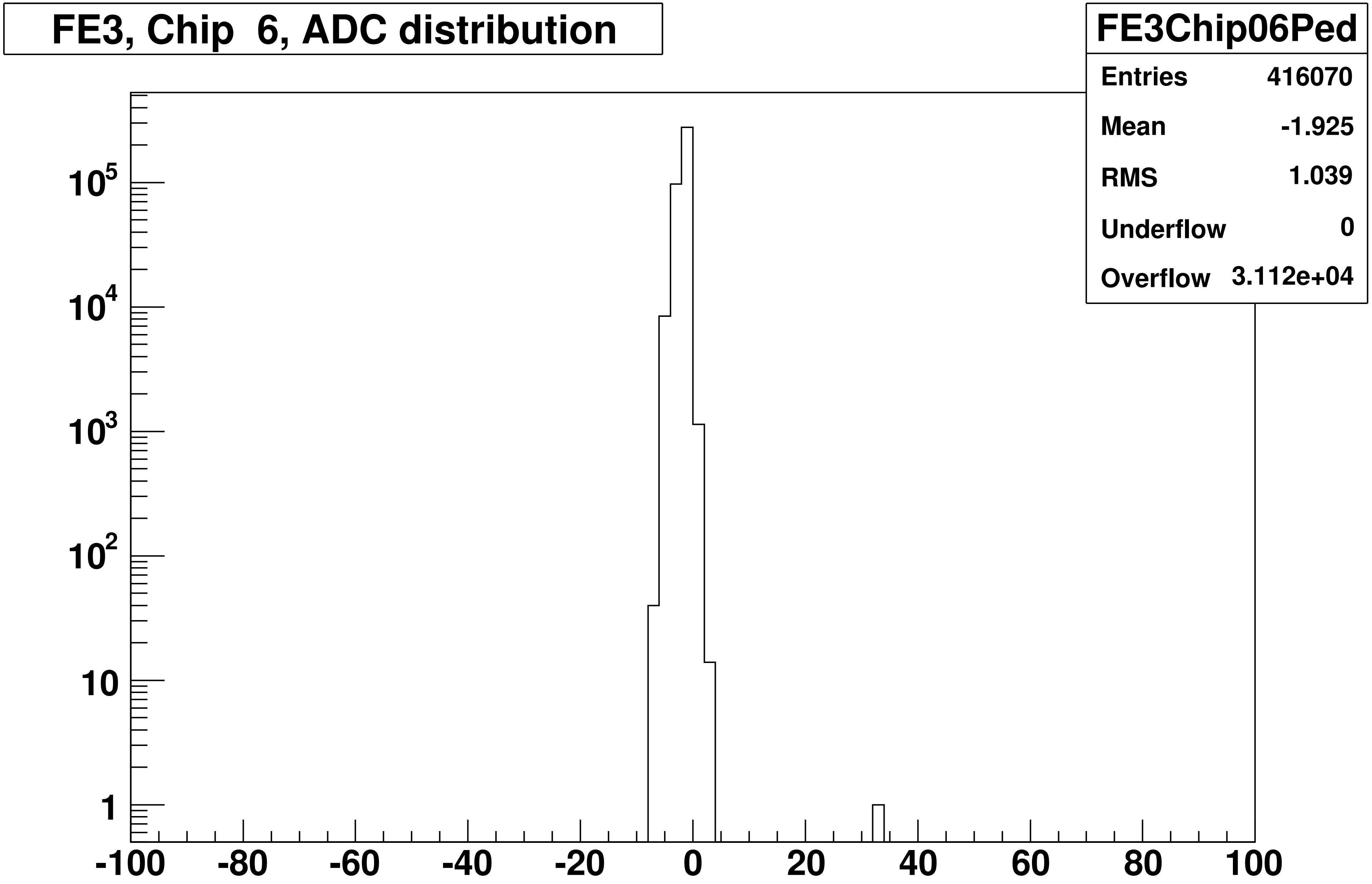
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 416070
Mean -1.925
RMS 1.039
Underflow 0
Overflow 3.112e+04
10^5
10^4
10^3
10^2
10
1
-100
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-20
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80
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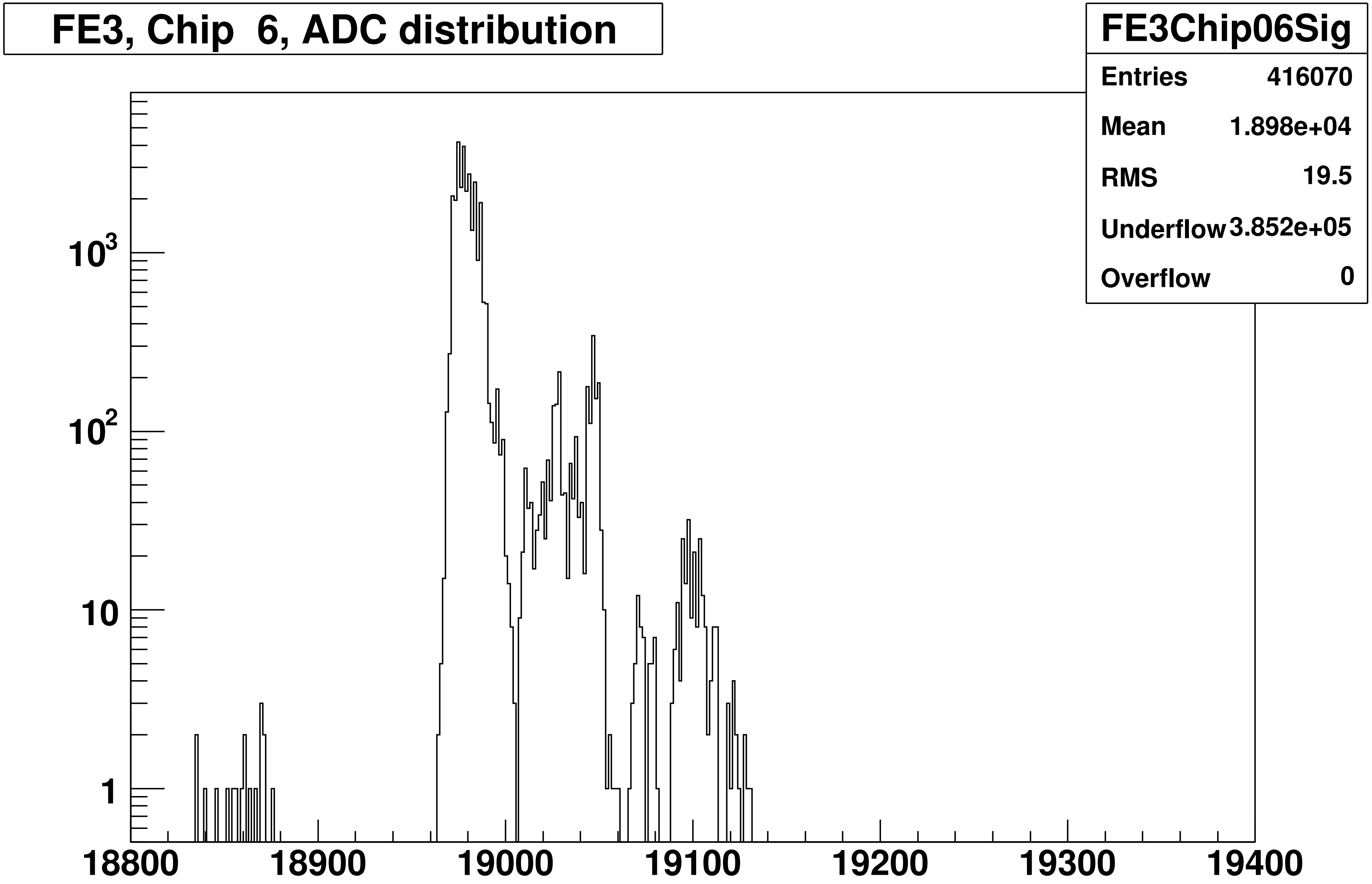
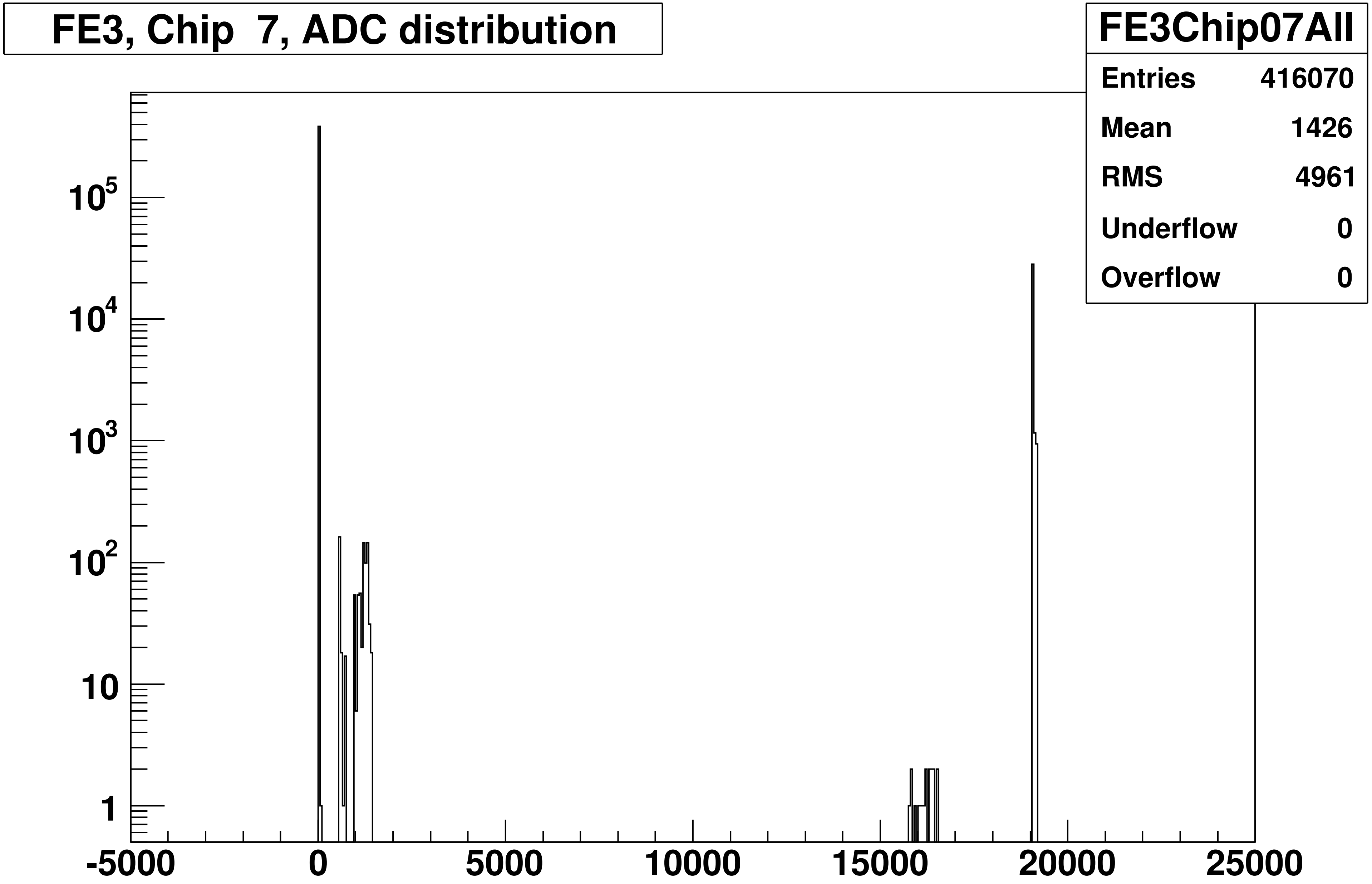


FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 416070
Mean 1.898e+04
RMS 19.5
Underflow 3.852e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 416070
Mean 1426
RMS 4961
Underflow 0
Overflow 0
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10^4
10^3
10^2
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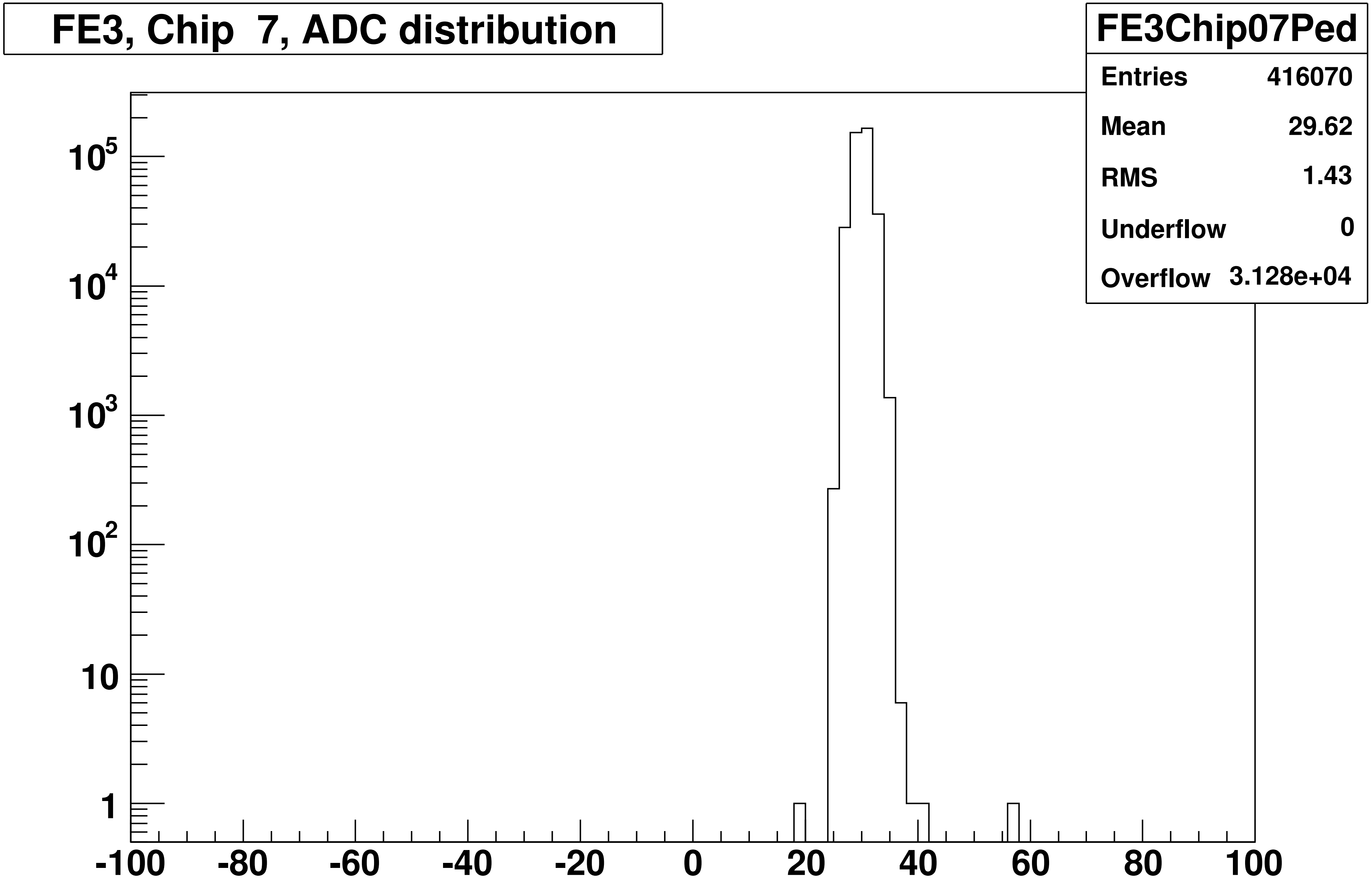
FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 416070
Mean 29.62
RMS 1.43
Underflow 0
Overflow 3.128e+04
10^5
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10^3
10^2
10
1
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-80
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-40
-20
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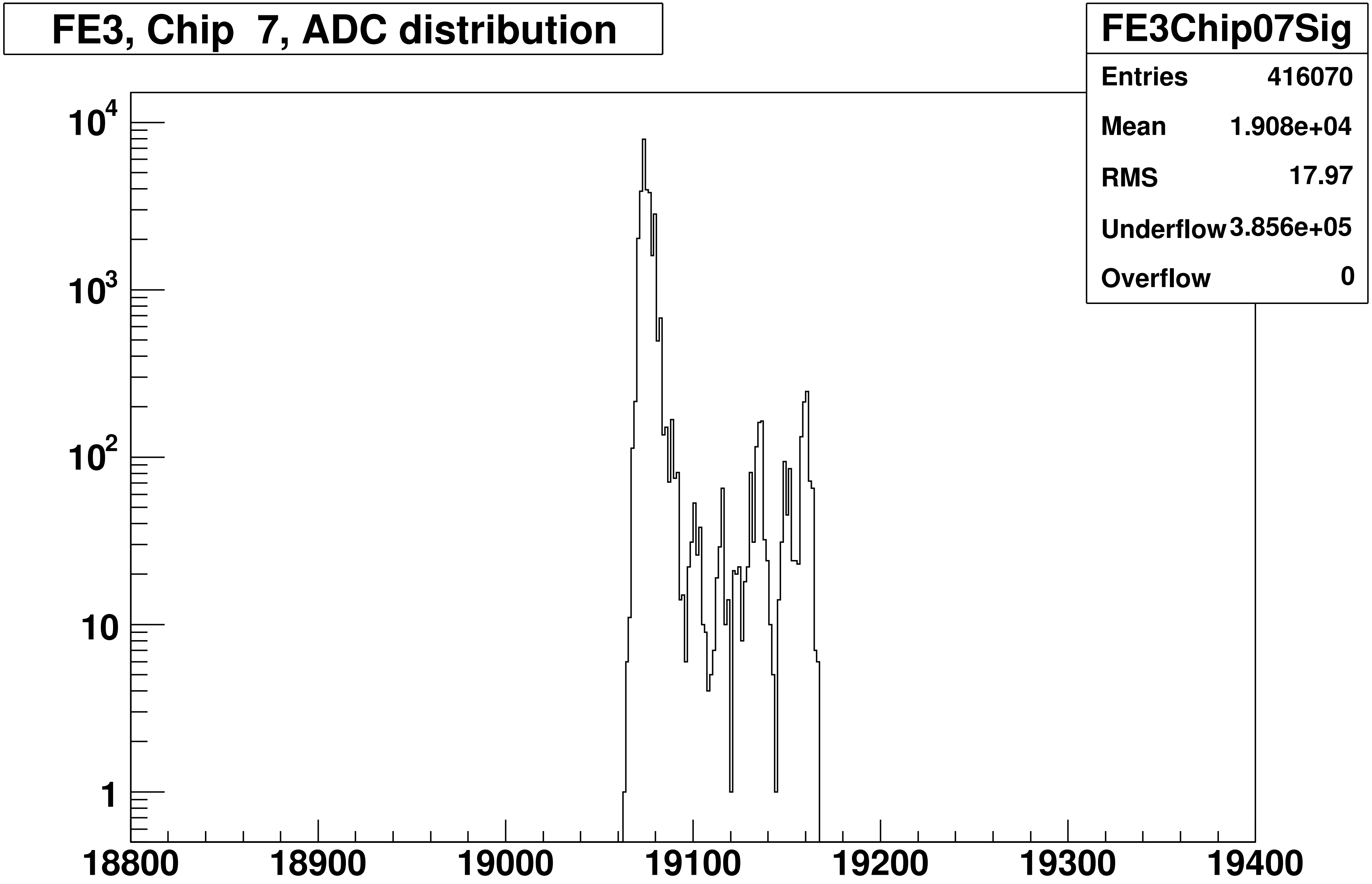
80
100


FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 416070
Mean 1.908e+04
RMS 17.97
Underflow 3.856e+05
Overflow 0
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10^3
10^2
10
1
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18900
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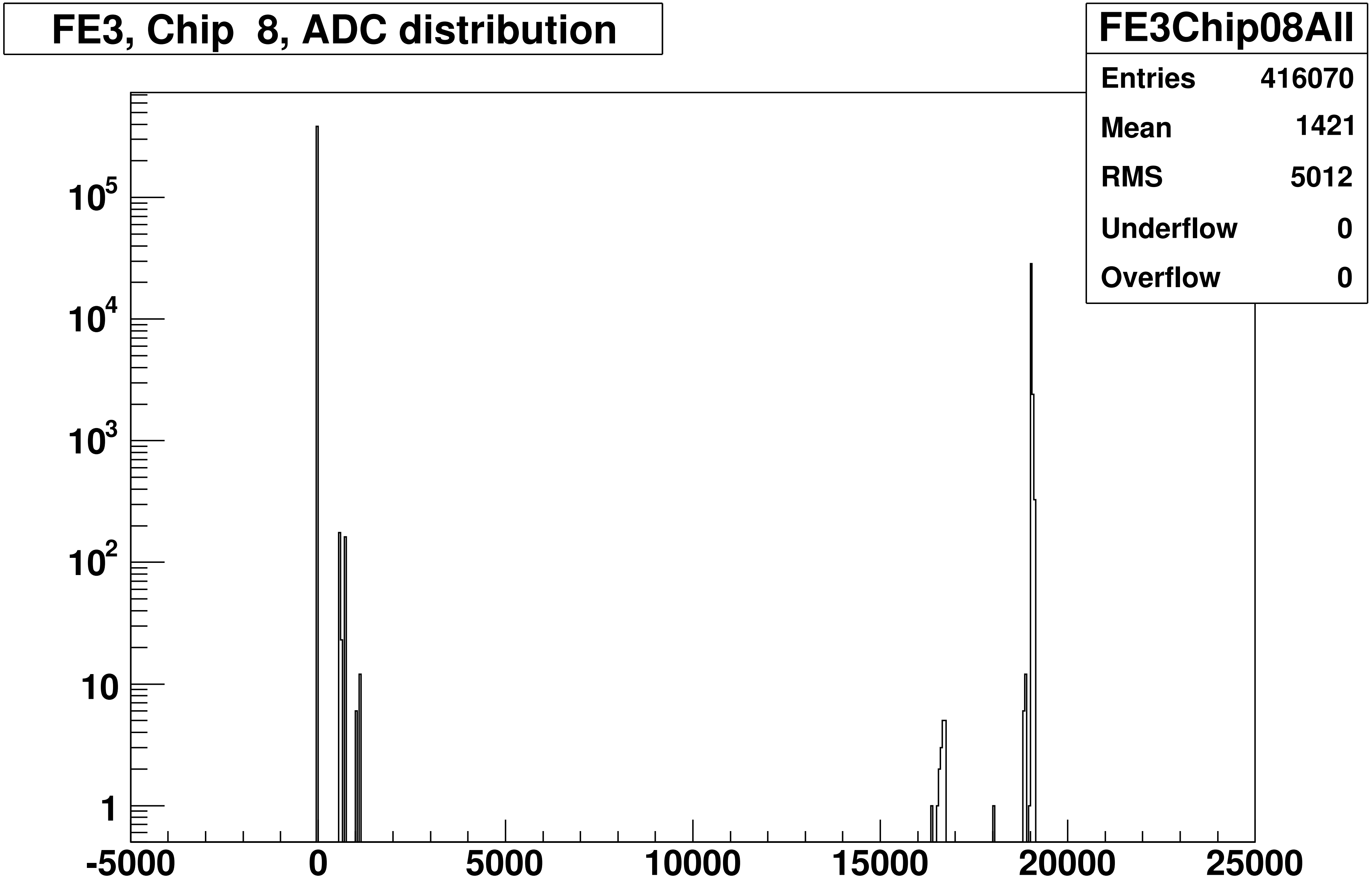
FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 416070
Mean 1421
RMS 5012
Underflow 0
Overflow 0
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10^4
10^3
10^2
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1
-5000
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10000
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20000
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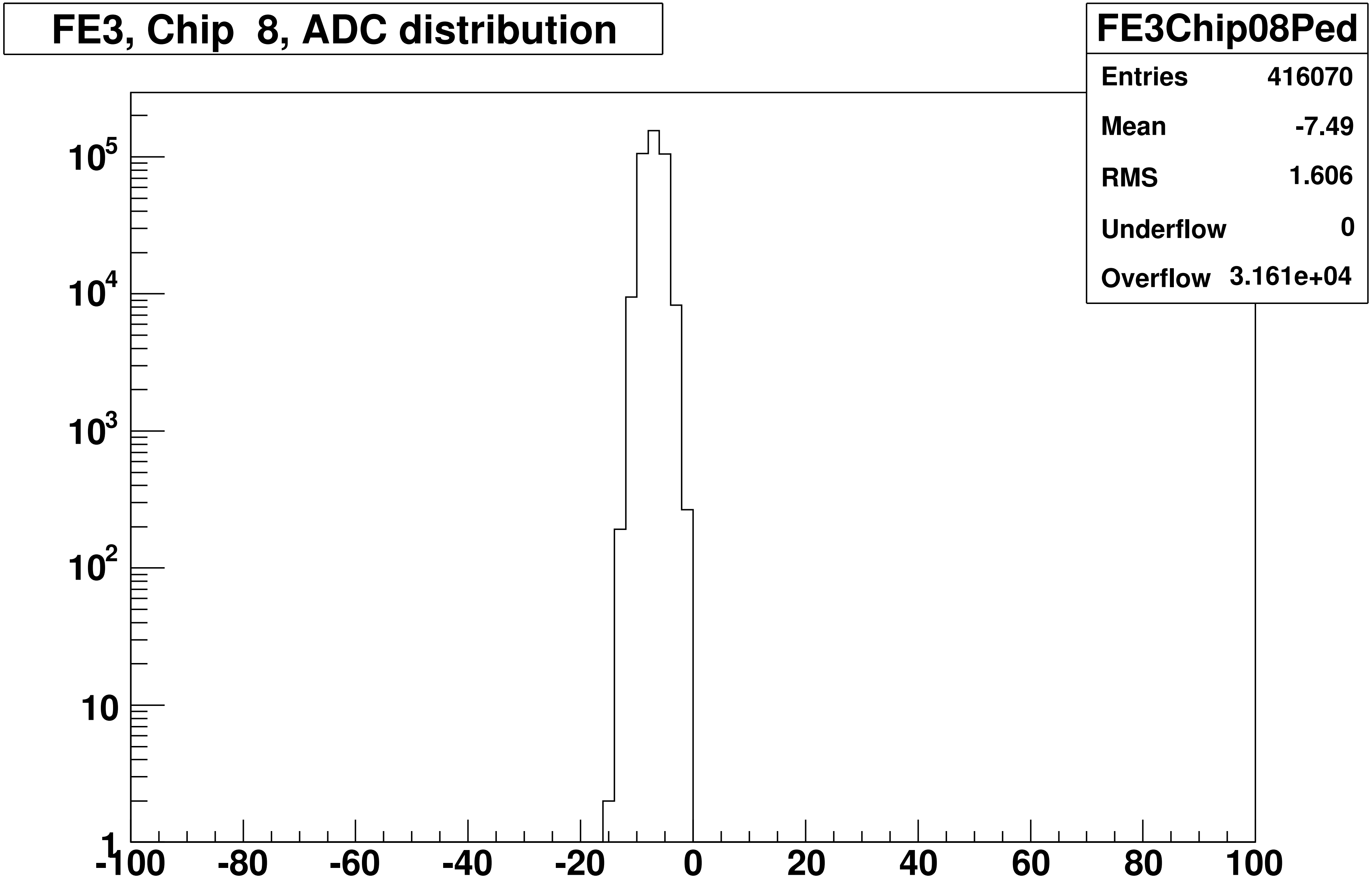
FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 416070
Mean -7.49
RMS 1.606
Underflow 0
Overflow 3.161e+04
10^5
10^4
10^3
10^2
10
1
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-20
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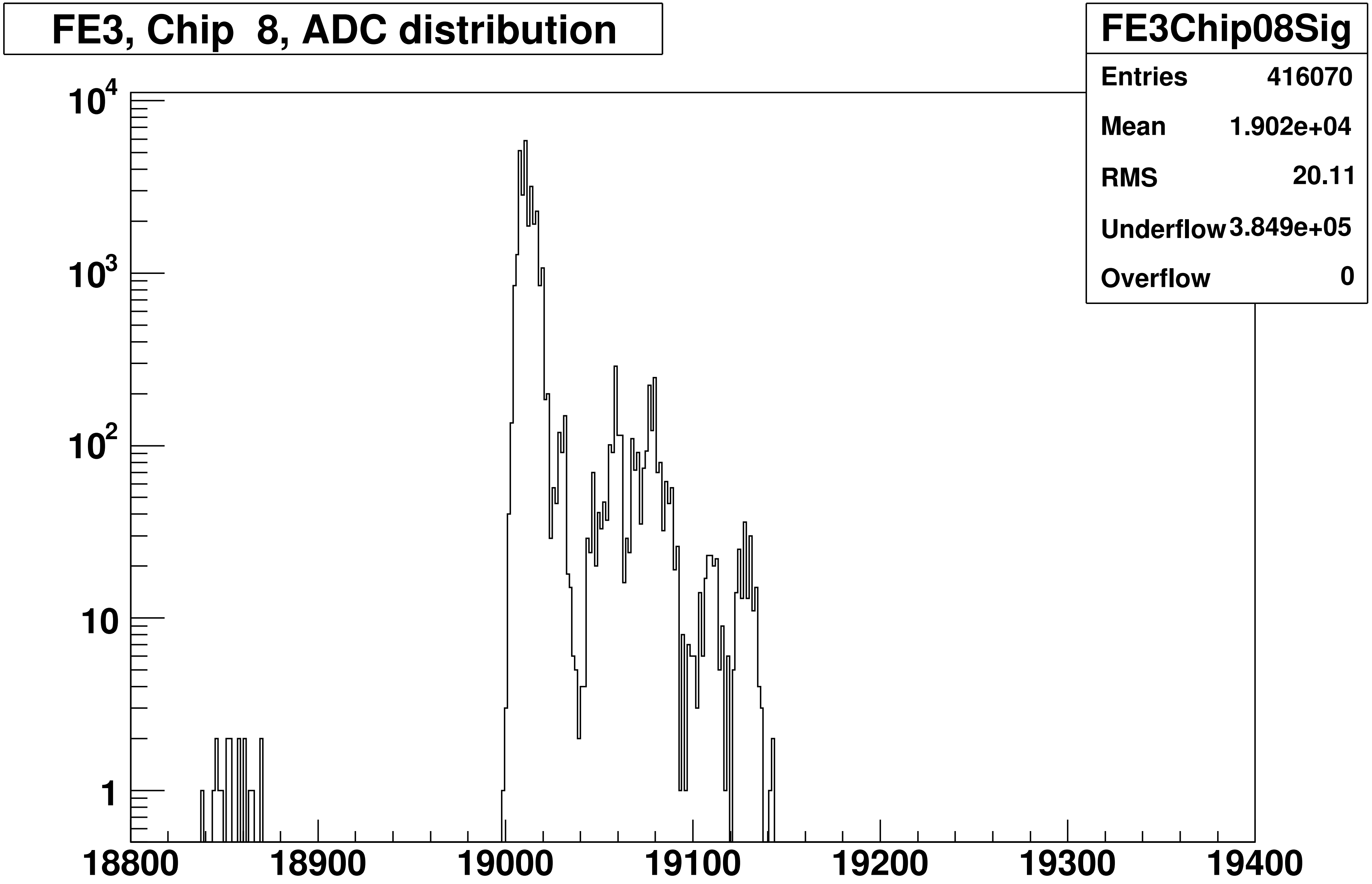
80
100


FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 416070
Mean 1.902e+04
RMS 20.11
Underflow 3.849e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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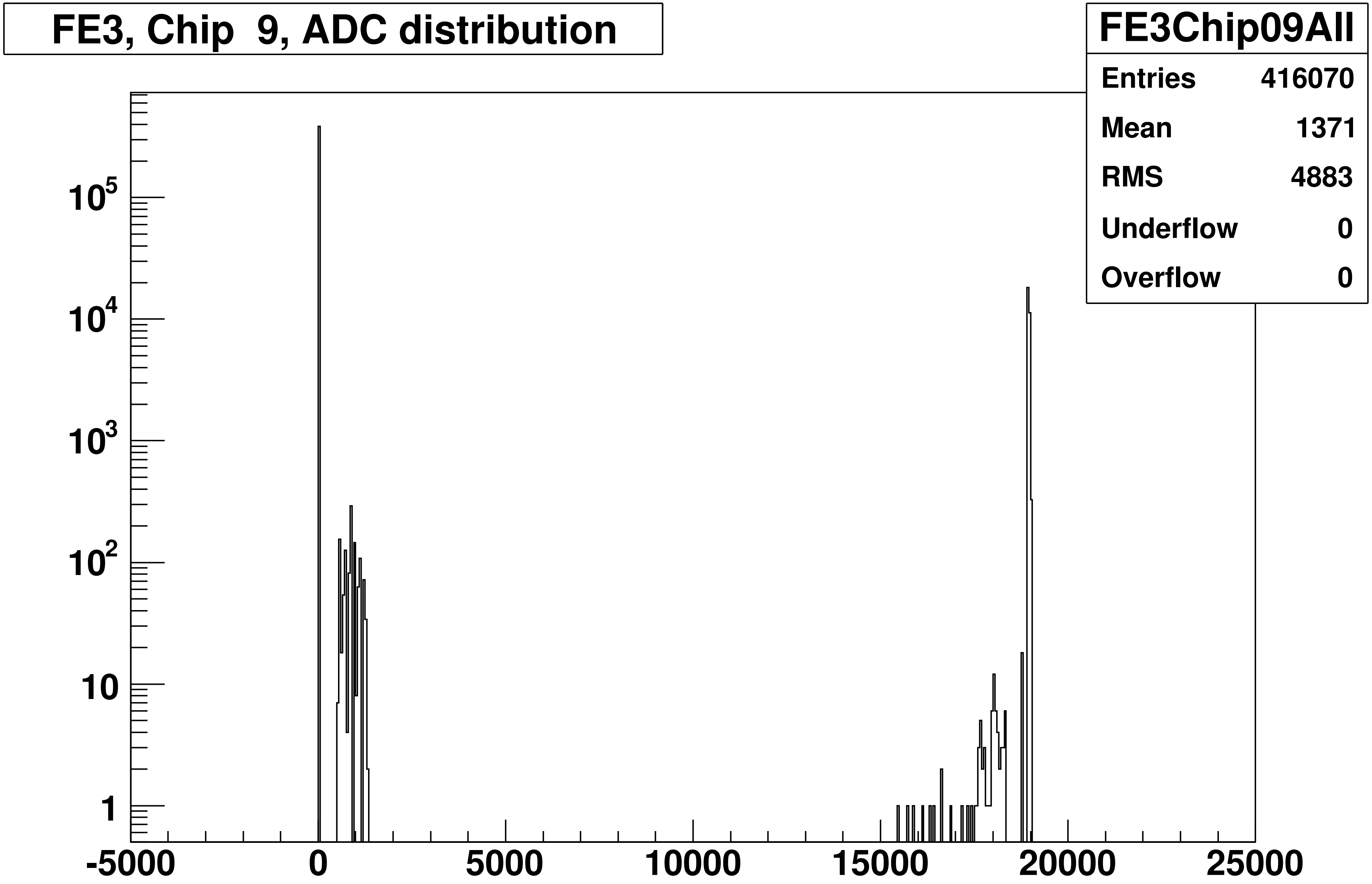
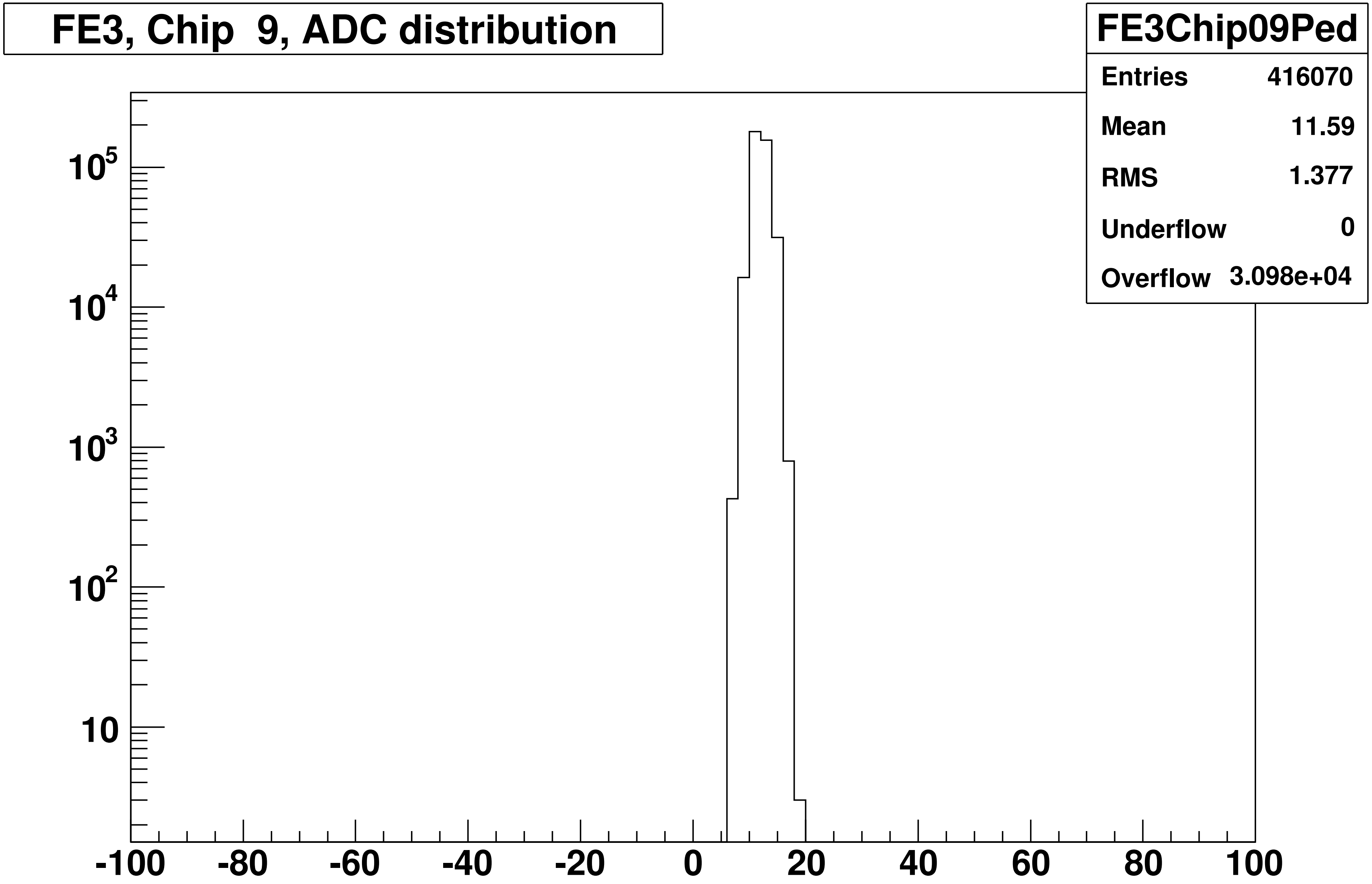
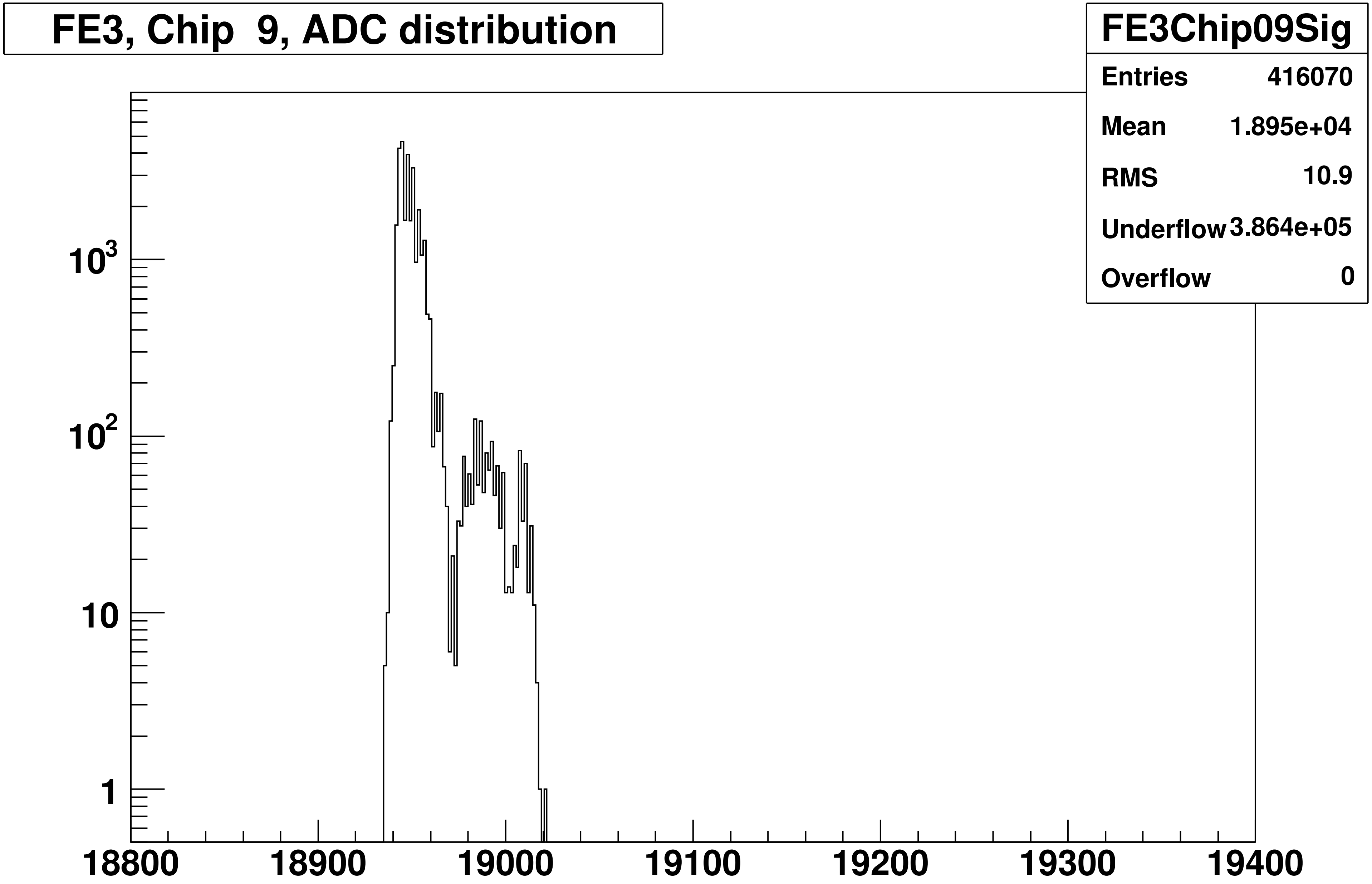
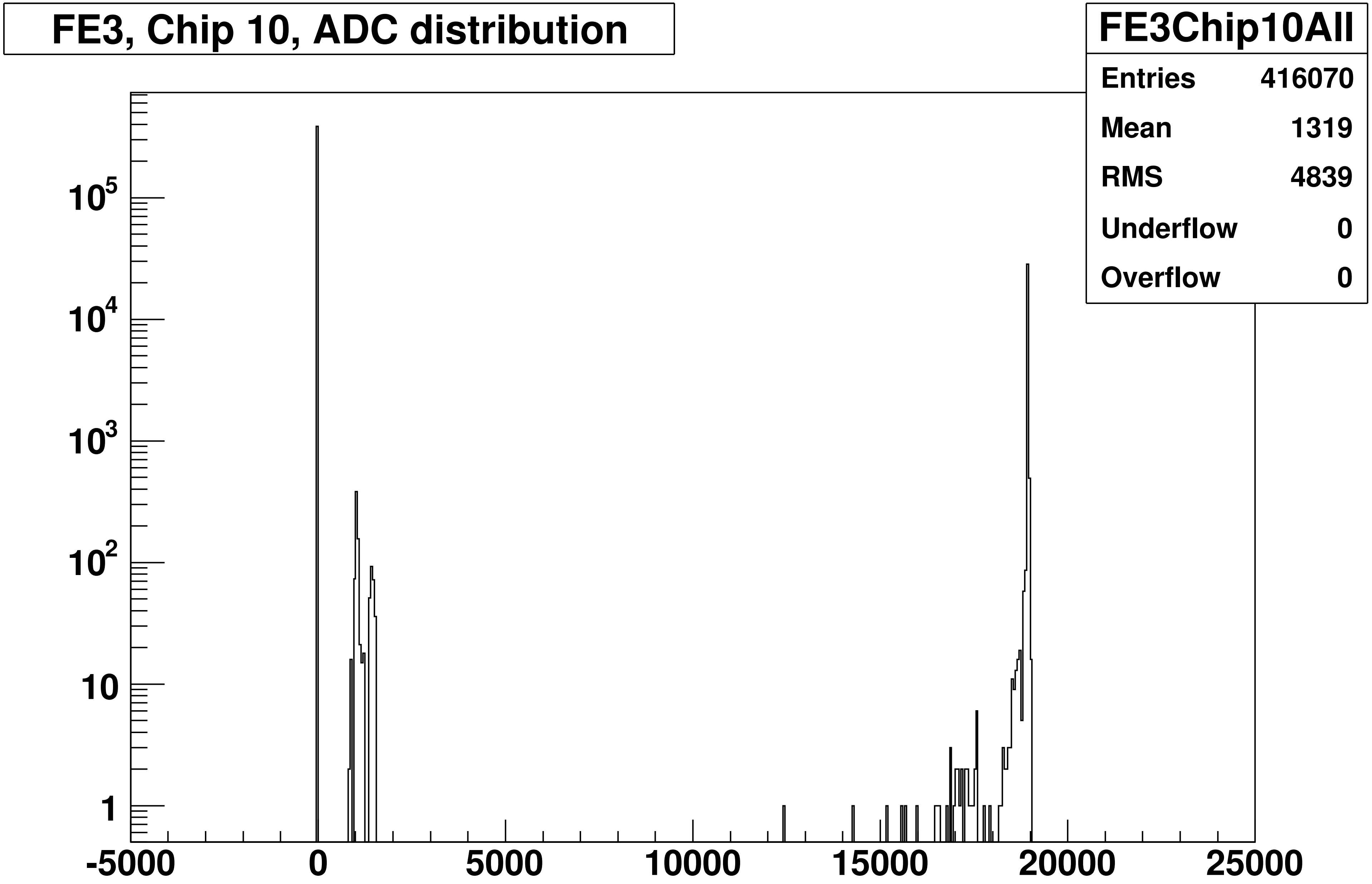
19200
19300
19400


FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 416070
Mean 1371
RMS 4883
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 416070
Mean 11.59
RMS 1.377
Underflow 0
Overflow 3.098e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 416070
Mean 1.895e+04
RMS 10.9
Underflow 3.864e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 416070
Mean 1319
RMS 4839
Underflow 0
Overflow 0
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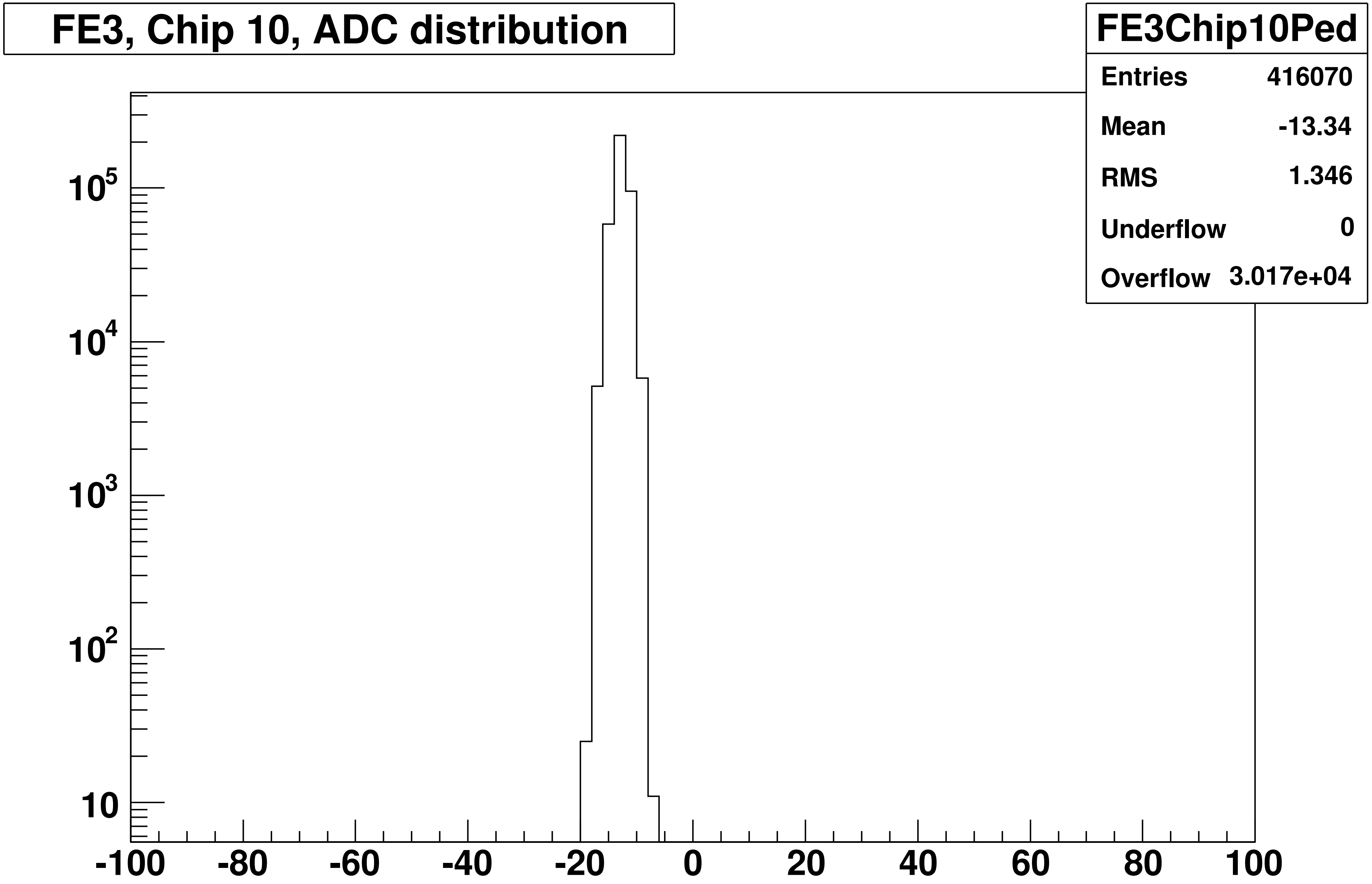
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 416070
Mean -13.34
RMS 1.346
Underflow 0
Overflow 3.017e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
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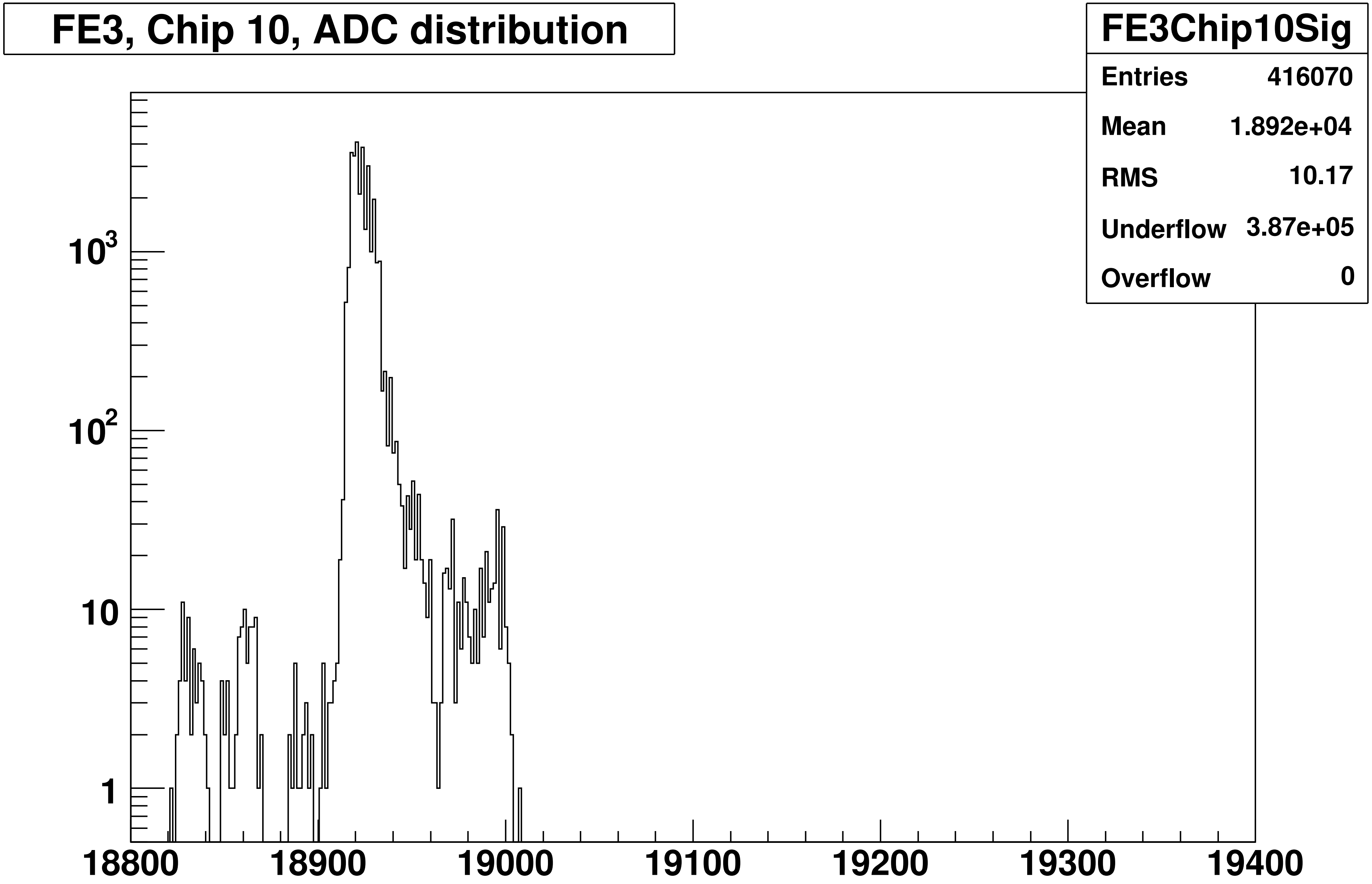
40
60
80
100



FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 416070
Mean 1.892e+04
RMS 10.17
Underflow 3.87e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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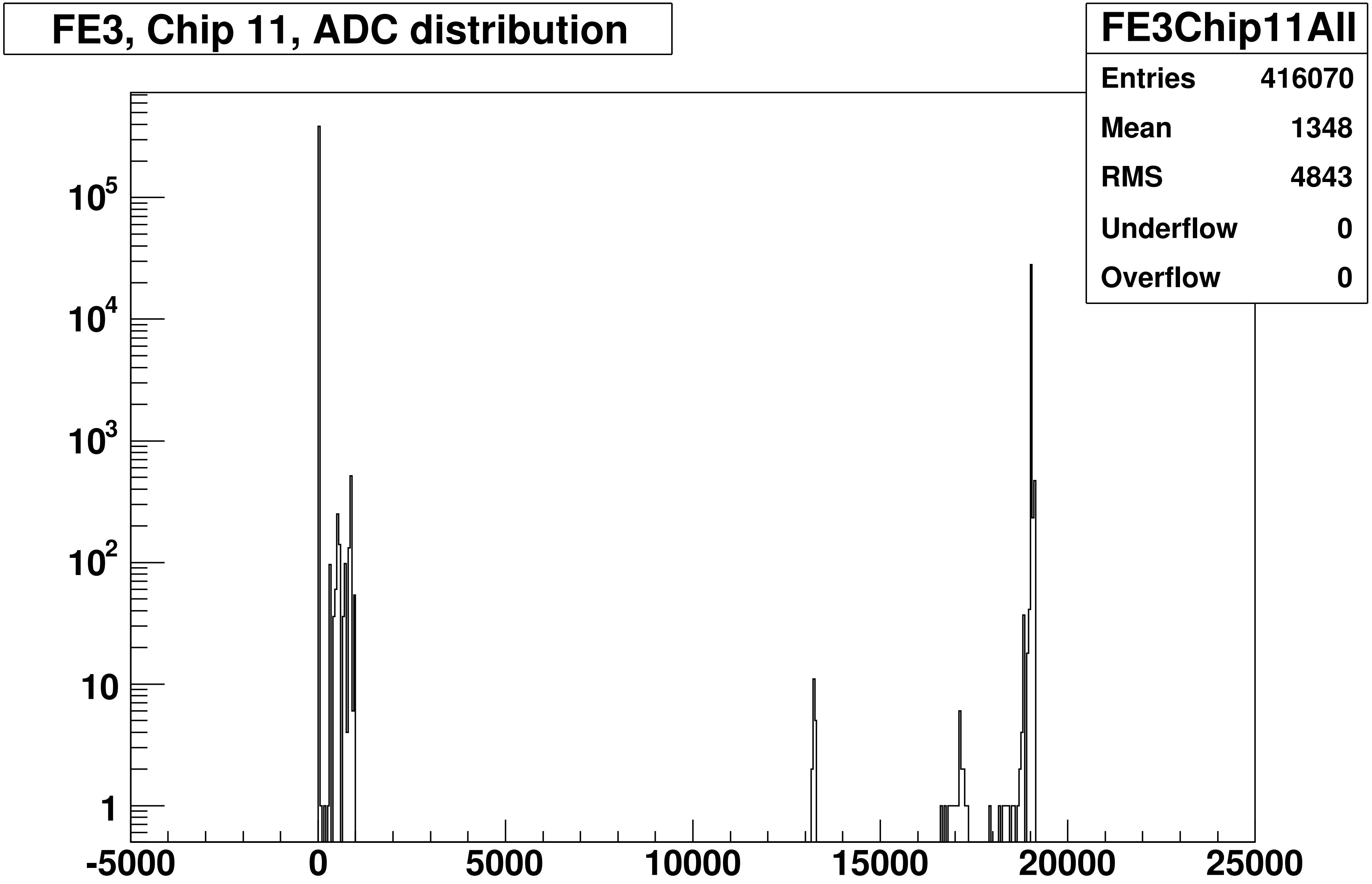
19100
19200
19300
19400



FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 416070
Mean 1348
RMS 4843
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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-5000
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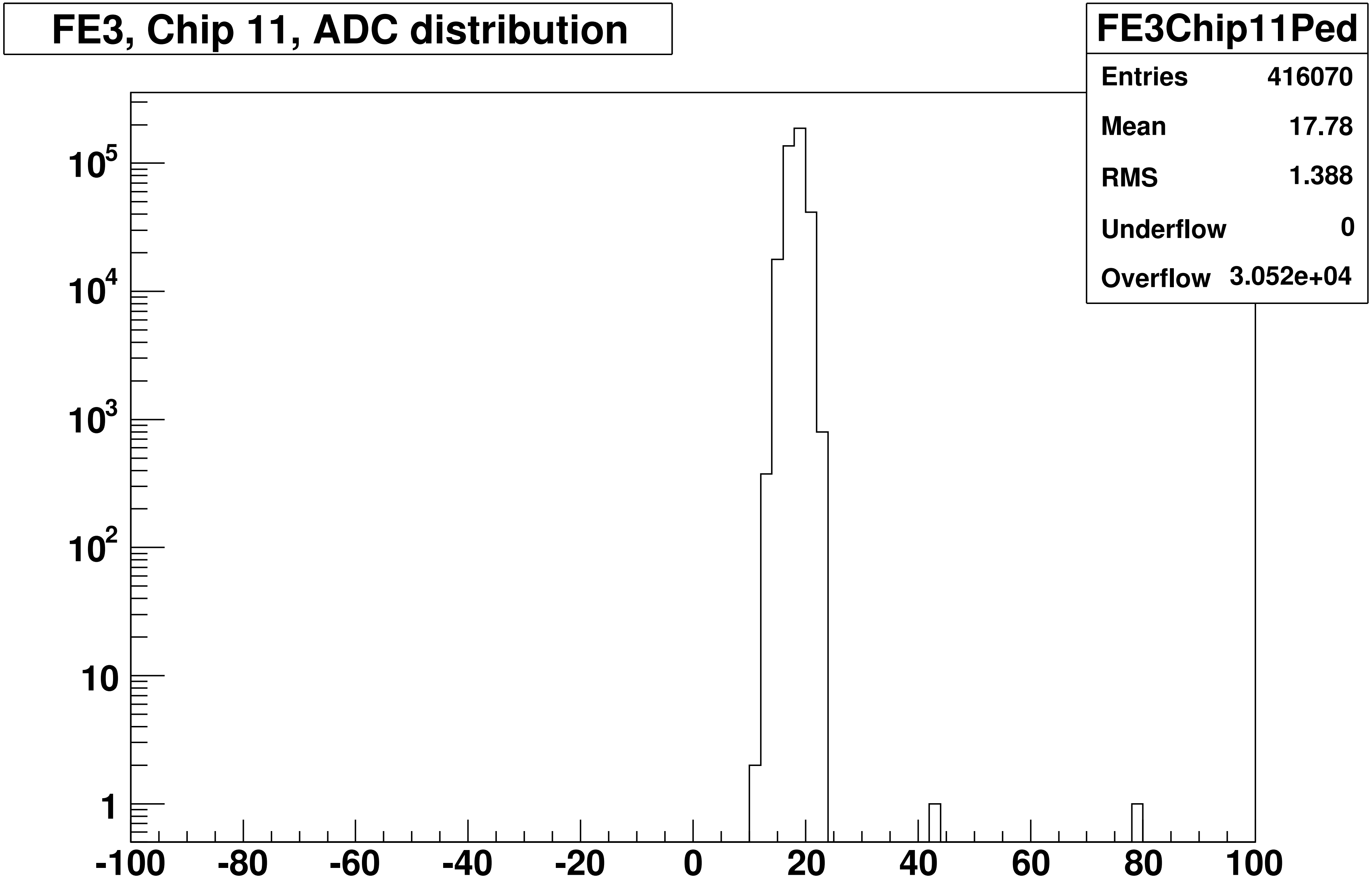
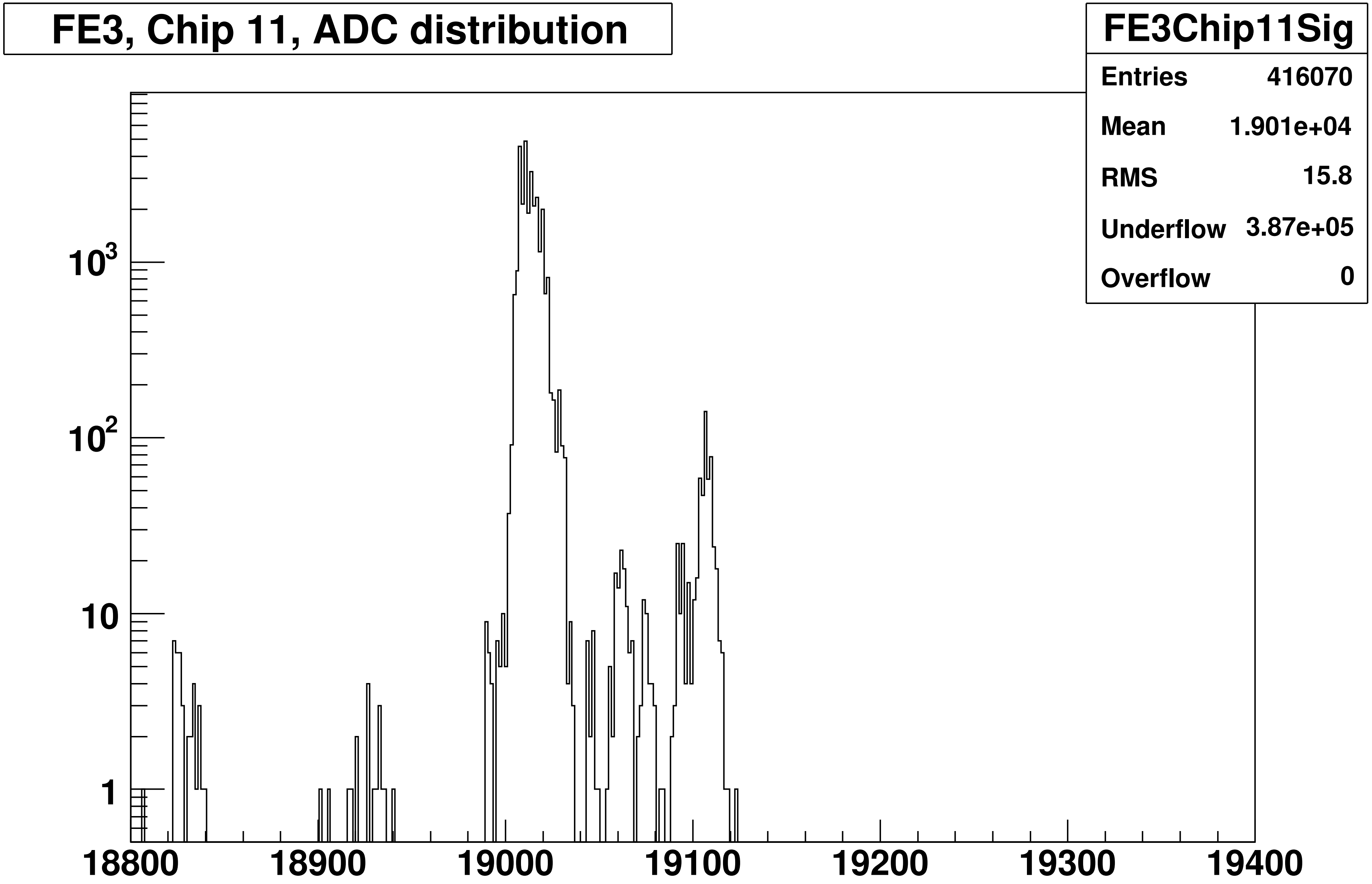
10000
15000
20000
25000


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 416070
Mean 17.78
RMS 1.388
Underflow 0
Overflow 3.052e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 416070
Mean 1.901e+04
RMS 15.8
Underflow 3.87e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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19100
19200
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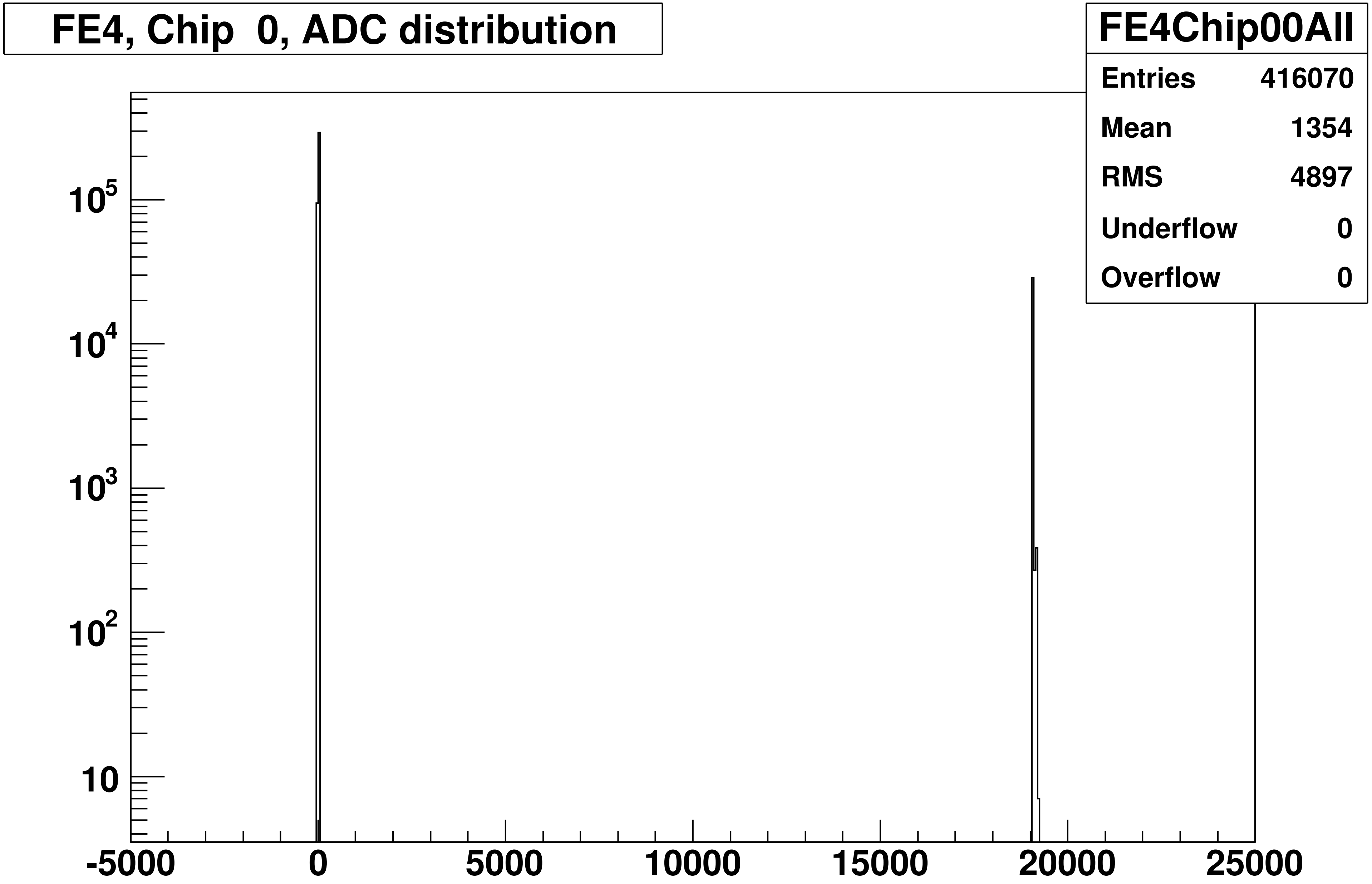
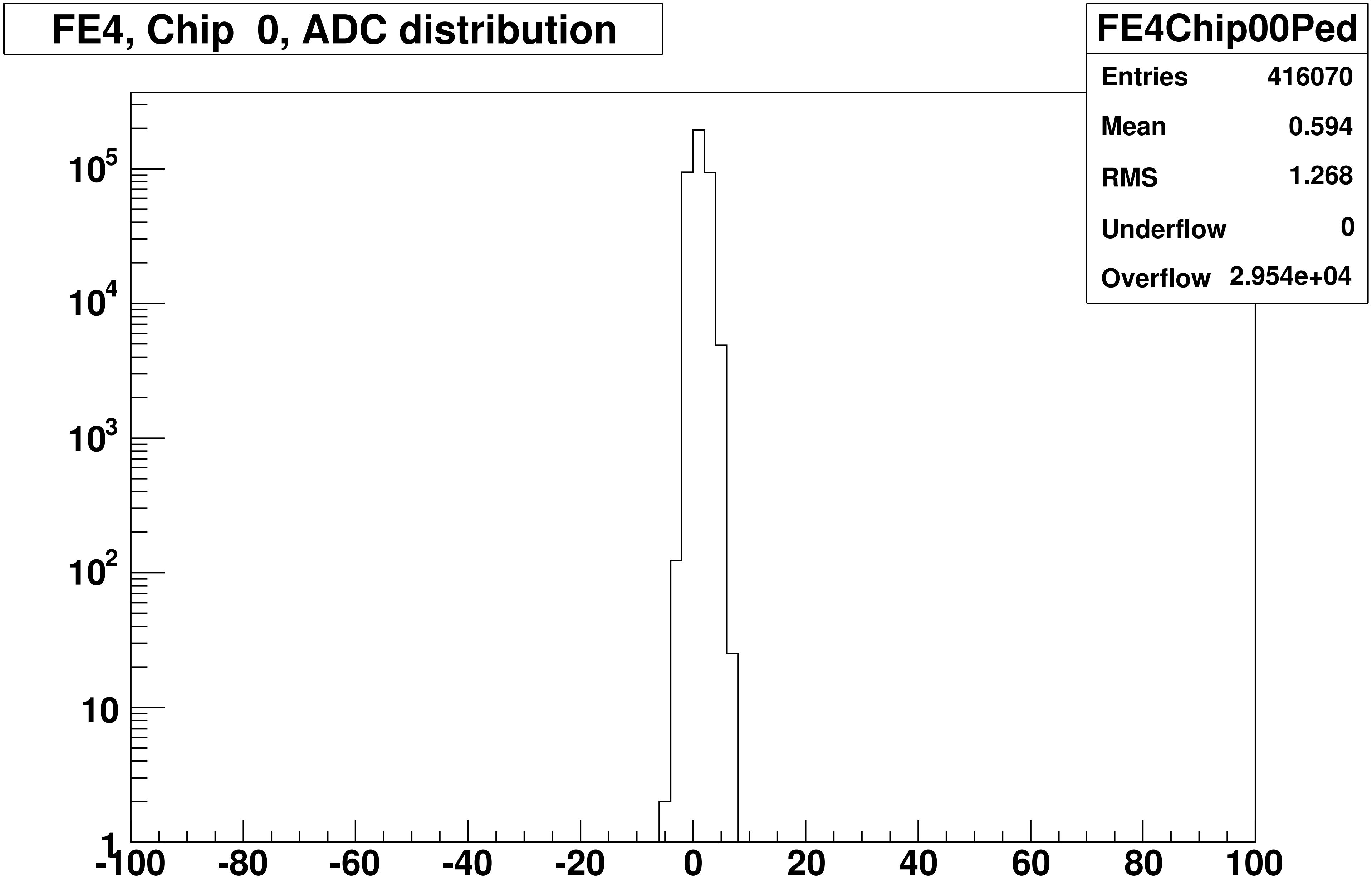
19400



FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 416070
Mean 1354
RMS 4897
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 416070
Mean 0.594
RMS 1.268
Underflow 0
Overflow 2.954e+04
10^5
10^4
10^3
10^2
10
1
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-40
-20
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20
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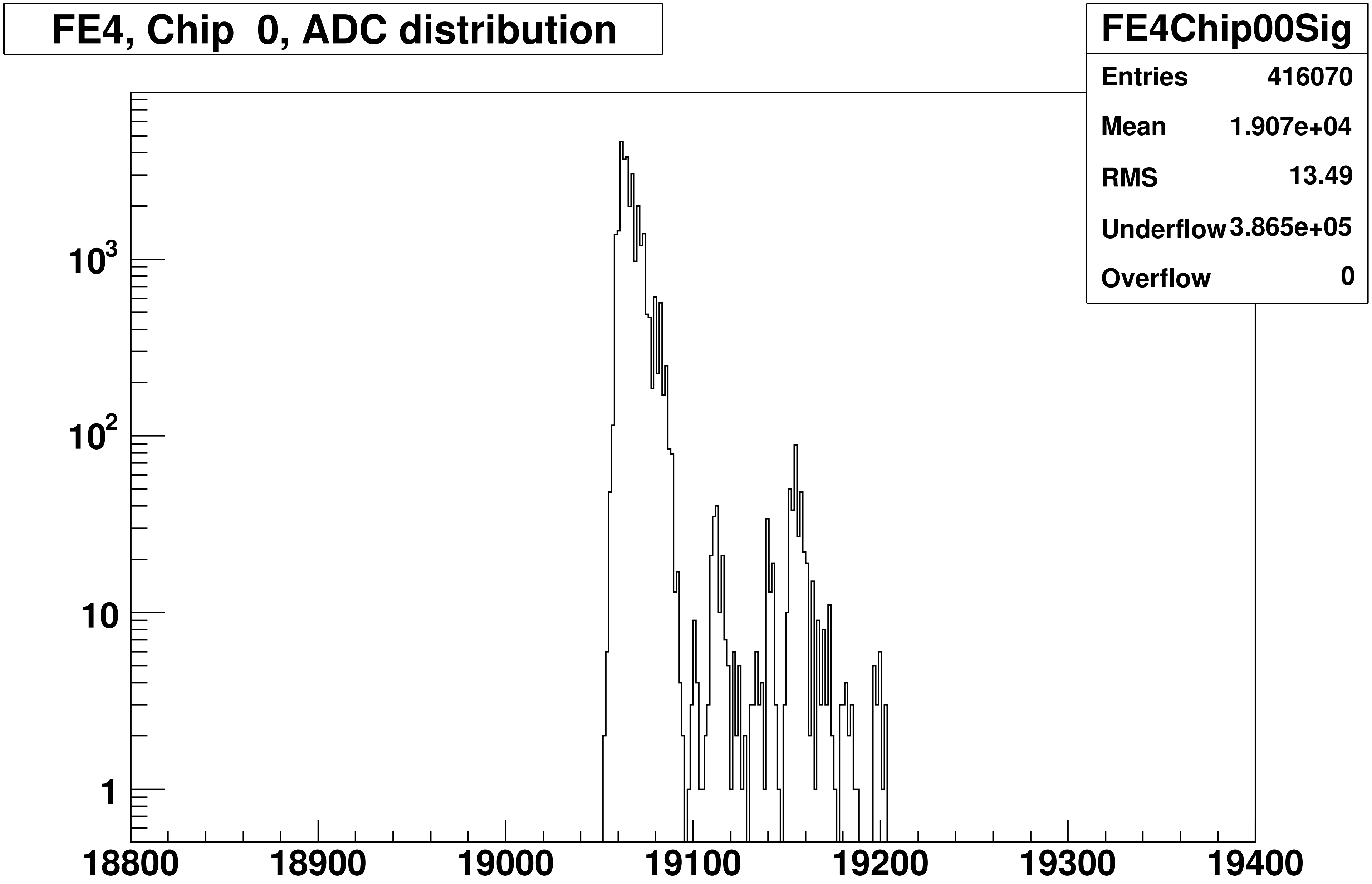
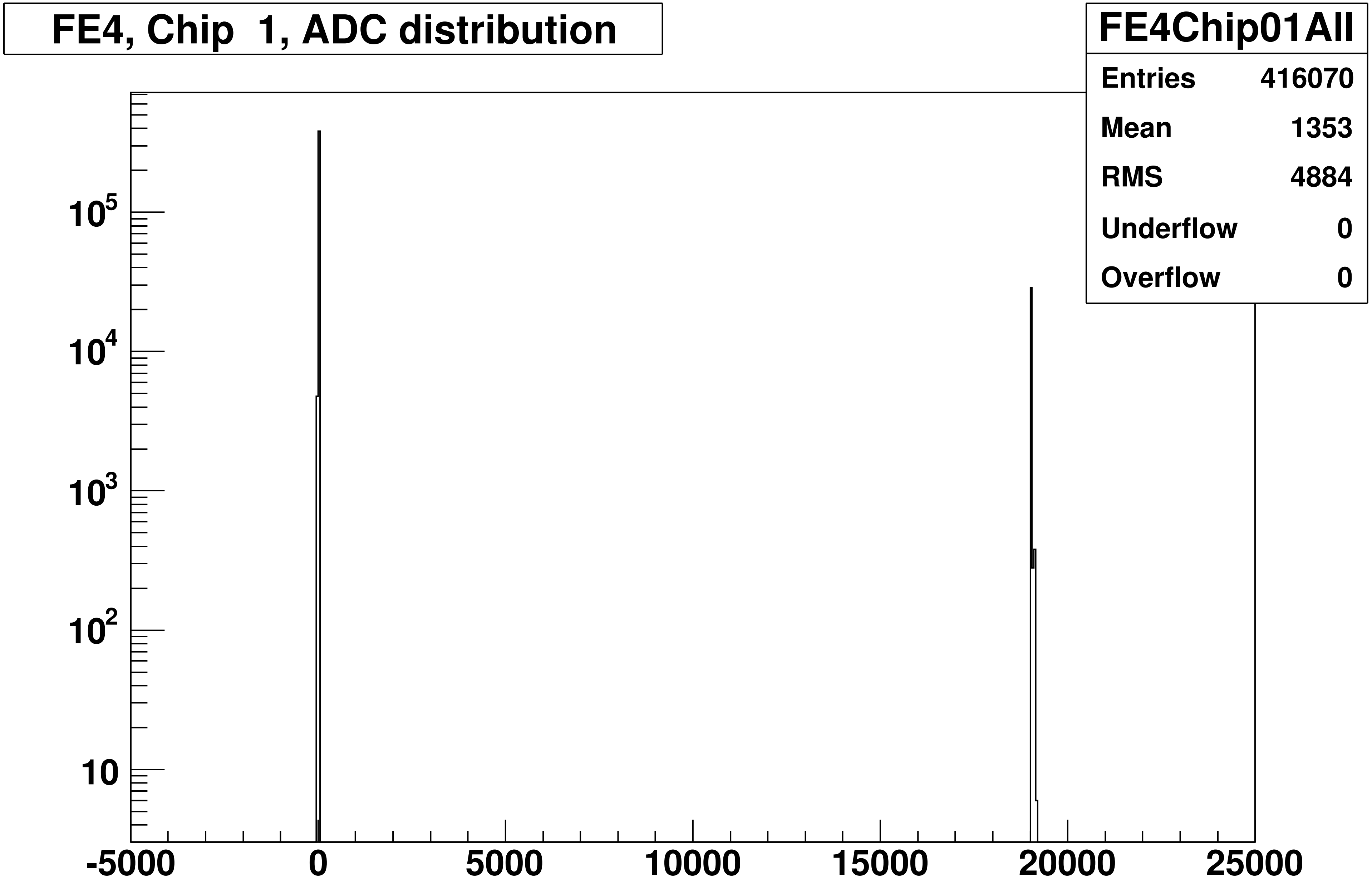
60
80
100



FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 416070
Mean 1.907e+04
RMS 13.49
Underflow 3.865e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 416070
Mean 1353
RMS 4884
Underflow 0
Overflow 0
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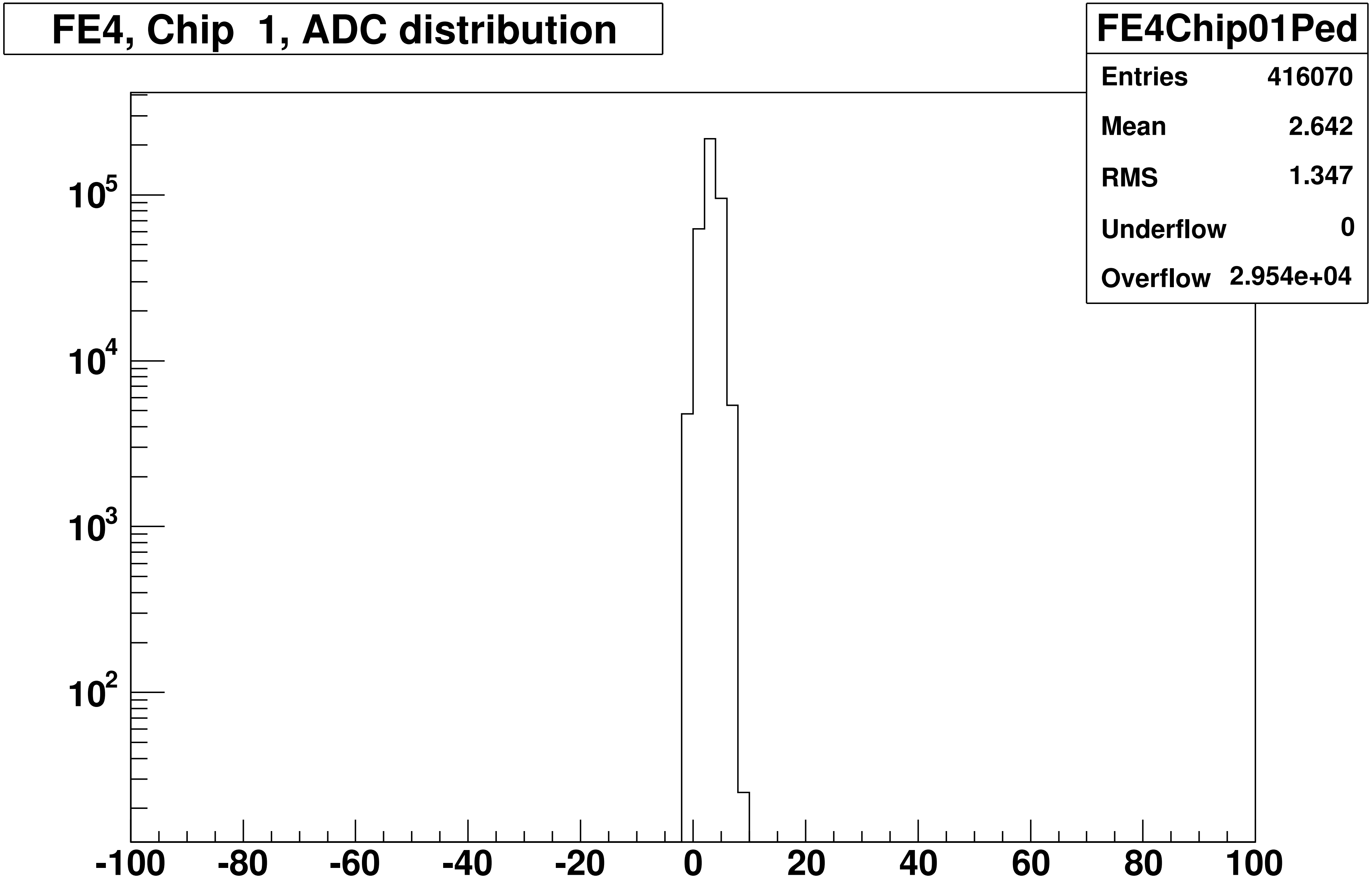
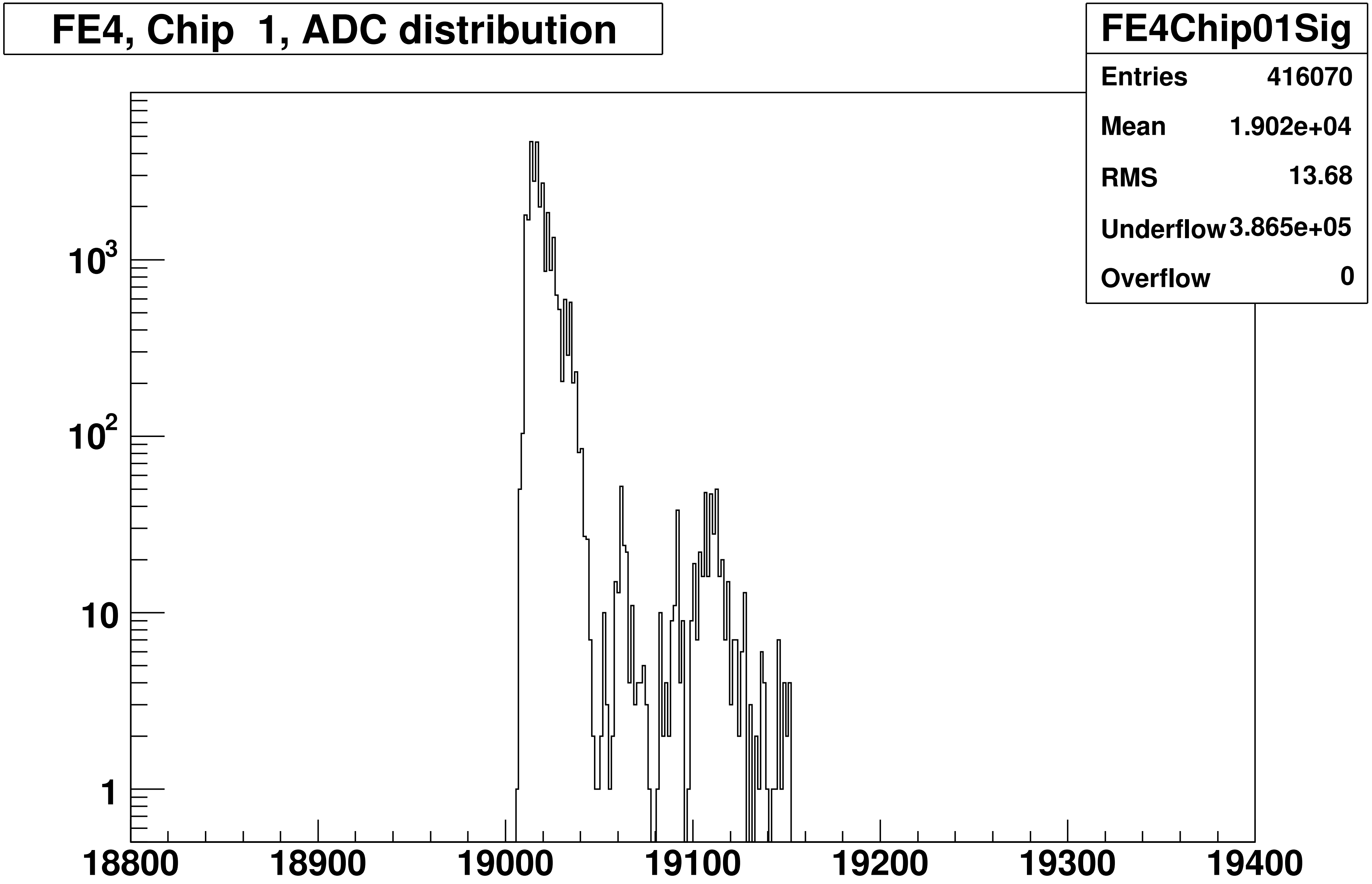
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 416070
Mean 2.642
RMS 1.347
Underflow 0
Overflow 2.954e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 416070
Mean 1.902e+04
RMS 13.68
Underflow 3.865e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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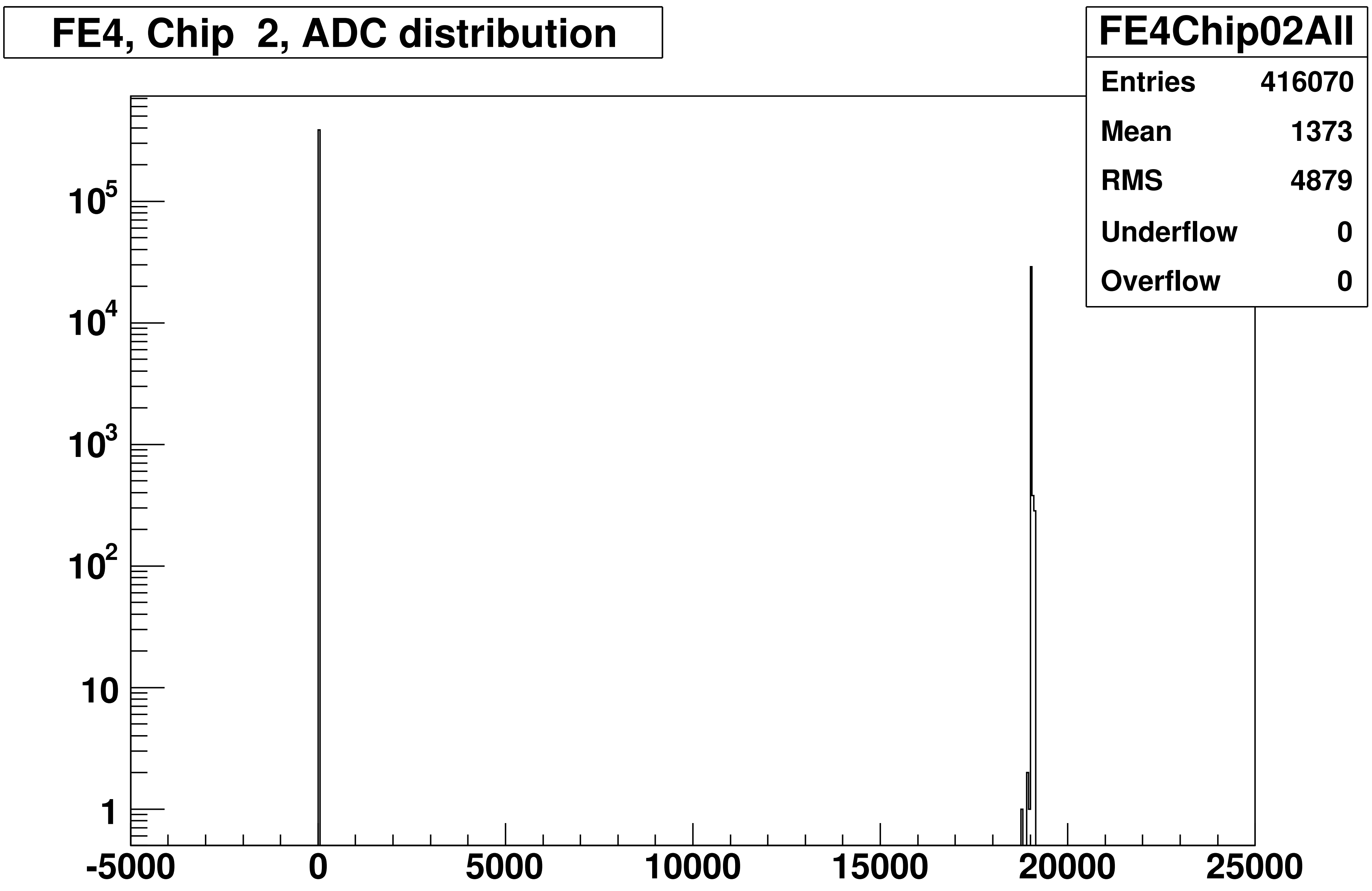
19100
19200
19300
19400



FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 416070
Mean 1373
RMS 4879
Underflow 0
Overflow 0
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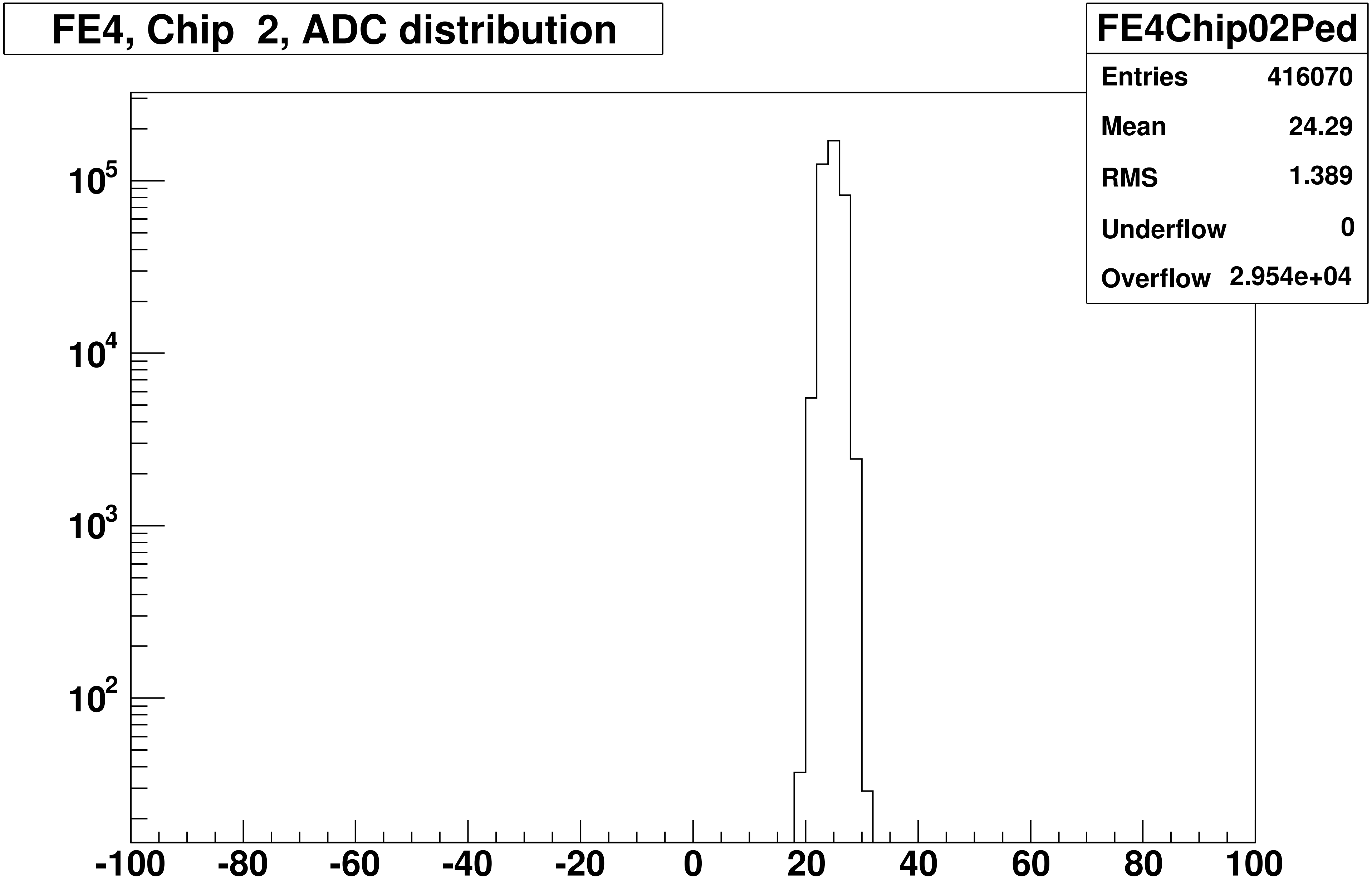
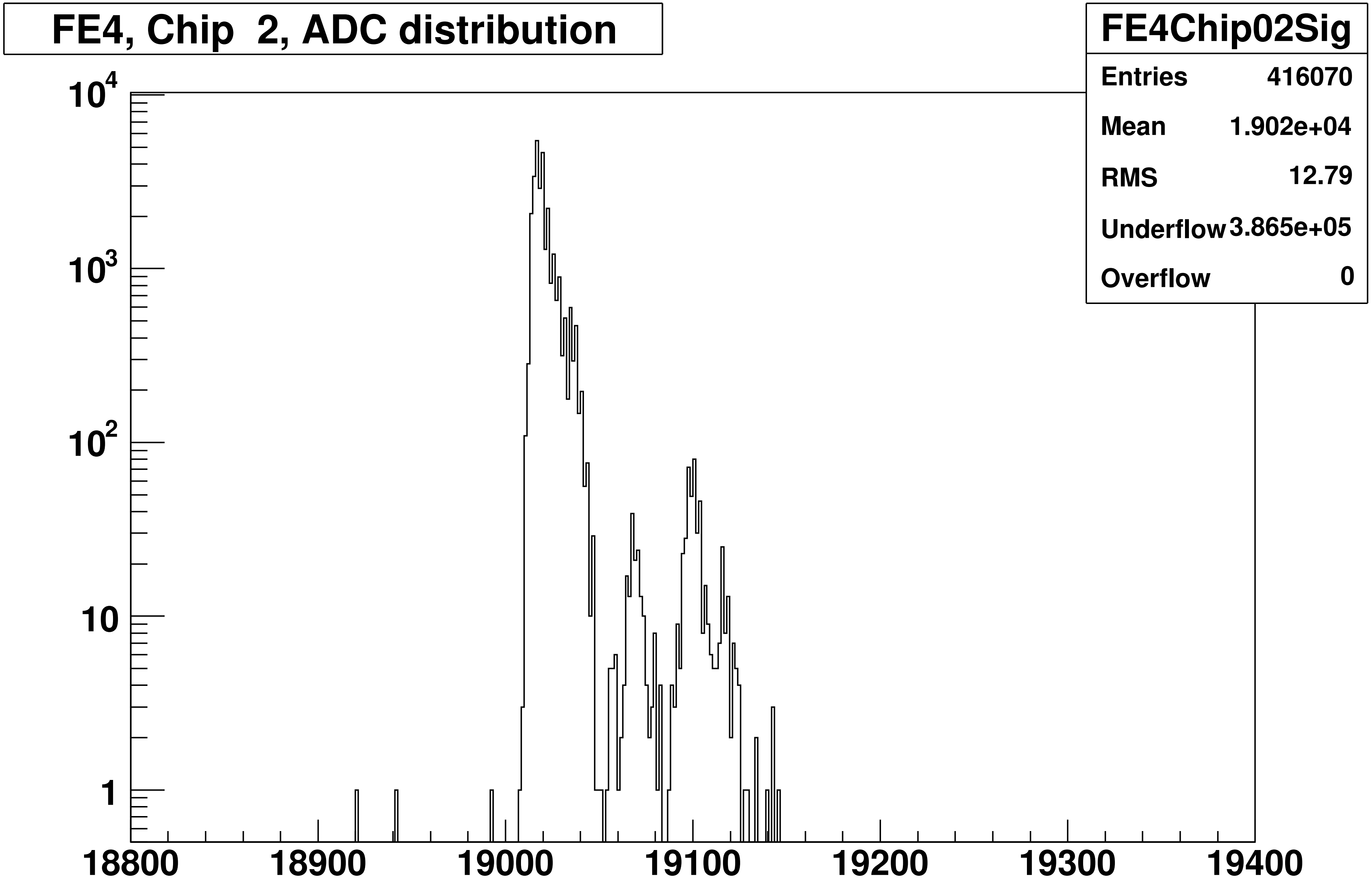
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 416070
Mean 24.29
RMS 1.389
Underflow 0
Overflow 2.954e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 416070
Mean 1.902e+04
RMS 12.79
Underflow 3.865e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
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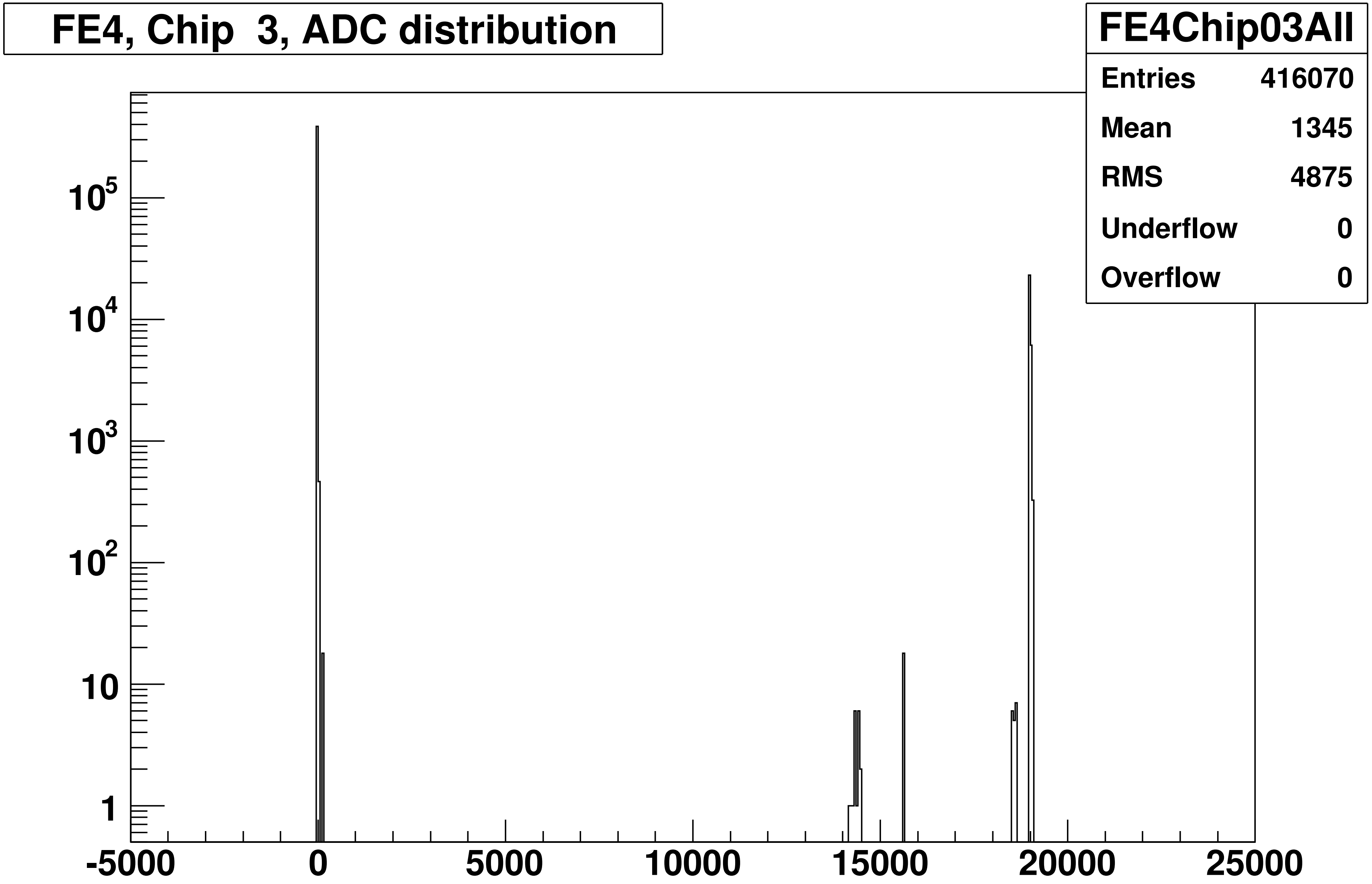
19400


FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 416070
Mean 1345
RMS 4875
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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-5000
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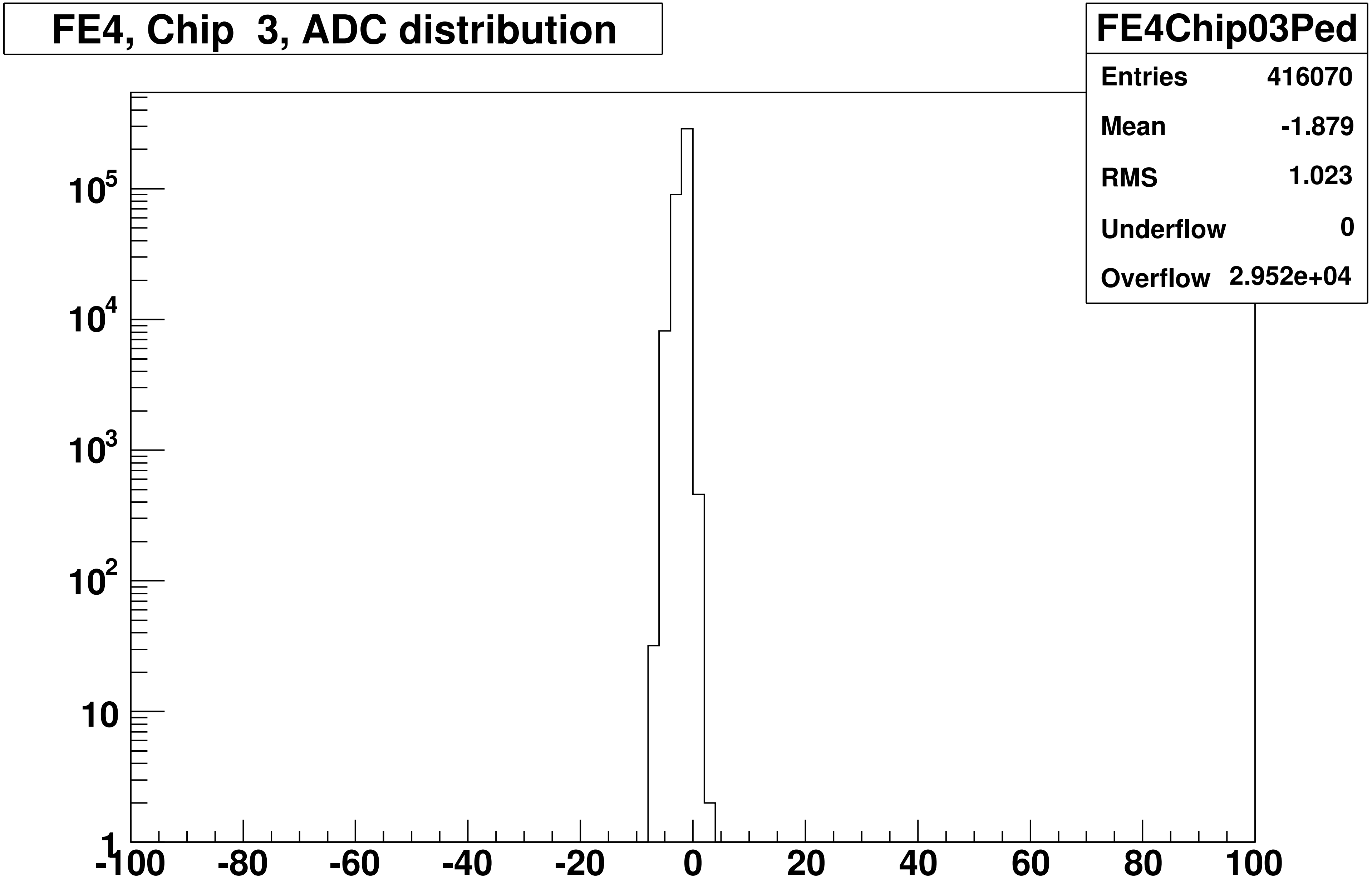

FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 416070
Mean -1.879
RMS 1.023
Underflow 0
Overflow 2.952e+04
10^5
10^4
10^3
10^2
10
1
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-20
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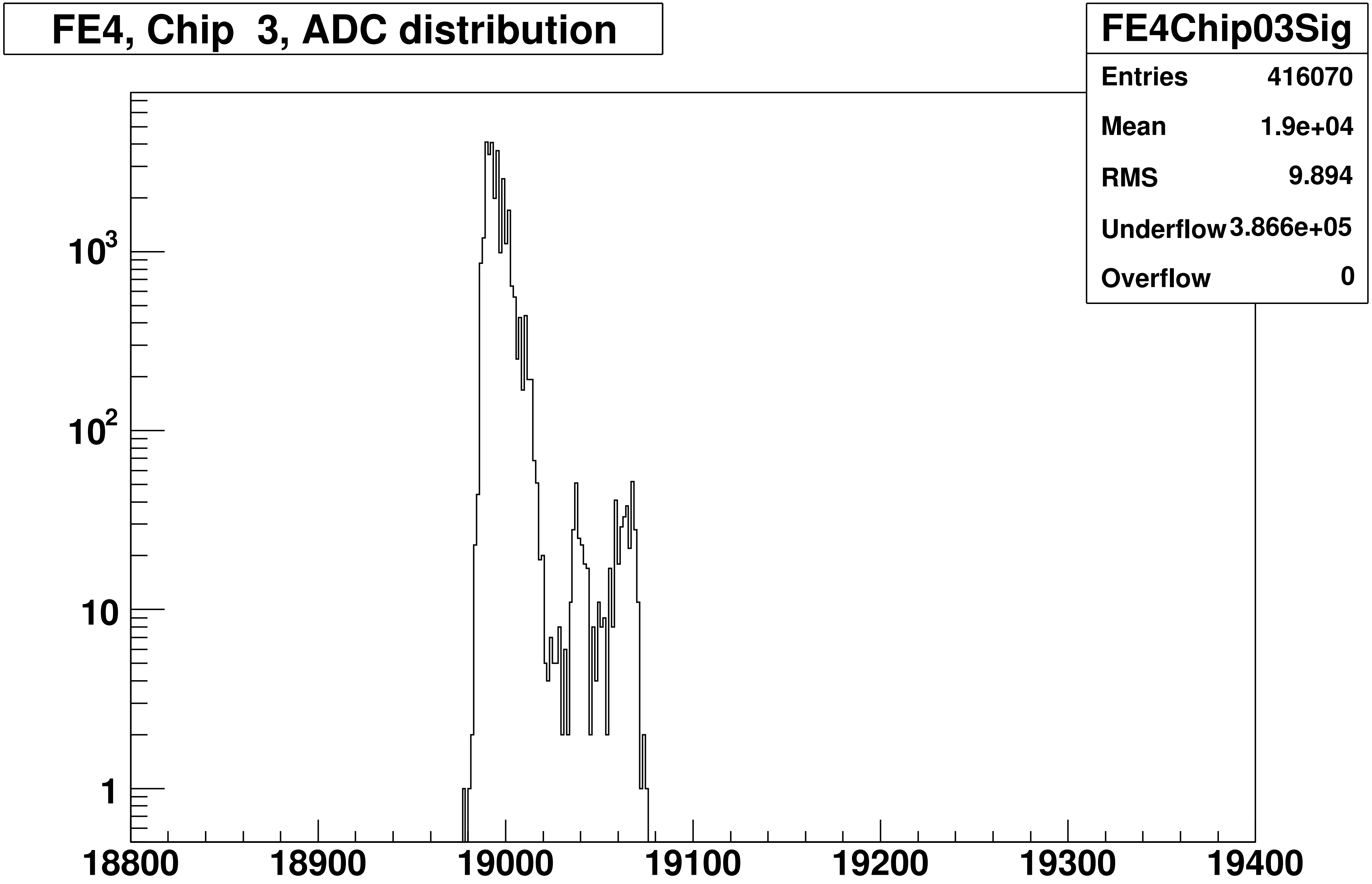
40
60
80
100


FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 416070
Mean 1.9e+04
RMS 9.894
Underflow 3.866e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
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19200
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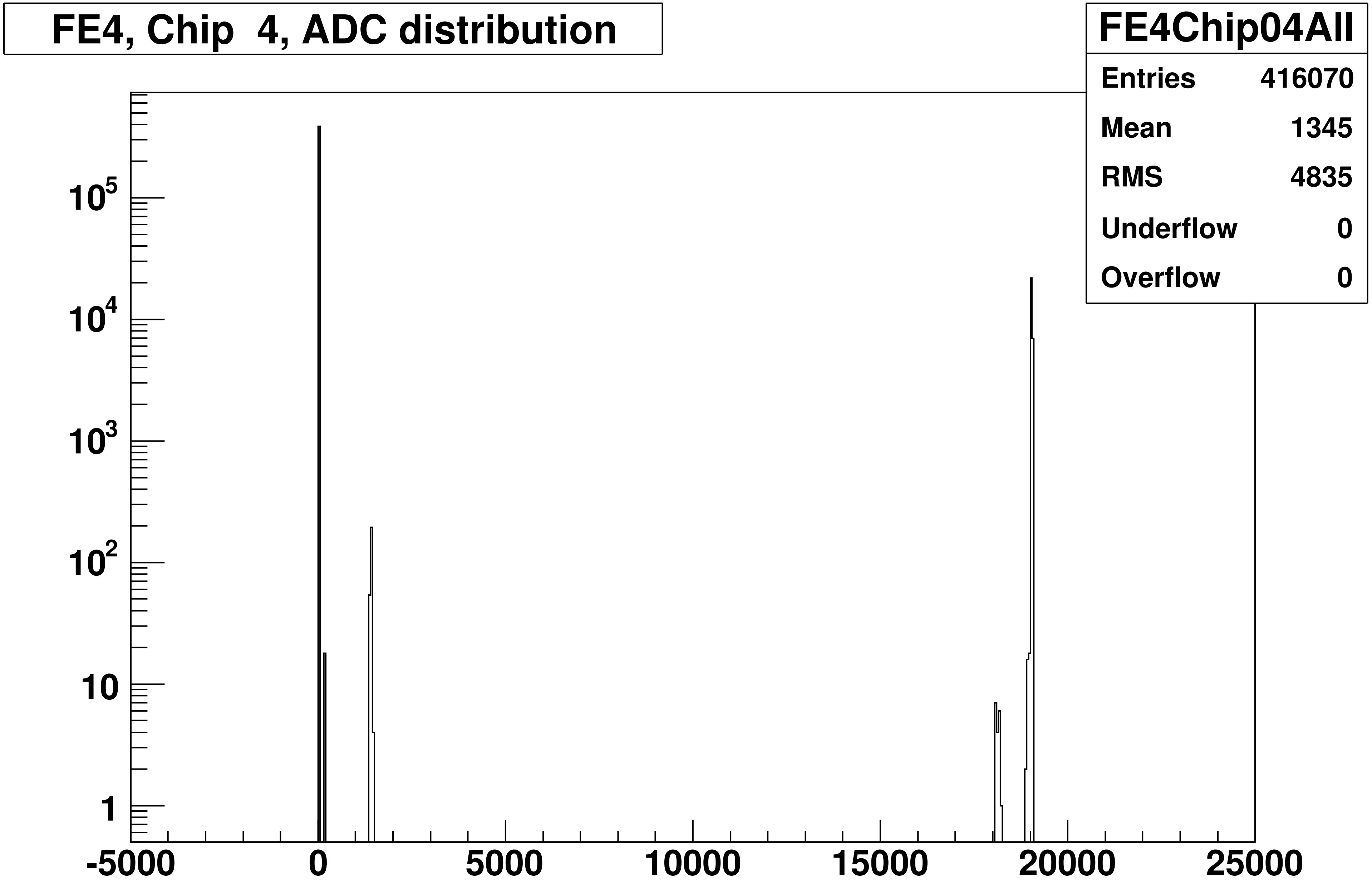
19400



FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 416070
Mean 1345
RMS 4835
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
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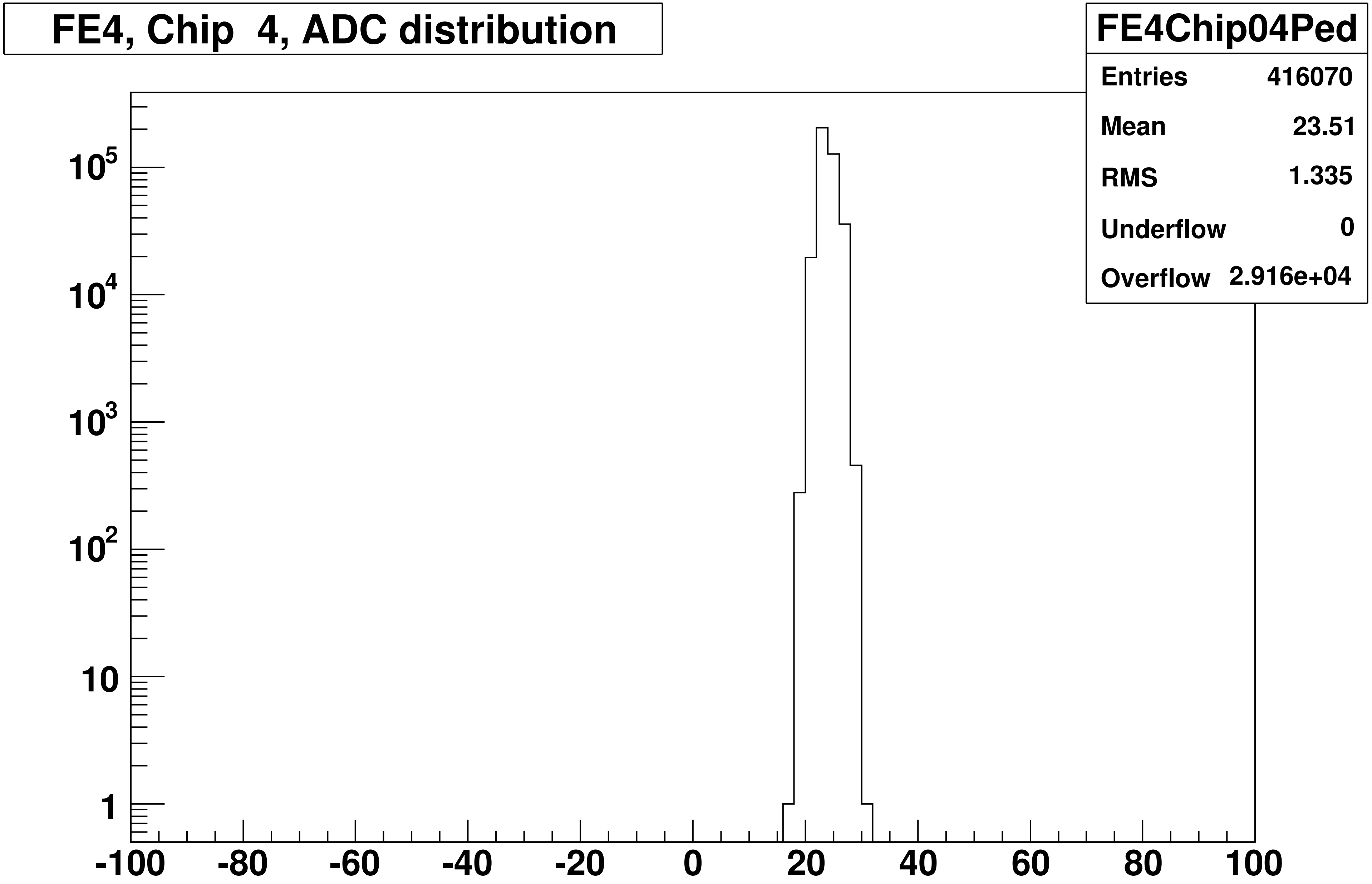
25000


FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 416070
Mean 23.51
RMS 1.335
Underflow 0
Overflow 2.916e+04
10^5
10^4
10^3
10^2
10
1
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-20
0
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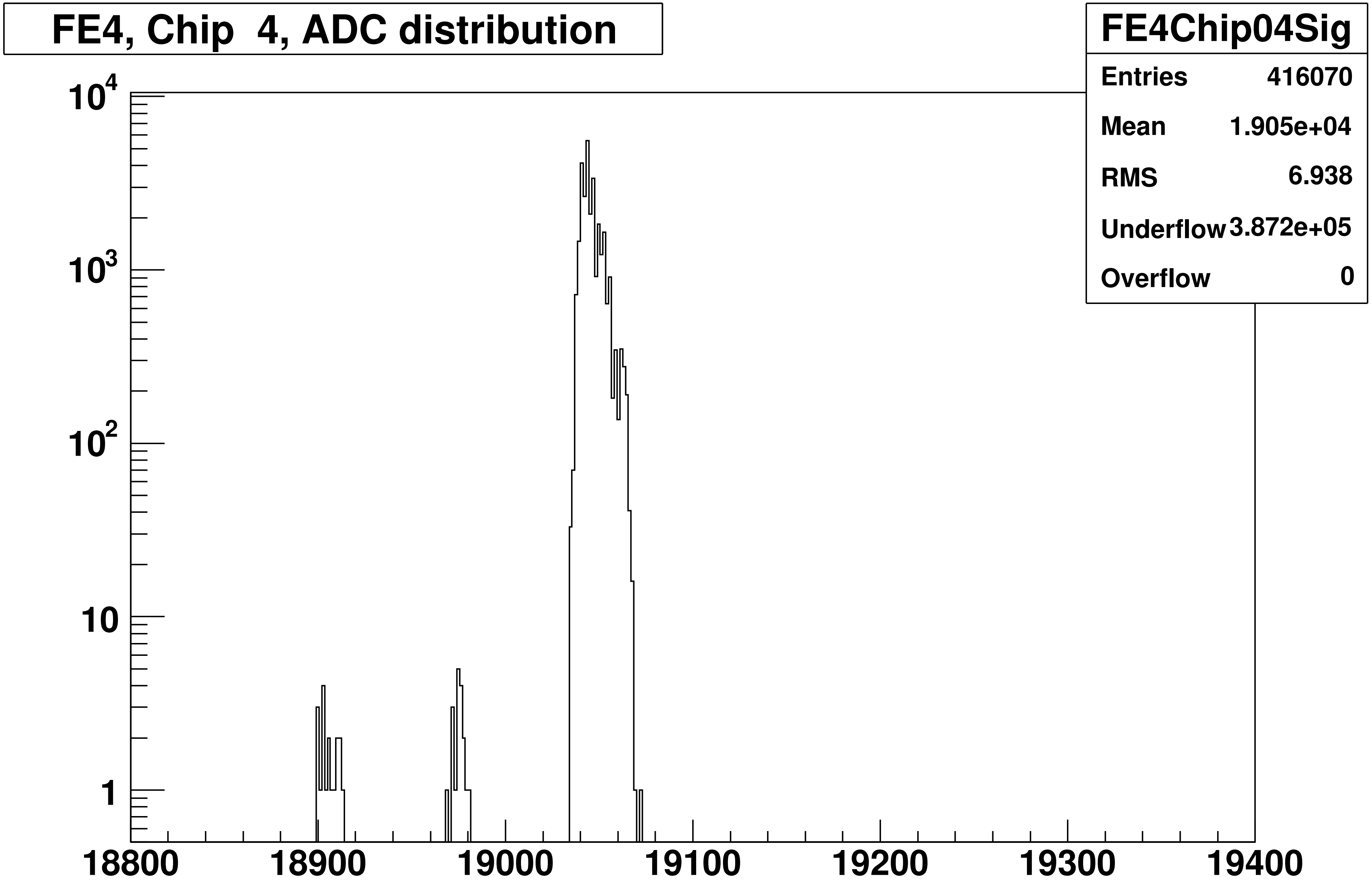
60
80
100



FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 416070
Mean 1.905e+04
RMS 6.938
Underflow 3.872e+05
Overflow 0
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10^3
10^2
10
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18800
18900
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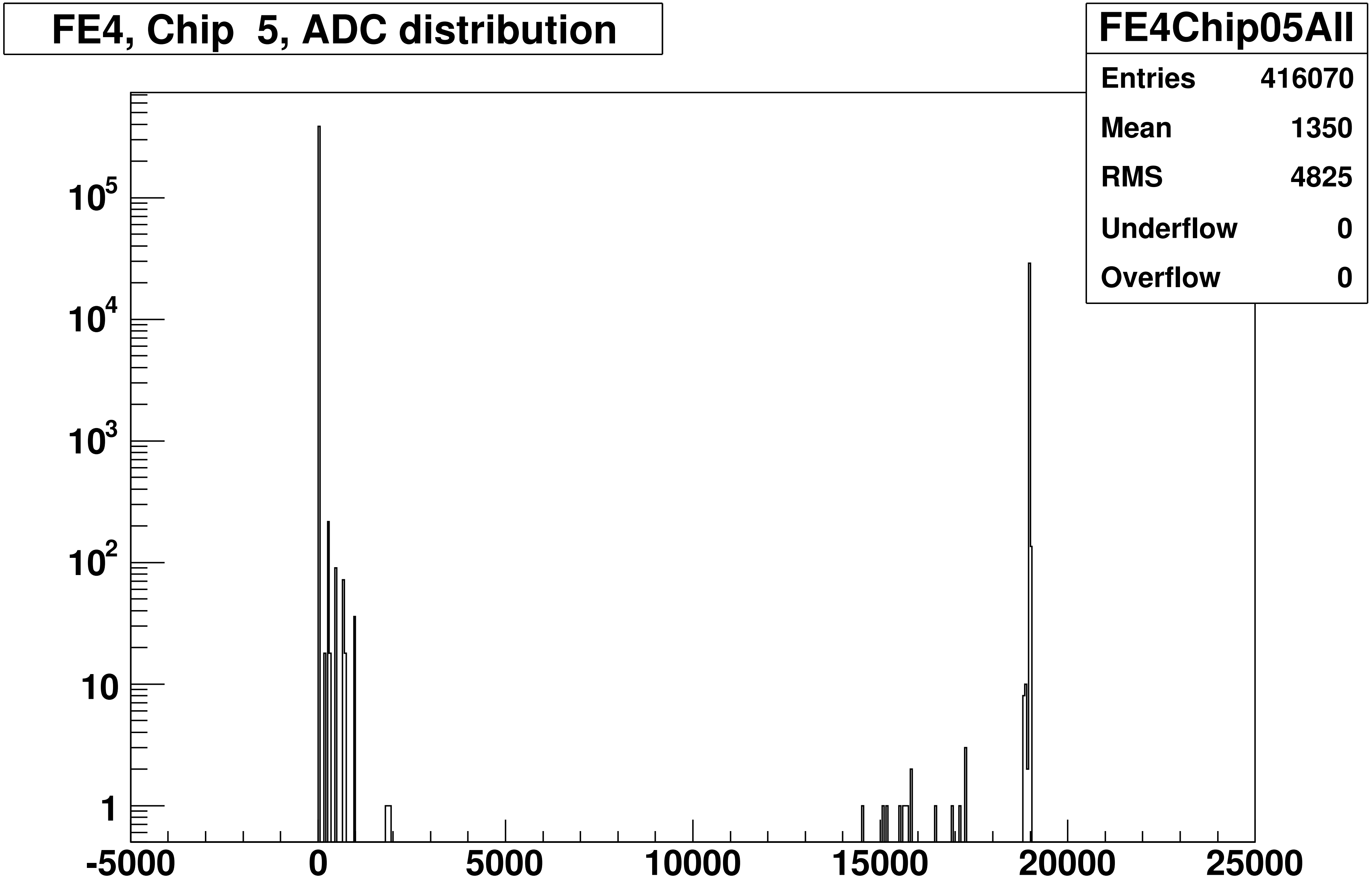
19300
19400



FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 416070
Mean 1350
RMS 4825
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
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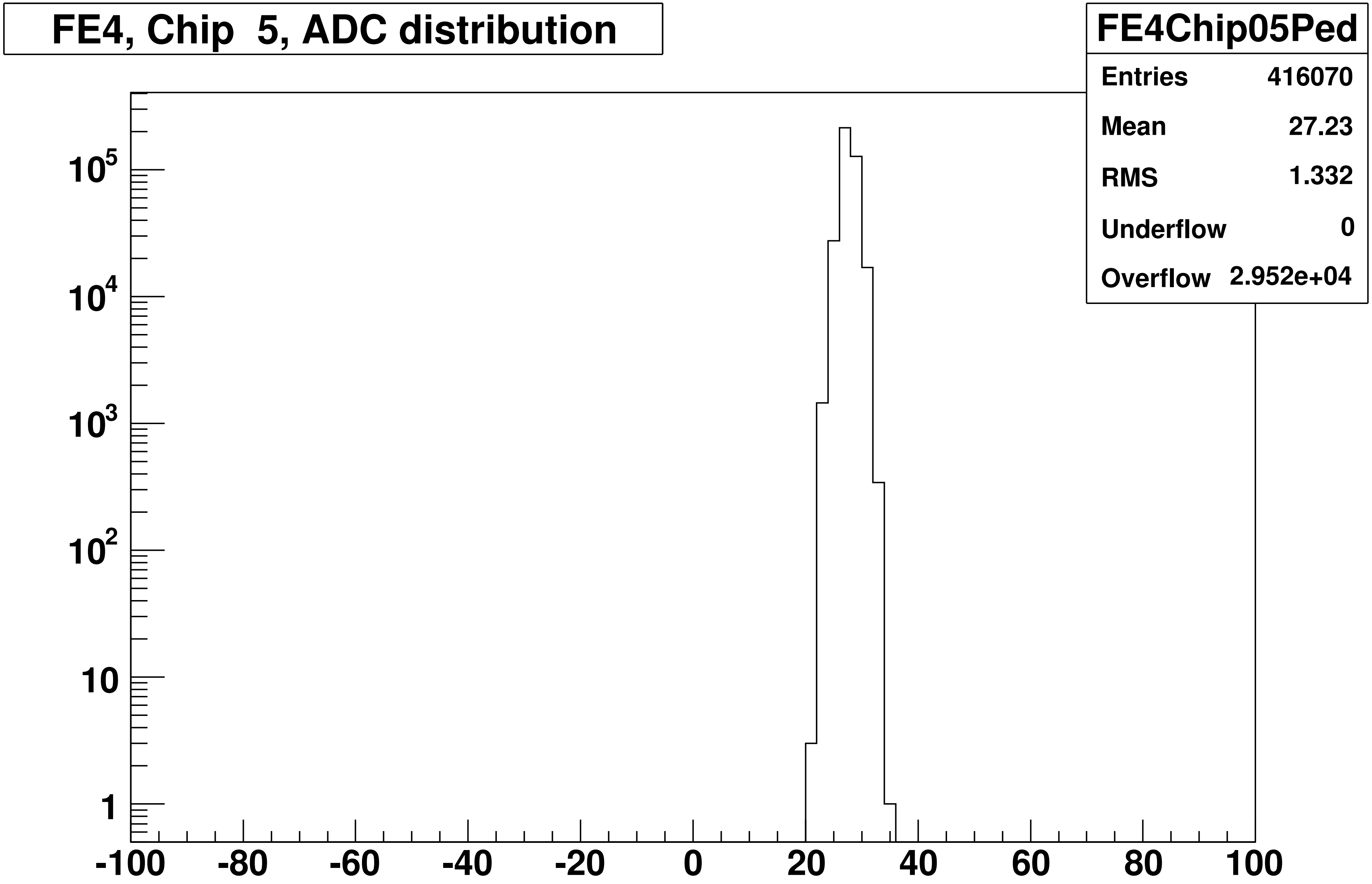
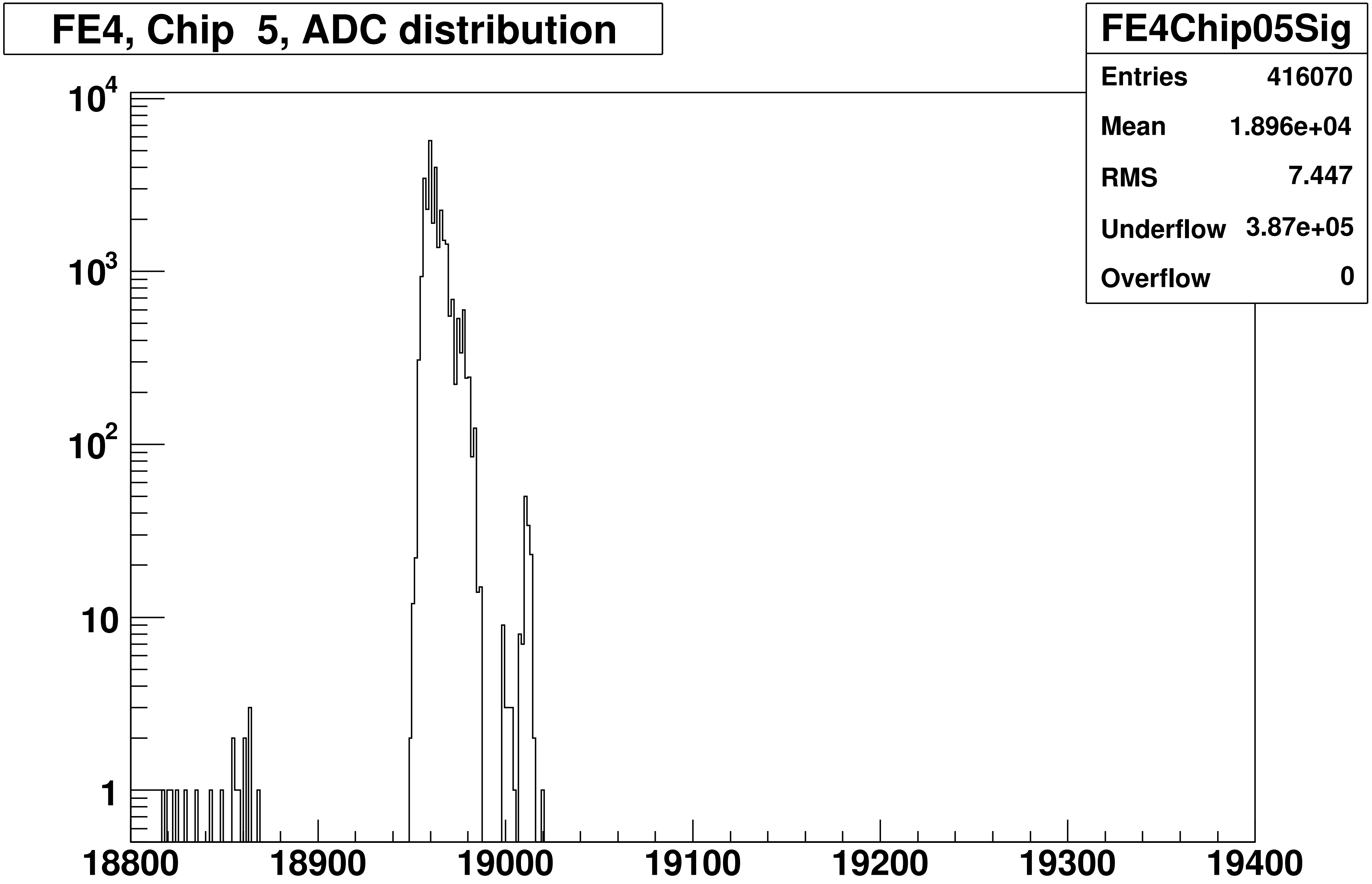
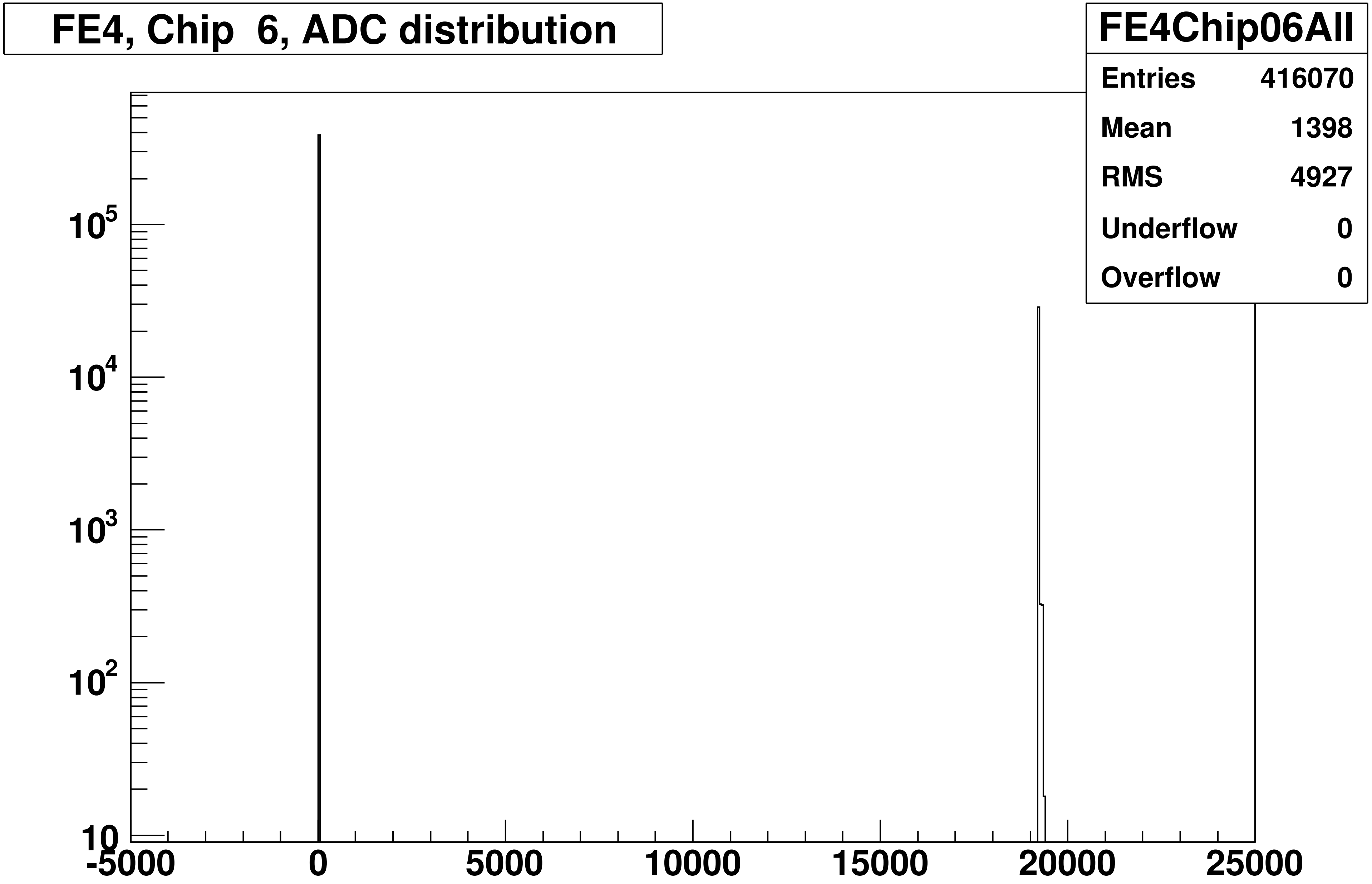
10000
15000
20000
25000

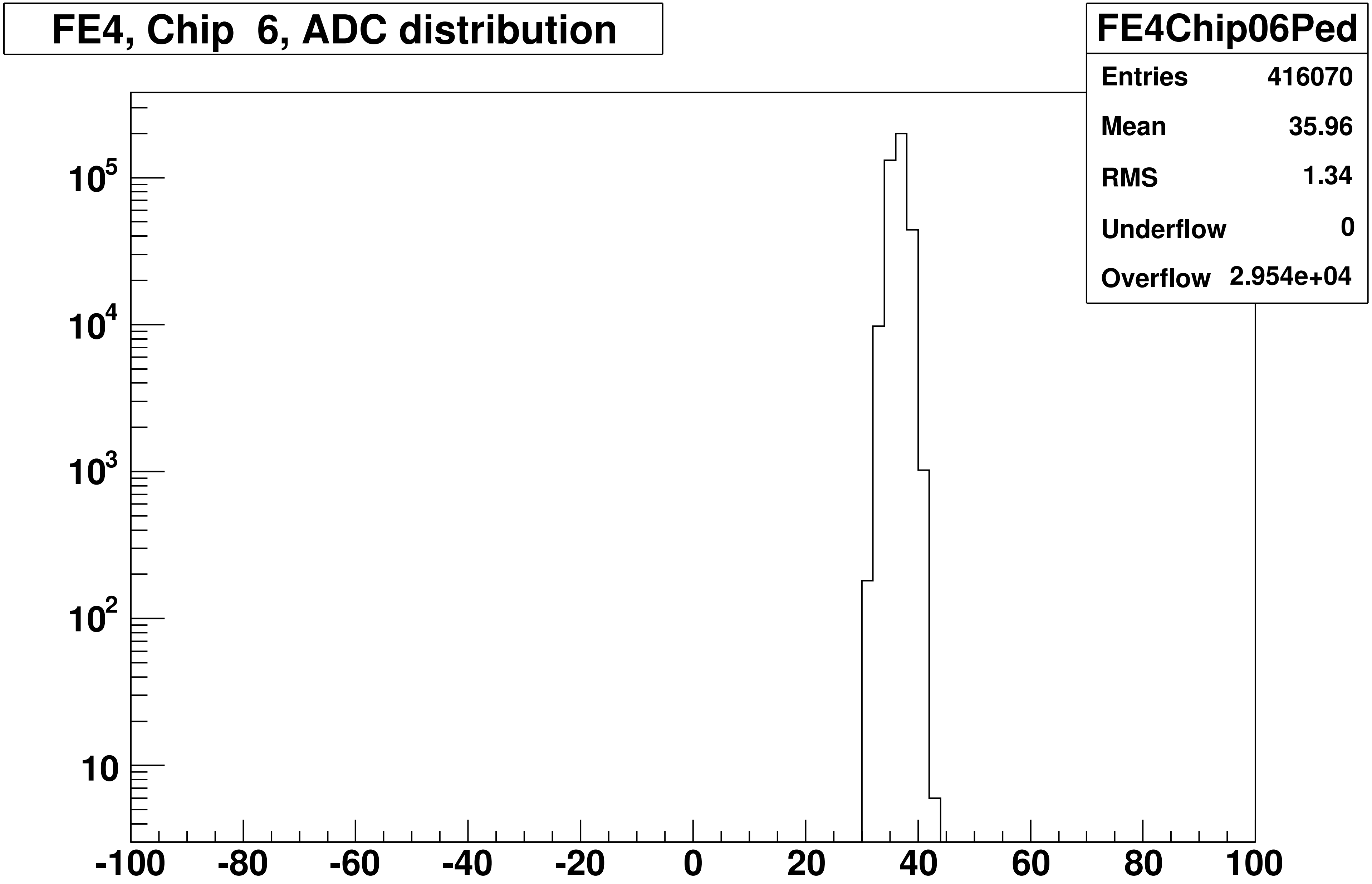

FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 416070
Mean 27.23
RMS 1.332
Underflow 0
Overflow 2.952e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 416070
Mean 1.896e+04
RMS 7.447
Underflow 3.87e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 416070
Mean 1398
RMS 4927
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 416070
Mean 35.96
RMS 1.34
Underflow 0
Overflow 2.954e+04
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10^4
10^3
10^2
10
-100
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-60
-40
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80
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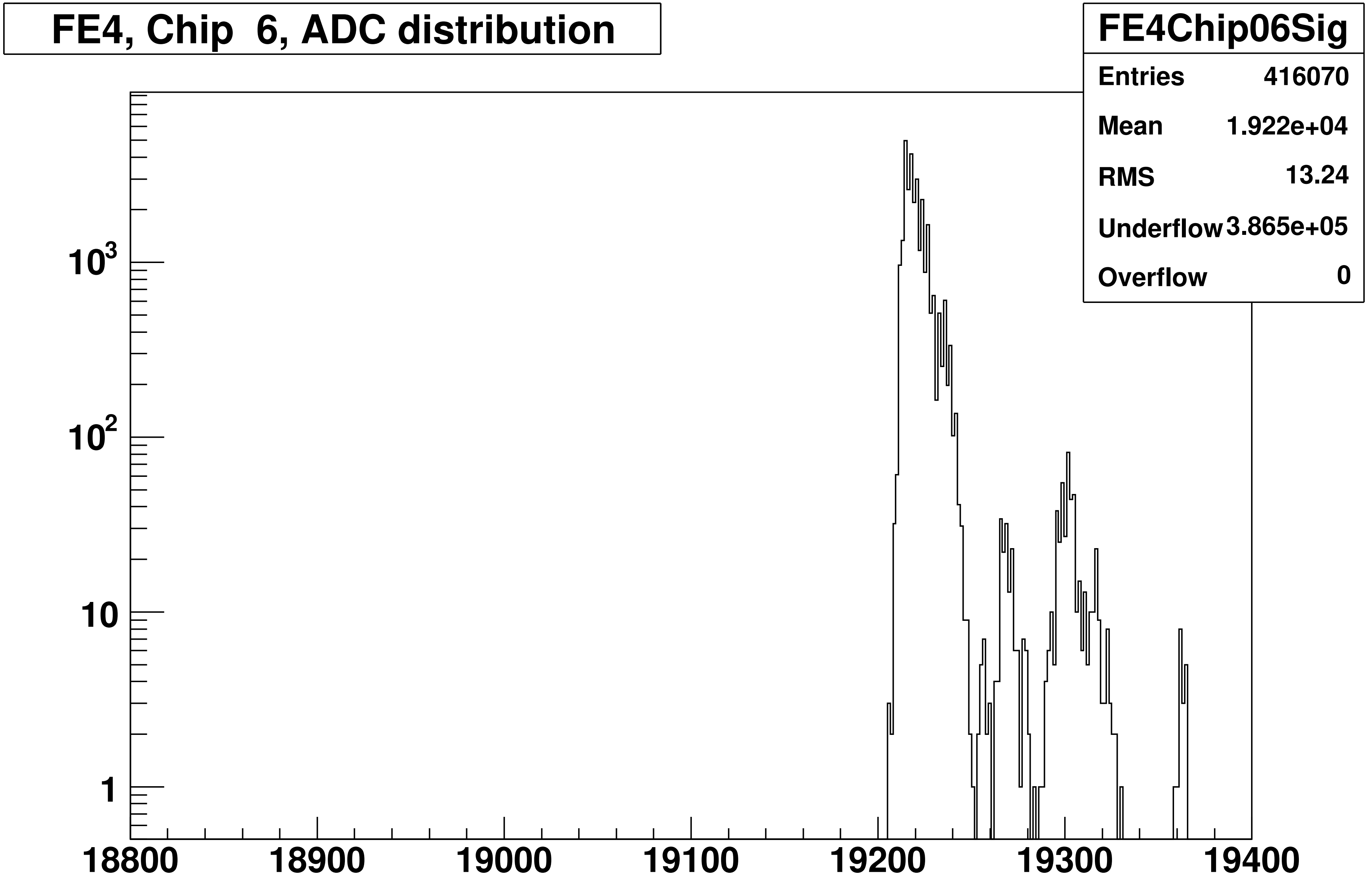

FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 416070
Mean 1.922e+04
RMS 13.24
Underflow 3.865e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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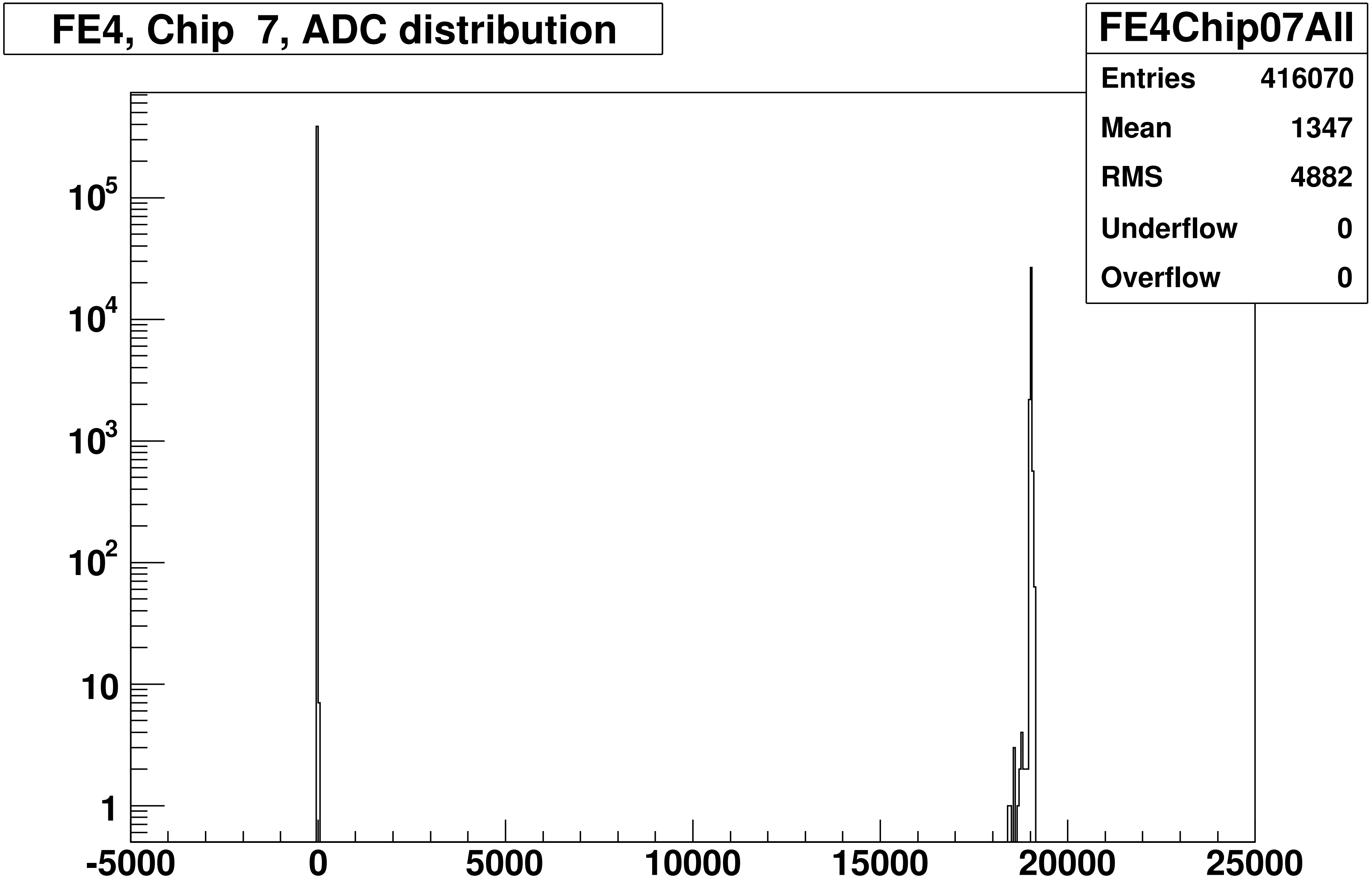

FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 416070
Mean 1347
RMS 4882
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
15000
20000
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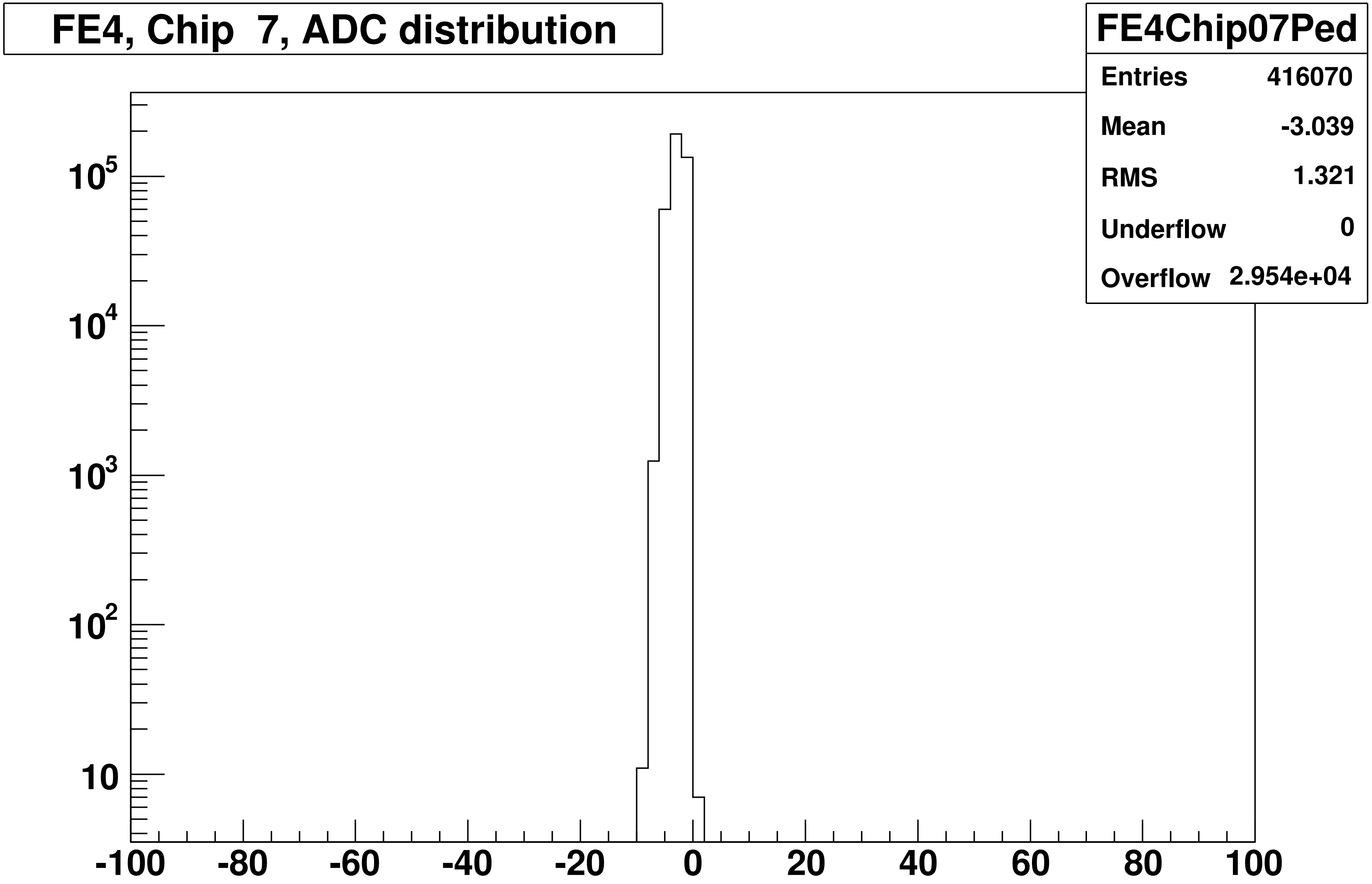

FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 416070
Mean -3.039
RMS 1.321
Underflow 0
Overflow 2.954e+04
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10^4
10^3
10^2
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-100
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-20
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80
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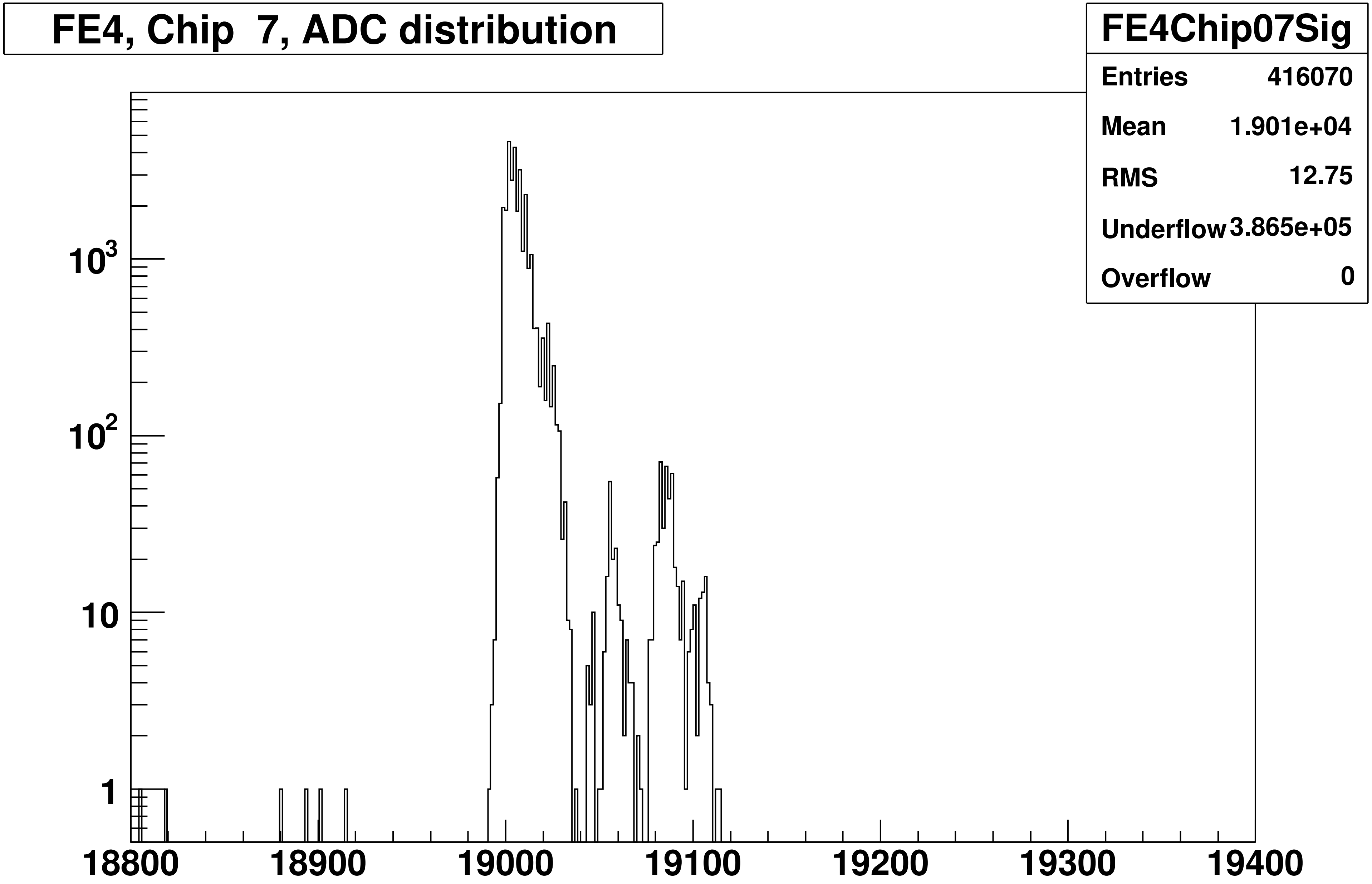
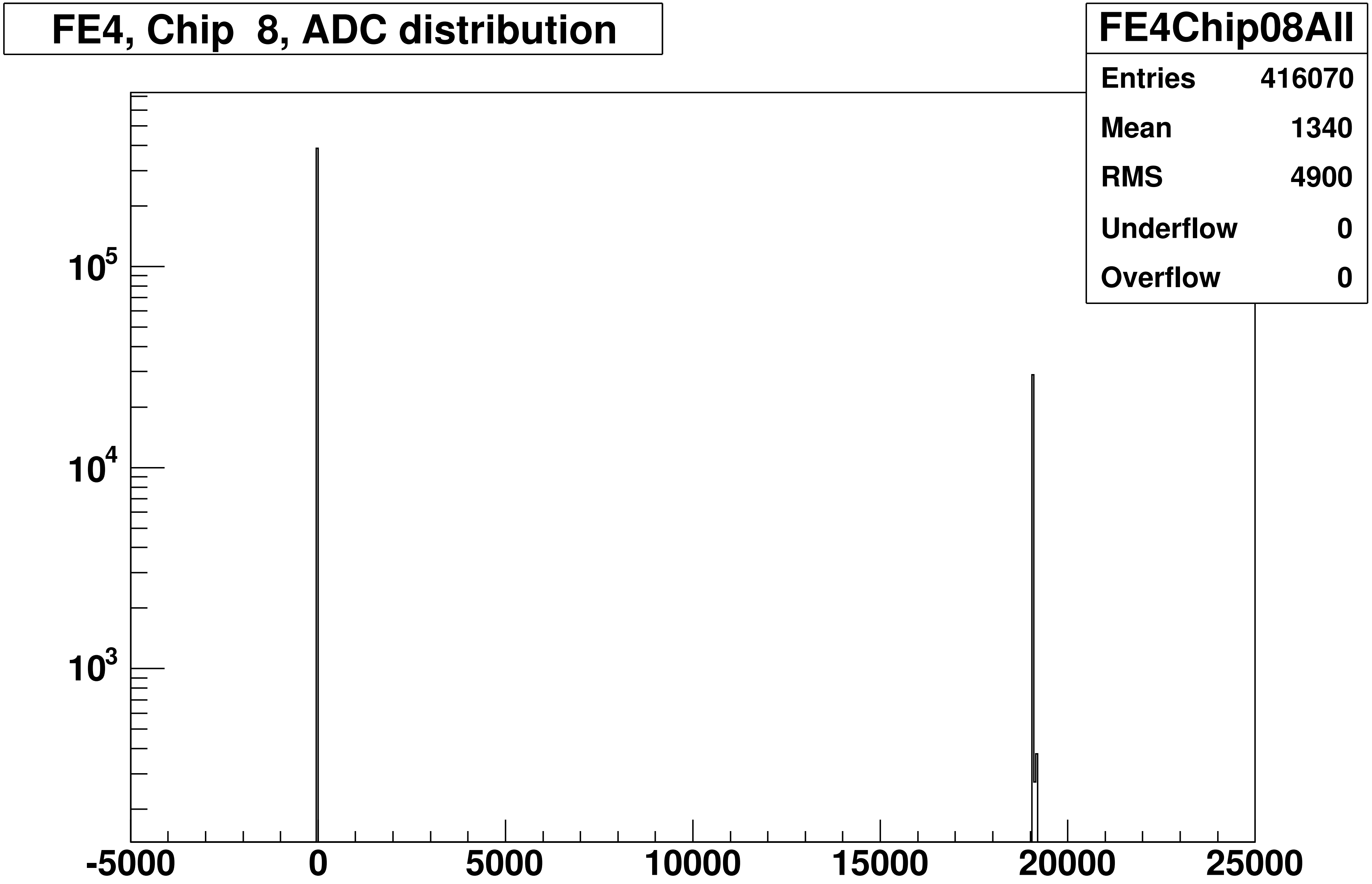
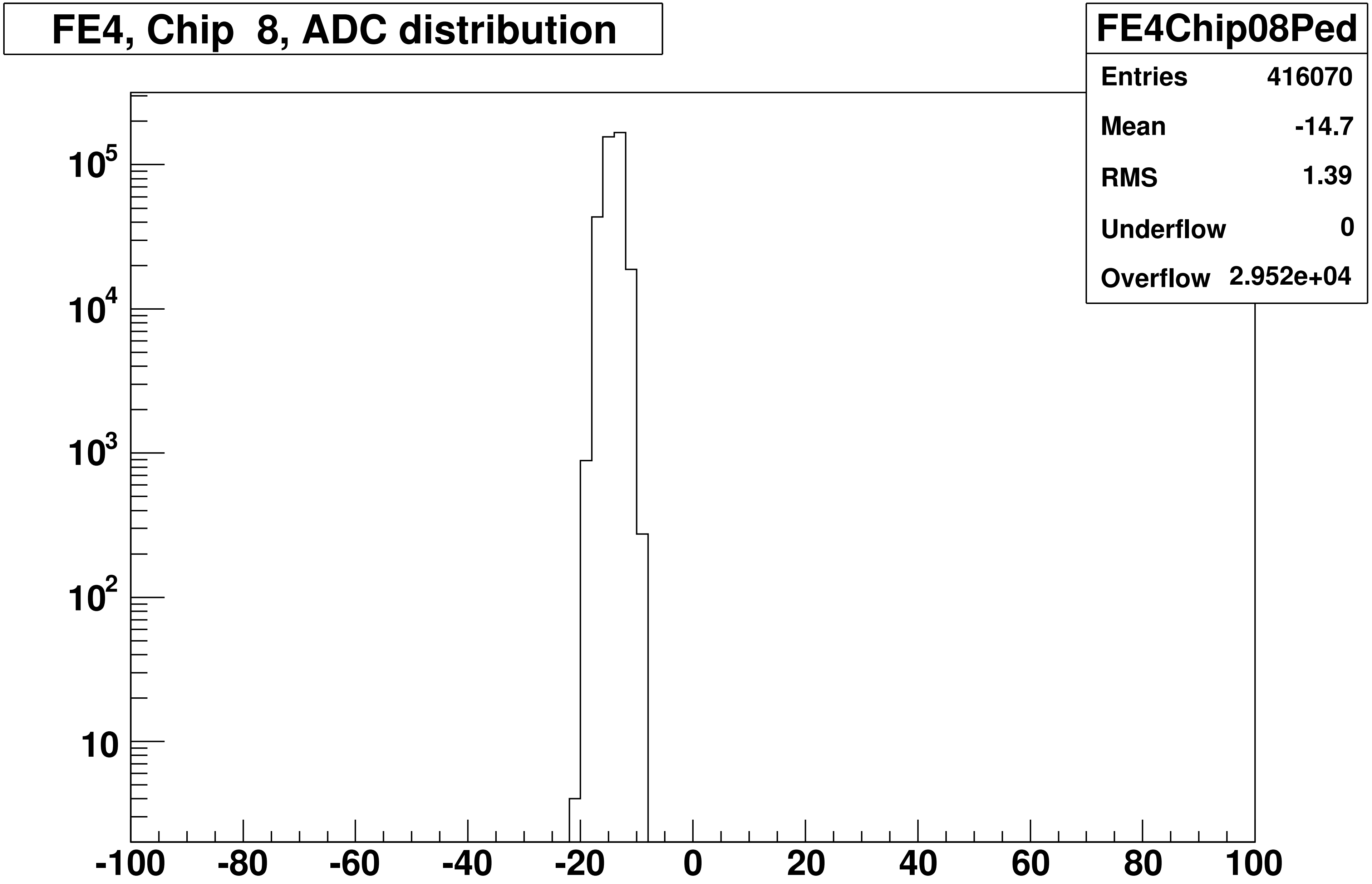

FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 416070
Mean 1.901e+04
RMS 12.75
Underflow 3.865e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 416070
Mean 1340
RMS 4900
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 416070
Mean -14.7
RMS 1.39
Underflow 0
Overflow 2.952e+04
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10^4
10^3
10^2
10
-100
-80
-60
-40
-20
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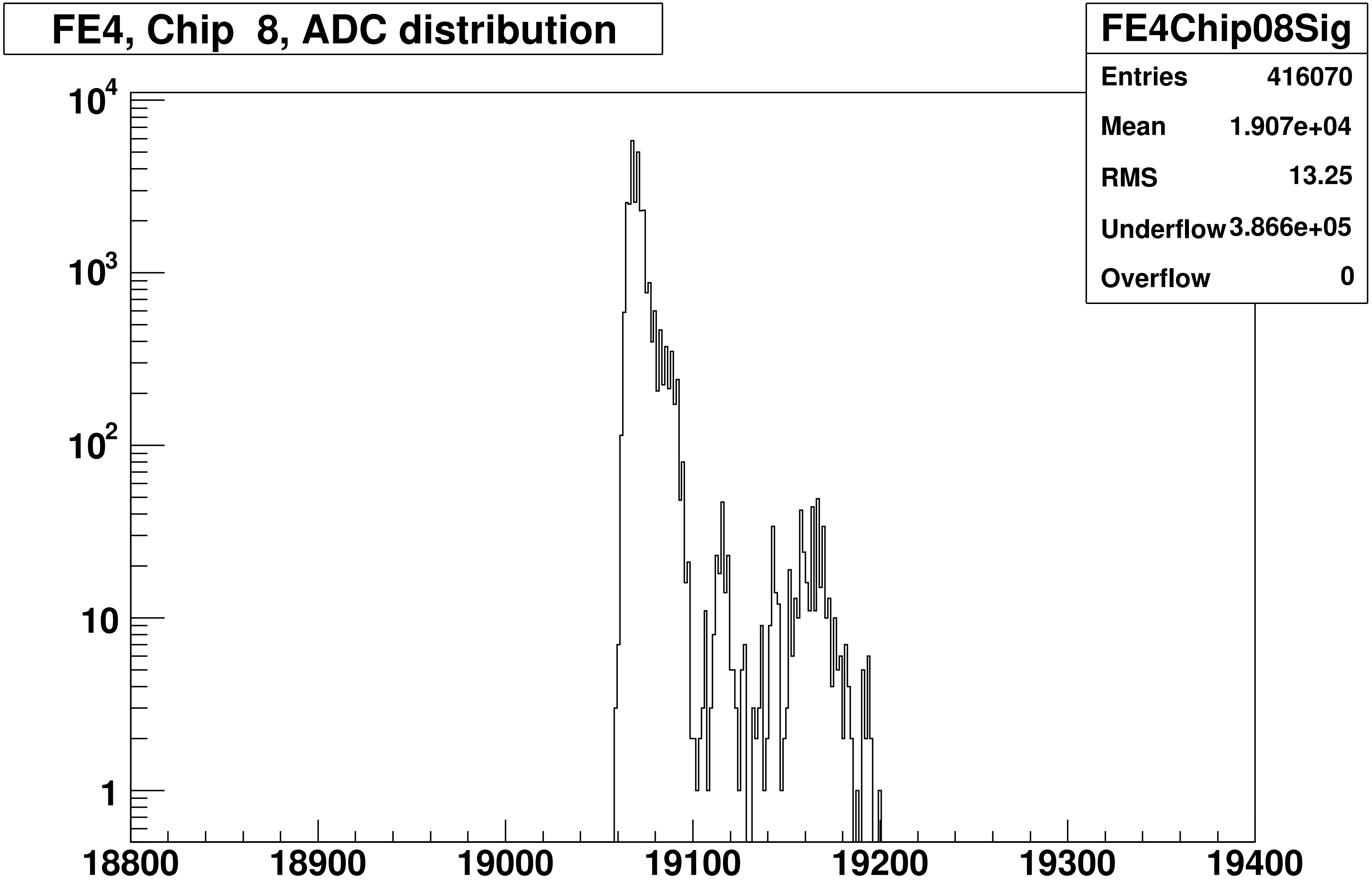
40
60
80
100



FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 416070
Mean 1.907e+04
RMS 13.25
Underflow 3.866e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
19100
19200
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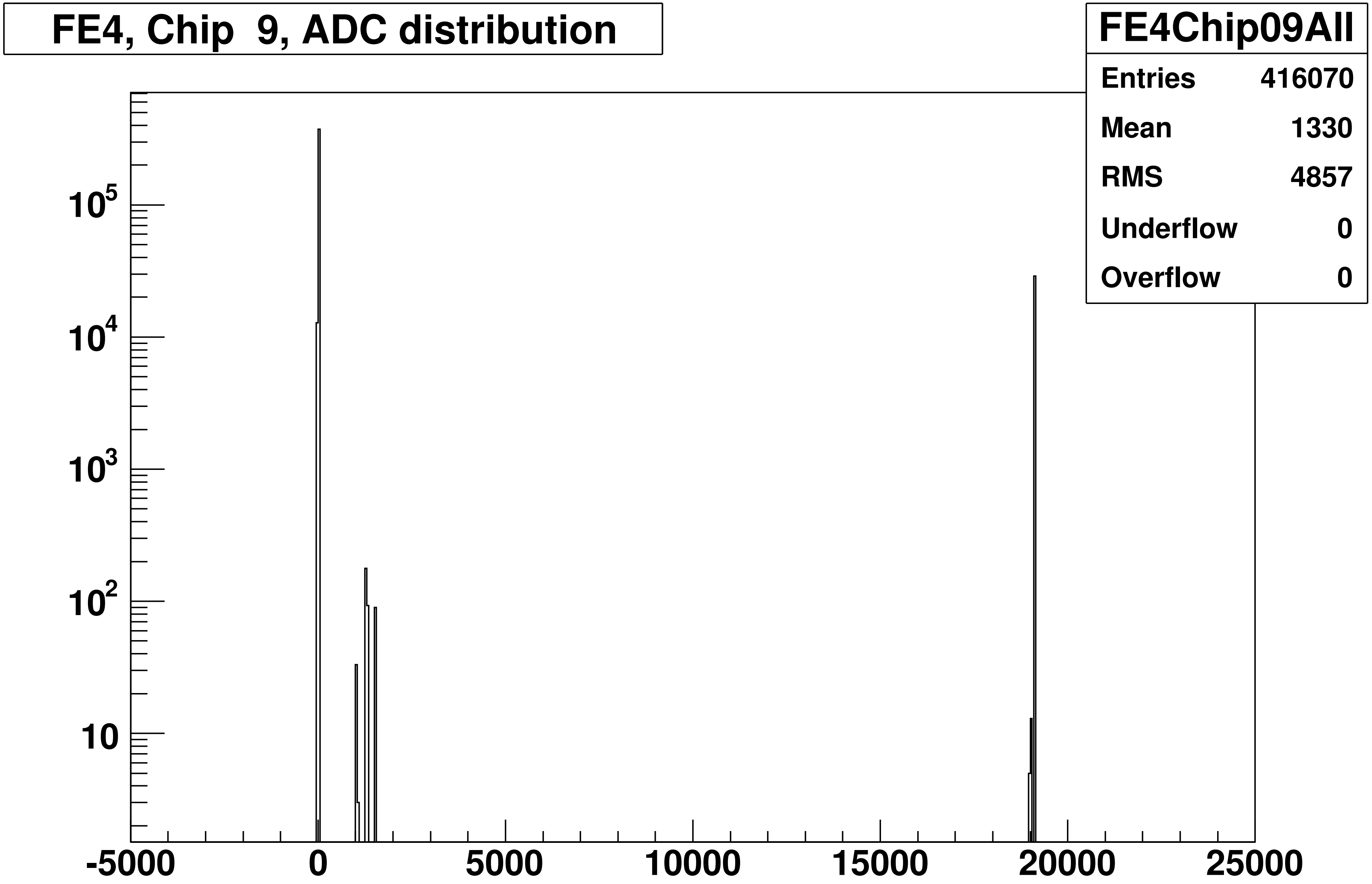
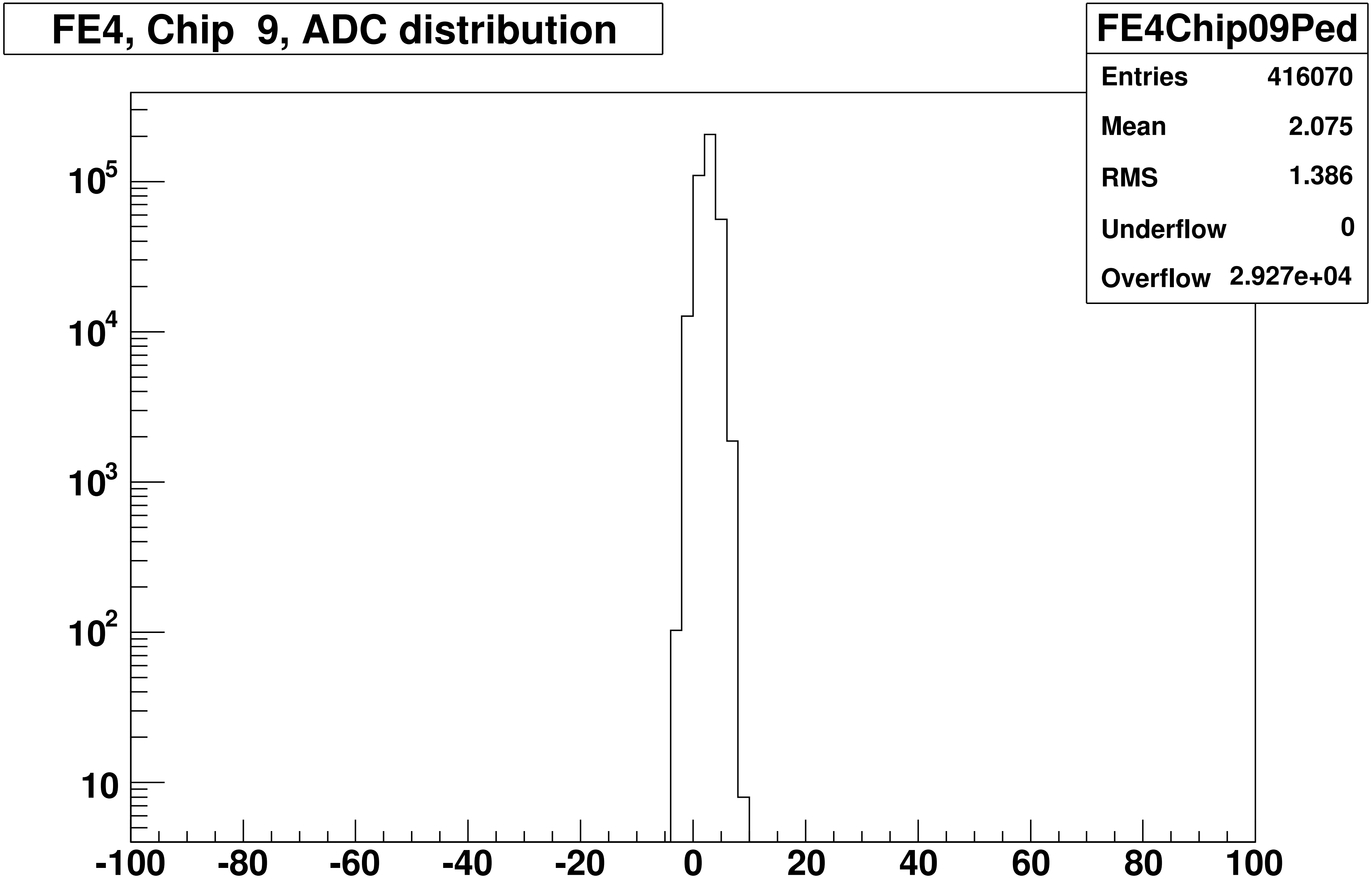
19400


FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 416070
Mean 1330
RMS 4857
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 416070
Mean 2.075
RMS 1.386
Underflow 0
Overflow 2.927e+04
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10^4
10^3
10^2
10
-100
-80
-60
-40
-20
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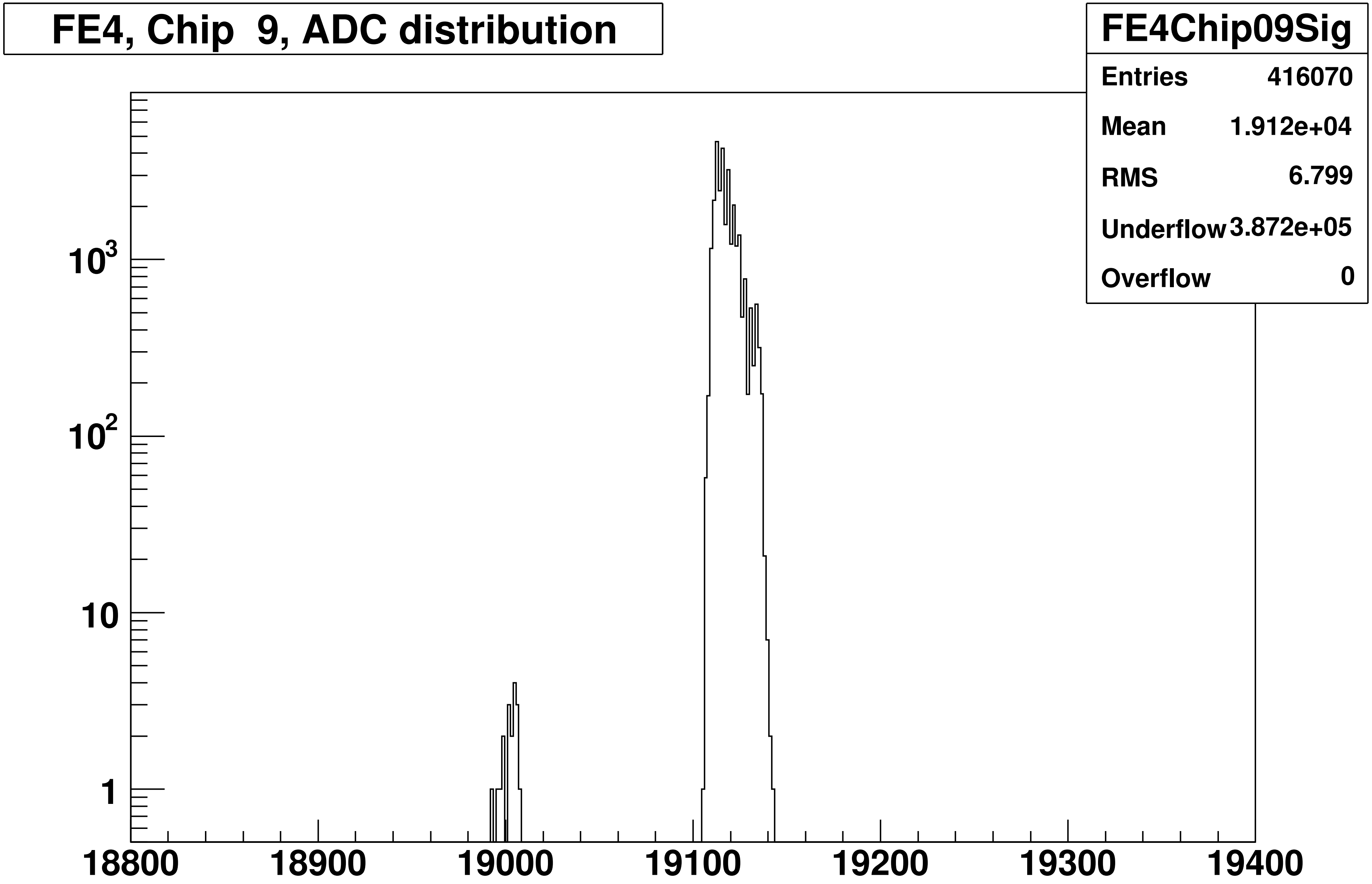
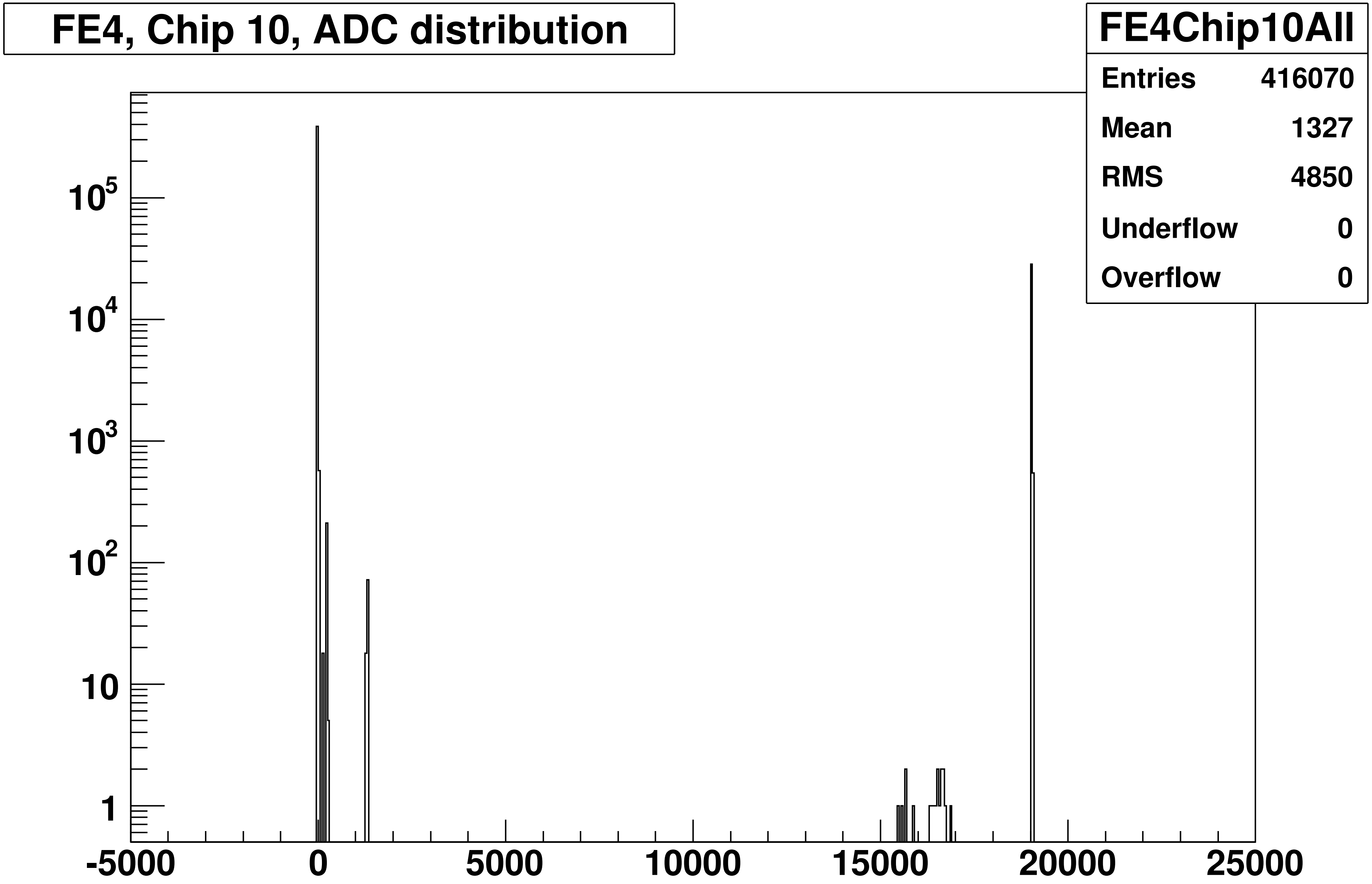
40
60
80
100



FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 416070
Mean 1.912e+04
RMS 6.799
Underflow 3.872e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 416070
Mean 1327
RMS 4850
Underflow 0
Overflow 0
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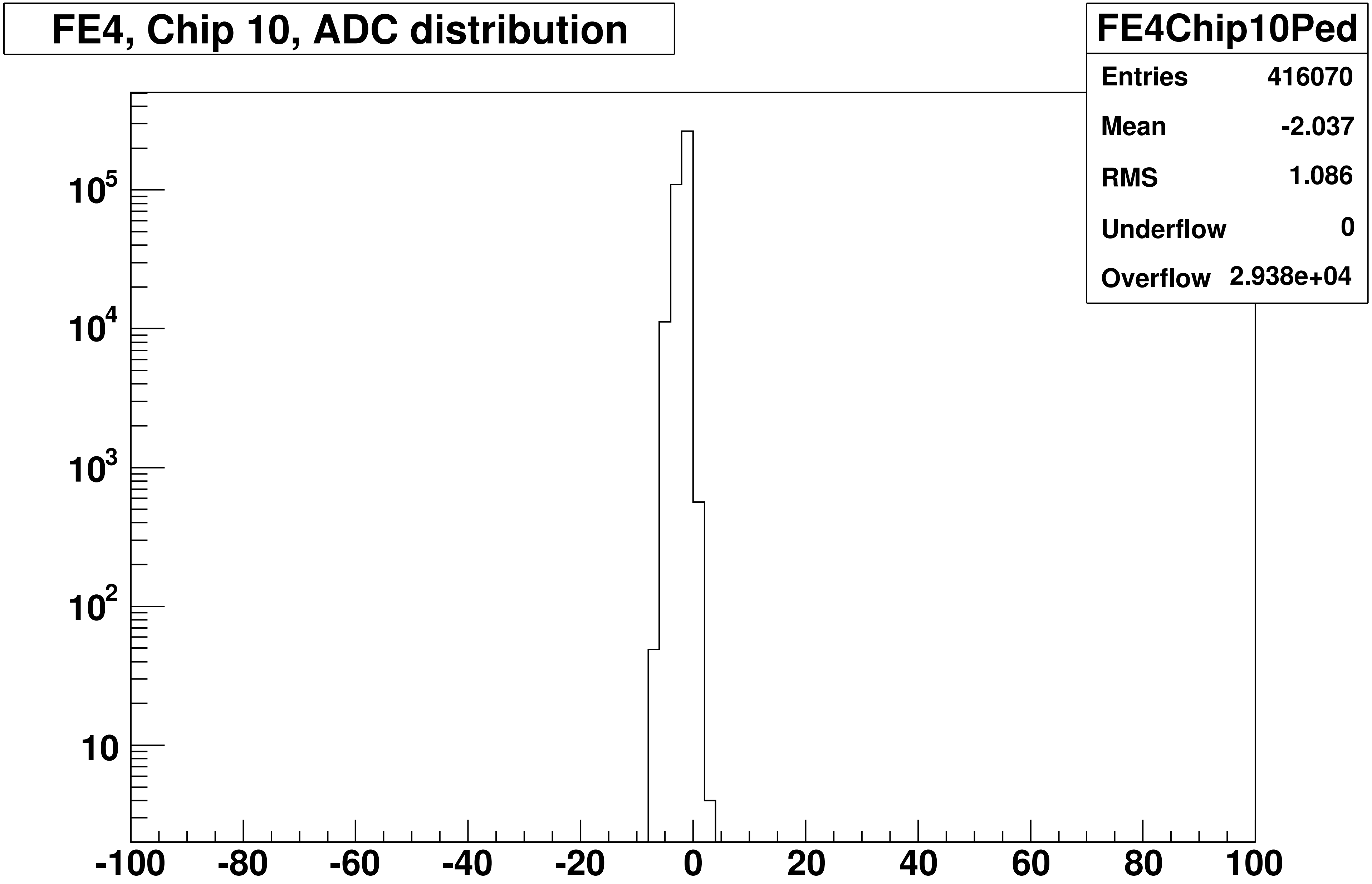
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 416070
Mean -2.037
RMS 1.086
Underflow 0
Overflow 2.938e+04
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10^4
10^3
10^2
10
-100
-80
-60
-40
-20
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40
60
80
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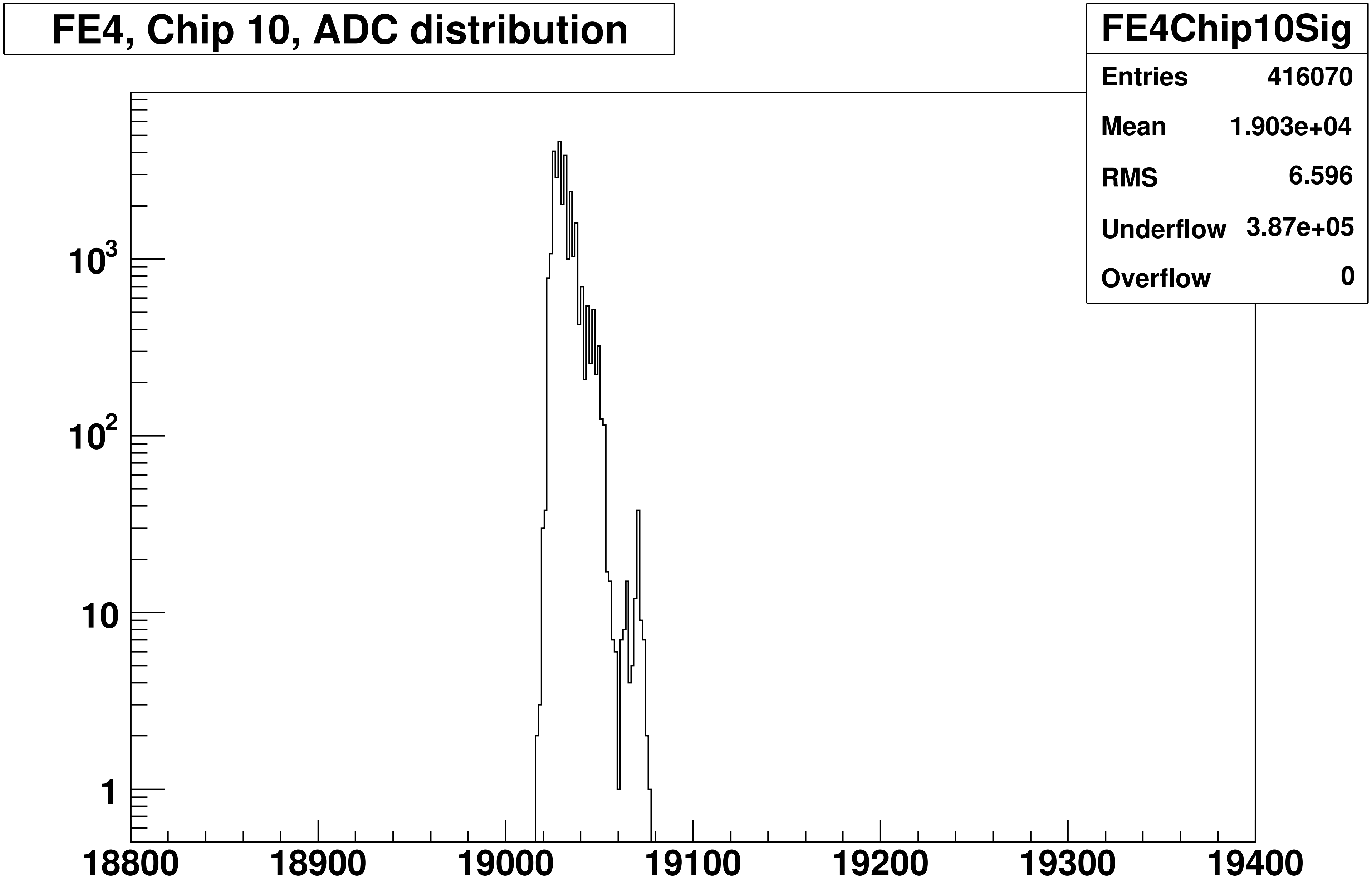
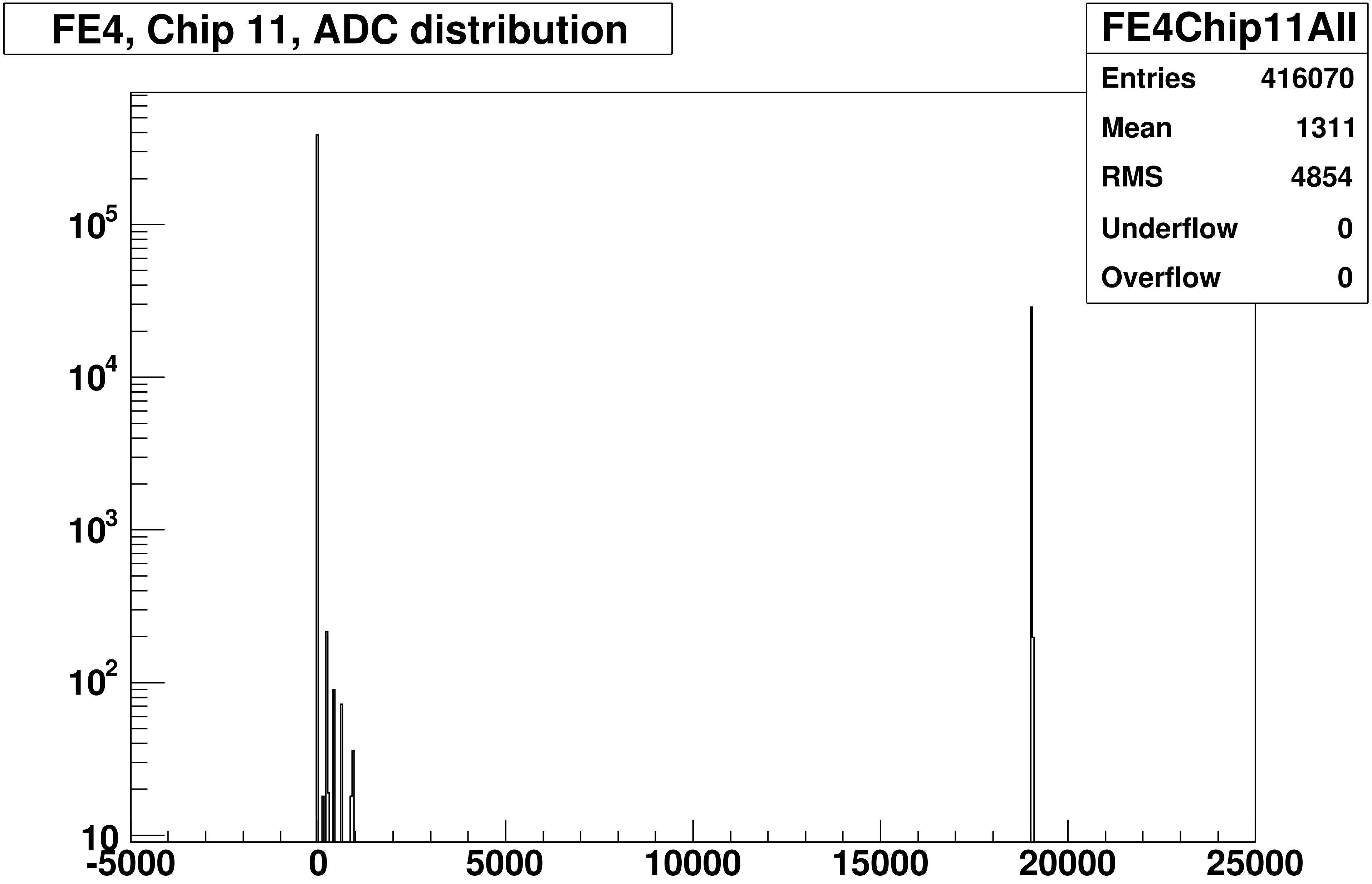
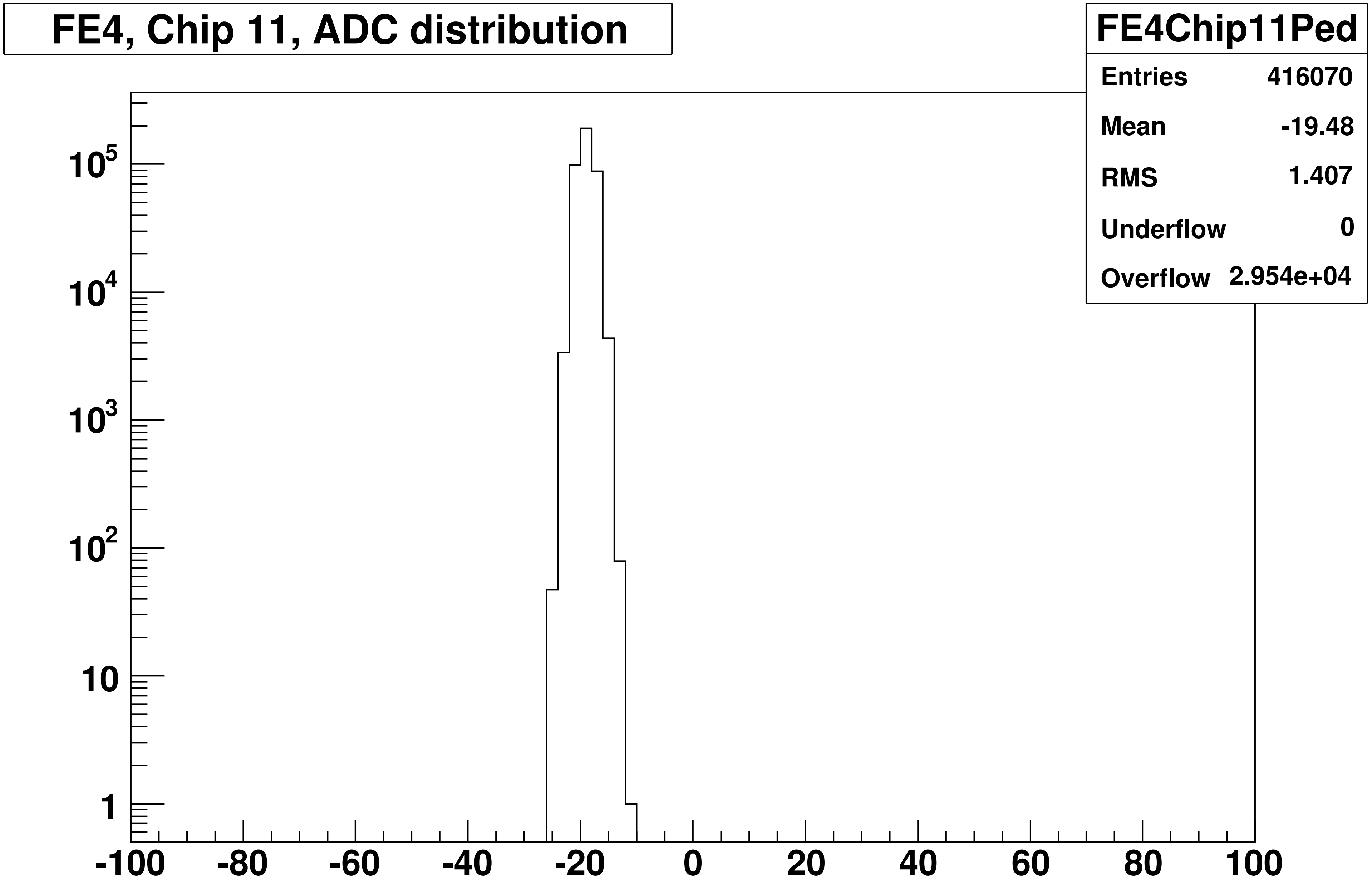
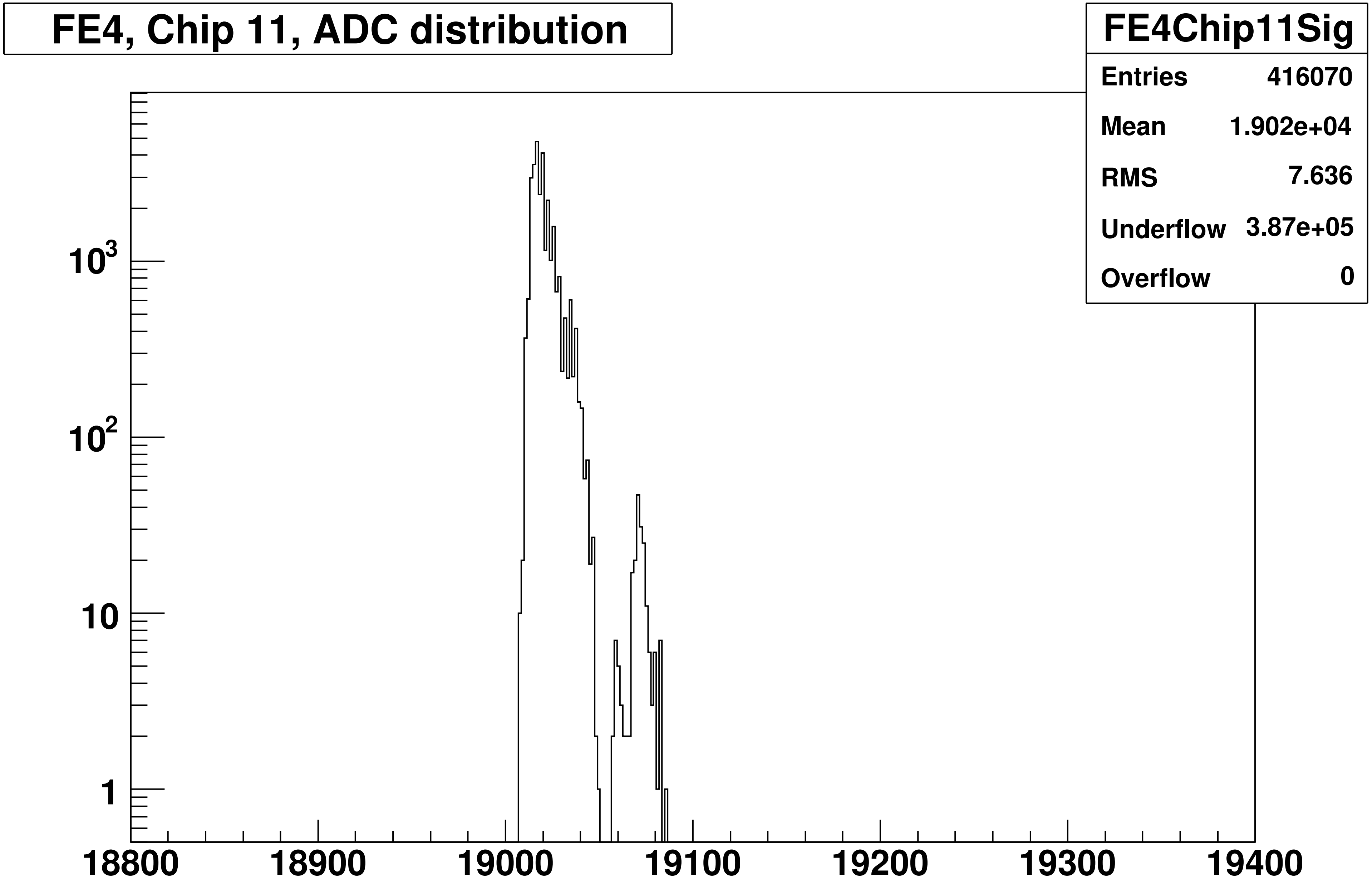


FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 416070
Mean 1.903e+04
RMS 6.596
Underflow 3.87e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

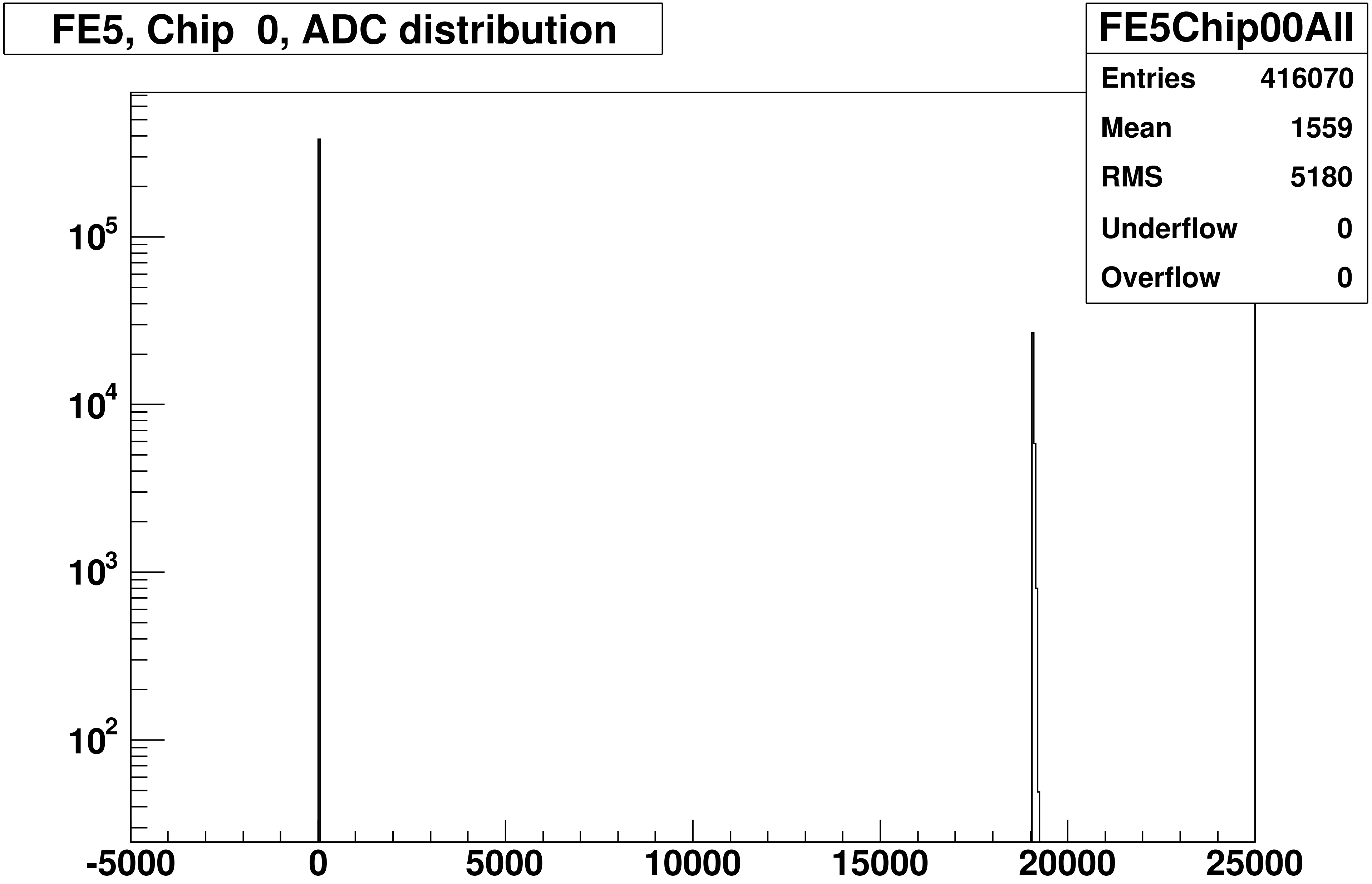


FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 416070
Mean 1311
RMS 4854
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

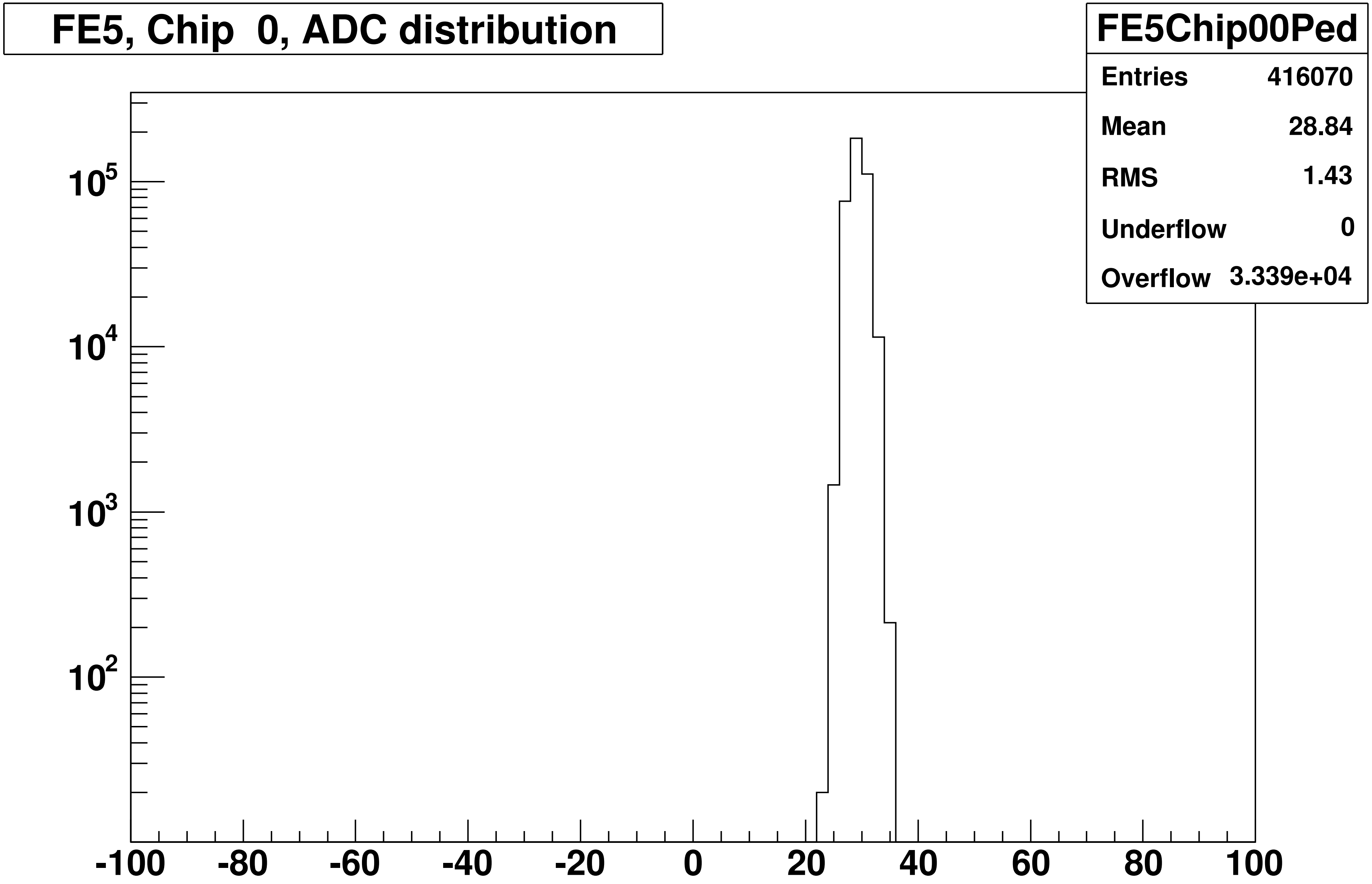

FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 416070
Mean -19.48
RMS 1.407
Underflow 0
Overflow 2.954e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

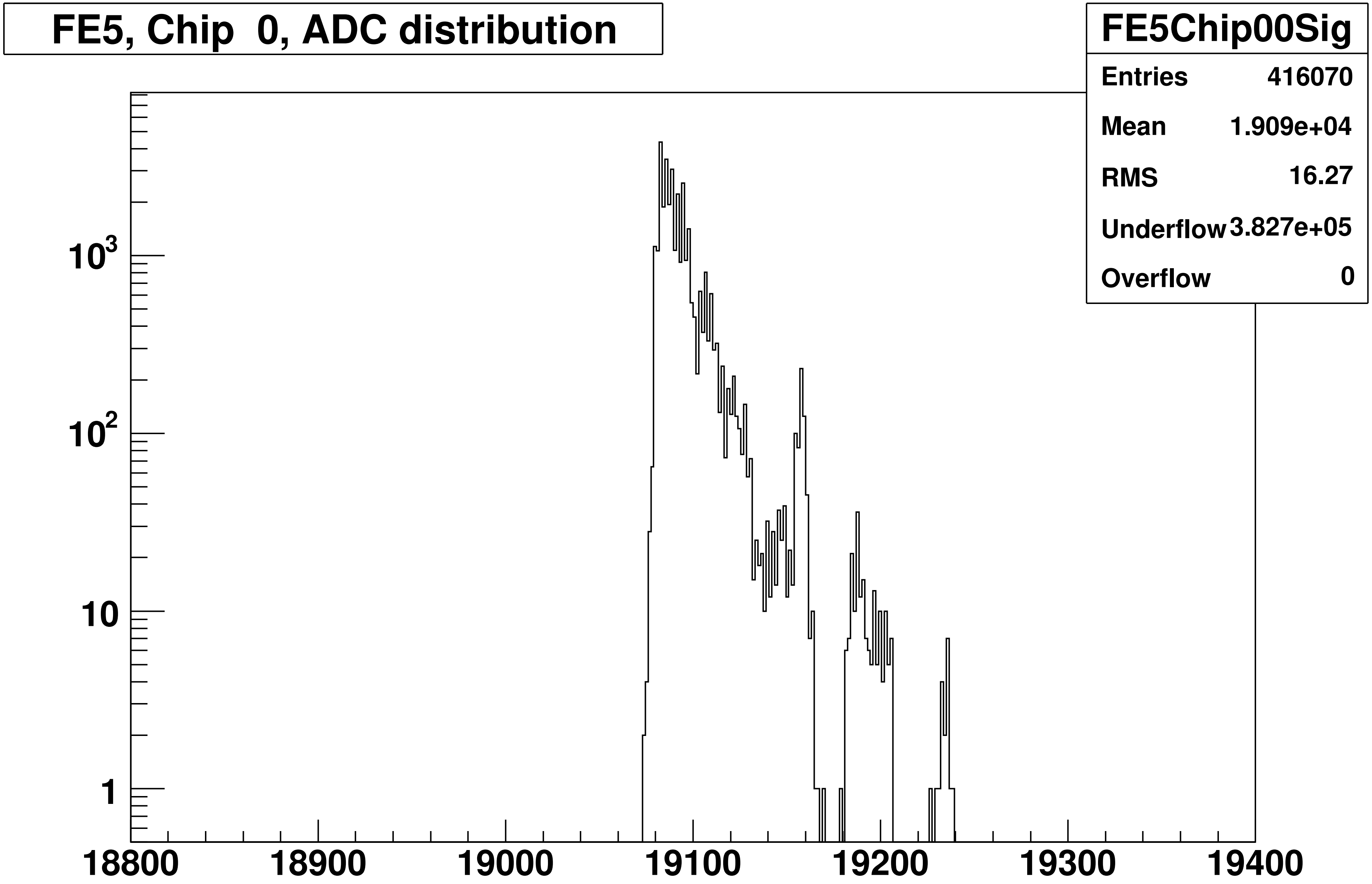


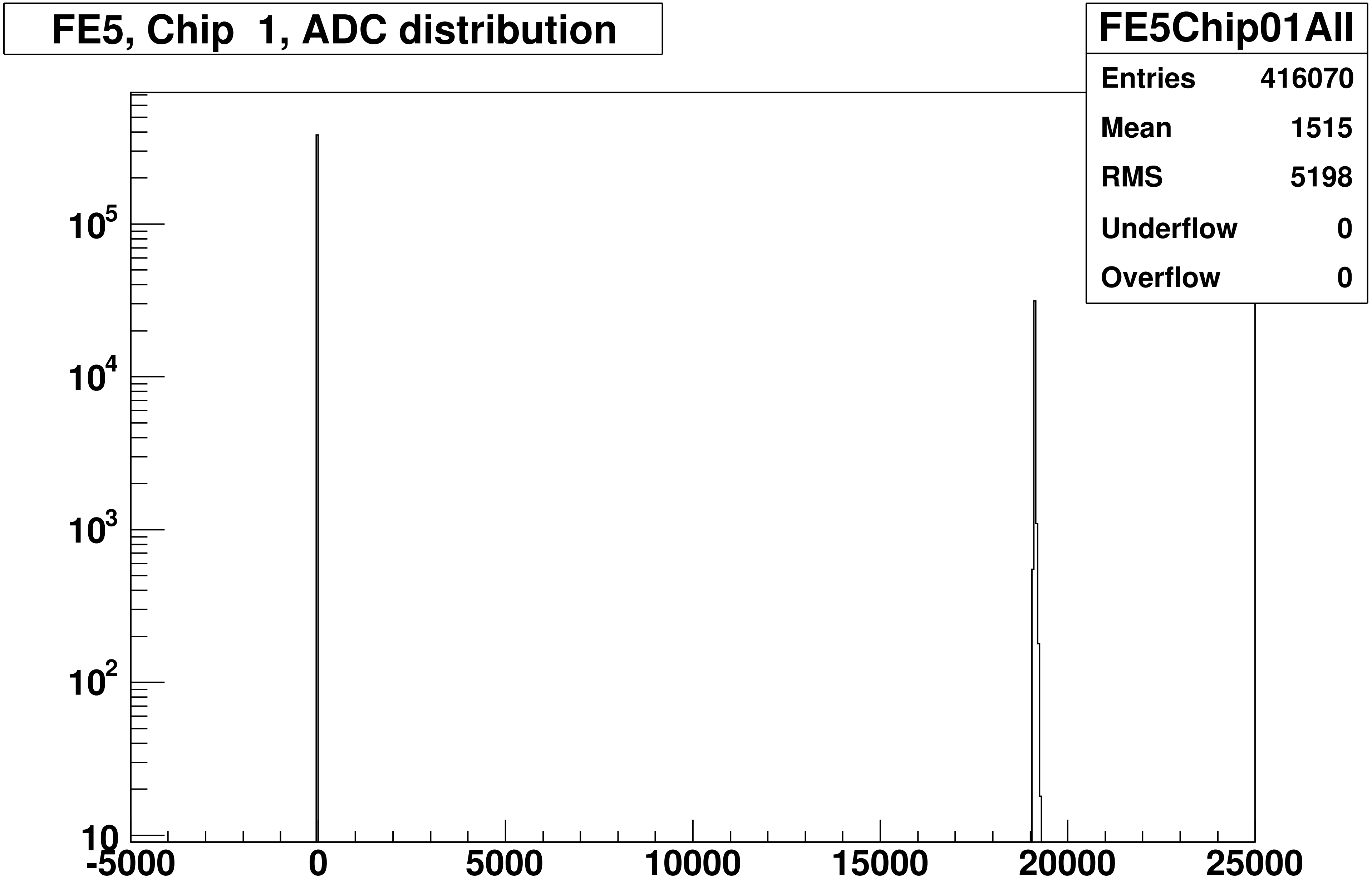
FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 416070
Mean 1.902e+04
RMS 7.636
Underflow 3.87e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



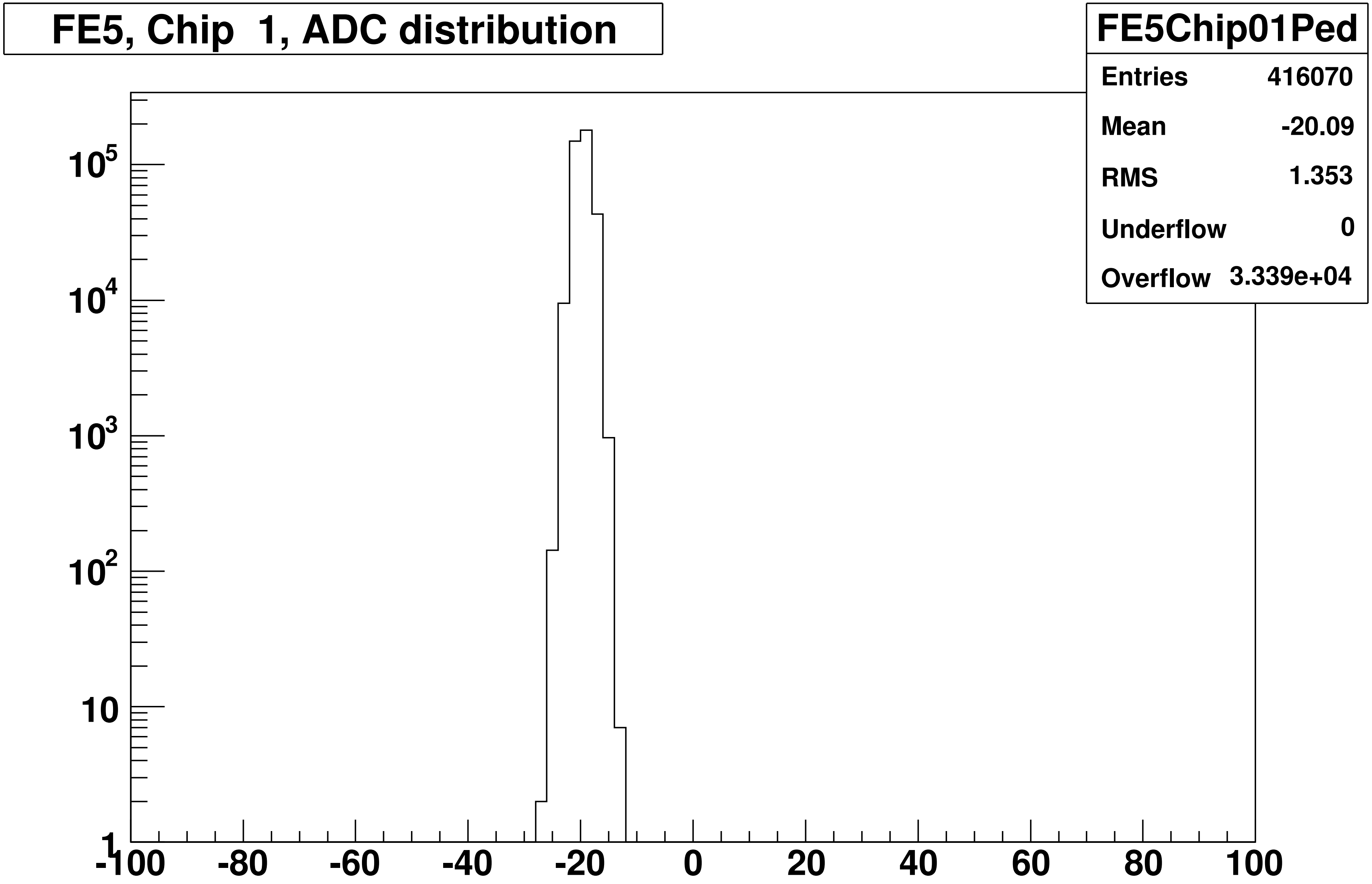
FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 416070
Mean 1559
RMS 5180
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 416070
Mean 28.84
RMS 1.43
Underflow 0
Overflow 3.339e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

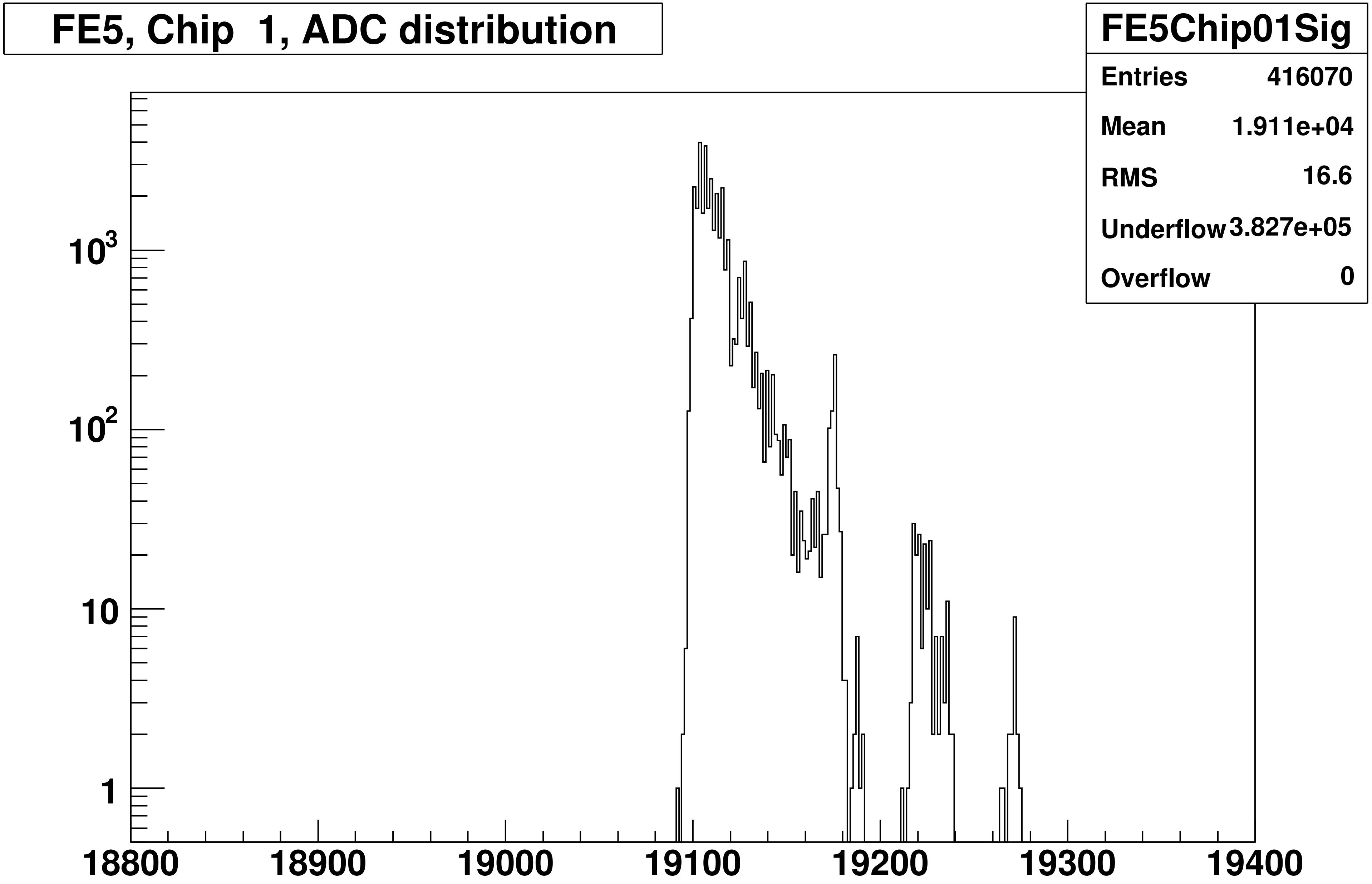

FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 416070
Mean 1.909e+04
RMS 16.27
Underflow 3.827e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

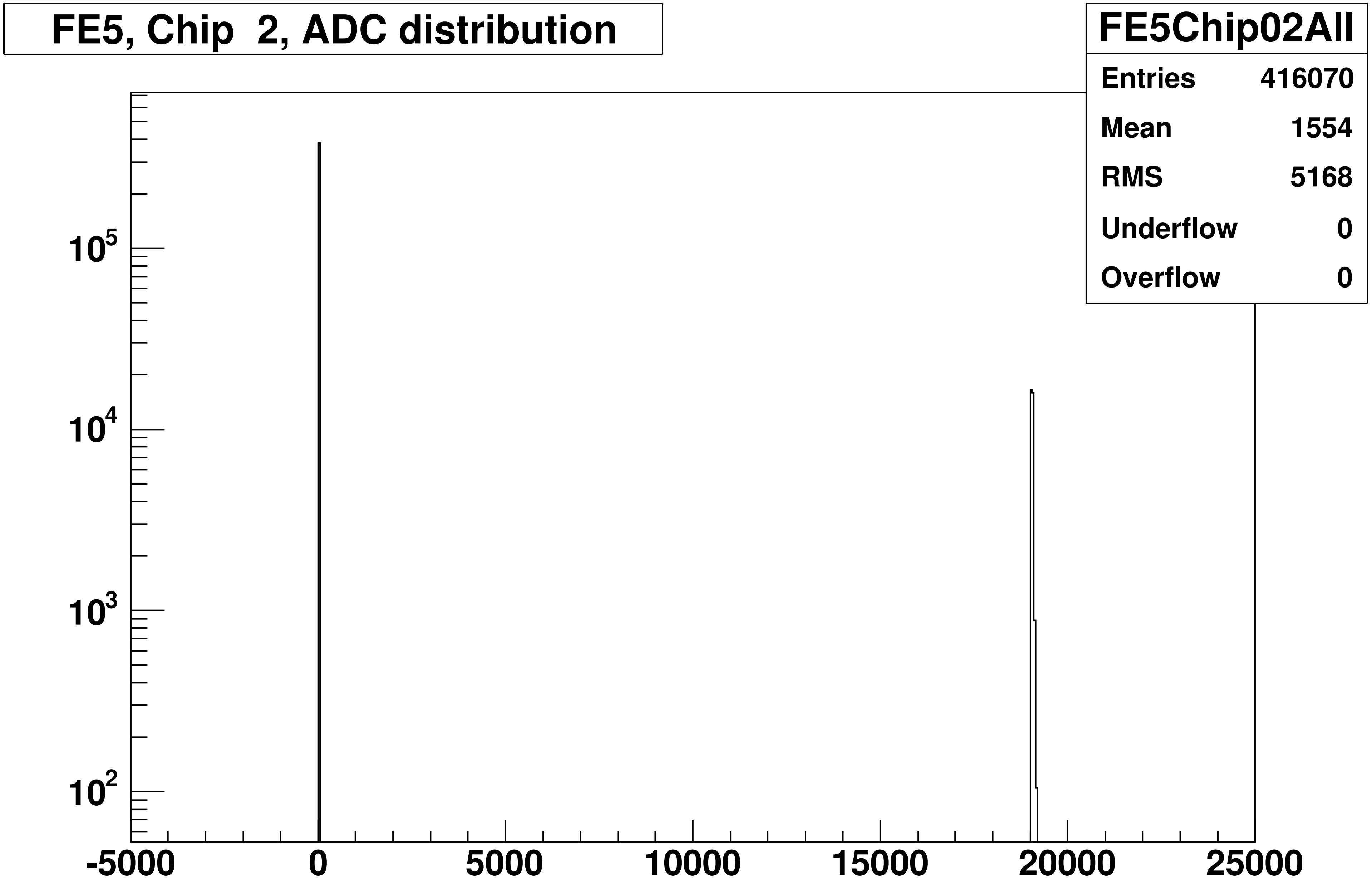


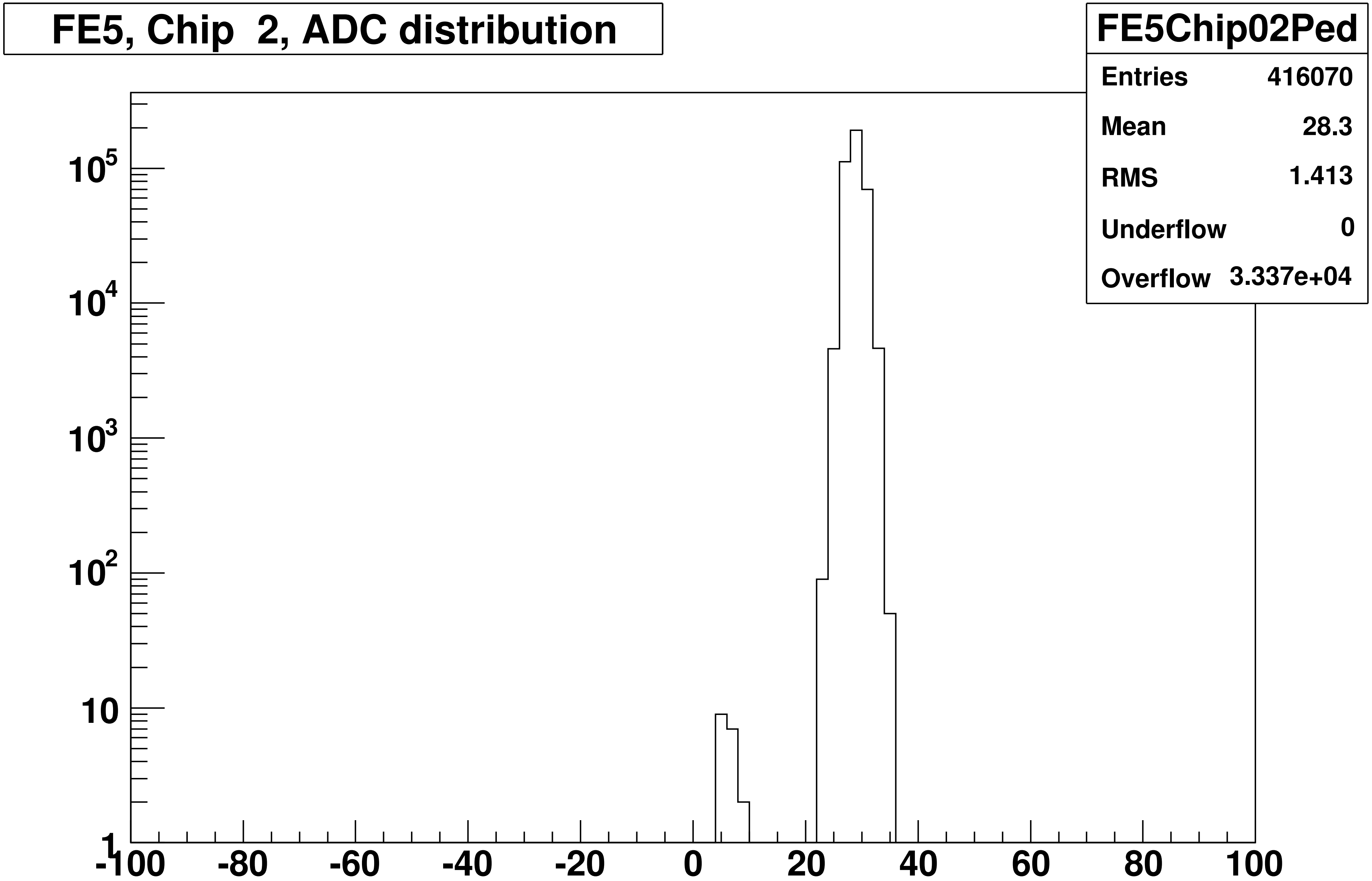
FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 416070
Mean 1515
RMS 5198
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

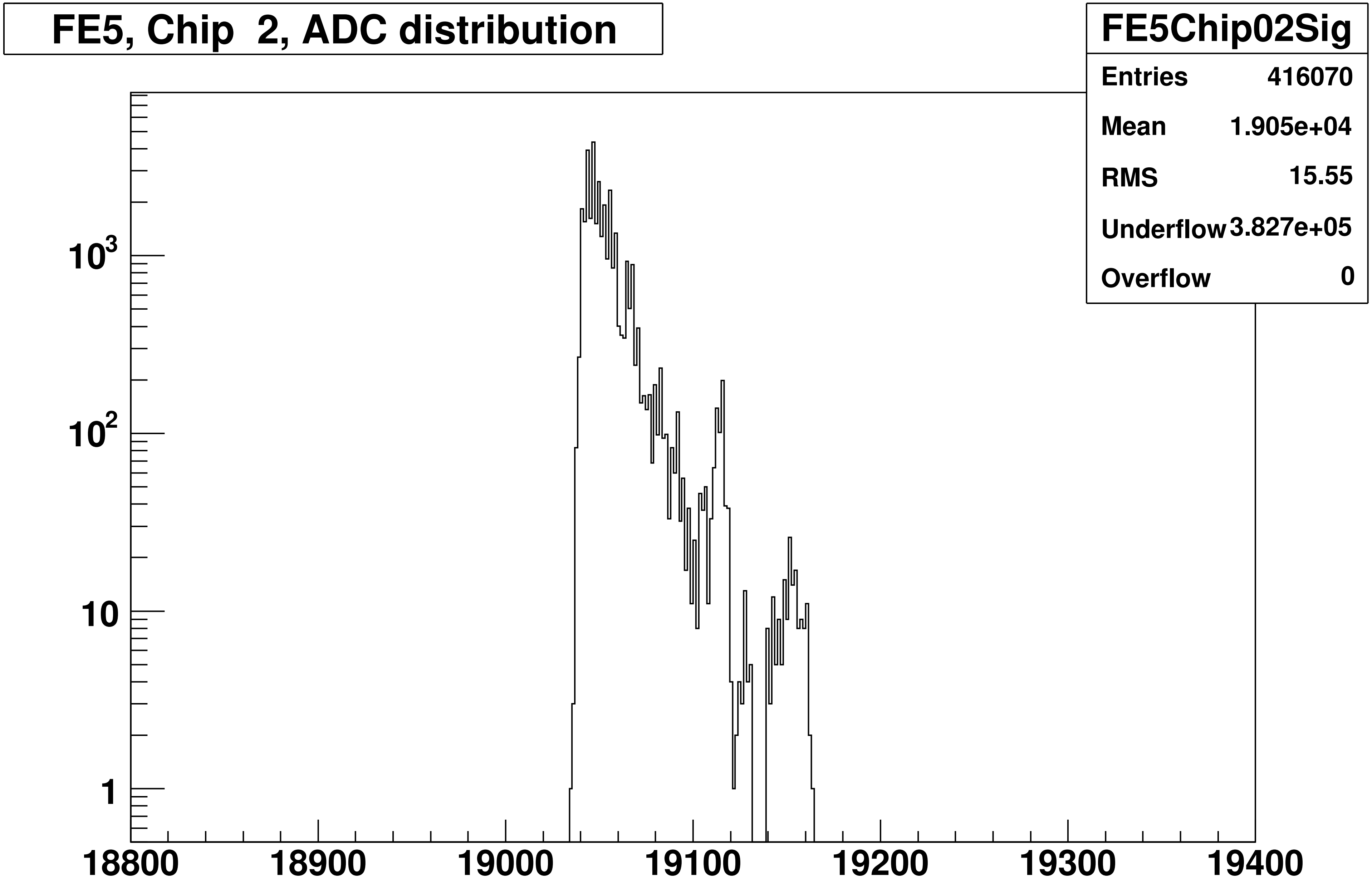


FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 416070
Mean -20.09
RMS 1.353
Underflow 0
Overflow 3.339e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 416070
Mean 1.911e+04
RMS 16.6
Underflow 3.827e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 416070
Mean 1554
RMS 5168
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 416070
Mean 28.3
RMS 1.413
Underflow 0
Overflow 3.337e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 416070
Mean 1.905e+04
RMS 15.55
Underflow 3.827e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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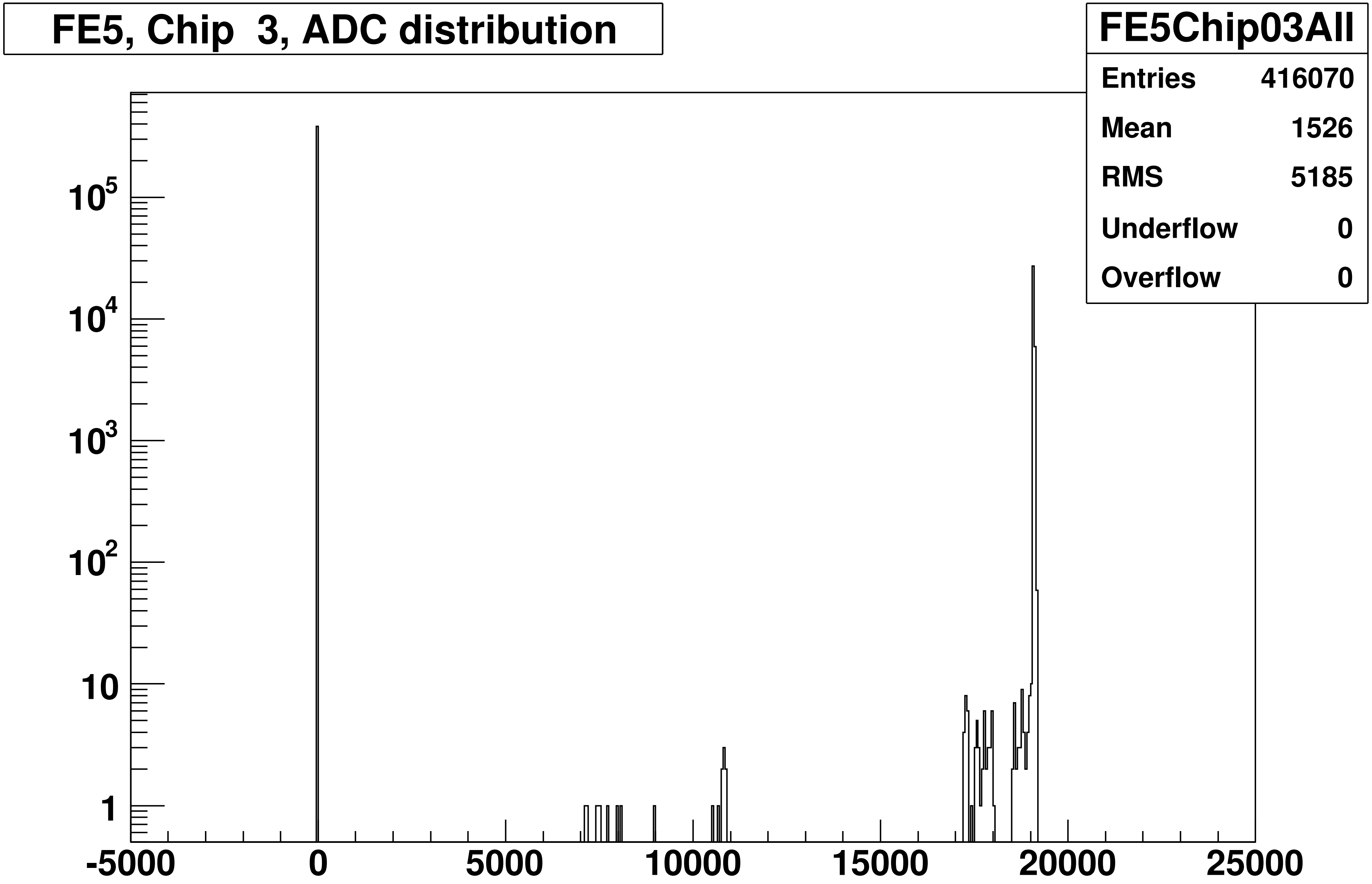
19100
19200
19300
19400


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 416070
Mean 1526
RMS 5185
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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10000
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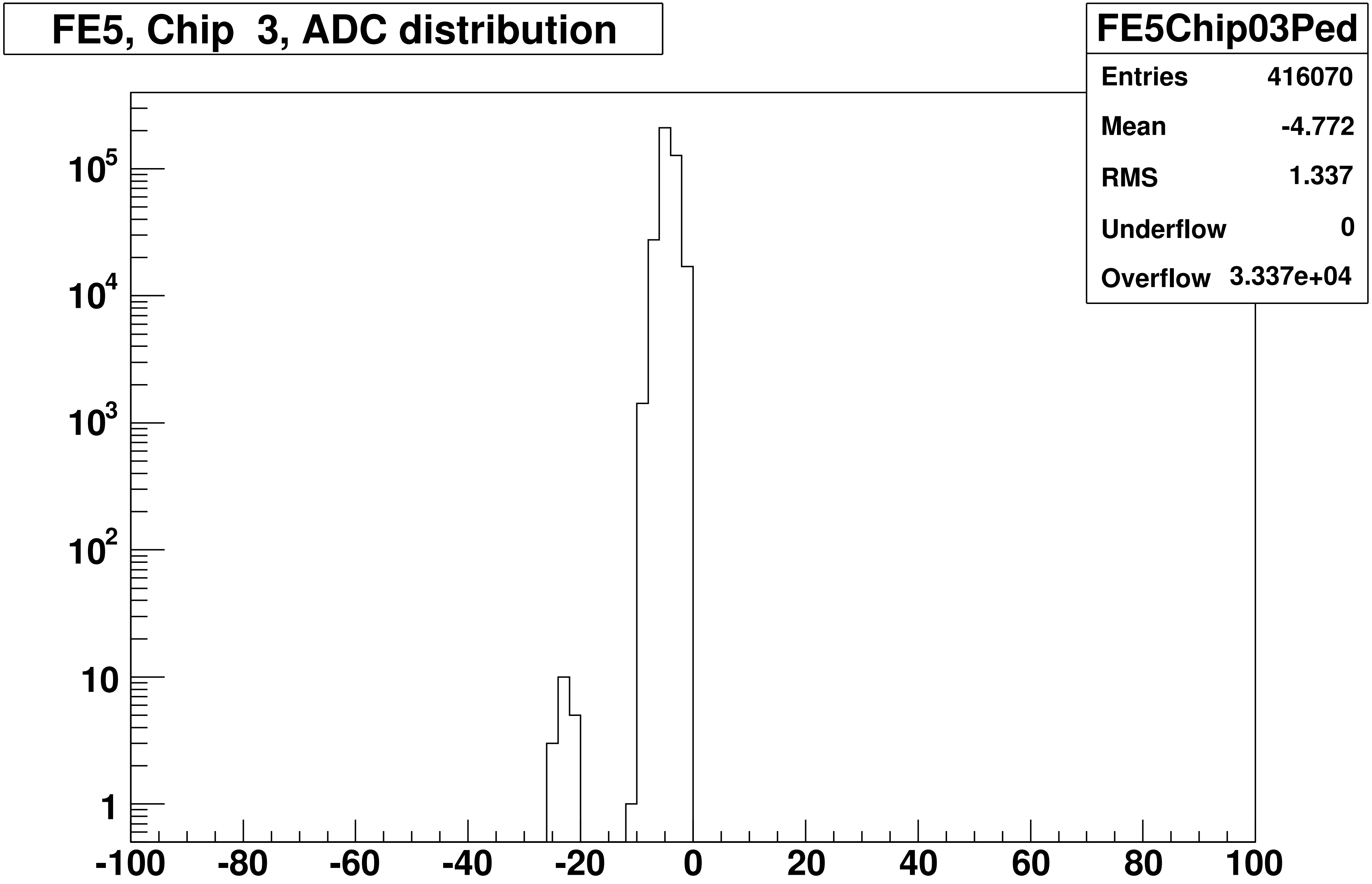
20000
25000


FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 416070
Mean -4.772
RMS 1.337
Underflow 0
Overflow 3.337e+04
10^5
10^4
10^3
10^2
10
1
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-20
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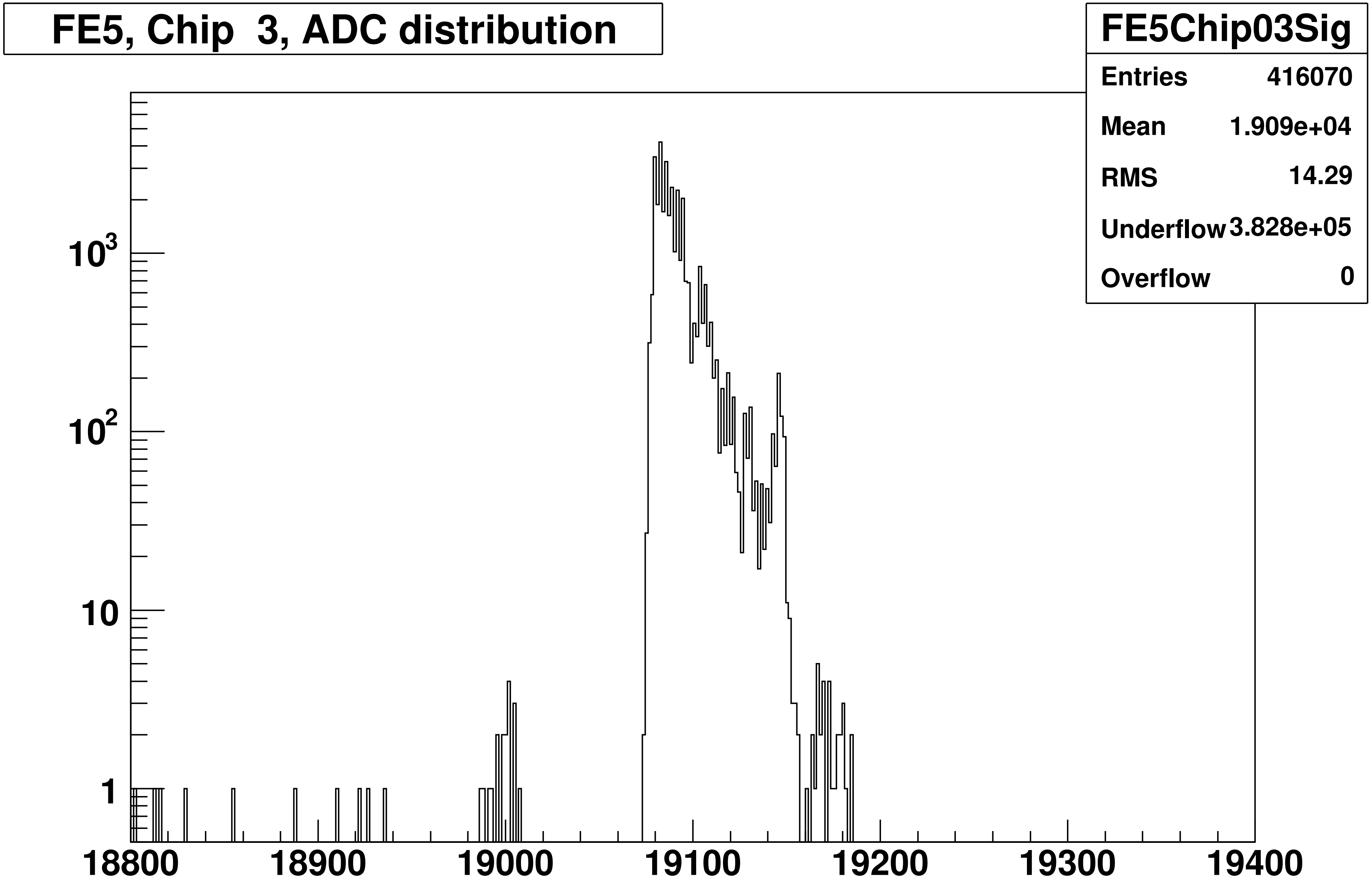

FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 416070
Mean 1.909e+04
RMS 14.29
Underflow 3.828e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
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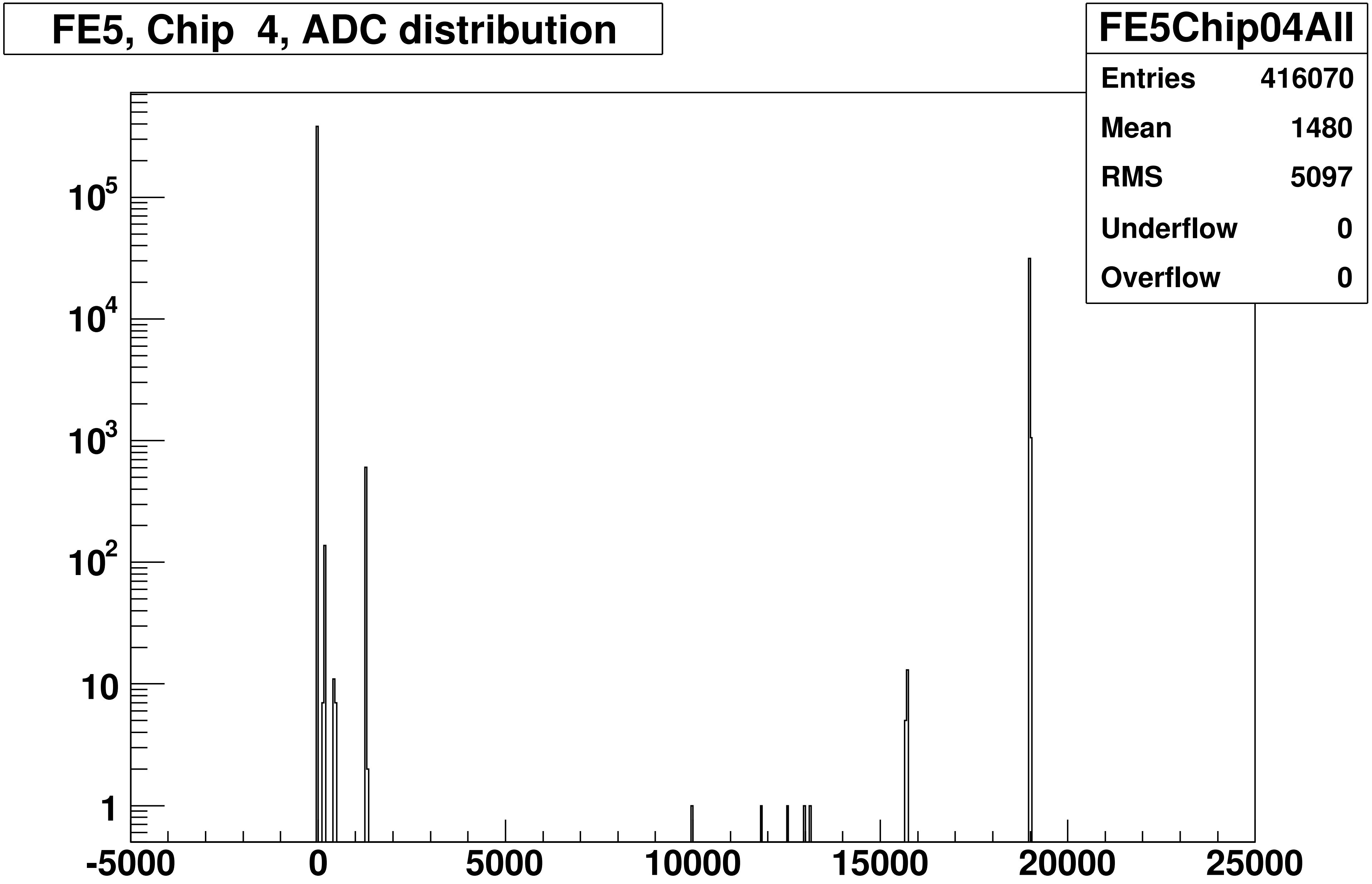
19300
19400



FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 416070
Mean 1480
RMS 5097
Underflow 0
Overflow 0
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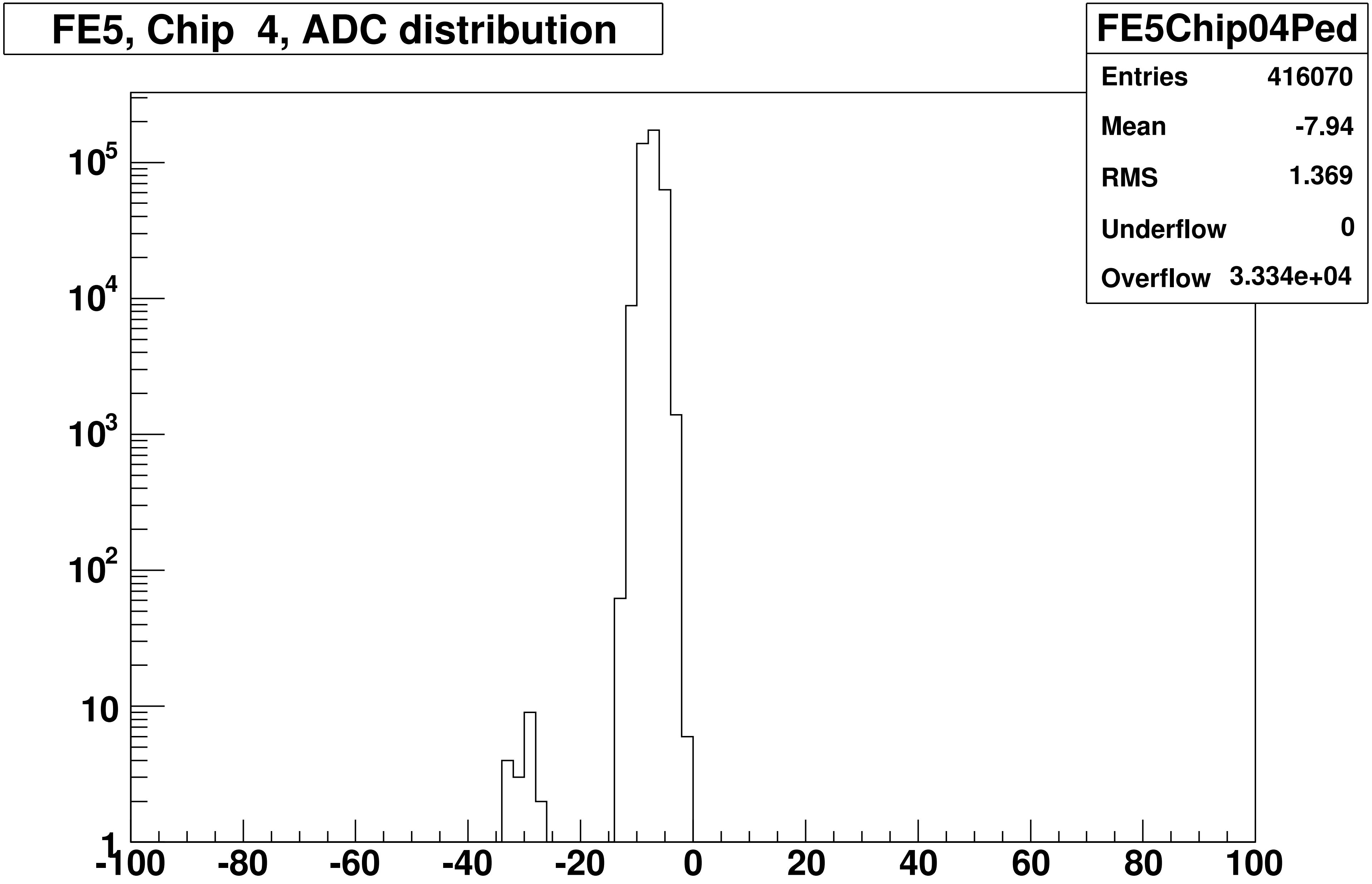
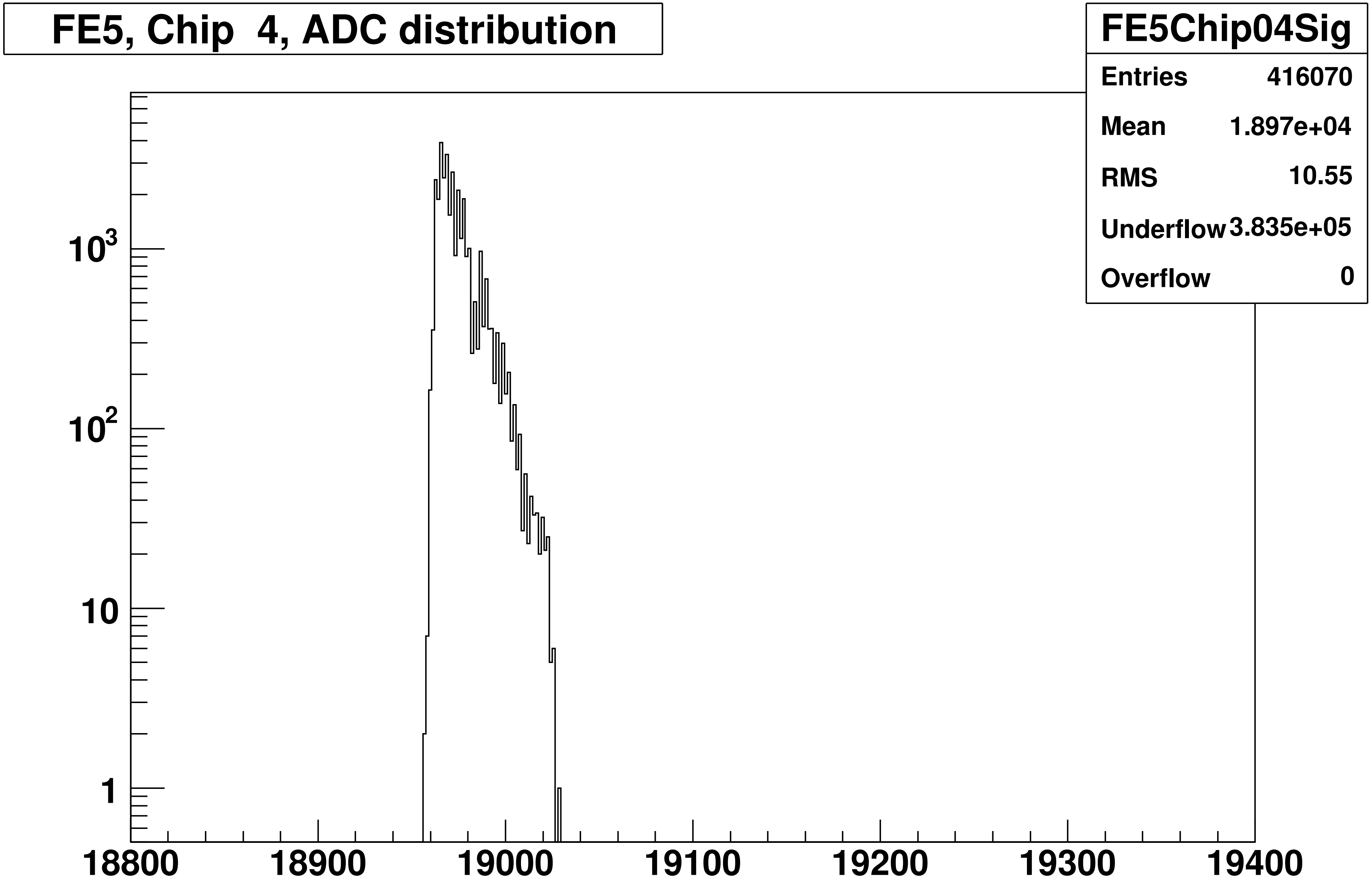
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 416070
Mean -7.94
RMS 1.369
Underflow 0
Overflow 3.334e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 416070
Mean 1.897e+04
RMS 10.55
Underflow 3.835e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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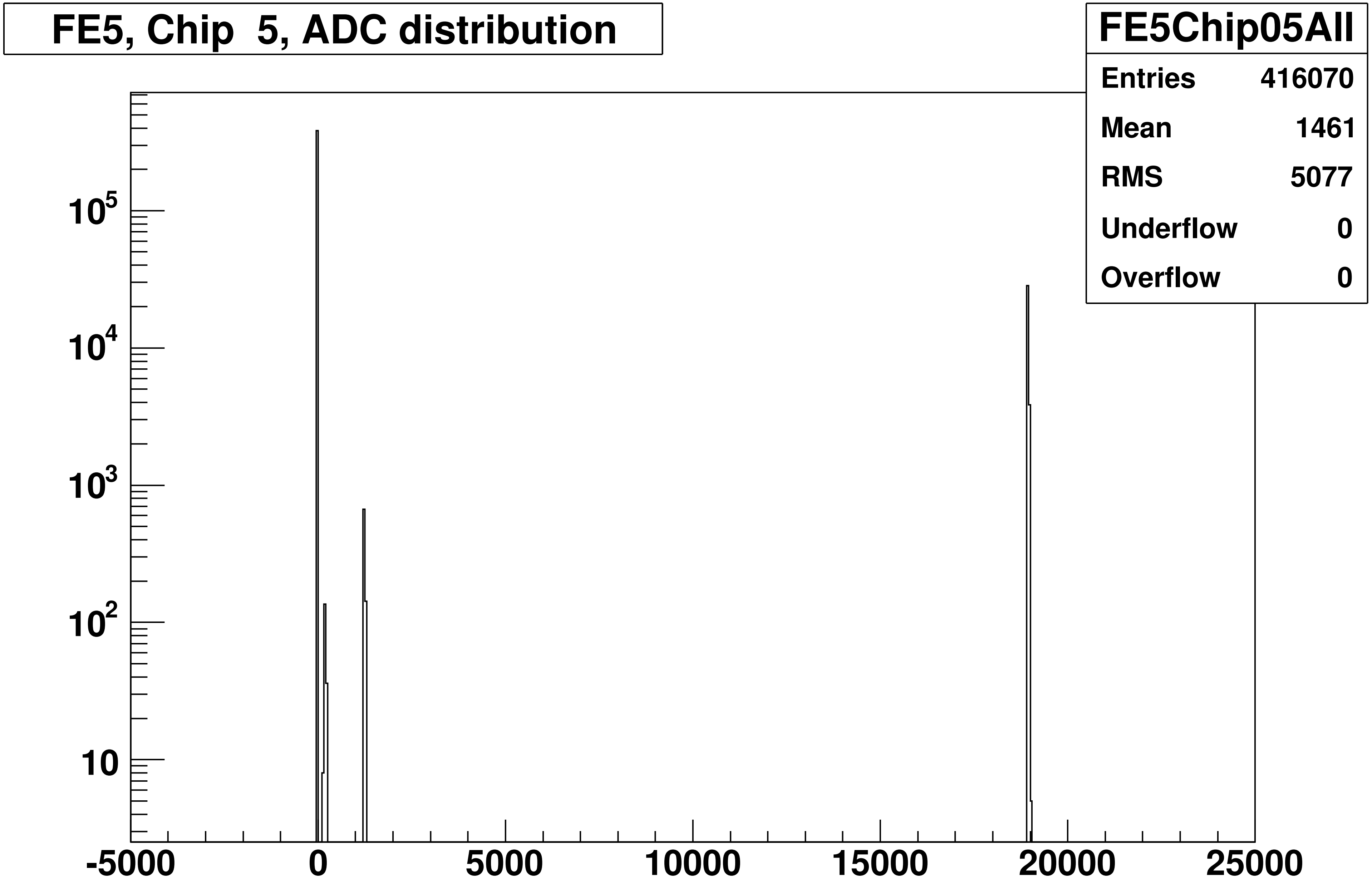
19300
19400


FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 416070
Mean 1461
RMS 5077
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
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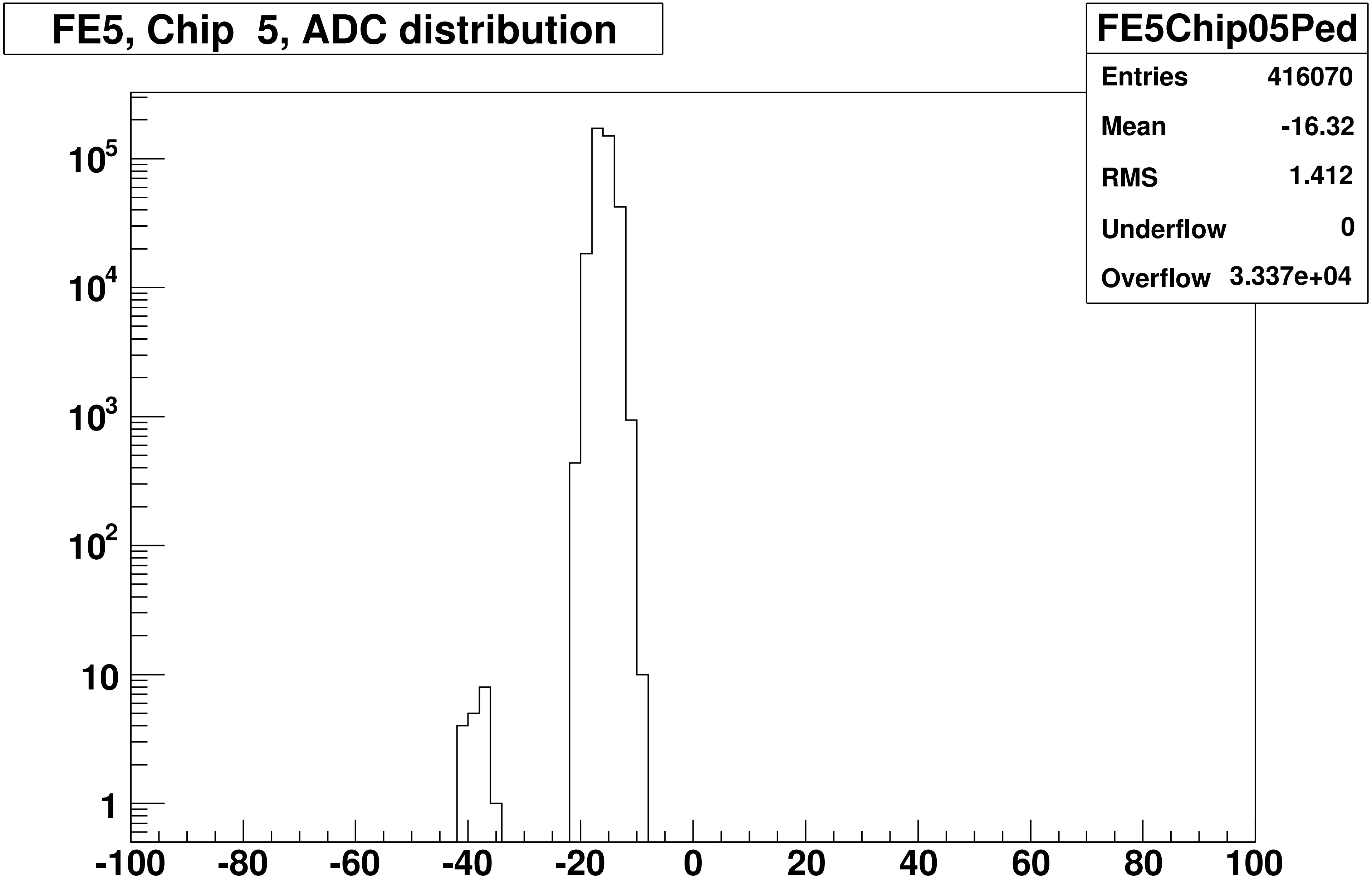
0
5000
10000
15000
20000
25000


FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 416070
Mean -16.32
RMS 1.412
Underflow 0
Overflow 3.337e+04
10^5
10^4
10^3
10^2
10
1
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-20
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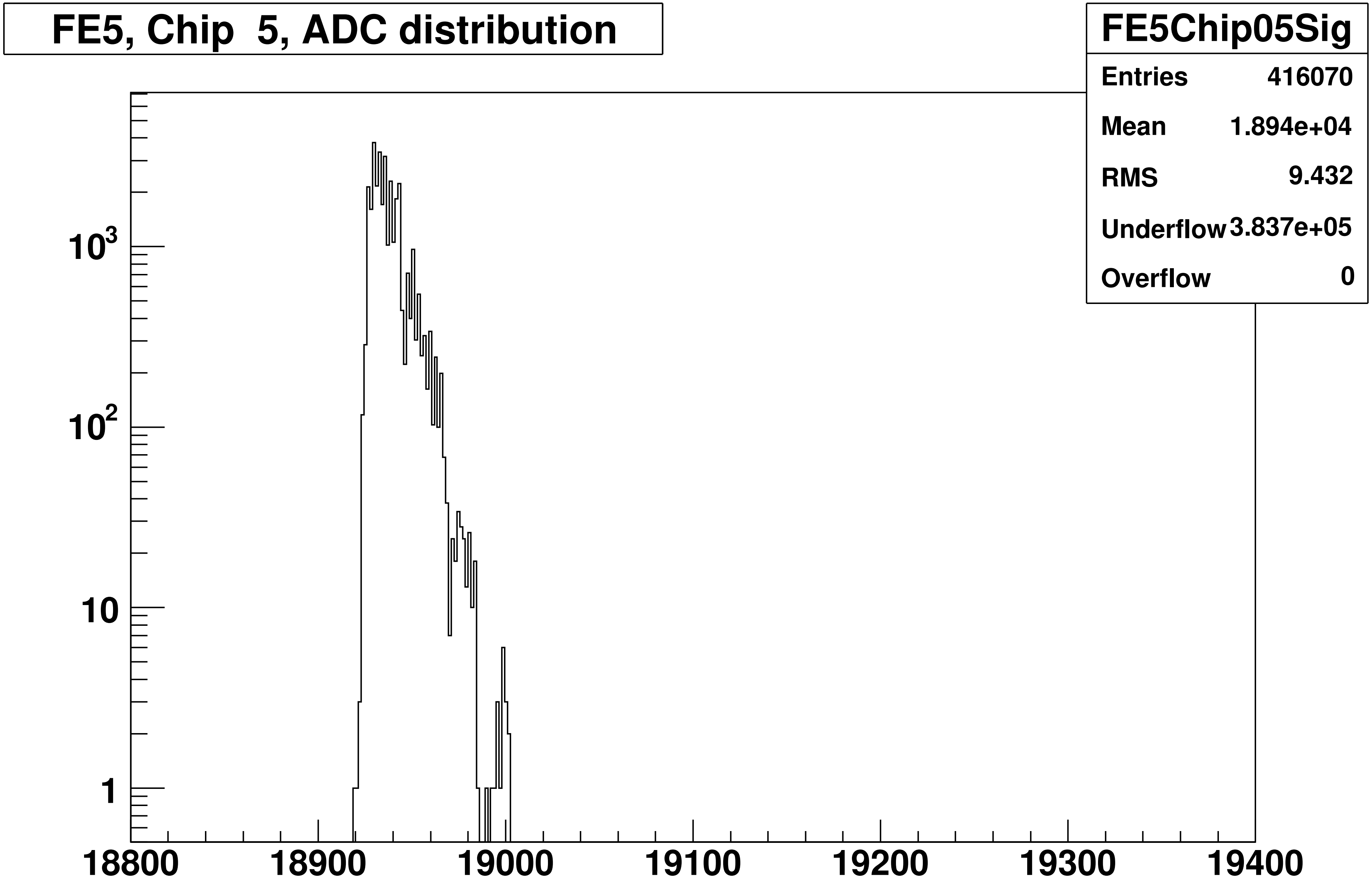
20
40
60
80
100



FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 416070
Mean 1.894e+04
RMS 9.432
Underflow 3.837e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
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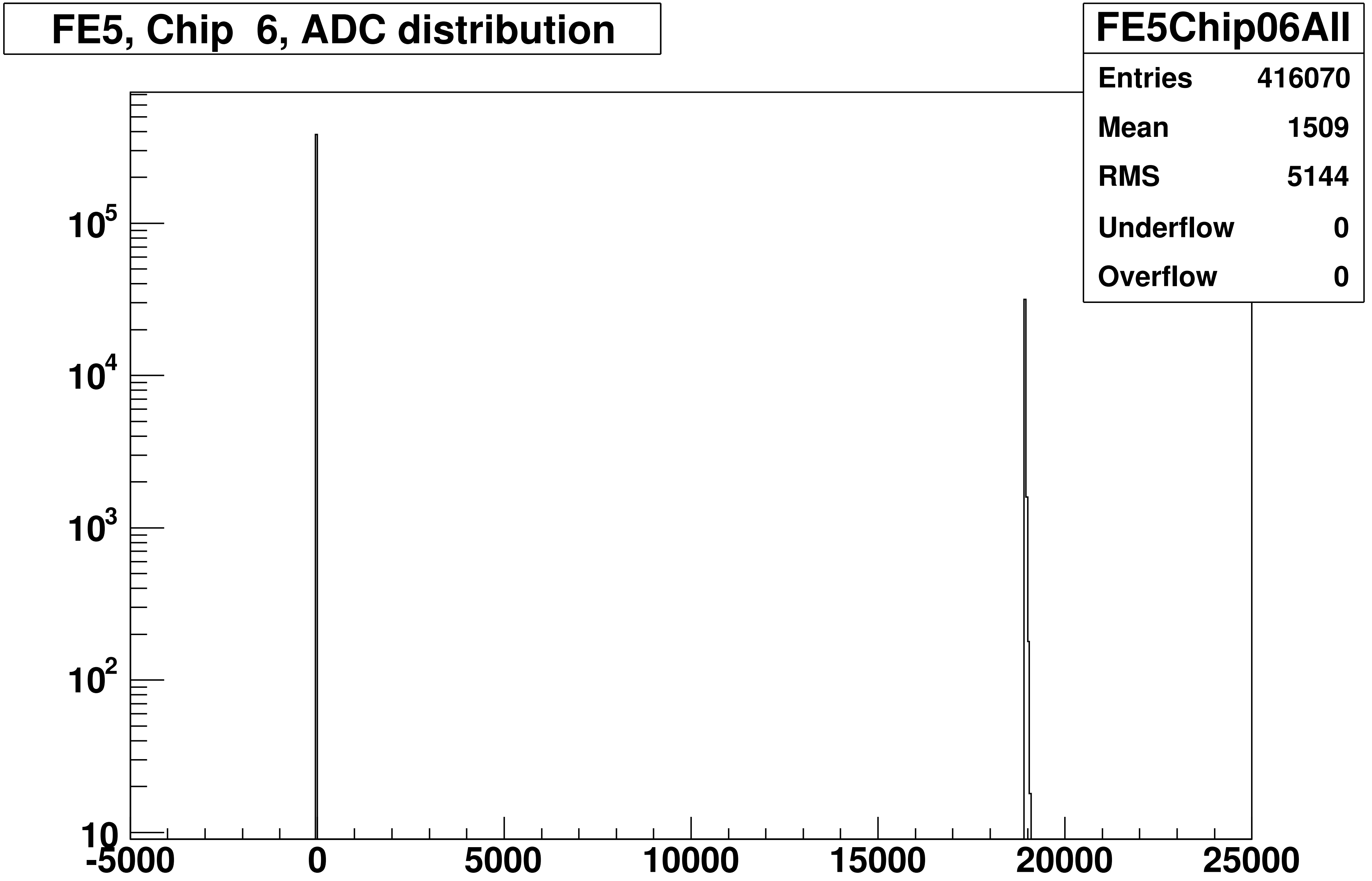
19200
19300
19400


FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 416070
Mean 1509
RMS 5144
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
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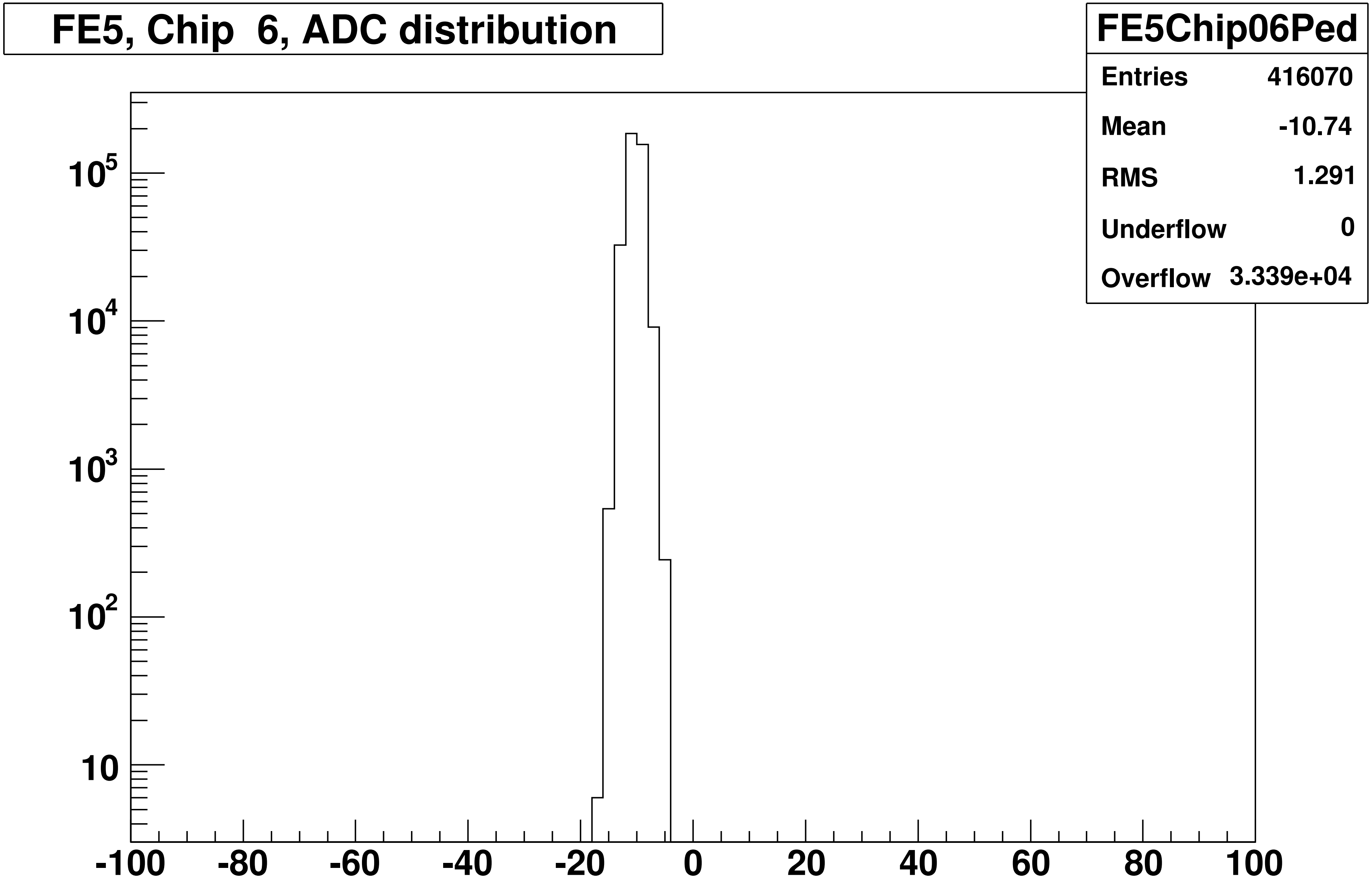
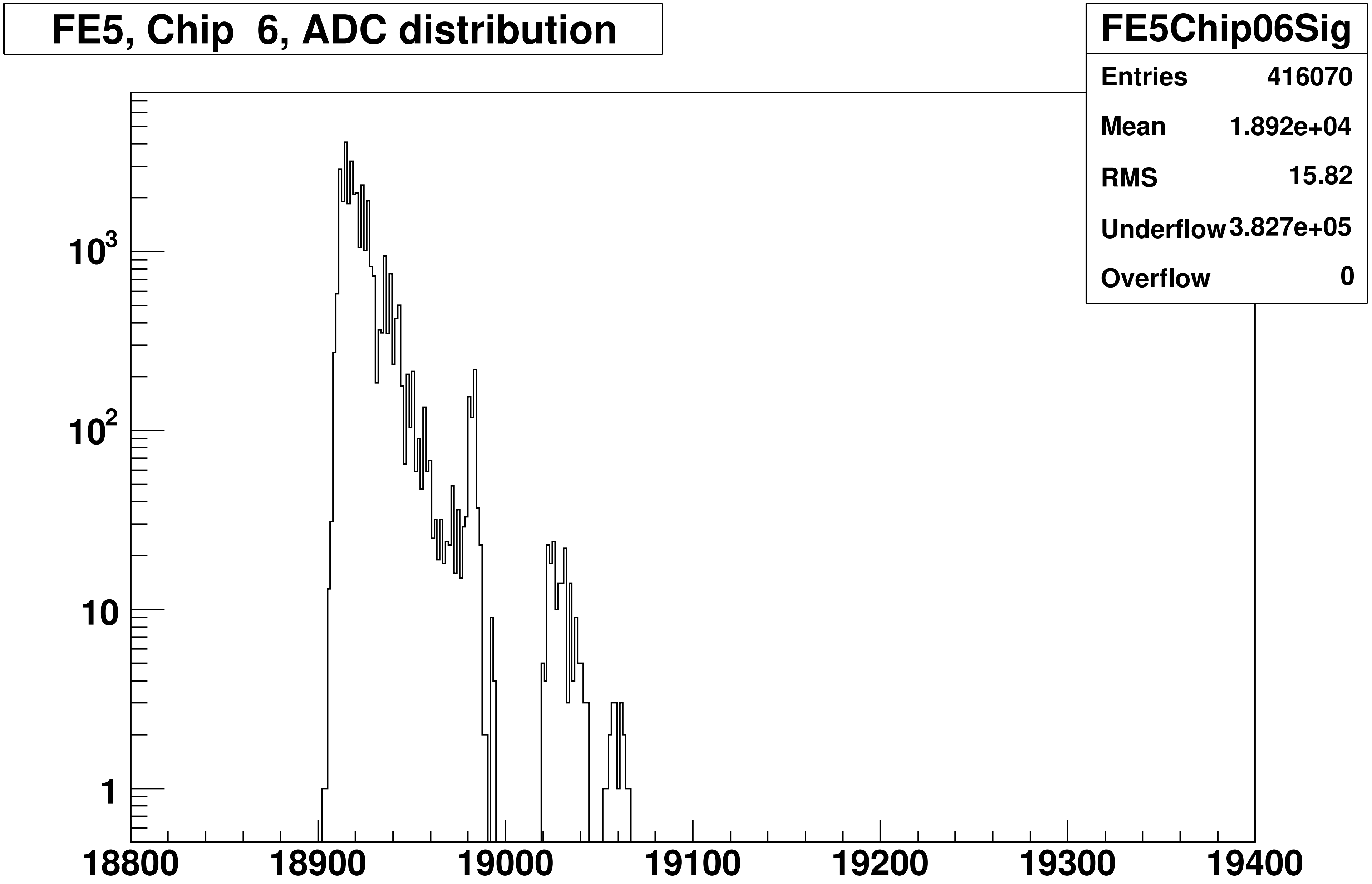
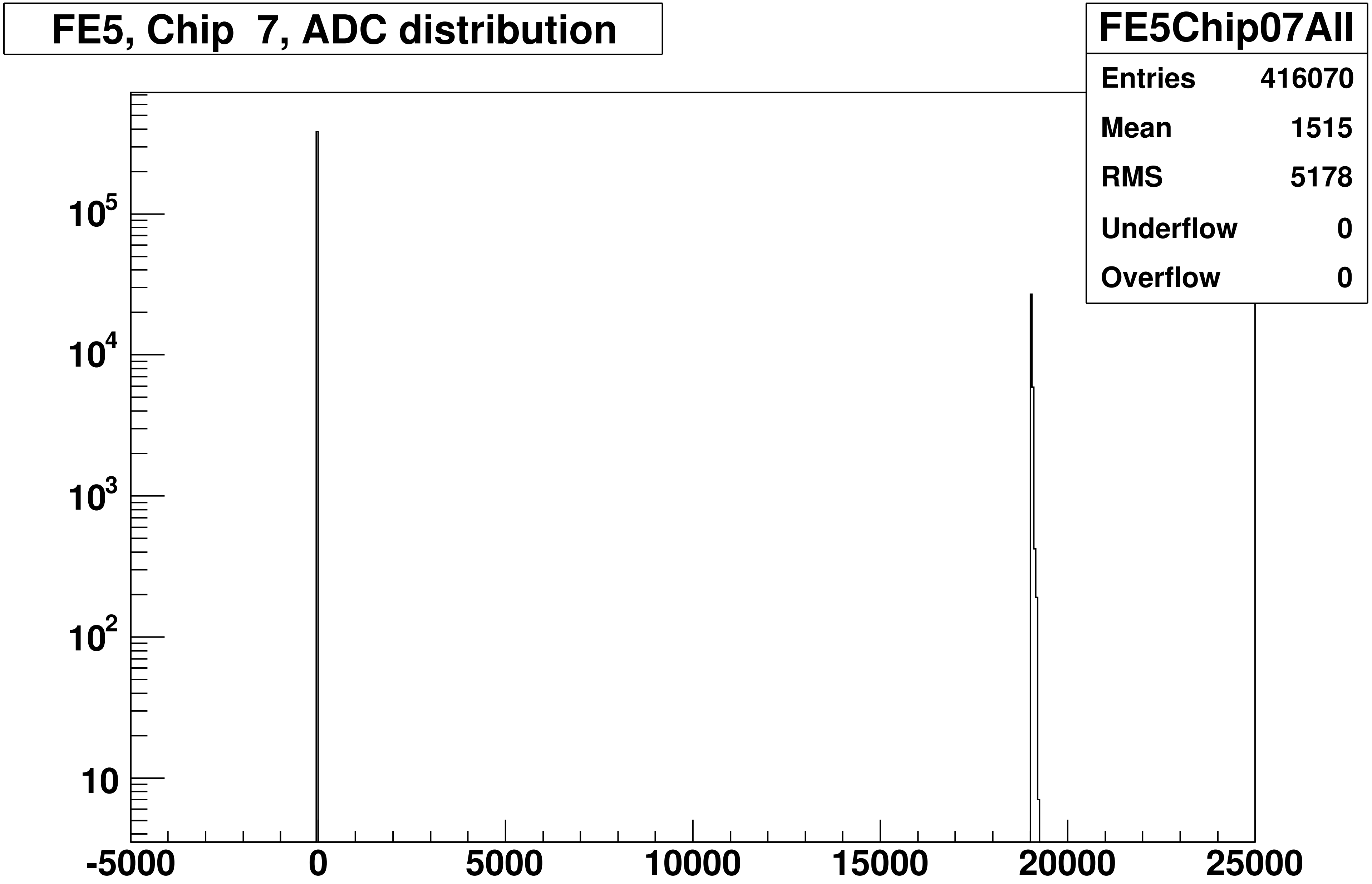
0
5000
10000
15000
20000
25000



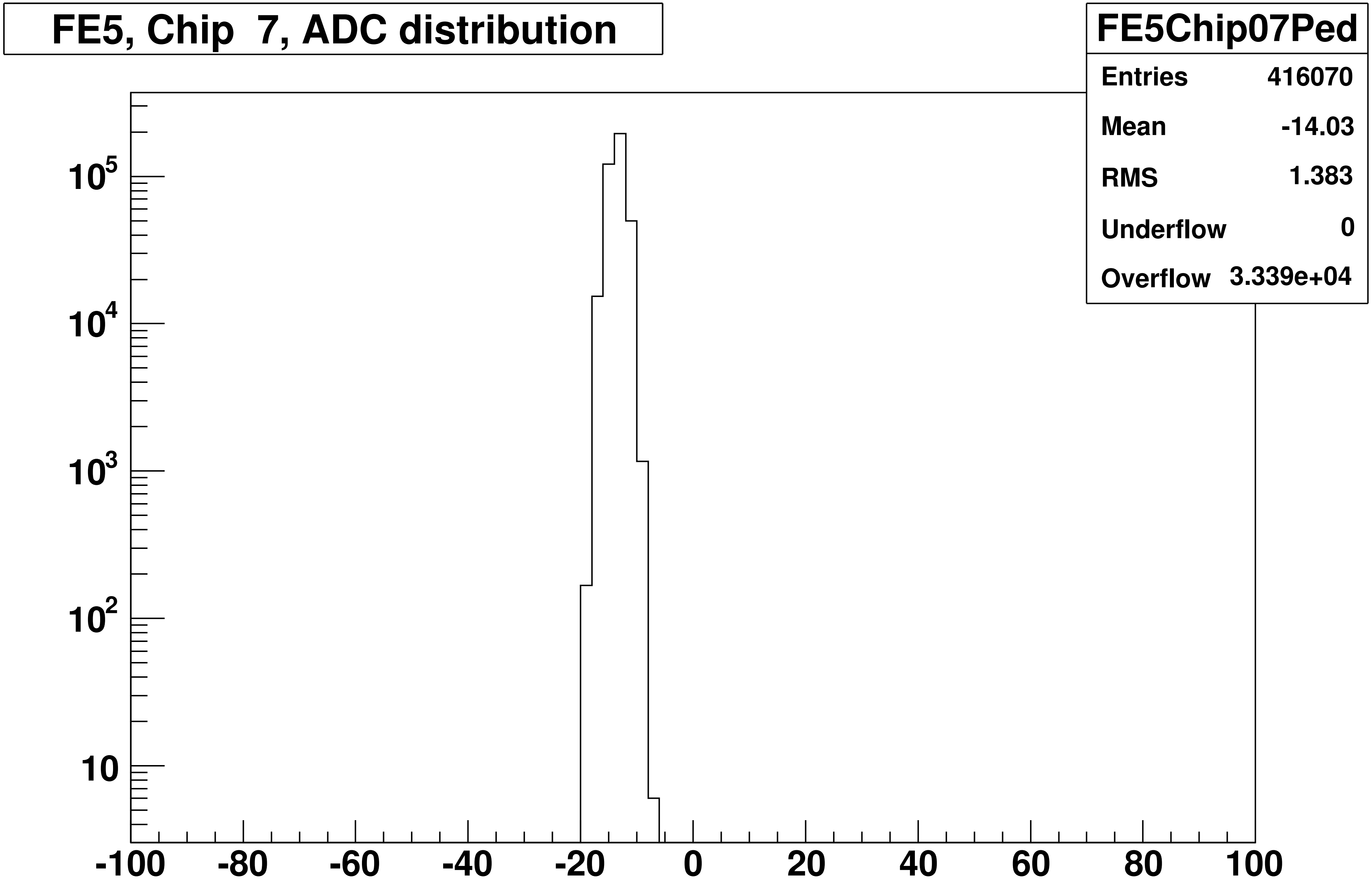
FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 416070
Mean -10.74
RMS 1.291
Underflow 0
Overflow 3.339e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

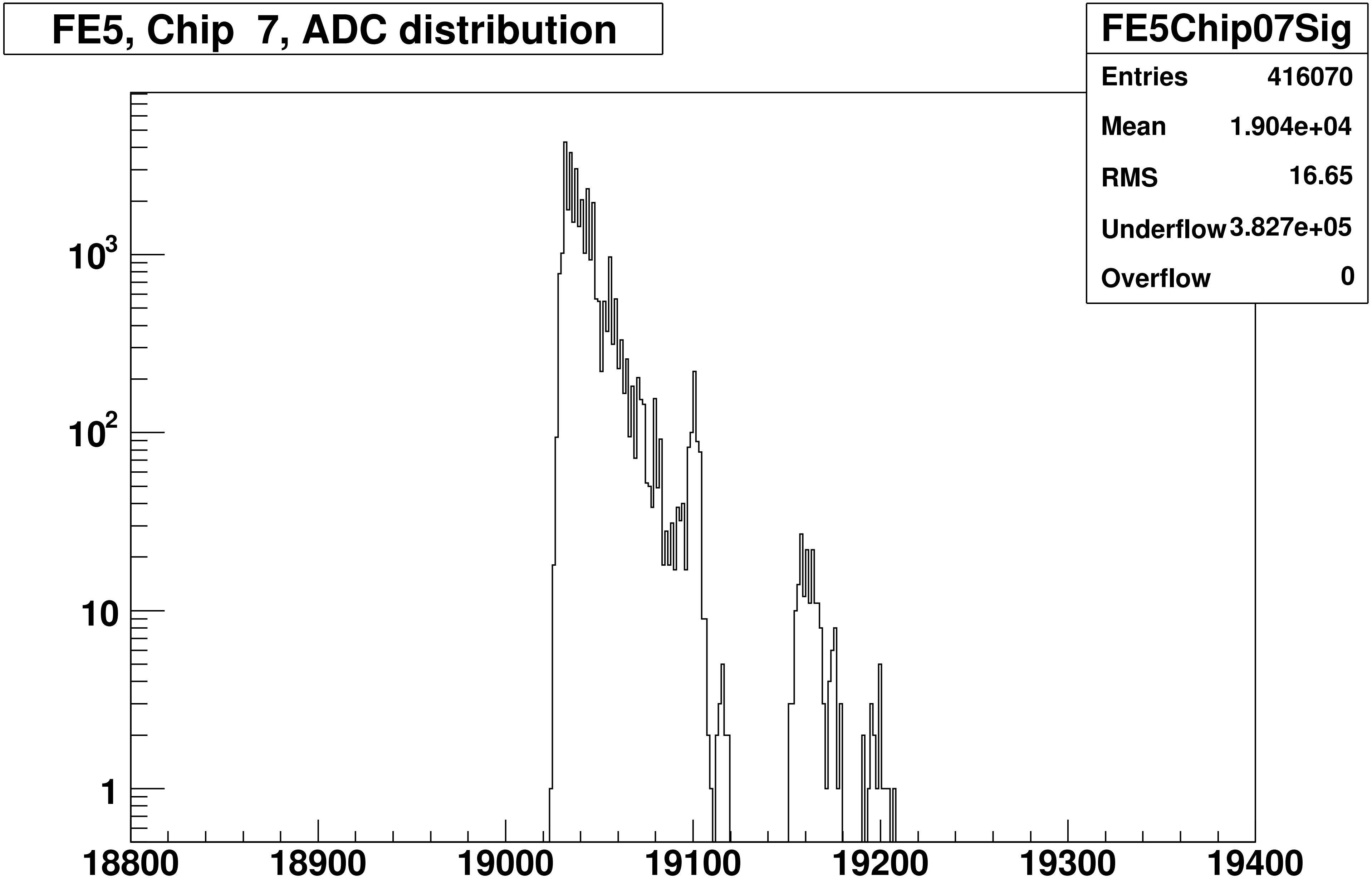


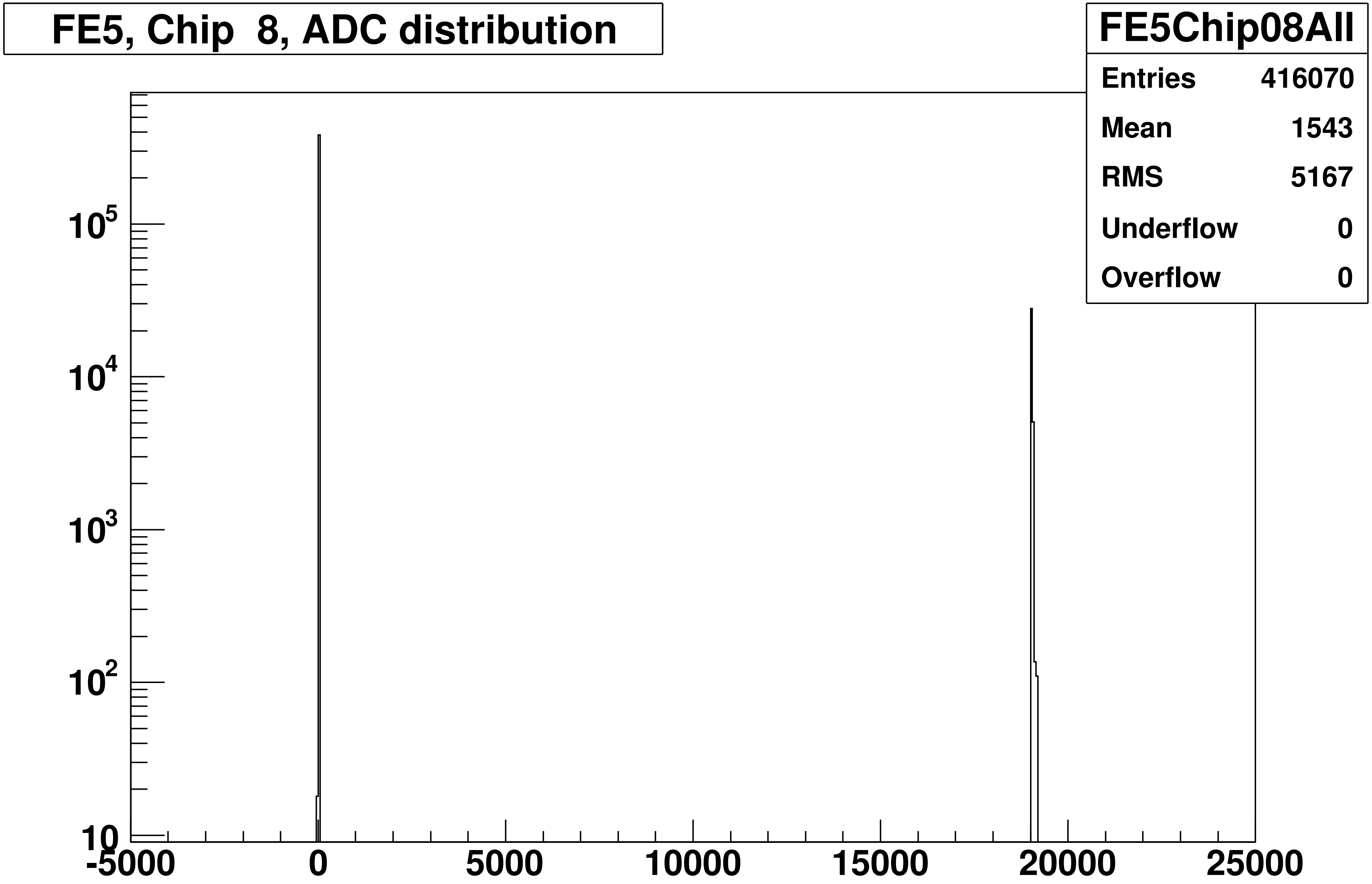
FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 416070
Mean 1.892e+04
RMS 15.82
Underflow 3.827e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



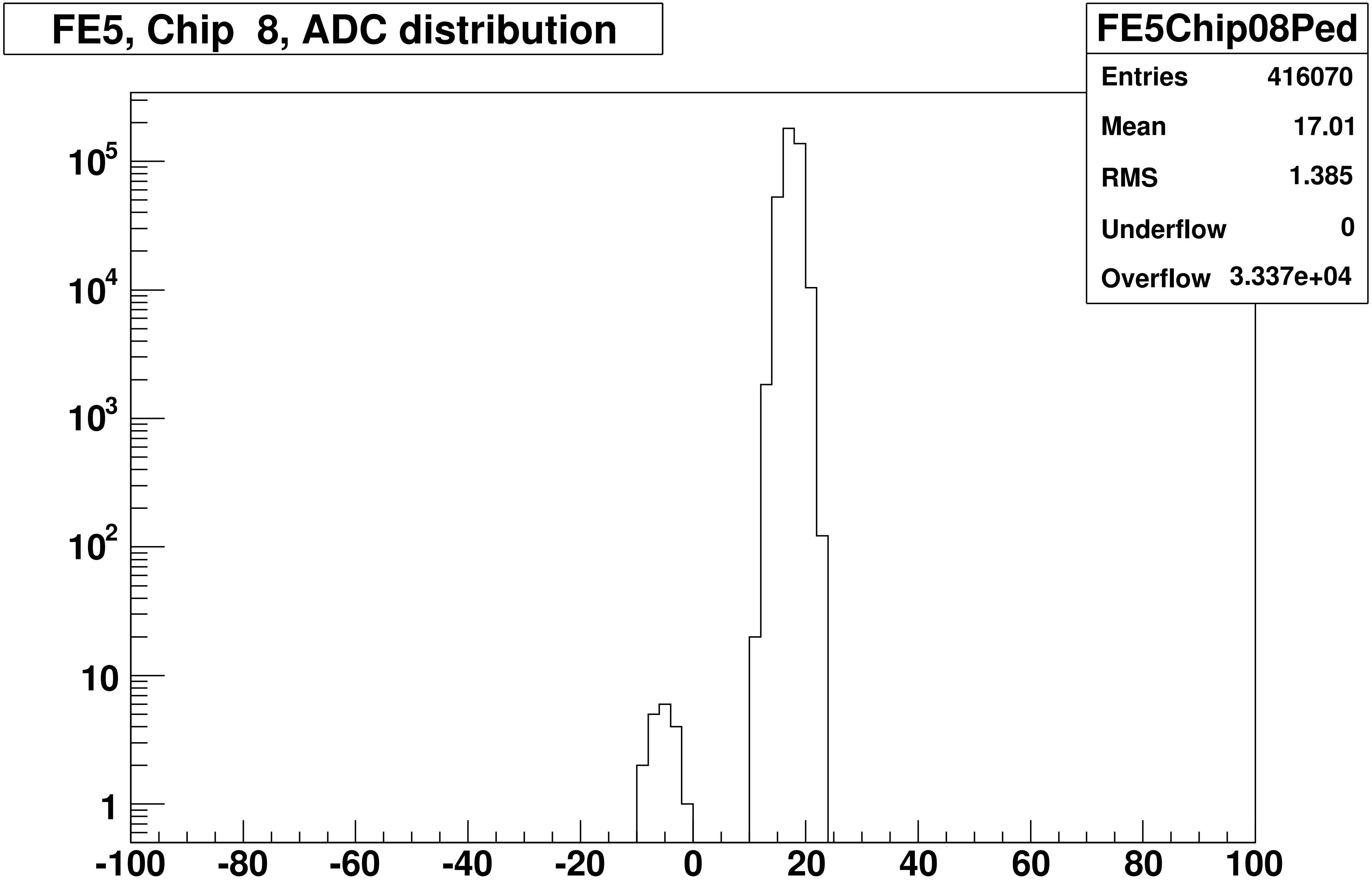
FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 416070
Mean 1515
RMS 5178
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 416070
Mean -14.03
RMS 1.383
Underflow 0
Overflow 3.339e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

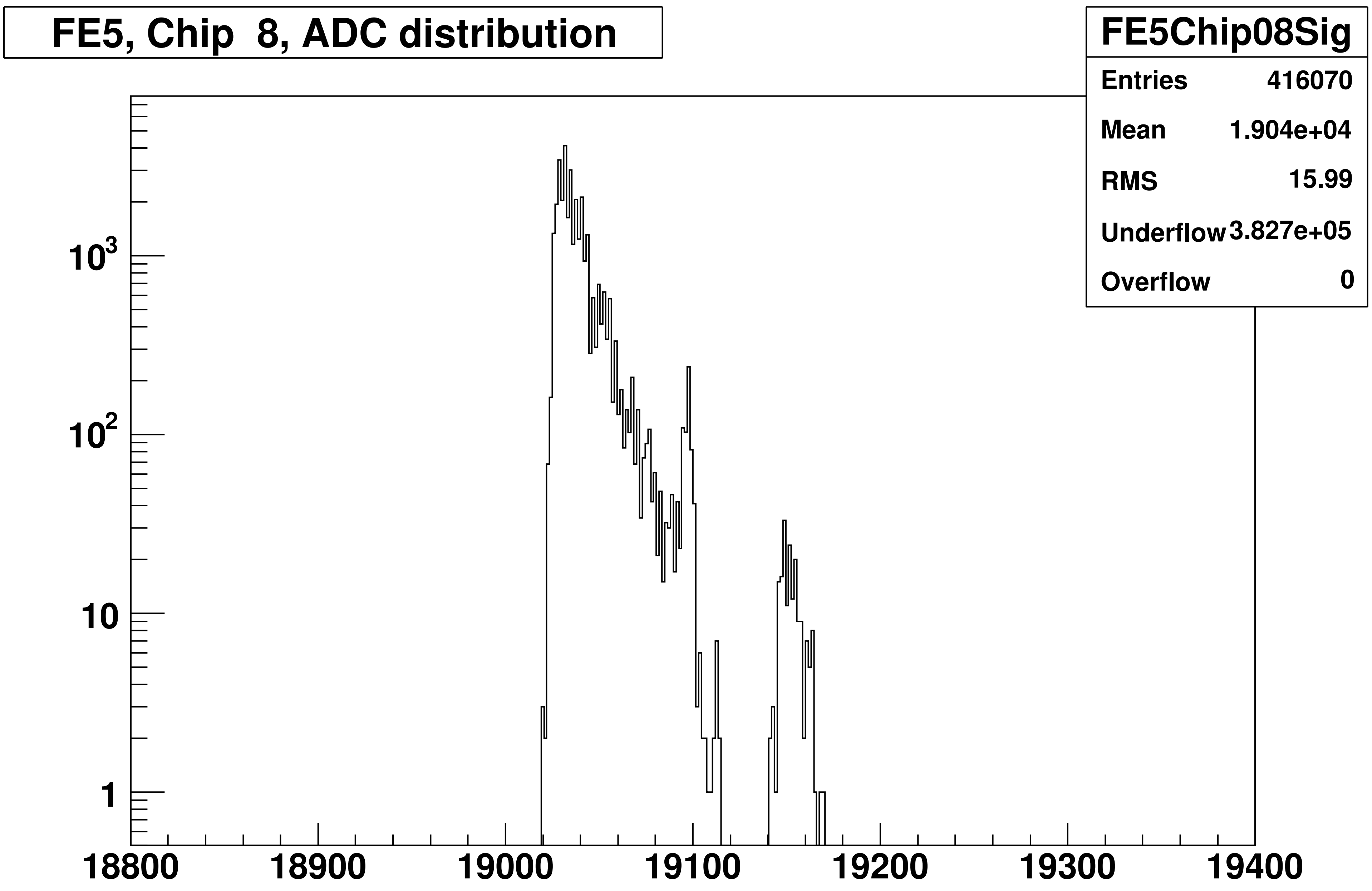

FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 416070
Mean 1.904e+04
RMS 16.65
Underflow 3.827e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

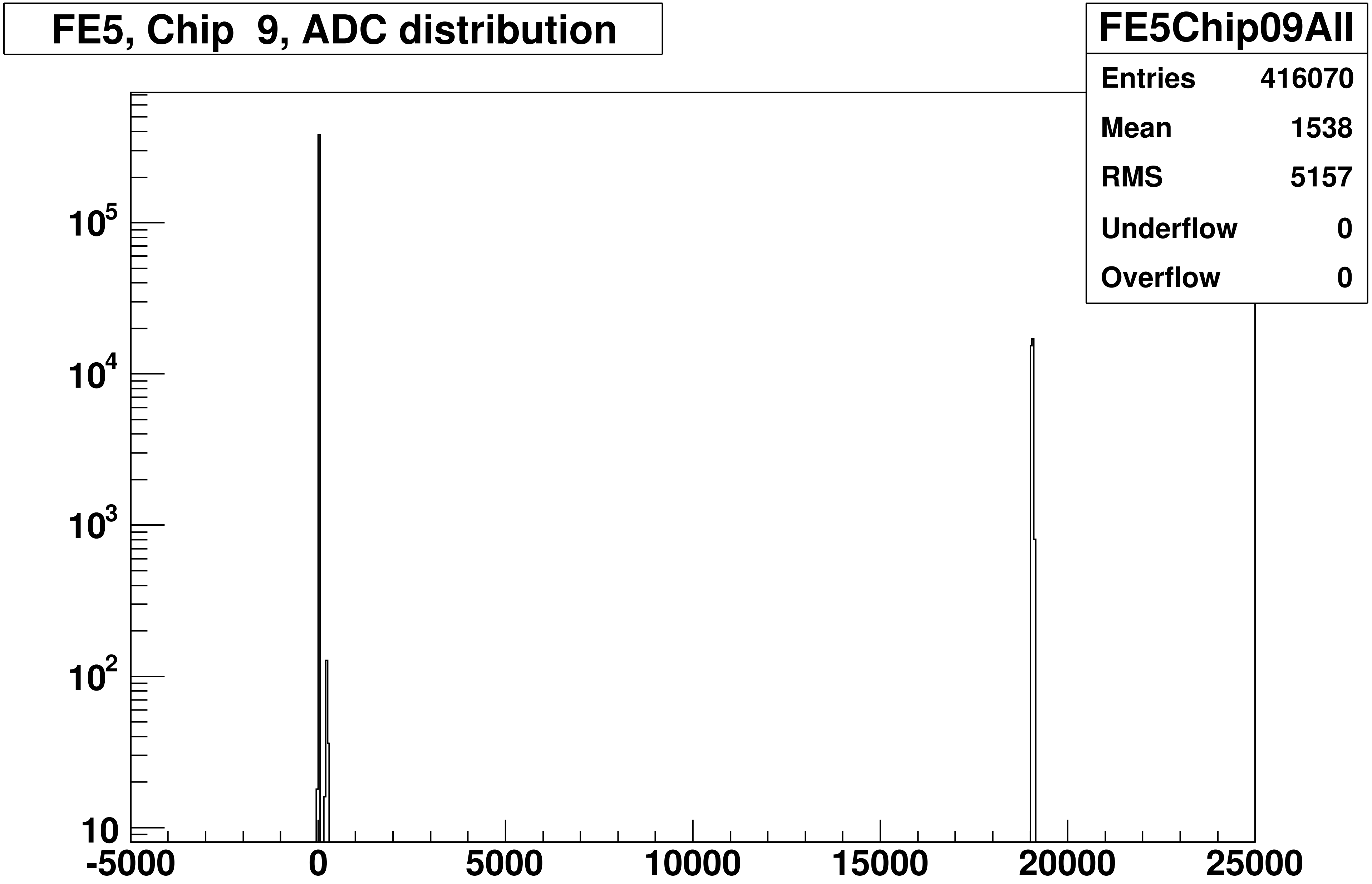


FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 416070
Mean 1543
RMS 5167
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

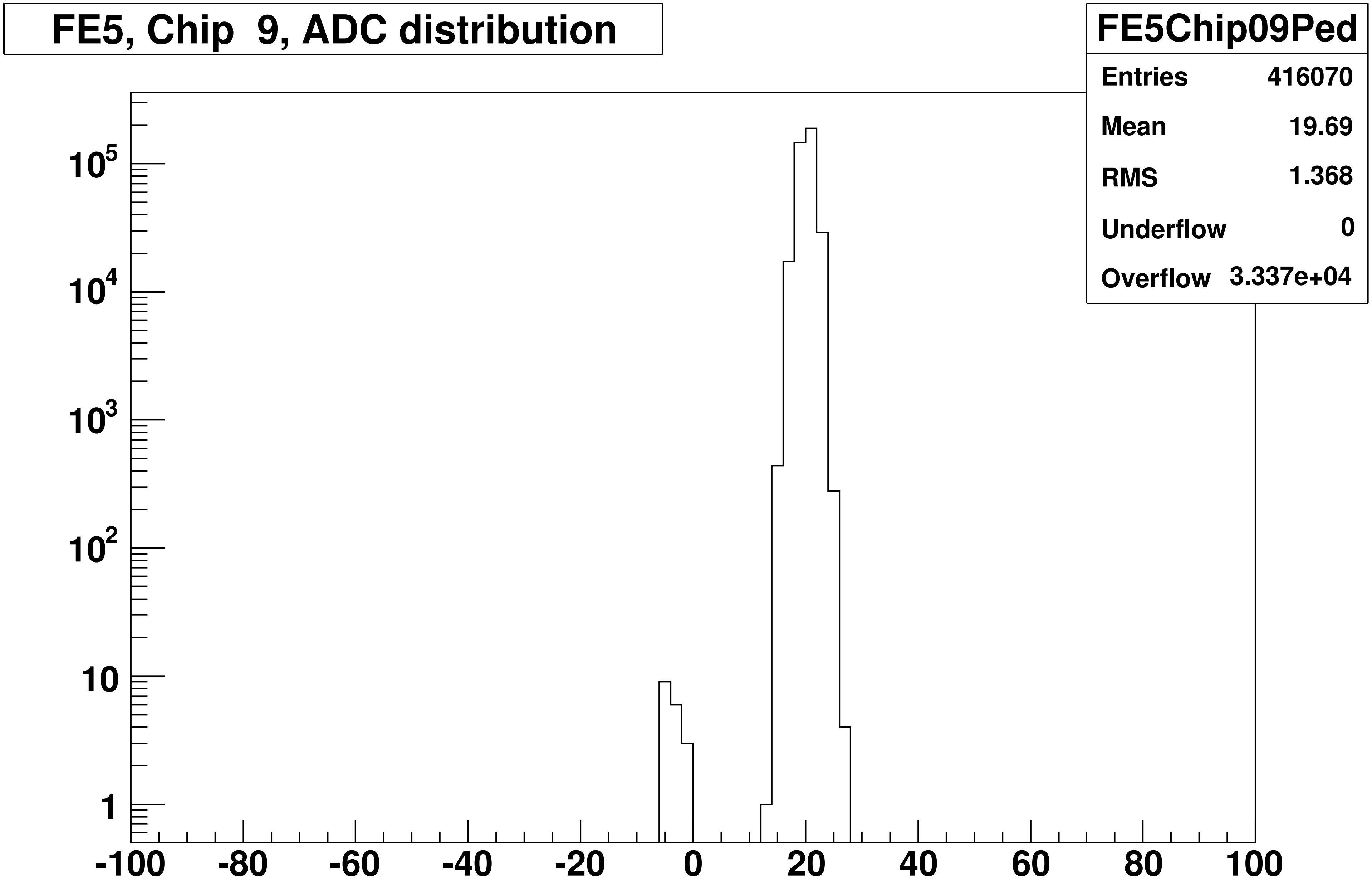


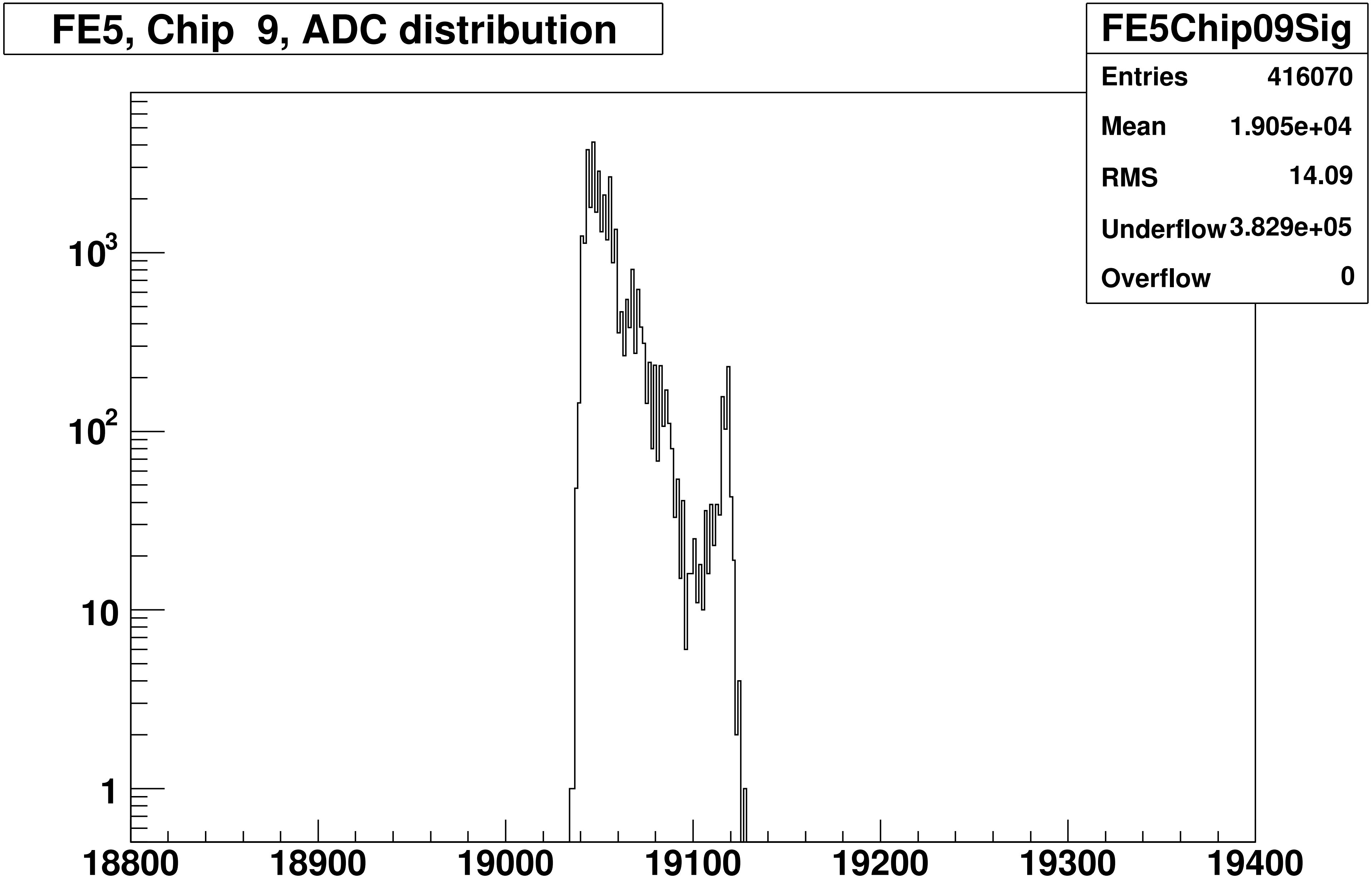
FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 416070
Mean 17.01
RMS 1.385
Underflow 0
Overflow 3.337e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

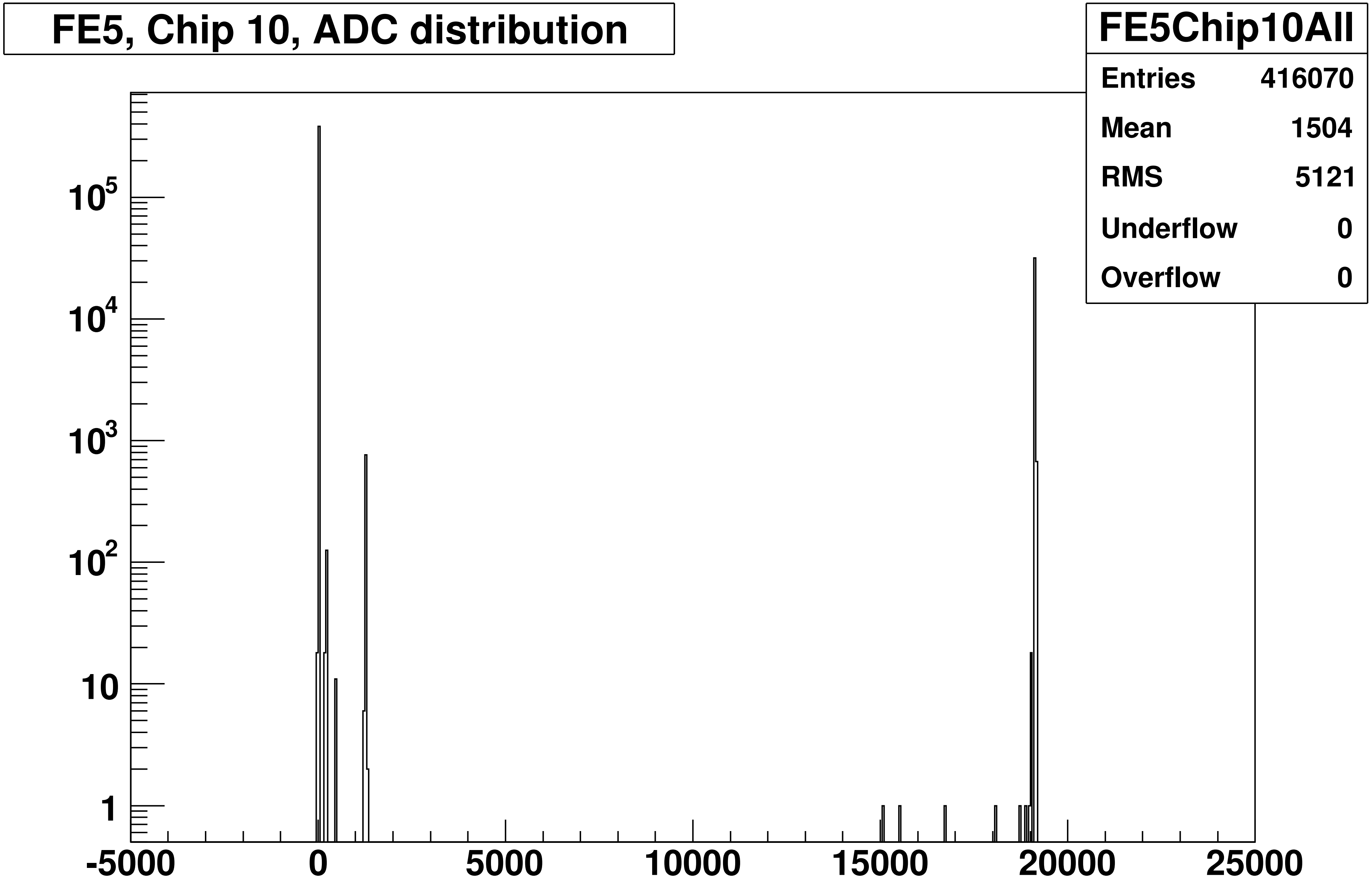

FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 416070
Mean 1.904e+04
RMS 15.99
Underflow 3.827e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



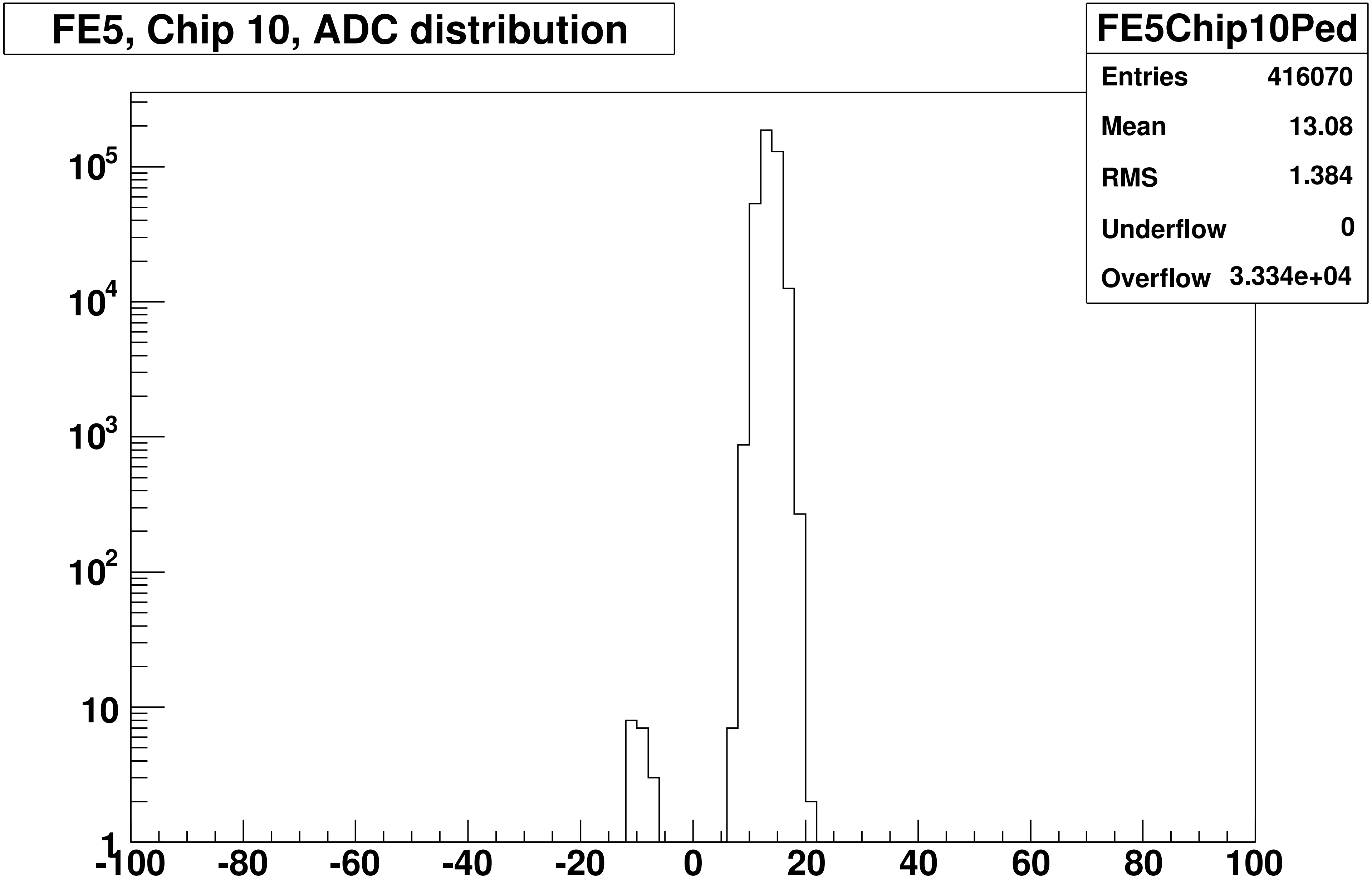
FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 416070
Mean 1538
RMS 5157
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

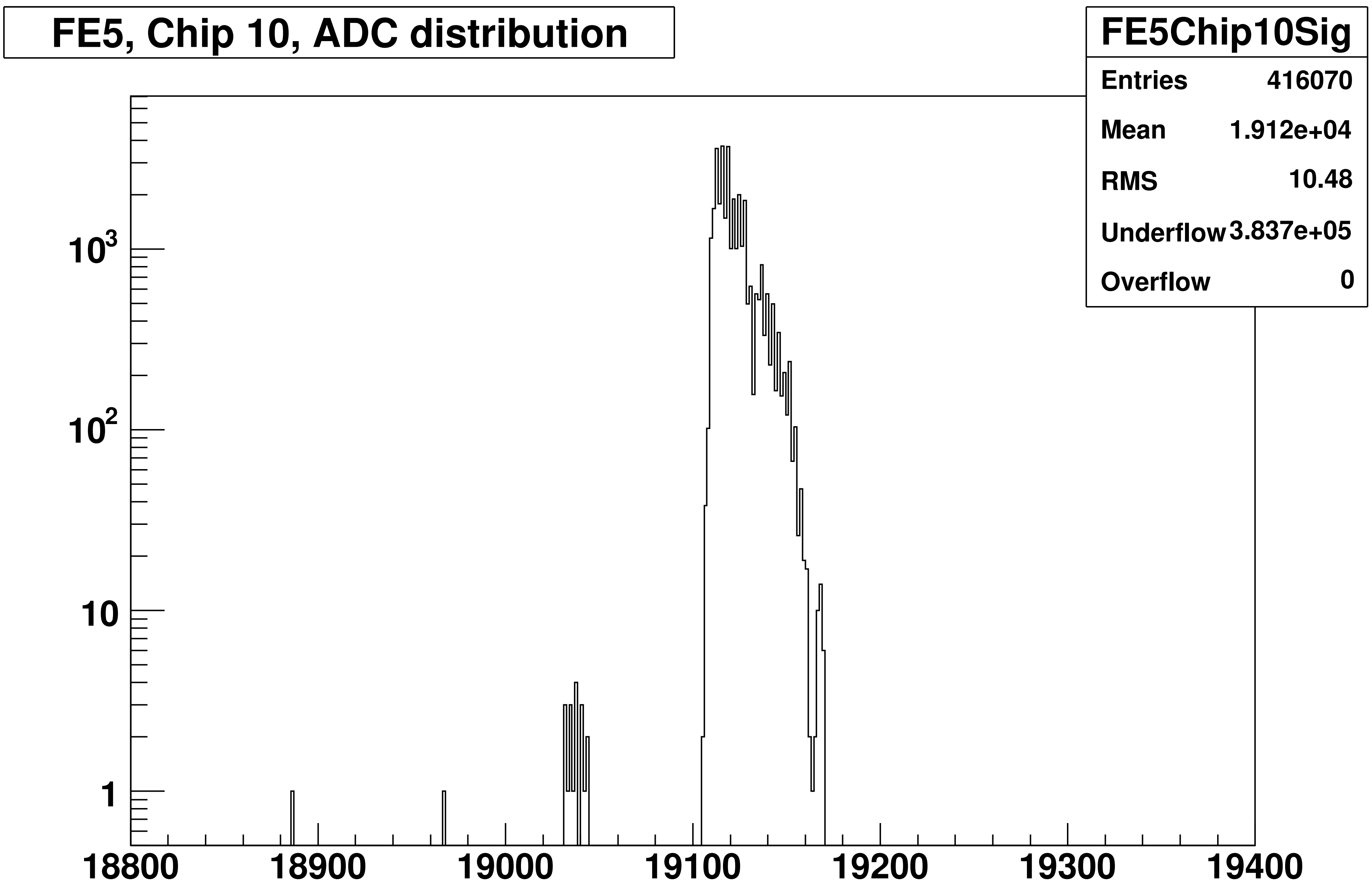

FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 416070
Mean 19.69
RMS 1.368
Underflow 0
Overflow 3.337e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 416070
Mean 1.905e+04
RMS 14.09
Underflow 3.829e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

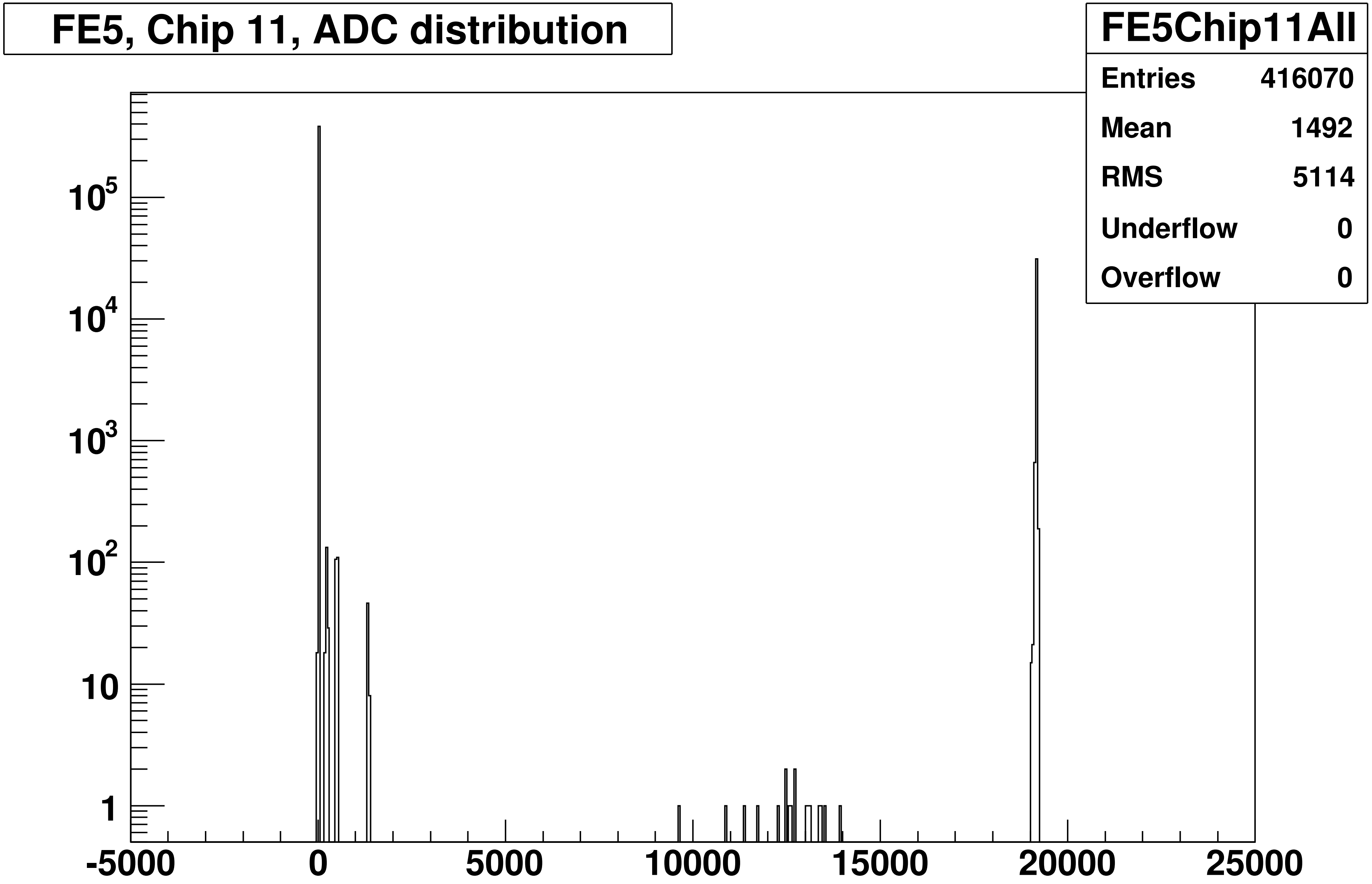


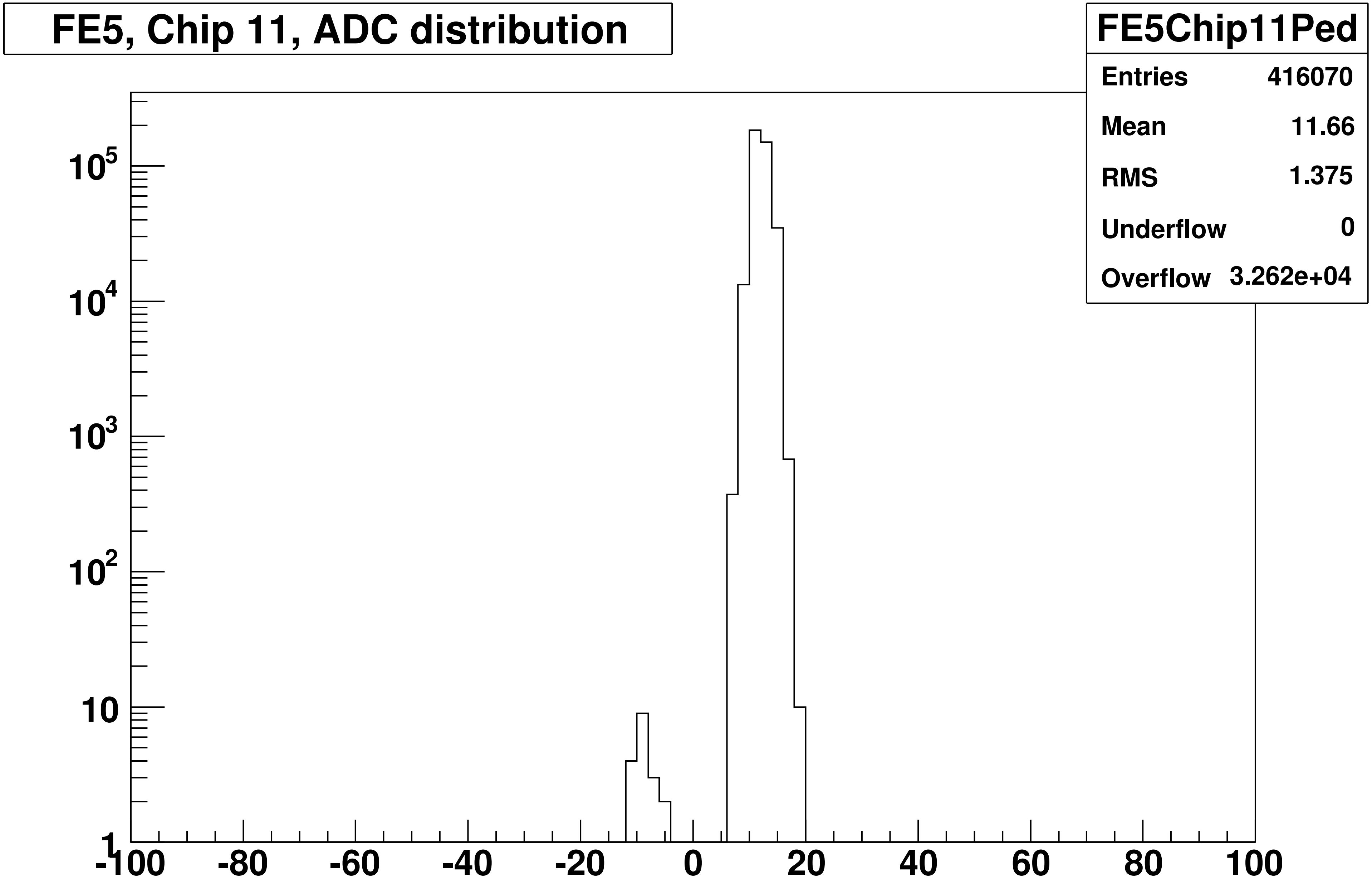
FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 416070
Mean 1504
RMS 5121
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 416070
Mean 13.08
RMS 1.384
Underflow 0
Overflow 3.334e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

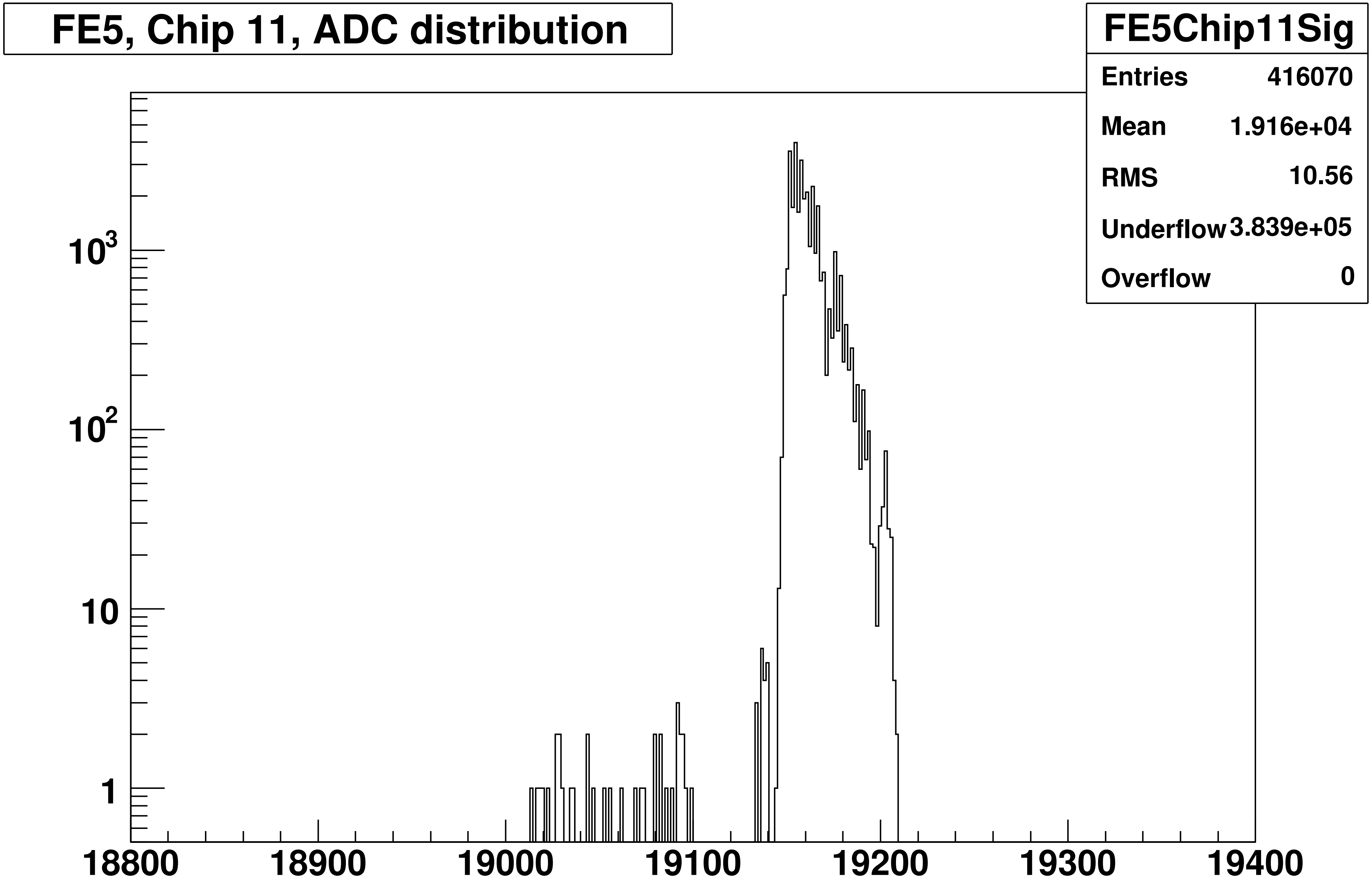


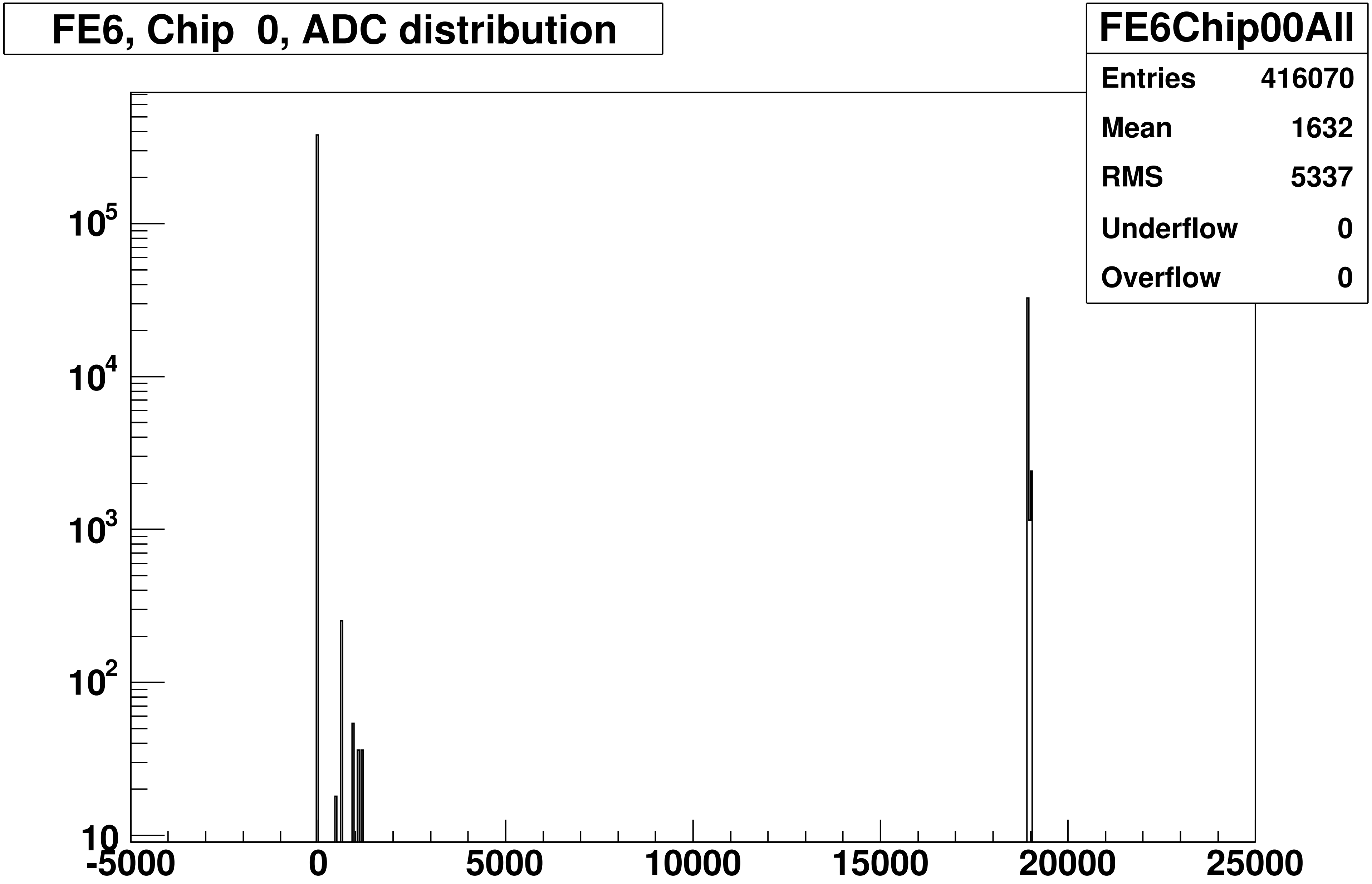
FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 416070
Mean 1.912e+04
RMS 10.48
Underflow 3.837e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

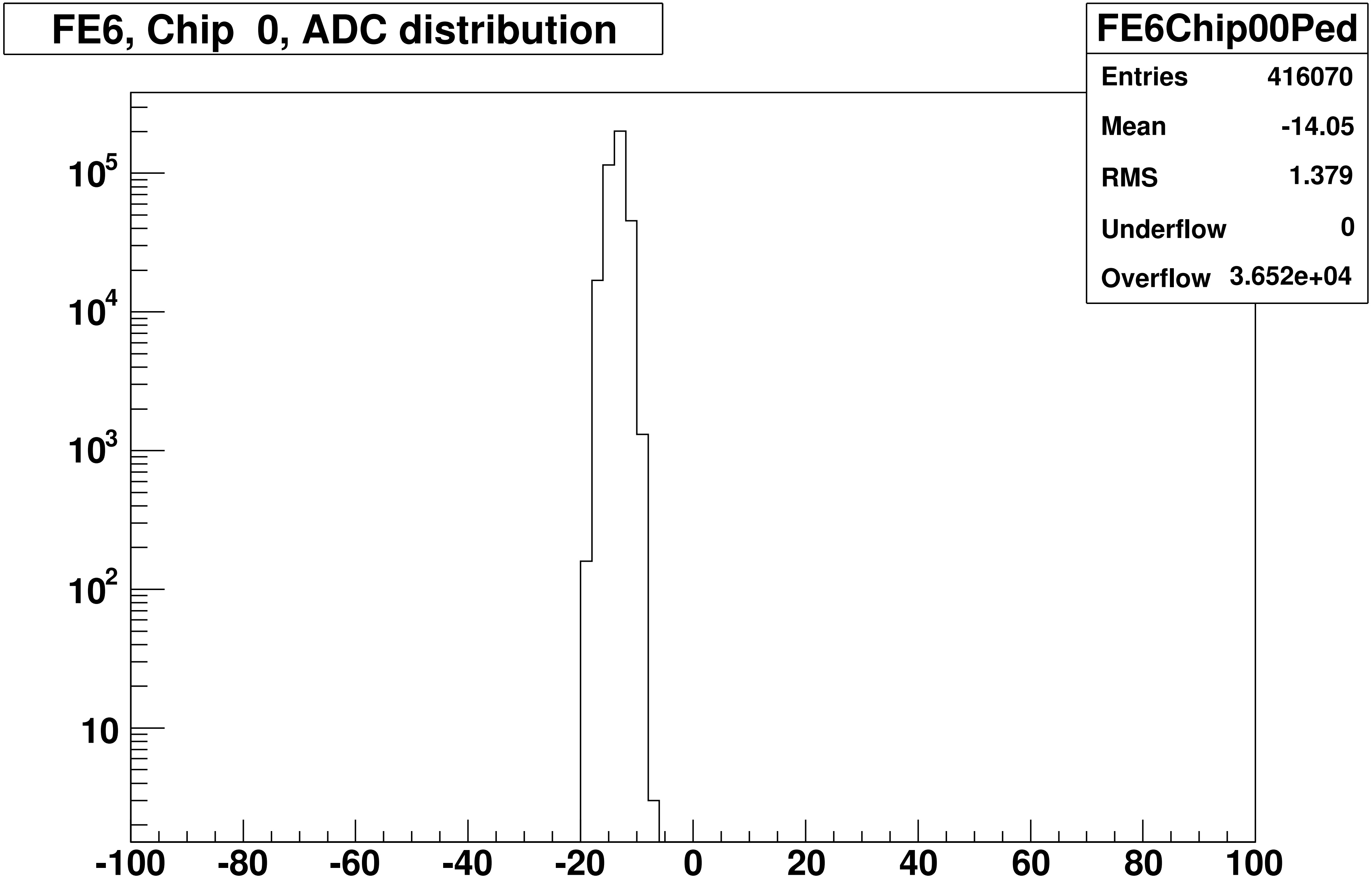

FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 416070
Mean 1492
RMS 5114
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

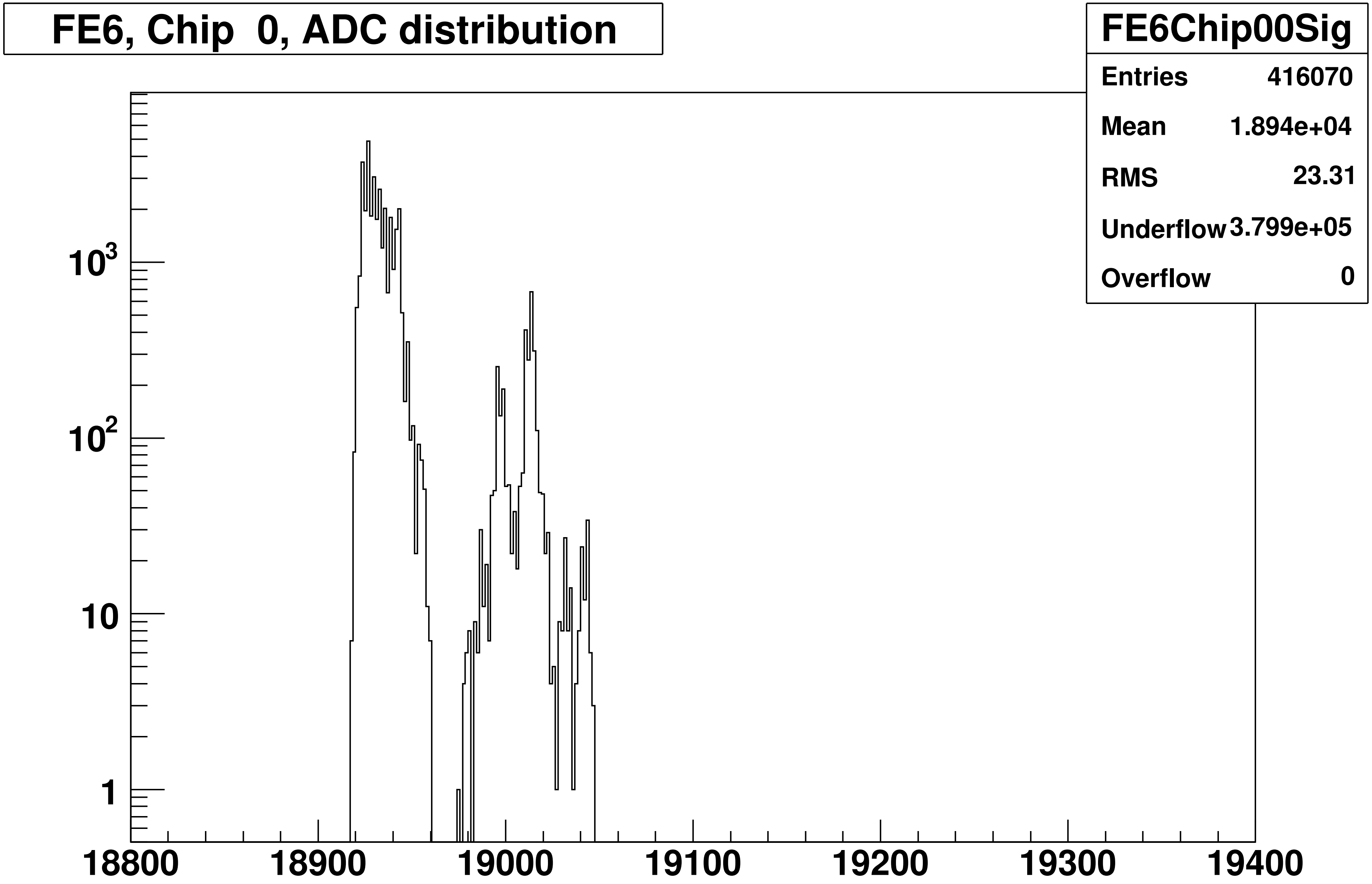


FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 416070
Mean 11.66
RMS 1.375
Underflow 0
Overflow 3.262e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

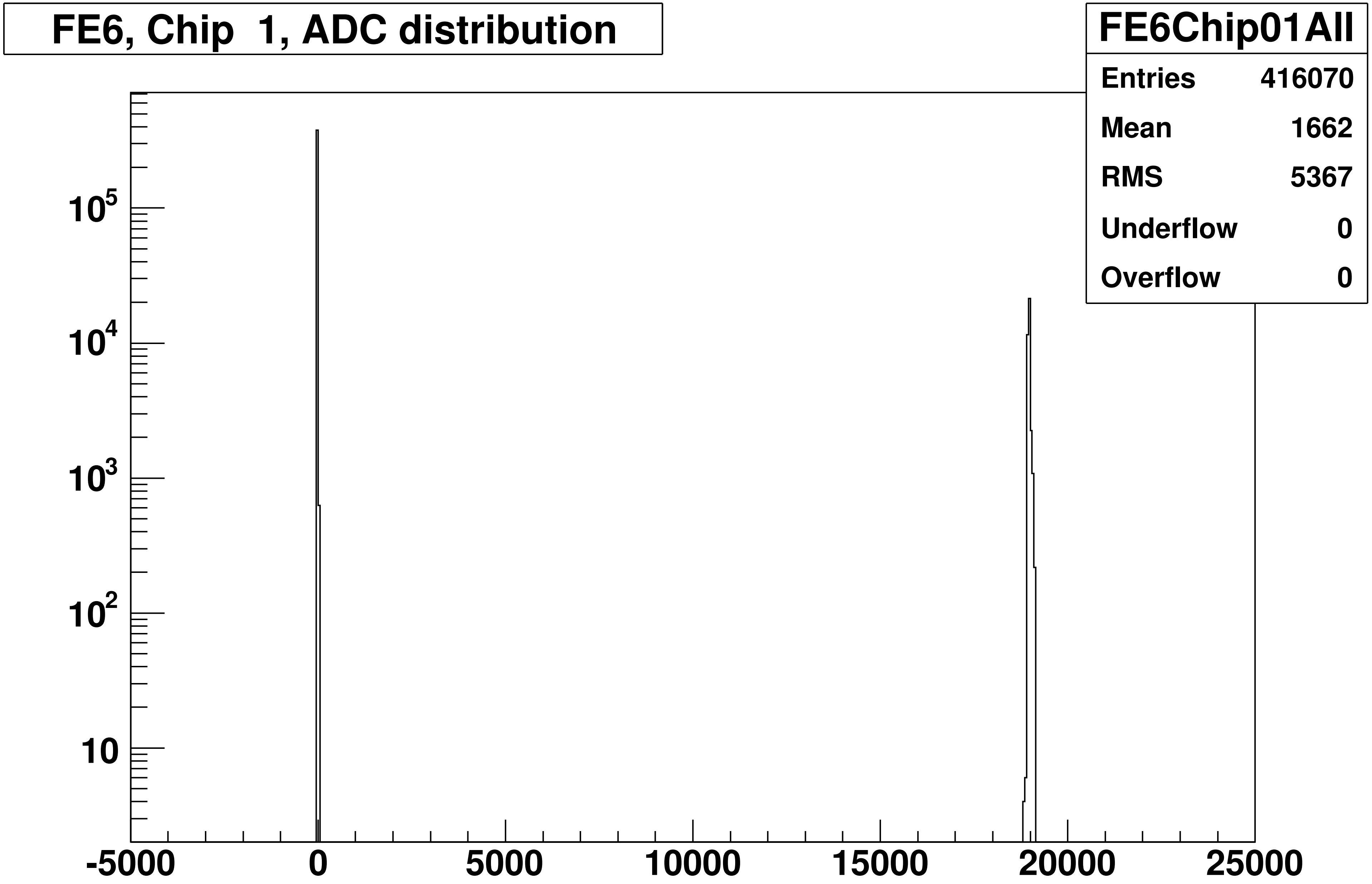

FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 416070
Mean 1.916e+04
RMS 10.56
Underflow 3.839e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 416070
Mean 1632
RMS 5337
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

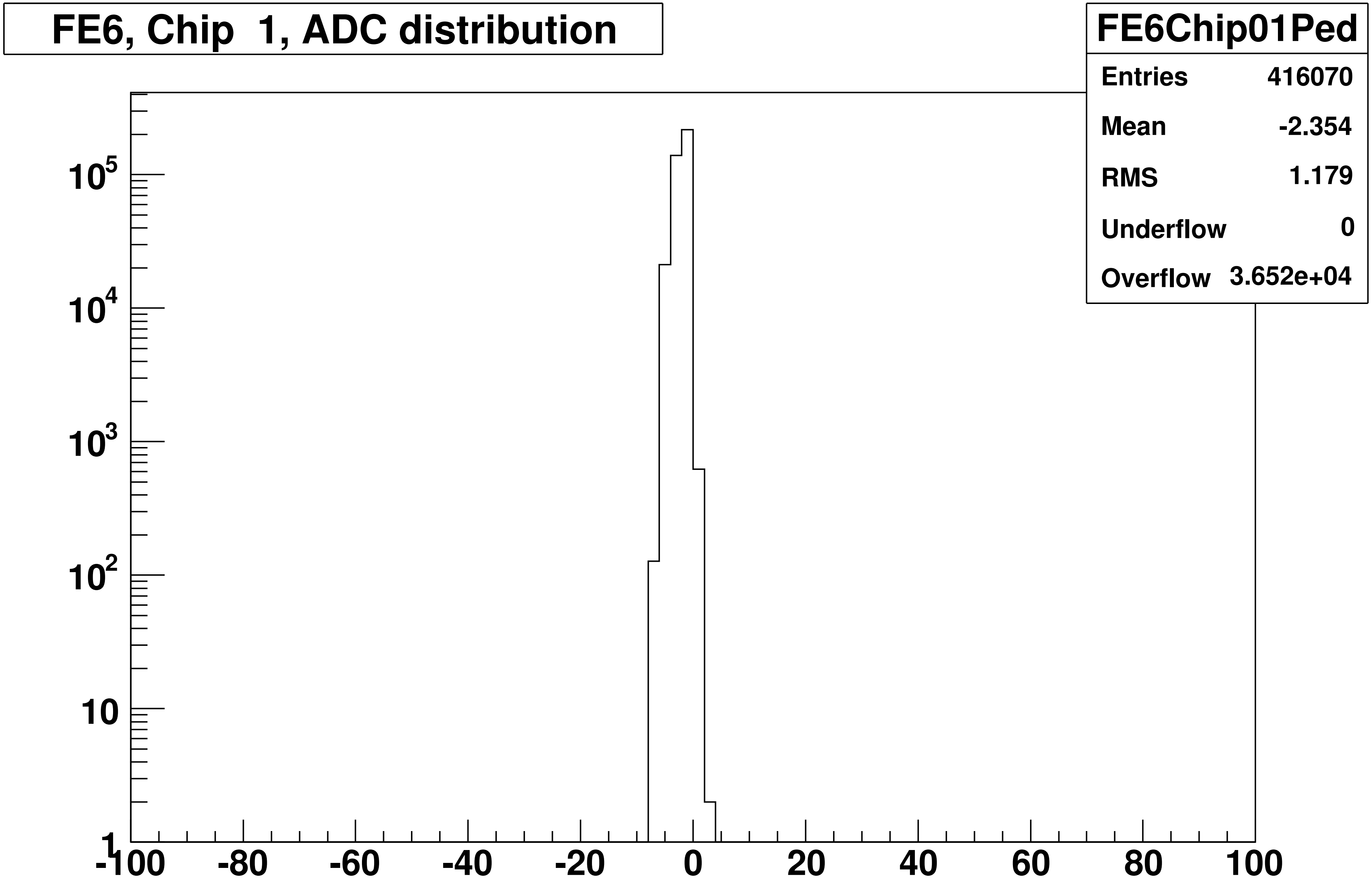

FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 416070
Mean -14.05
RMS 1.379
Underflow 0
Overflow 3.652e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

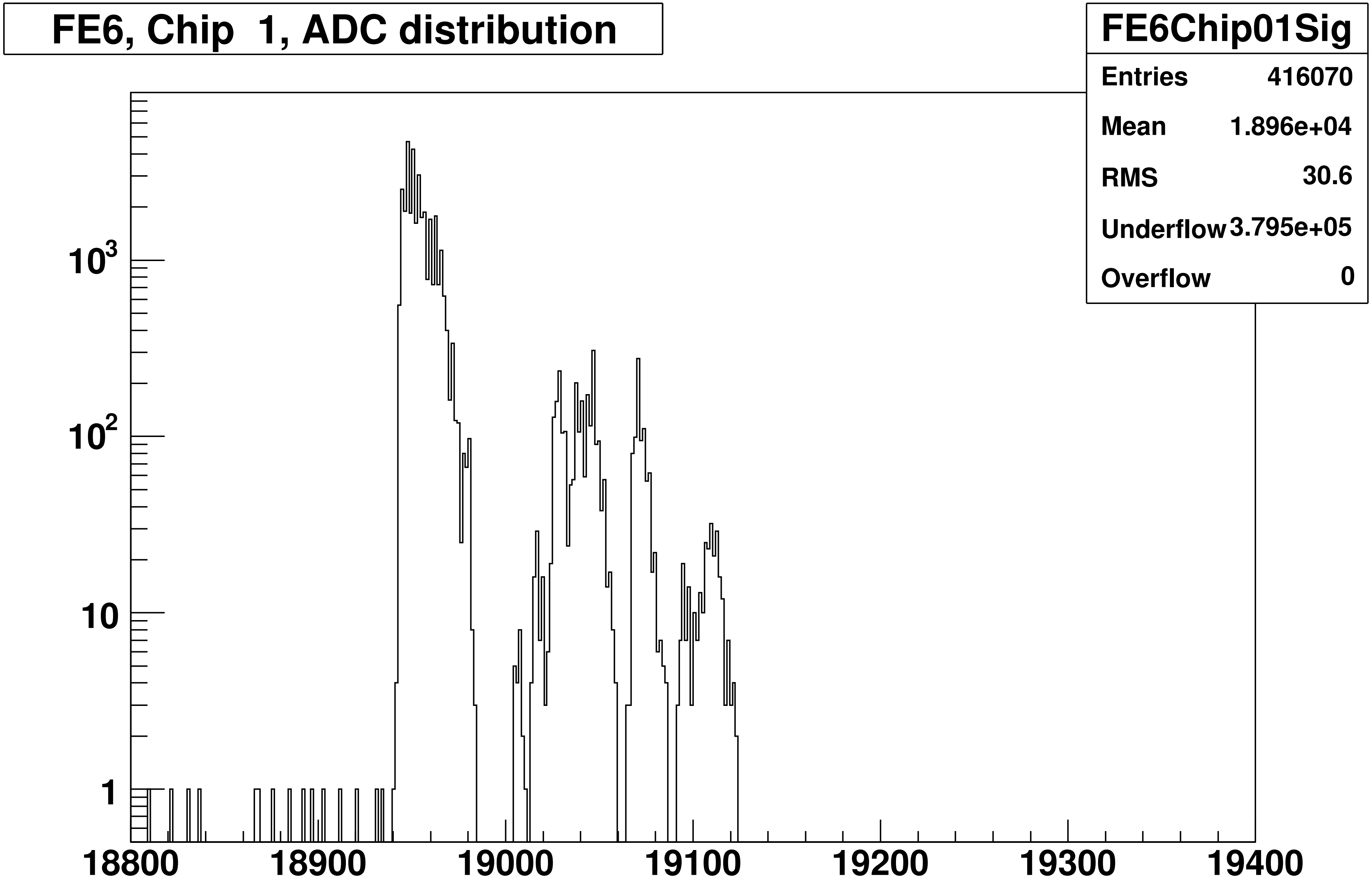


FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 416070
Mean 1.894e+04
RMS 23.31
Underflow 3.799e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

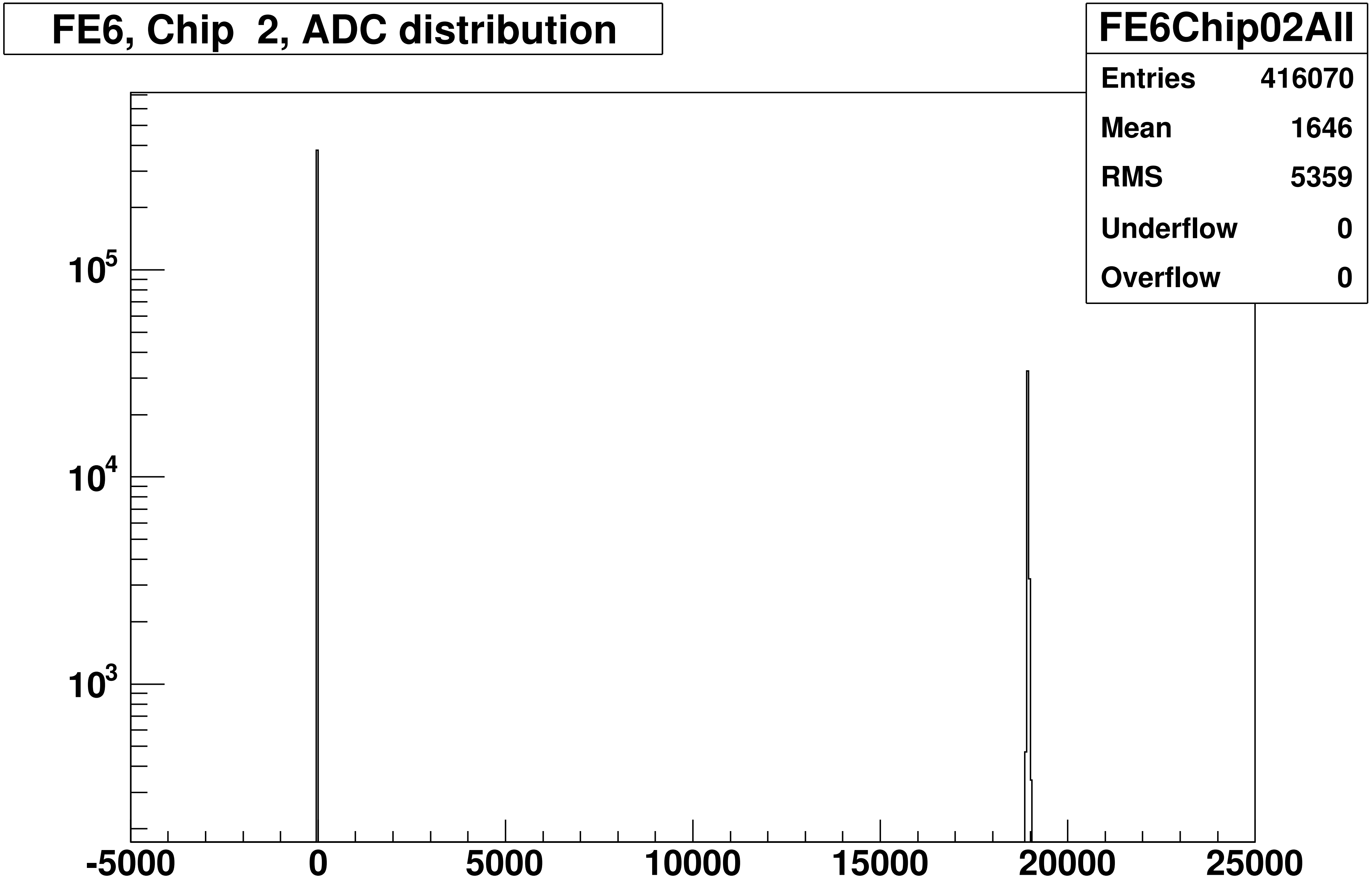


FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 416070
Mean 1662
RMS 5367
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

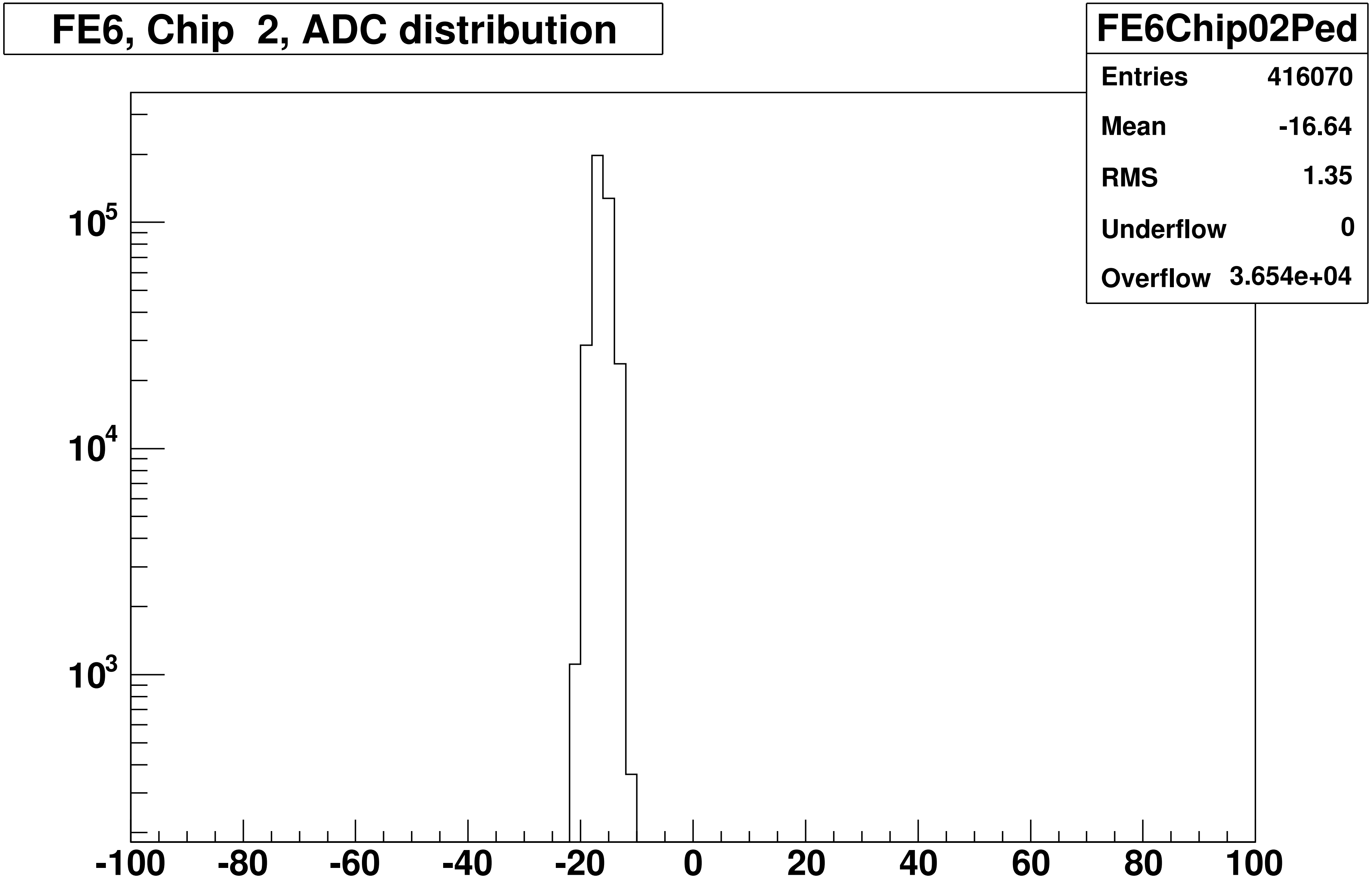

FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 416070
Mean -2.354
RMS 1.179
Underflow 0
Overflow 3.652e+04
1
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

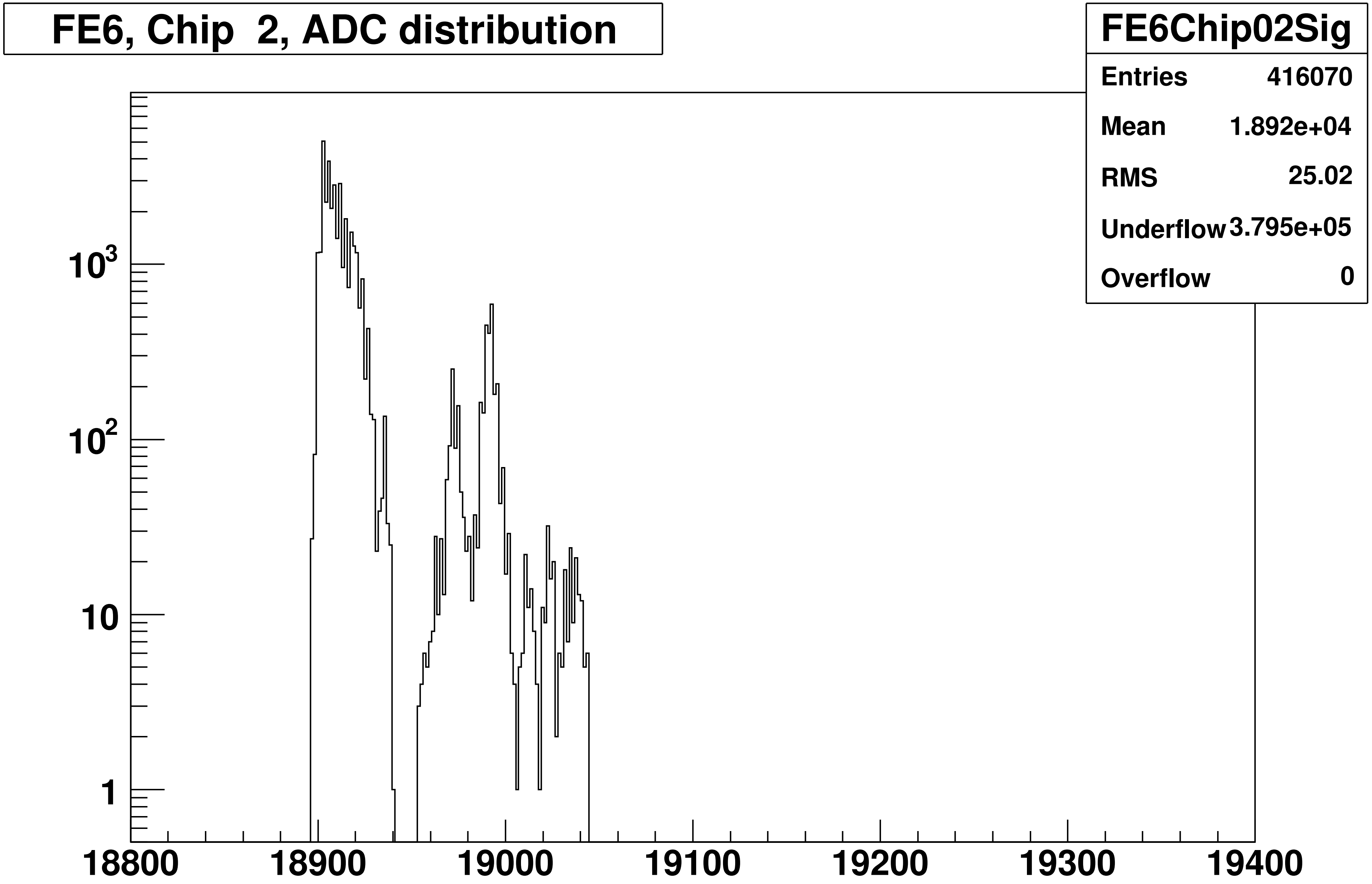


FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 416070
Mean 1.896e+04
RMS 30.6
Underflow 3.795e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

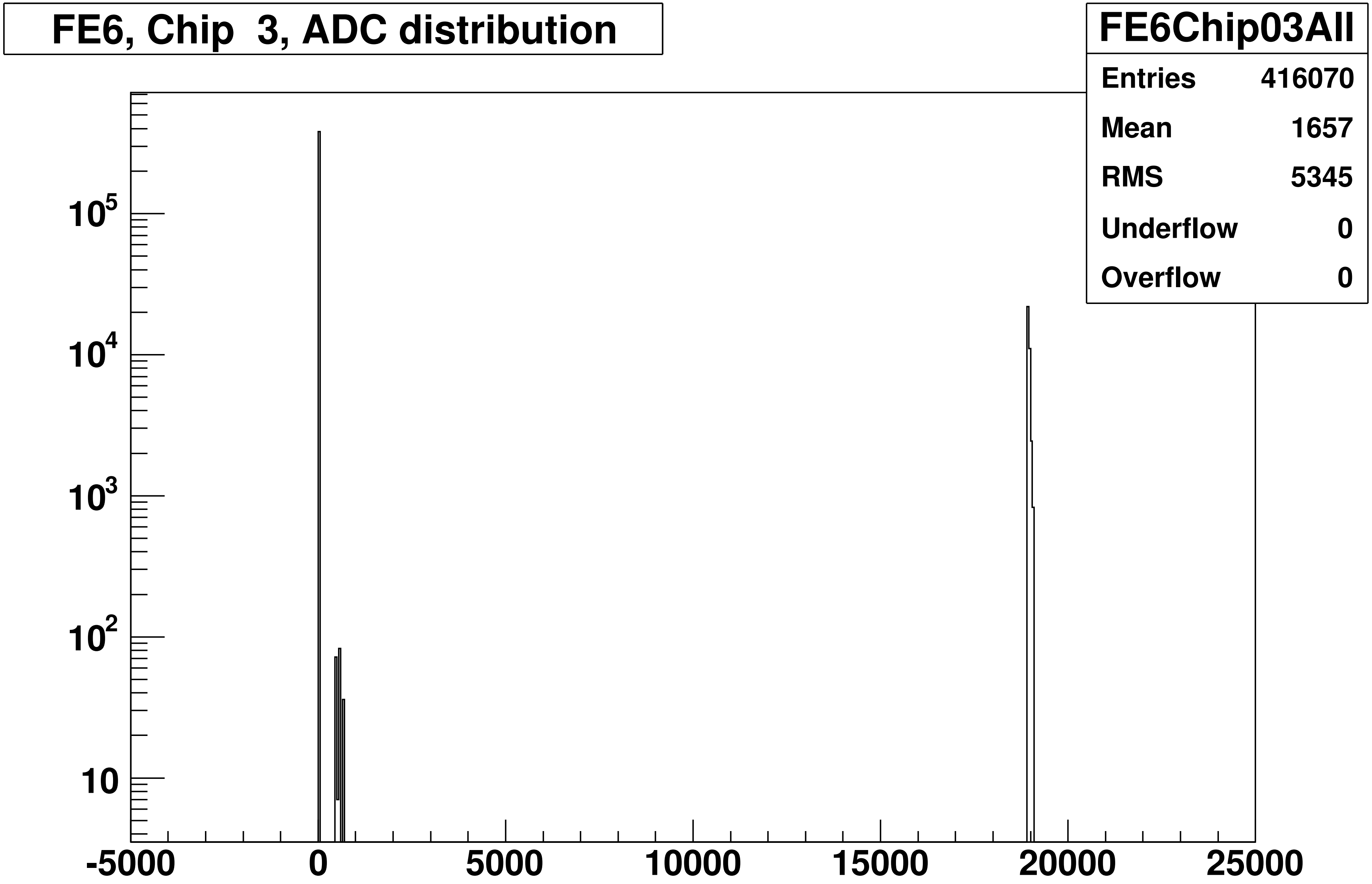


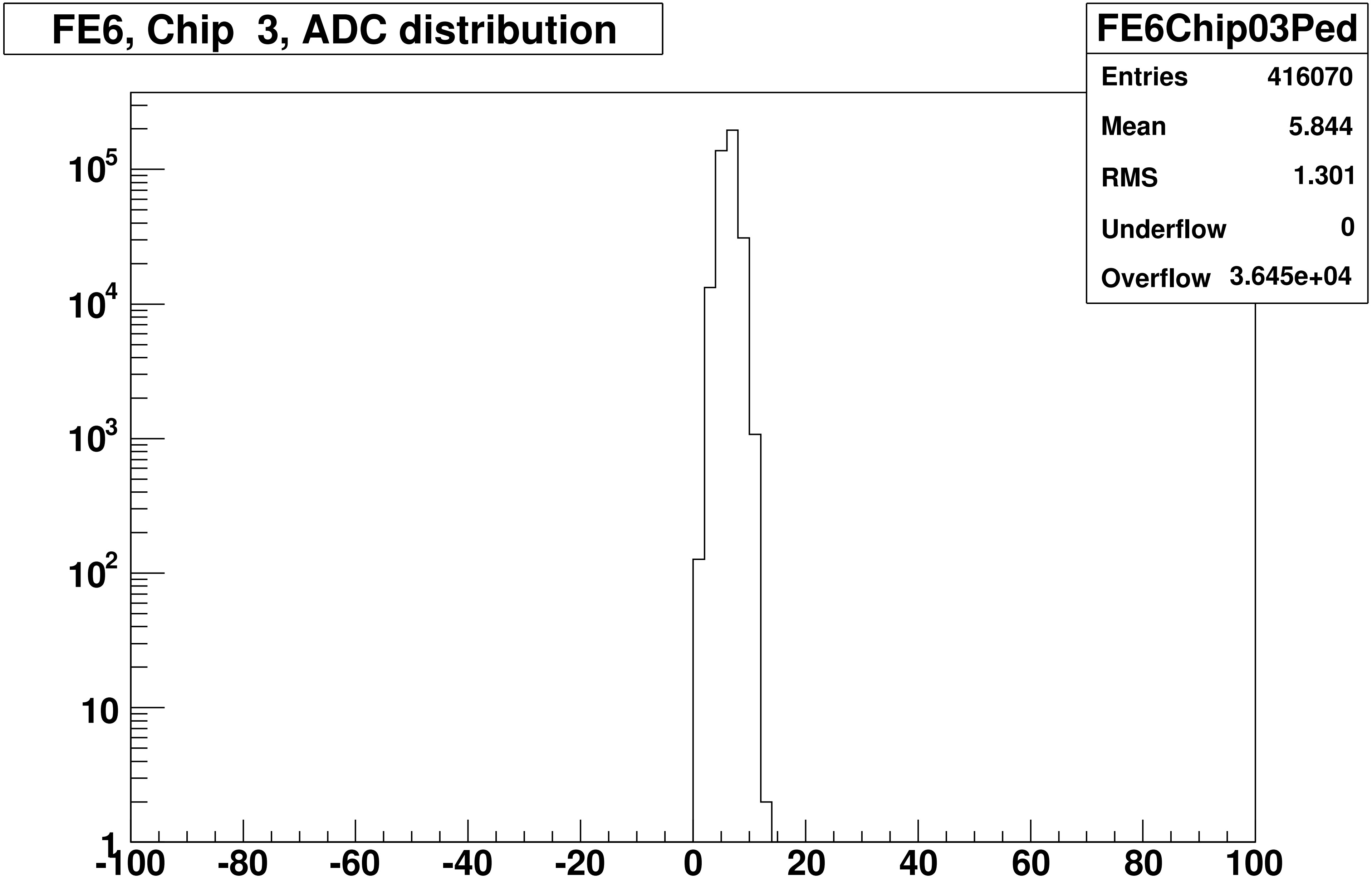
FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 416070
Mean 1646
RMS 5359
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

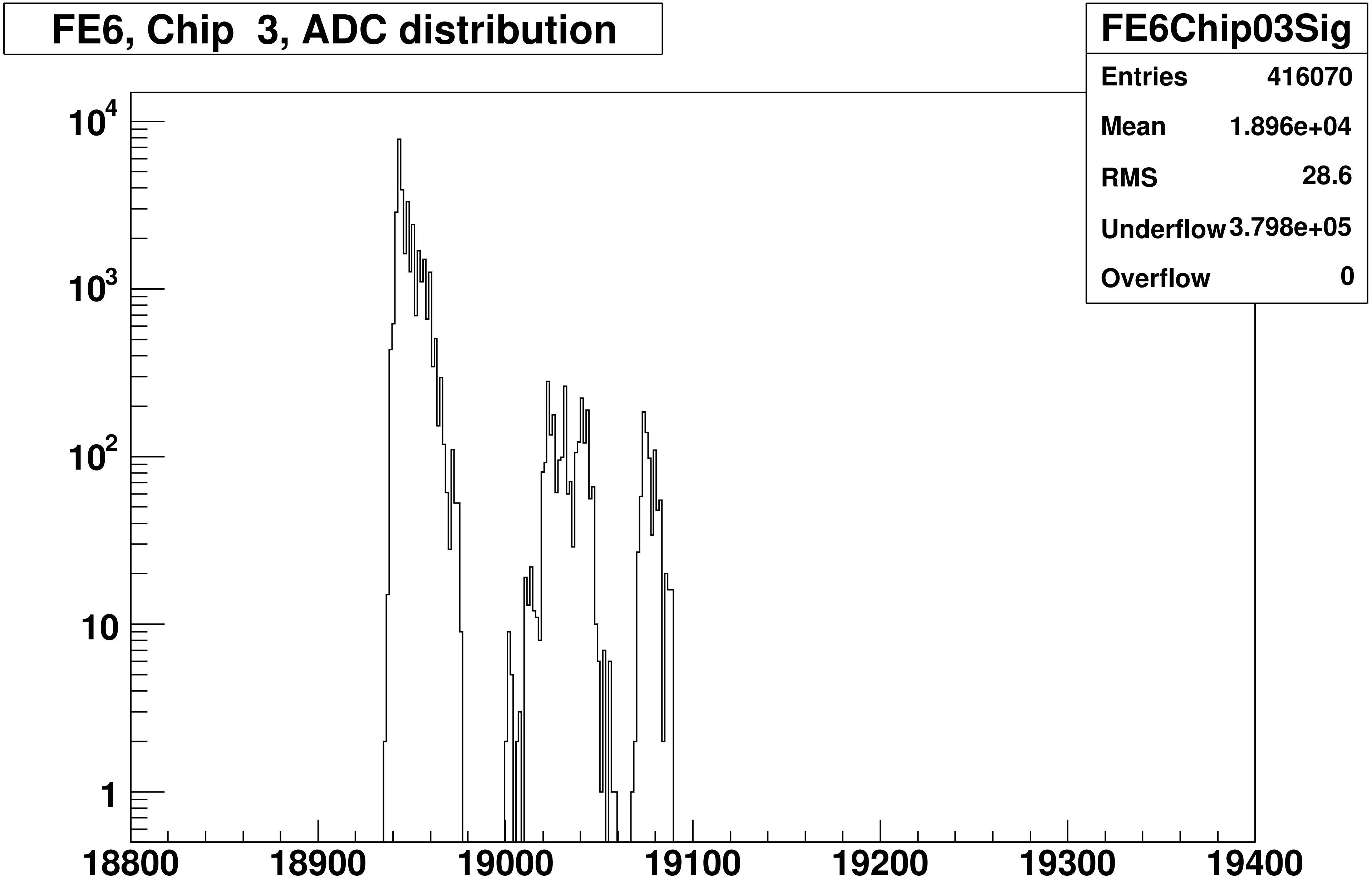

FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 416070
Mean -16.64
RMS 1.35
Underflow 0
Overflow 3.654e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 416070
Mean 1.892e+04
RMS 25.02
Underflow 3.795e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 416070
Mean 1657
RMS 5345
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 416070
Mean 5.844
RMS 1.301
Underflow 0
Overflow 3.645e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 416070
Mean 1.896e+04
RMS 28.6
Underflow 3.798e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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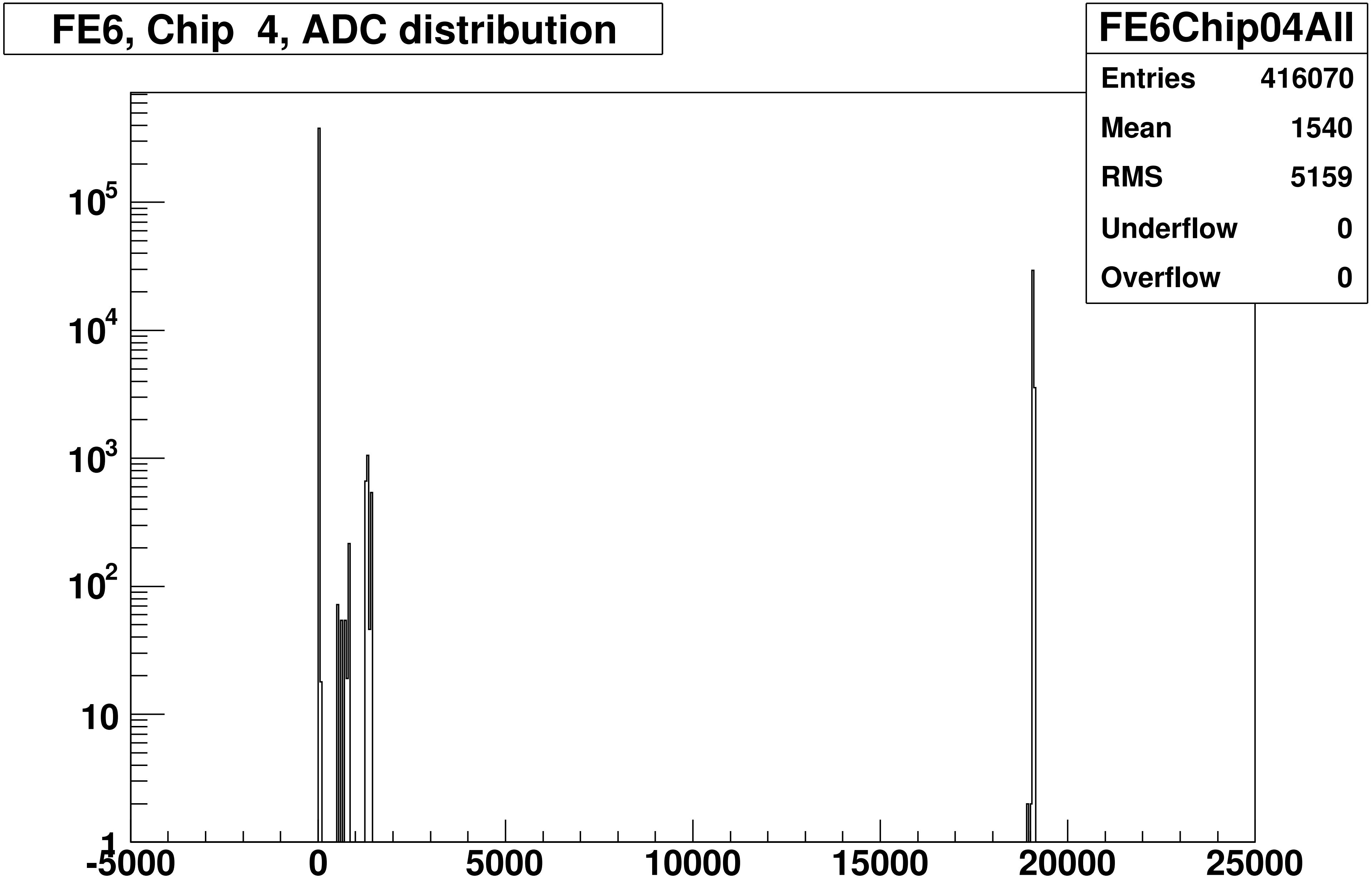
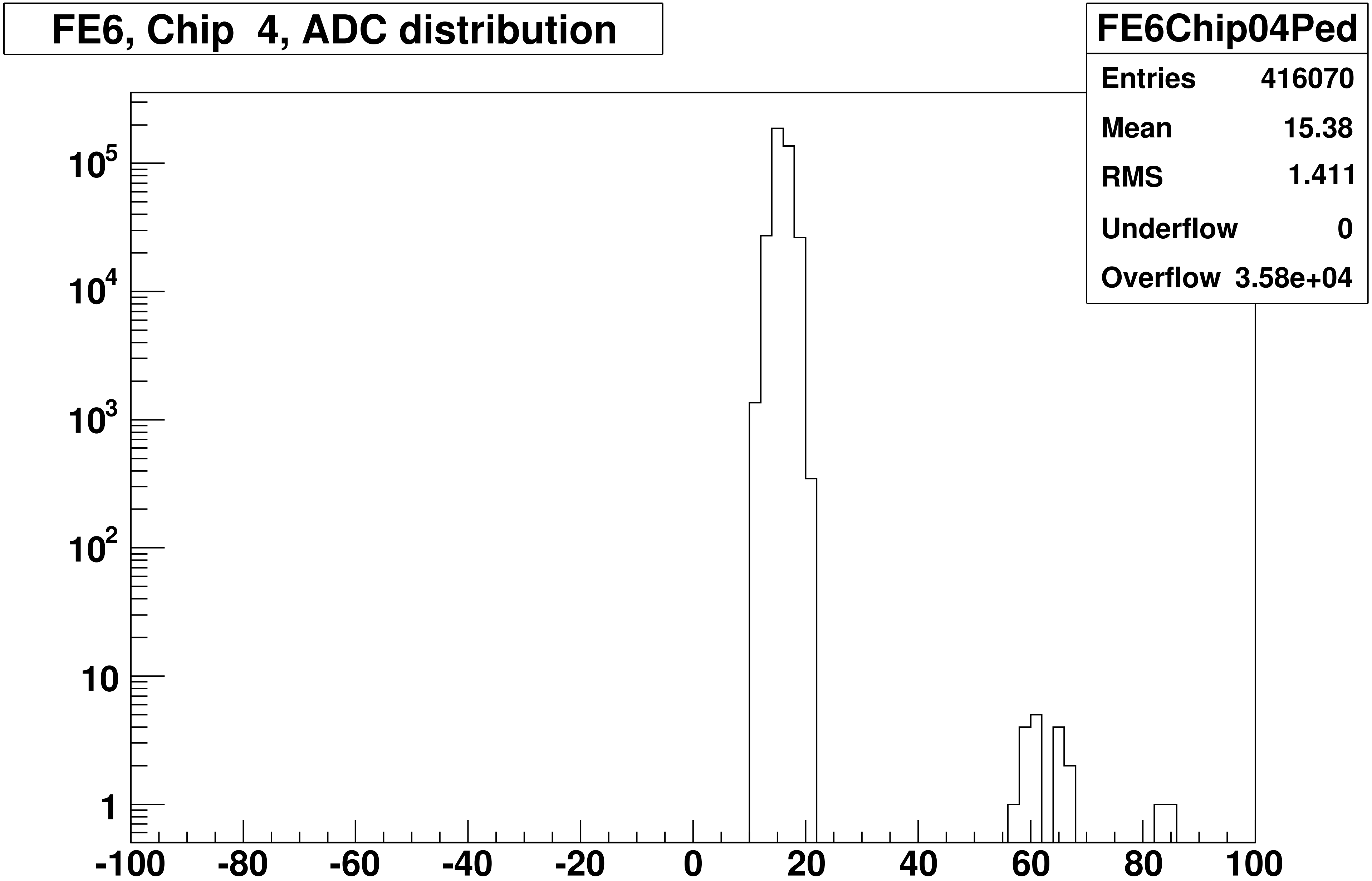
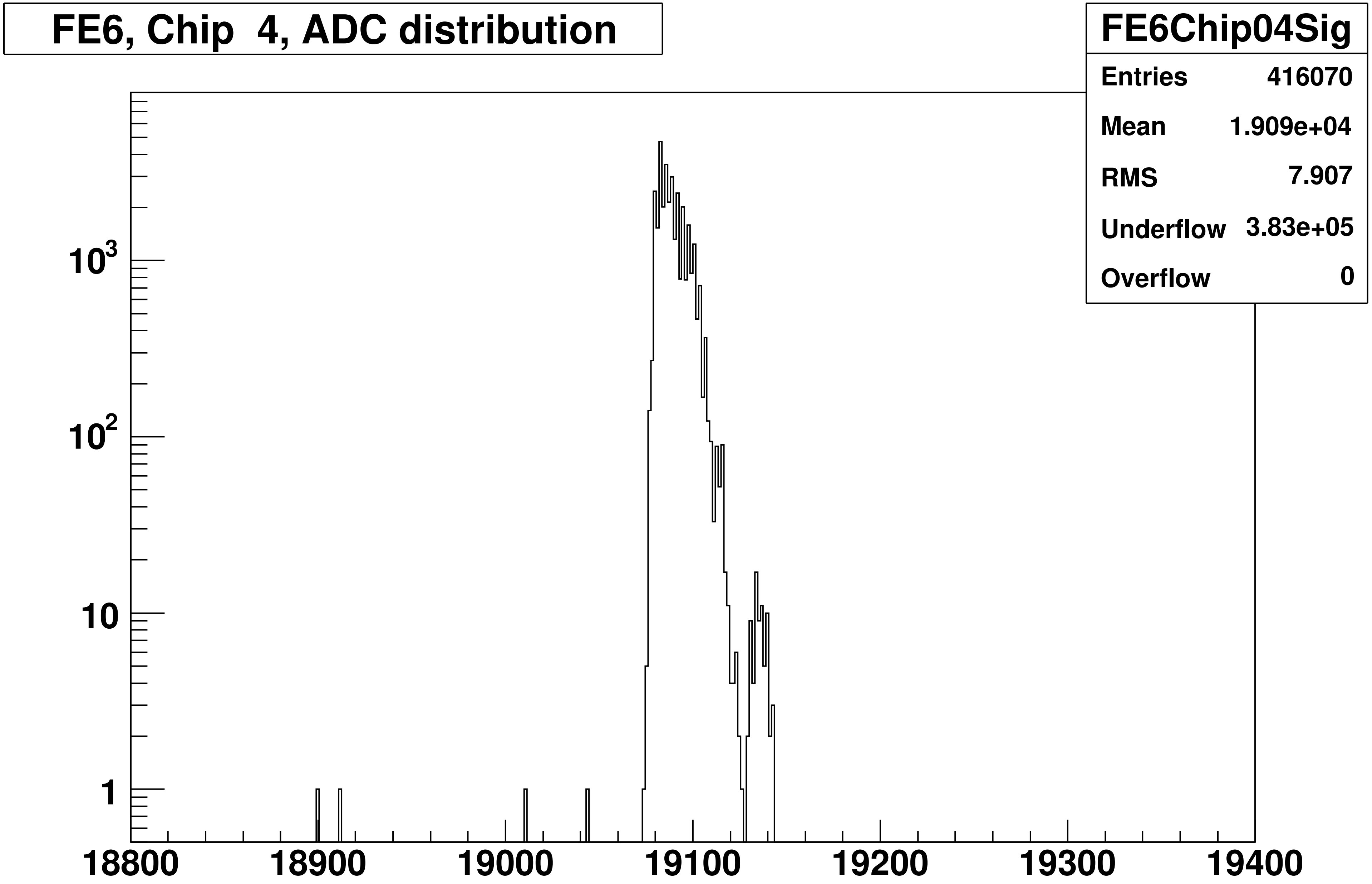
19200
19300
19400

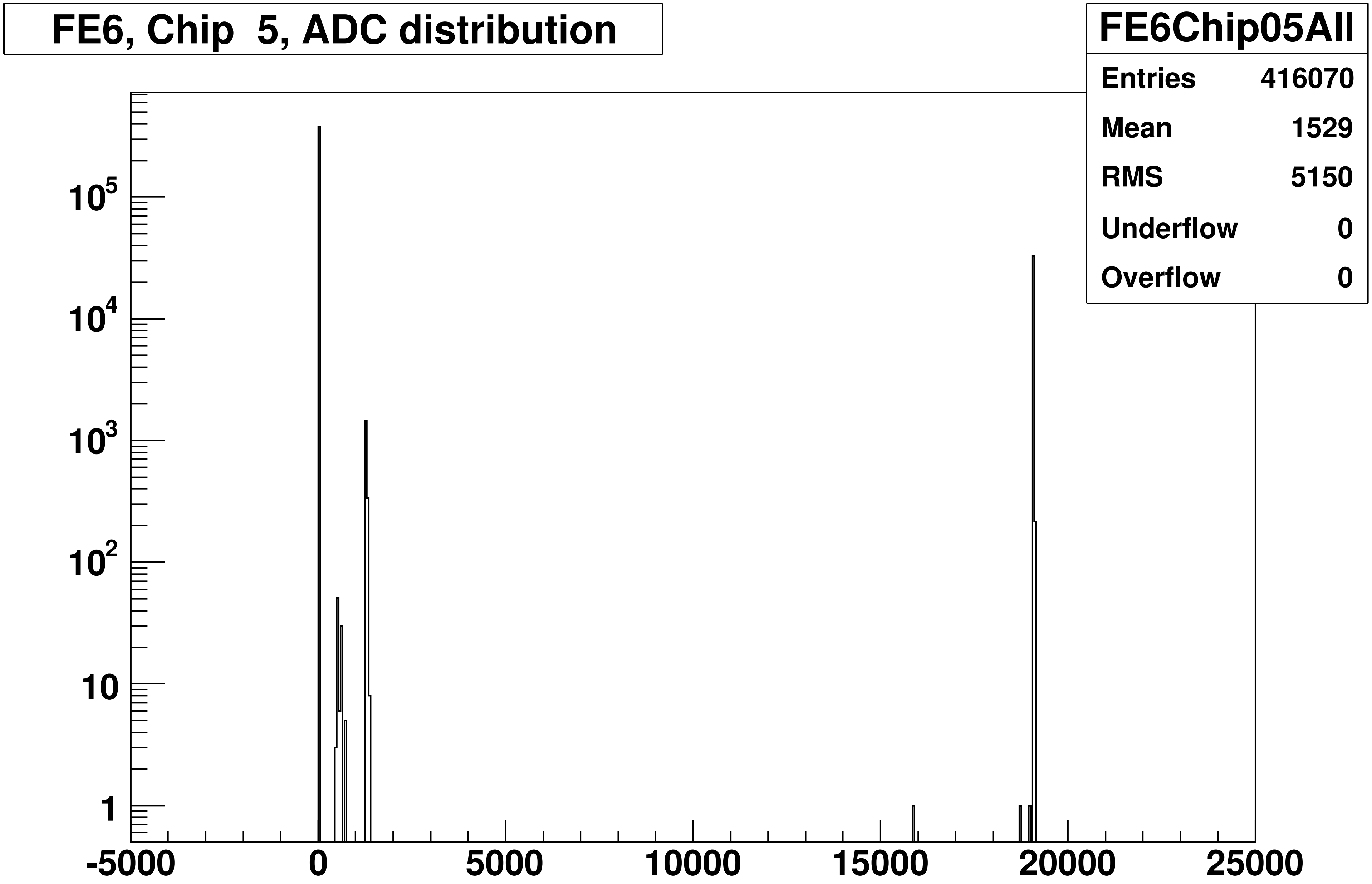

FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 416070
Mean 1540
RMS 5159
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



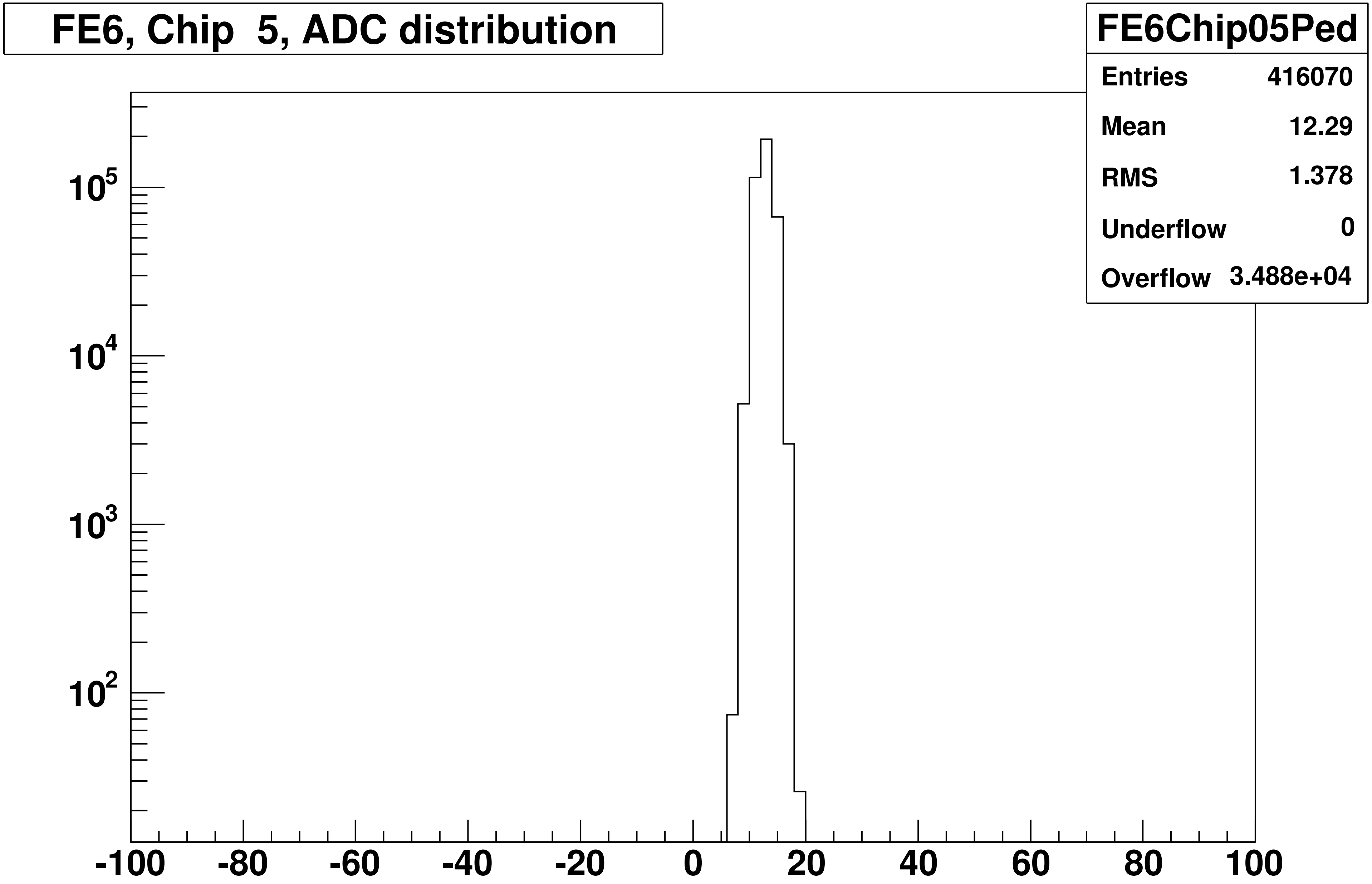
FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 416070
Mean 15.38
RMS 1.411
Underflow 0
Overflow 3.58e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 416070
Mean 1.909e+04
RMS 7.907
Underflow 3.83e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 416070
Mean 1529
RMS 5150
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 416070
Mean 12.29
RMS 1.378
Underflow 0
Overflow 3.488e+04
10^5
10^4
10^3
10^2
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-20
0
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40
60
80
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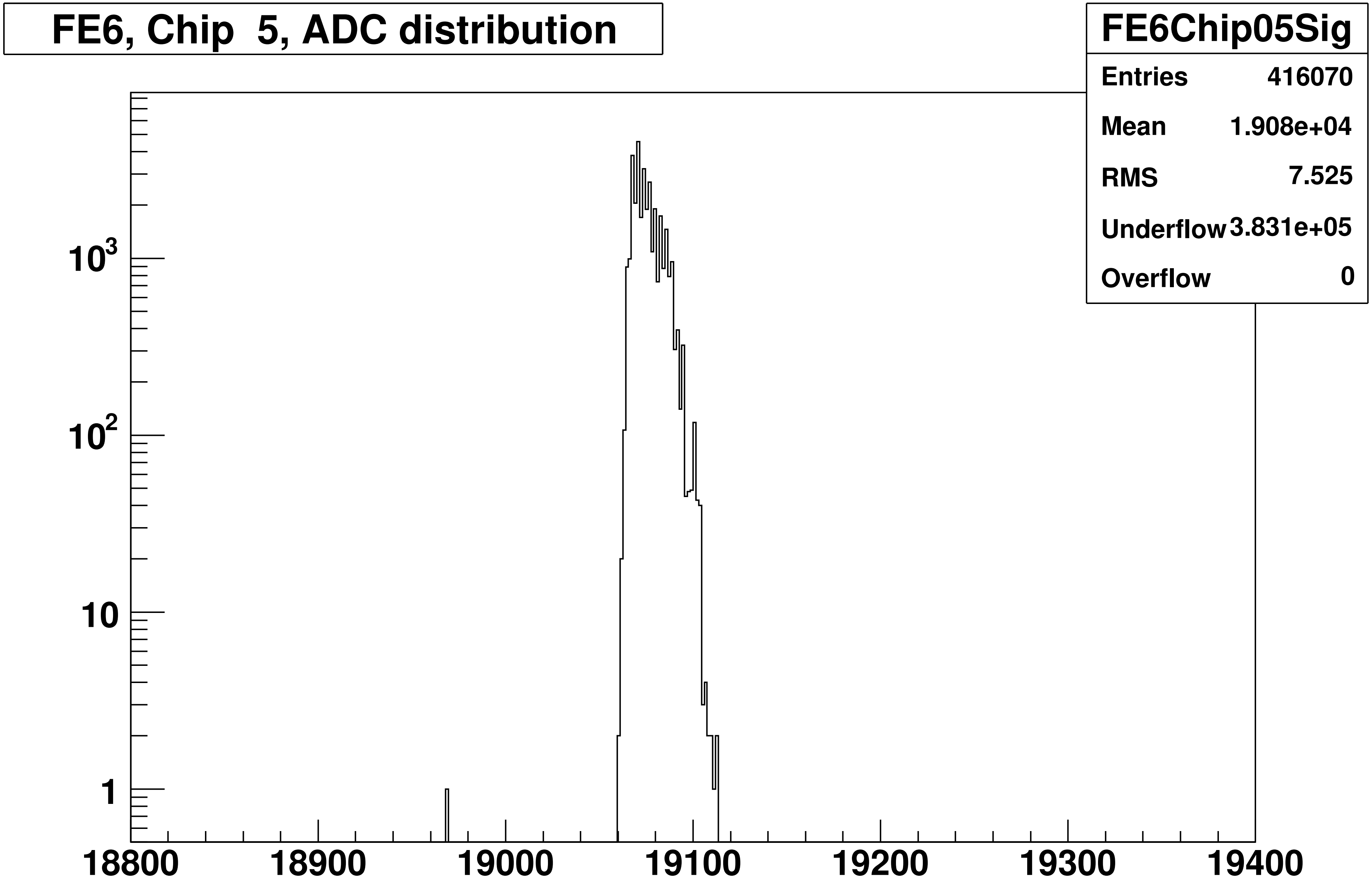
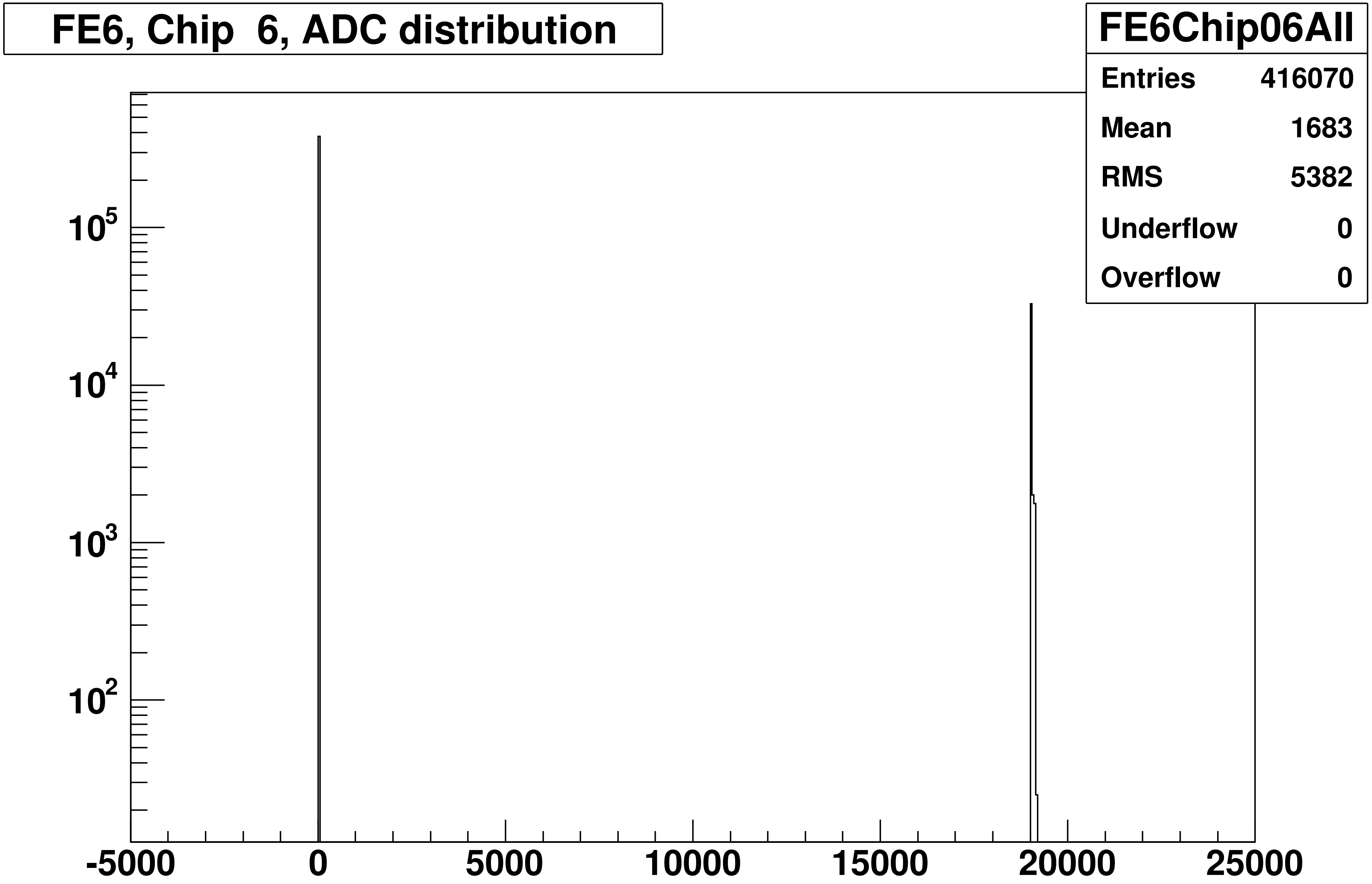
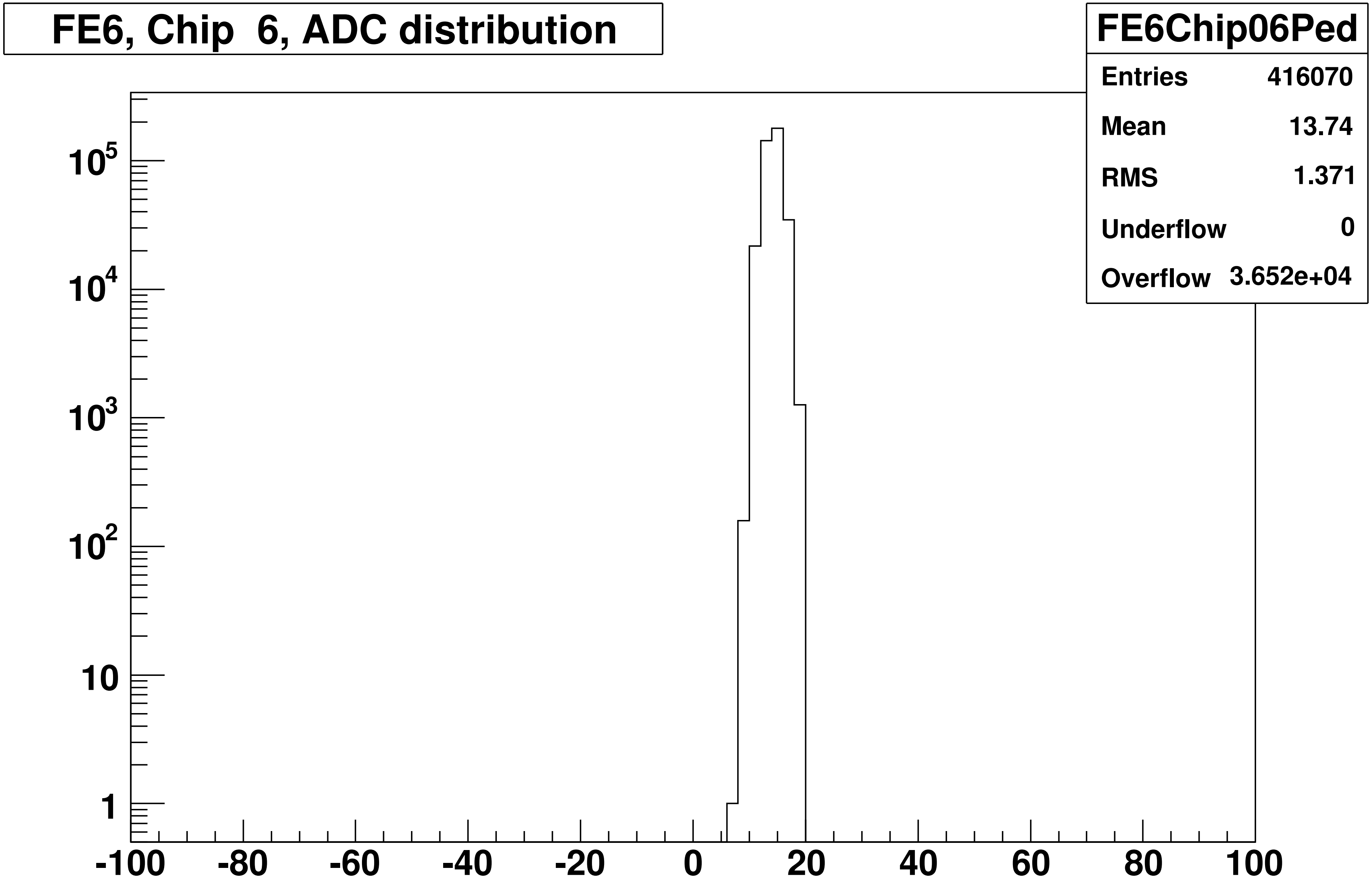

FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 416070
Mean 1.908e+04
RMS 7.525
Underflow 3.831e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 416070
Mean 1683
RMS 5382
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 416070
Mean 13.74
RMS 1.371
Underflow 0
Overflow 3.652e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
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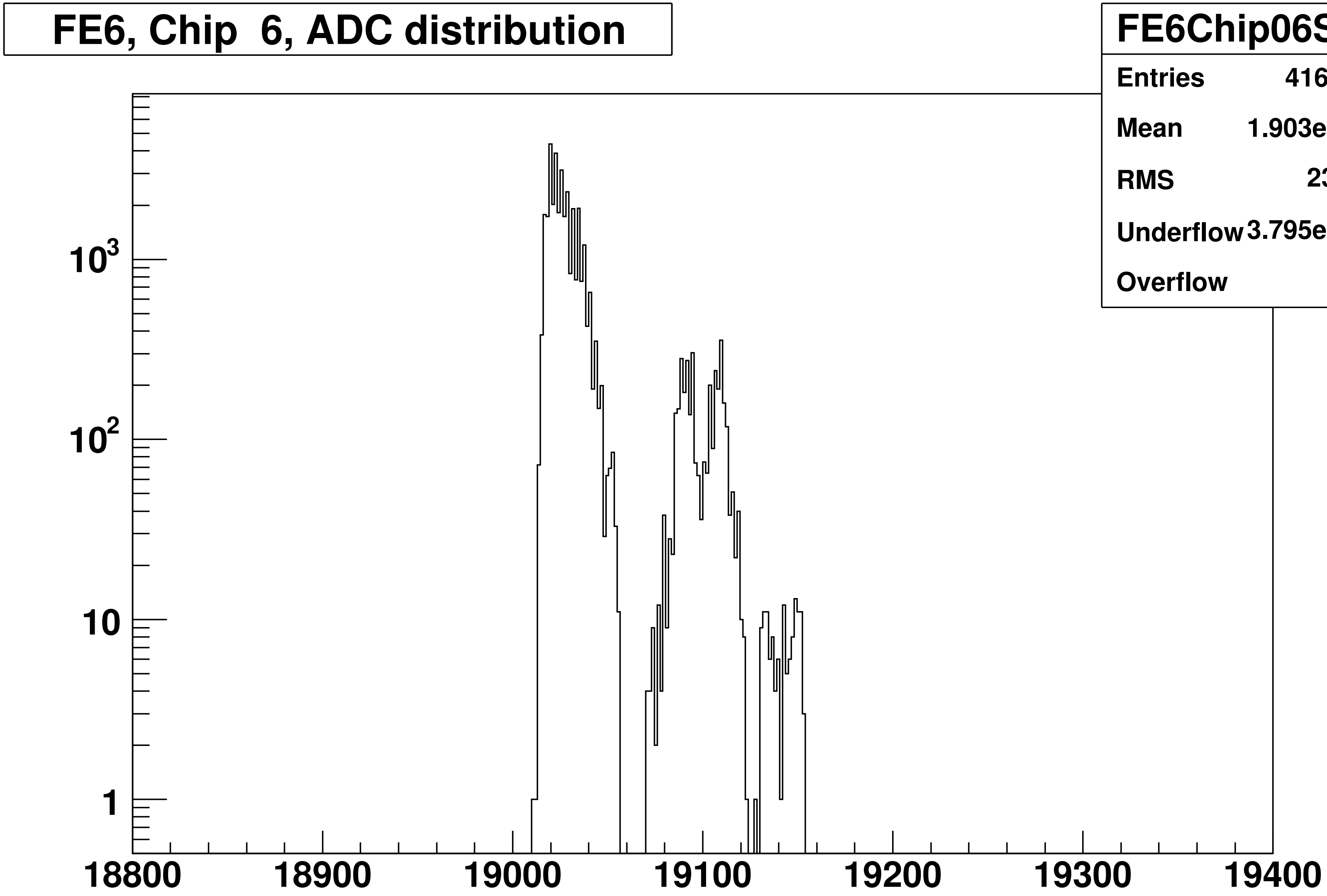
-40
-20
0
20
40
60
80
100


FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 416070
Mean 1.903e+04
RMS 23.47
Underflow 3.795e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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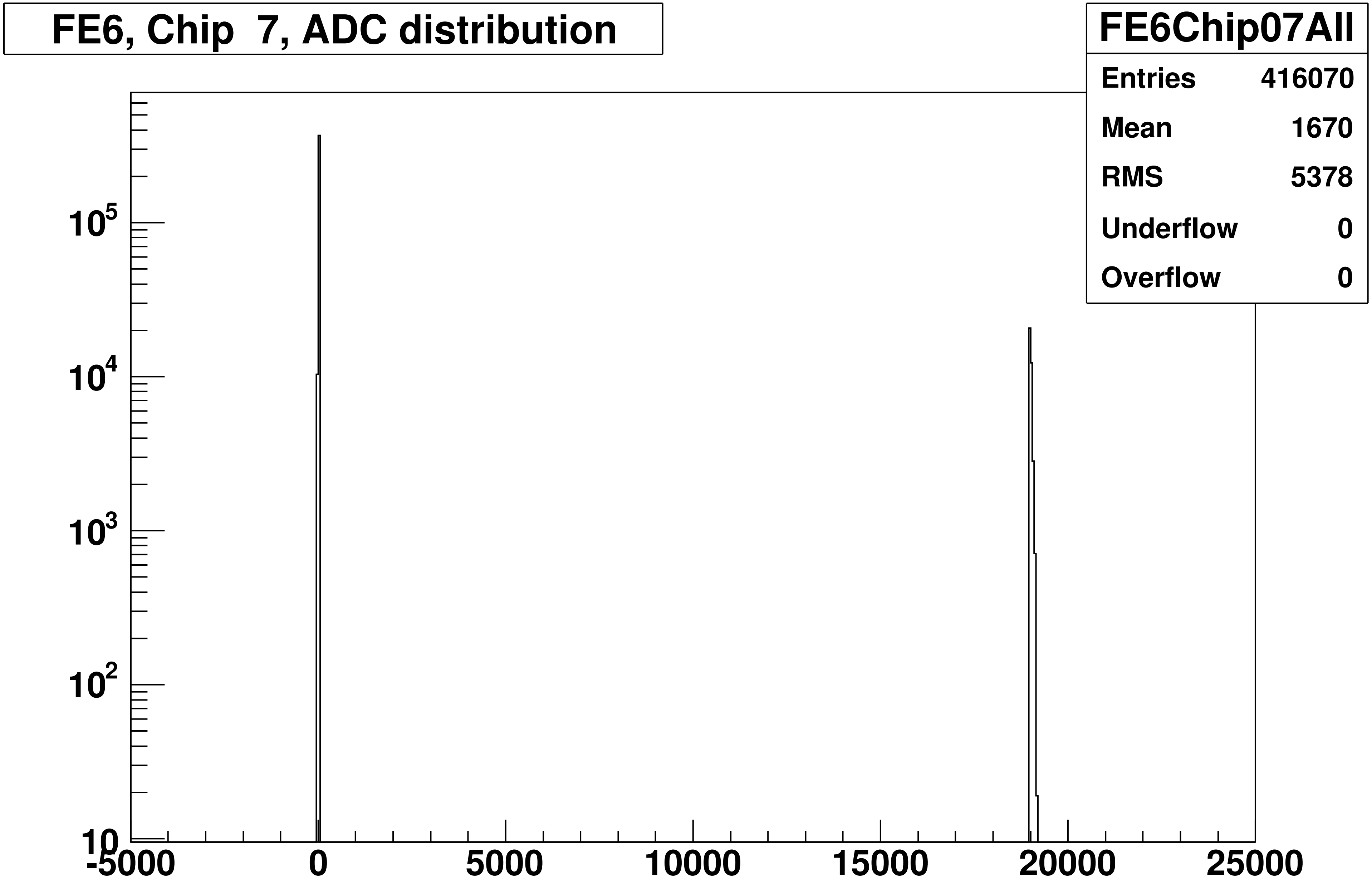
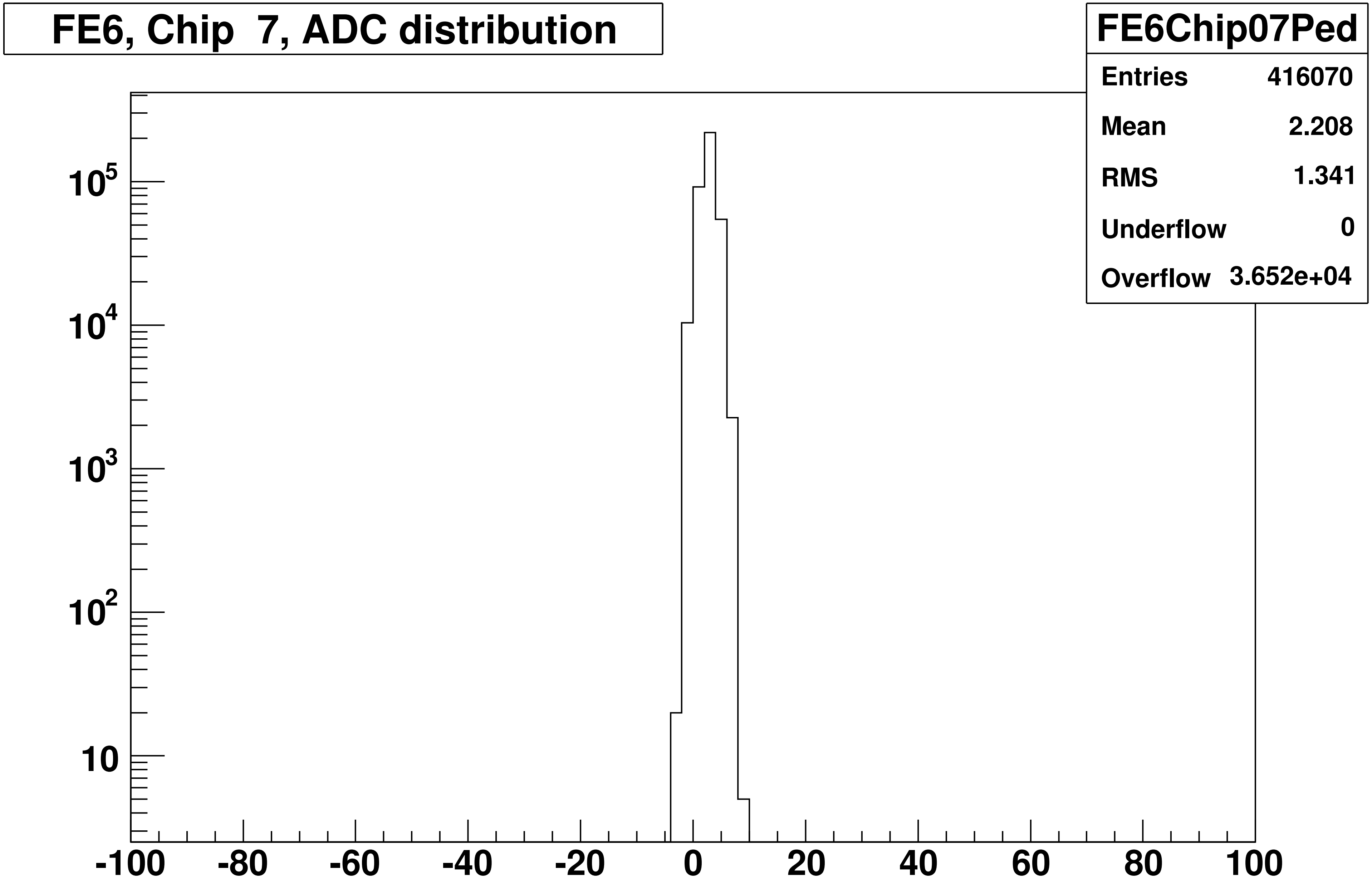


FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 416070
Mean 1670
RMS 5378
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 416070
Mean 2.208
RMS 1.341
Underflow 0
Overflow 3.652e+04
10
10^2
10^3
10^4
10^5
-100
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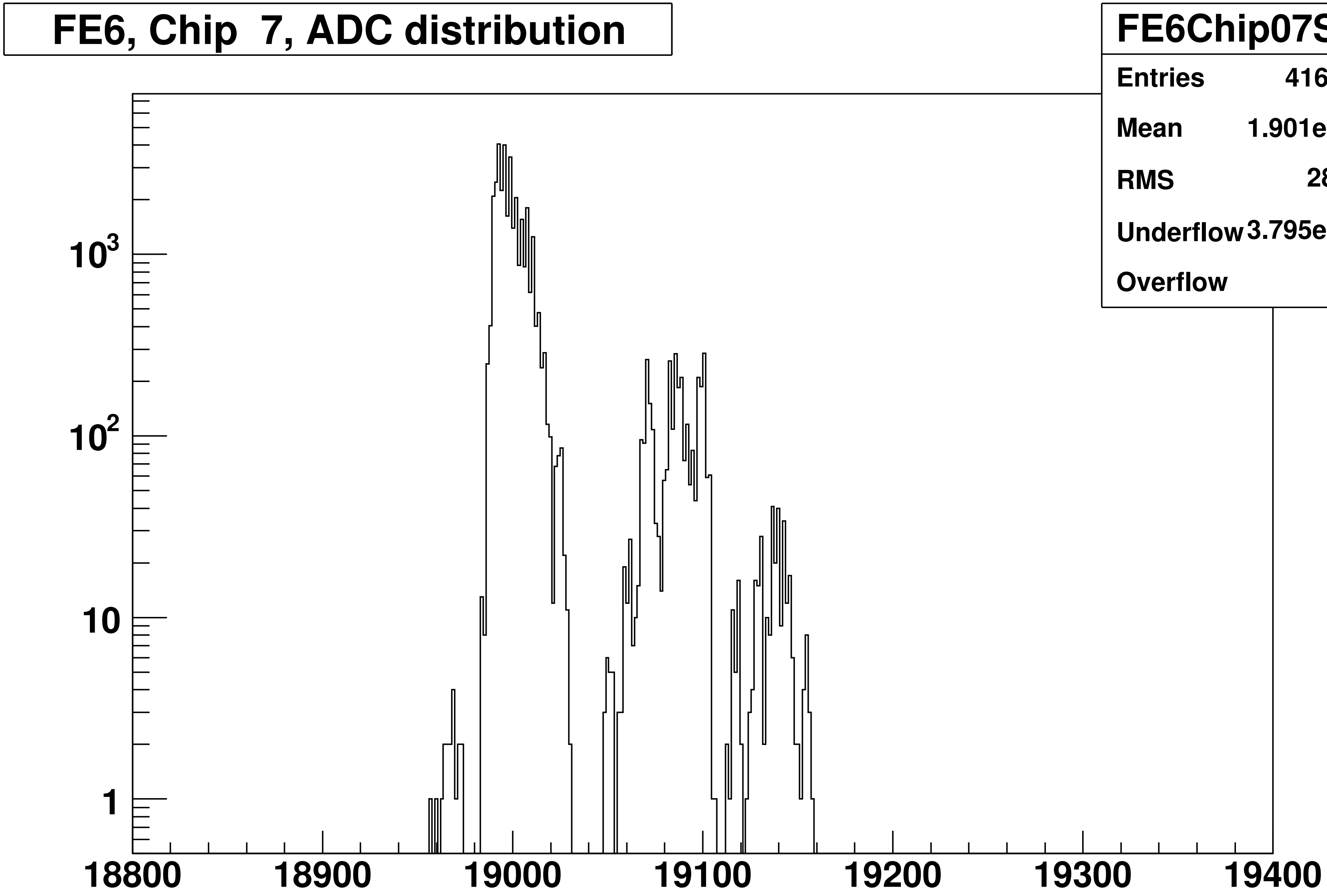
-20
0
20
40
60
80
100



FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 416070
Mean 1.901e+04
RMS 28.56
Underflow 3.795e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
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19300
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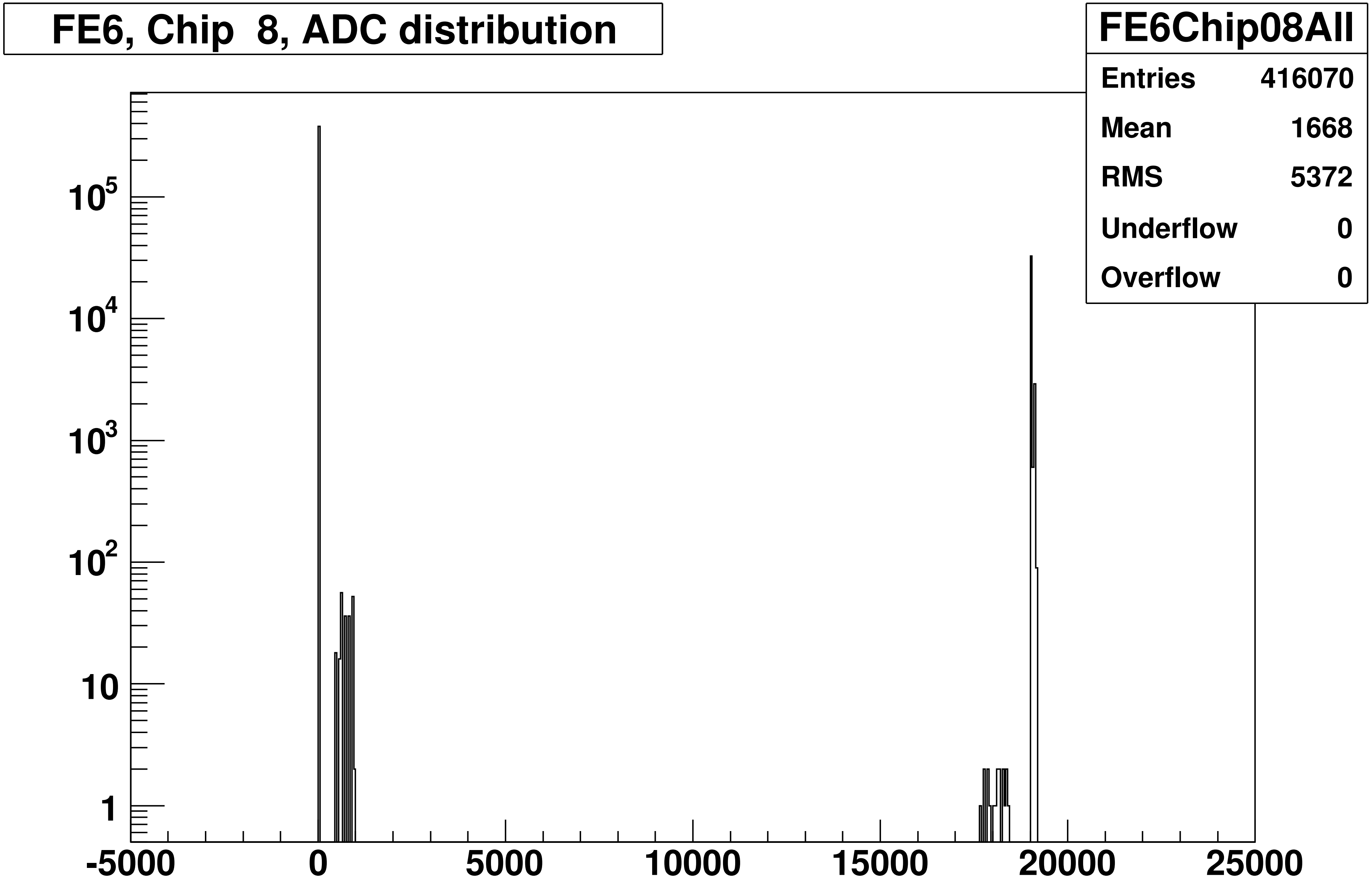
FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 416070
Mean 1668
RMS 5372
Underflow 0
Overflow 0
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10^4
10^3
10^2
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1
-5000
0
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10000
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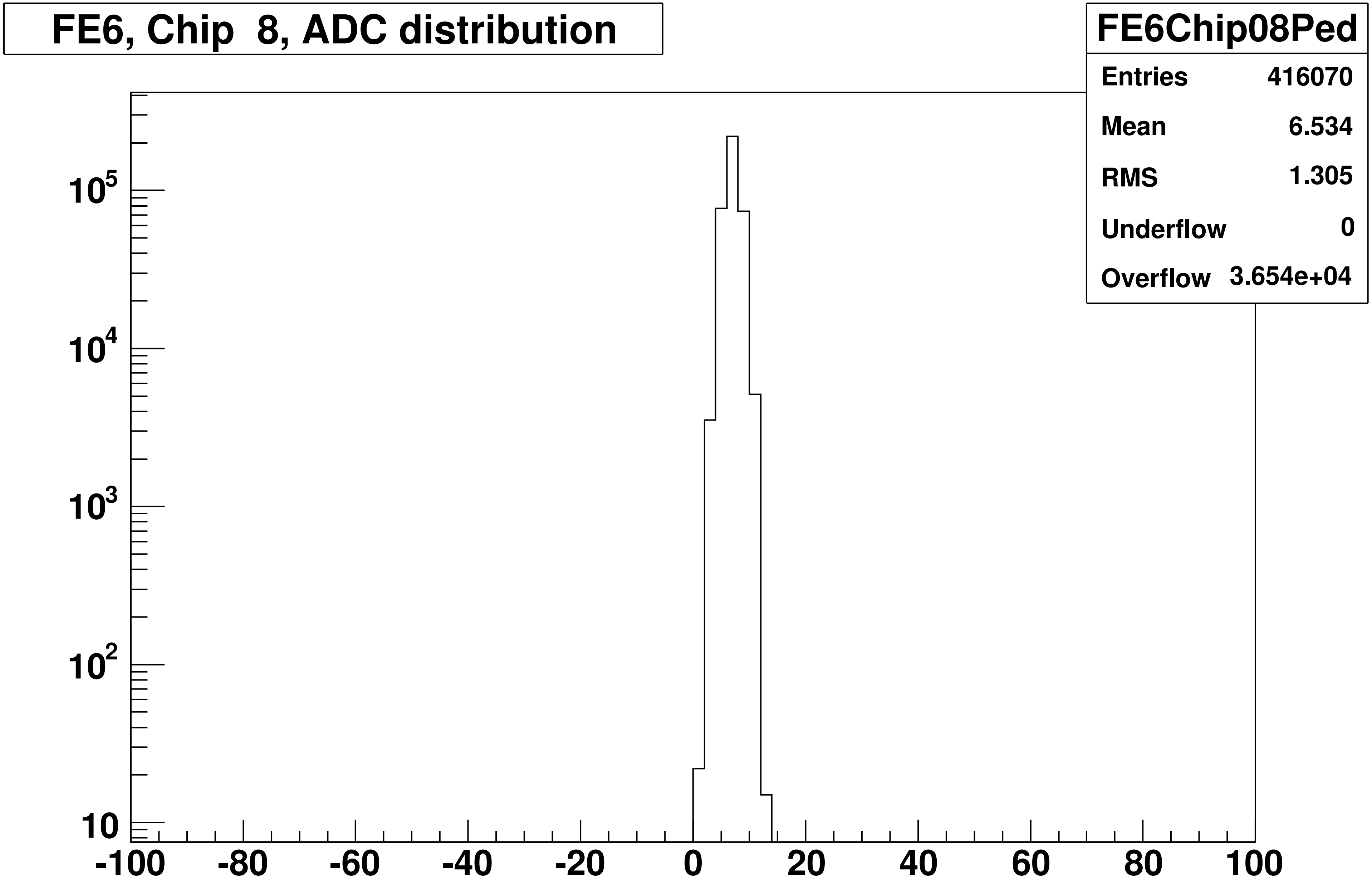
20000
25000


FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 416070
Mean 6.534
RMS 1.305
Underflow 0
Overflow 3.654e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
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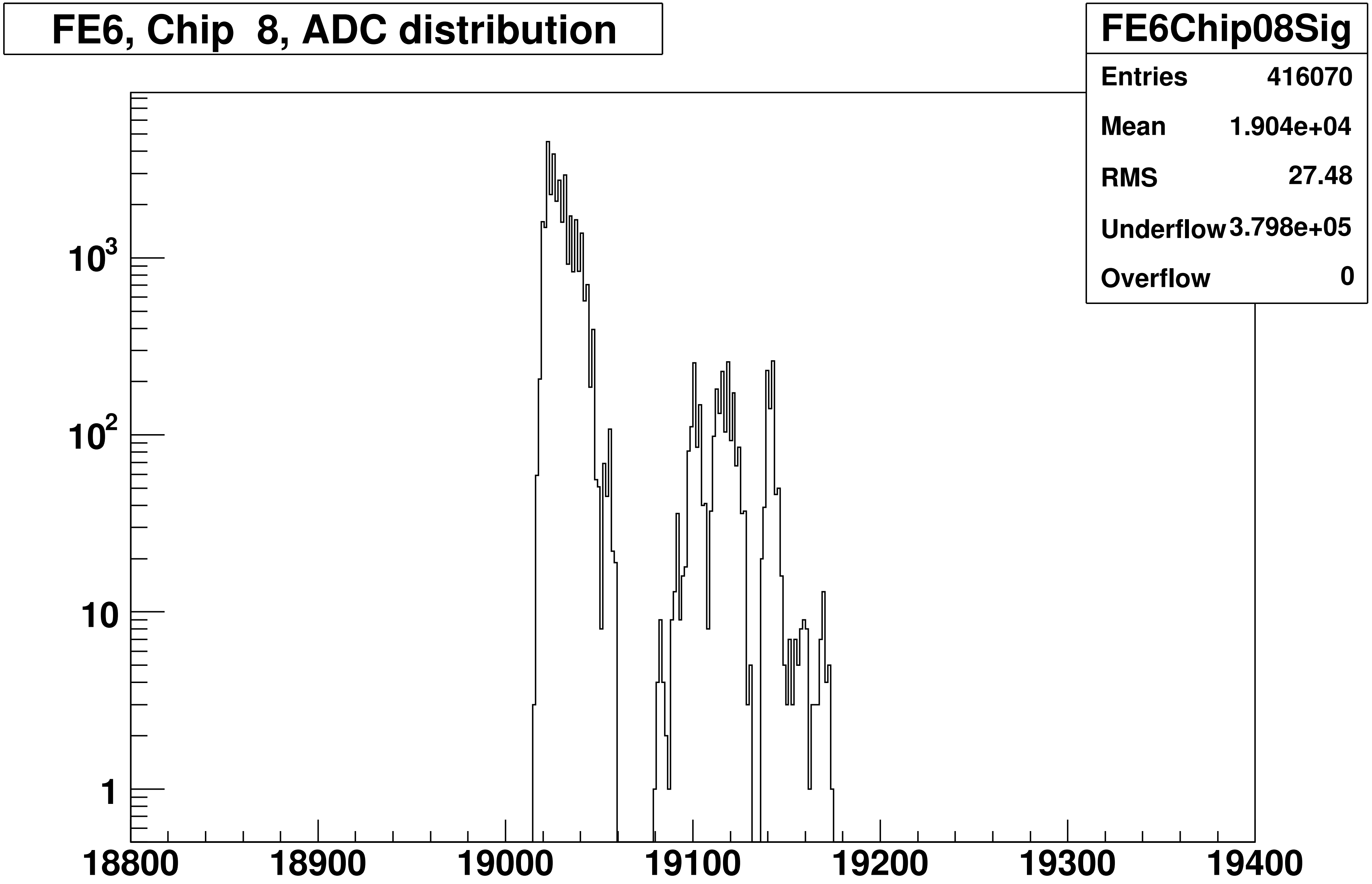
20
40
60
80
100


FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 416070
Mean 1.904e+04
RMS 27.48
Underflow 3.798e+05
Overflow 0
10^3
10^2
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1
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18900
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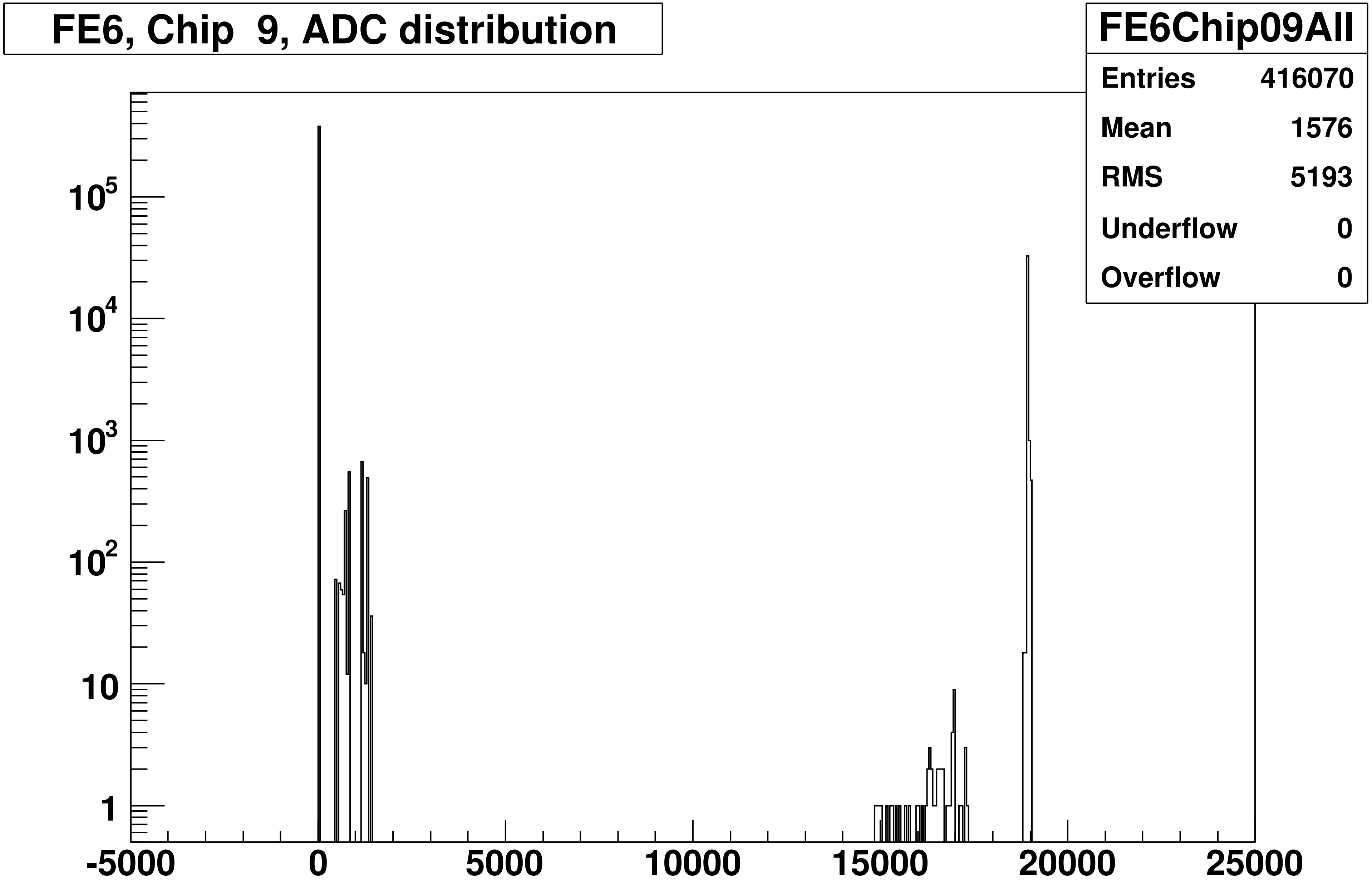

FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 416070
Mean 1576
RMS 5193
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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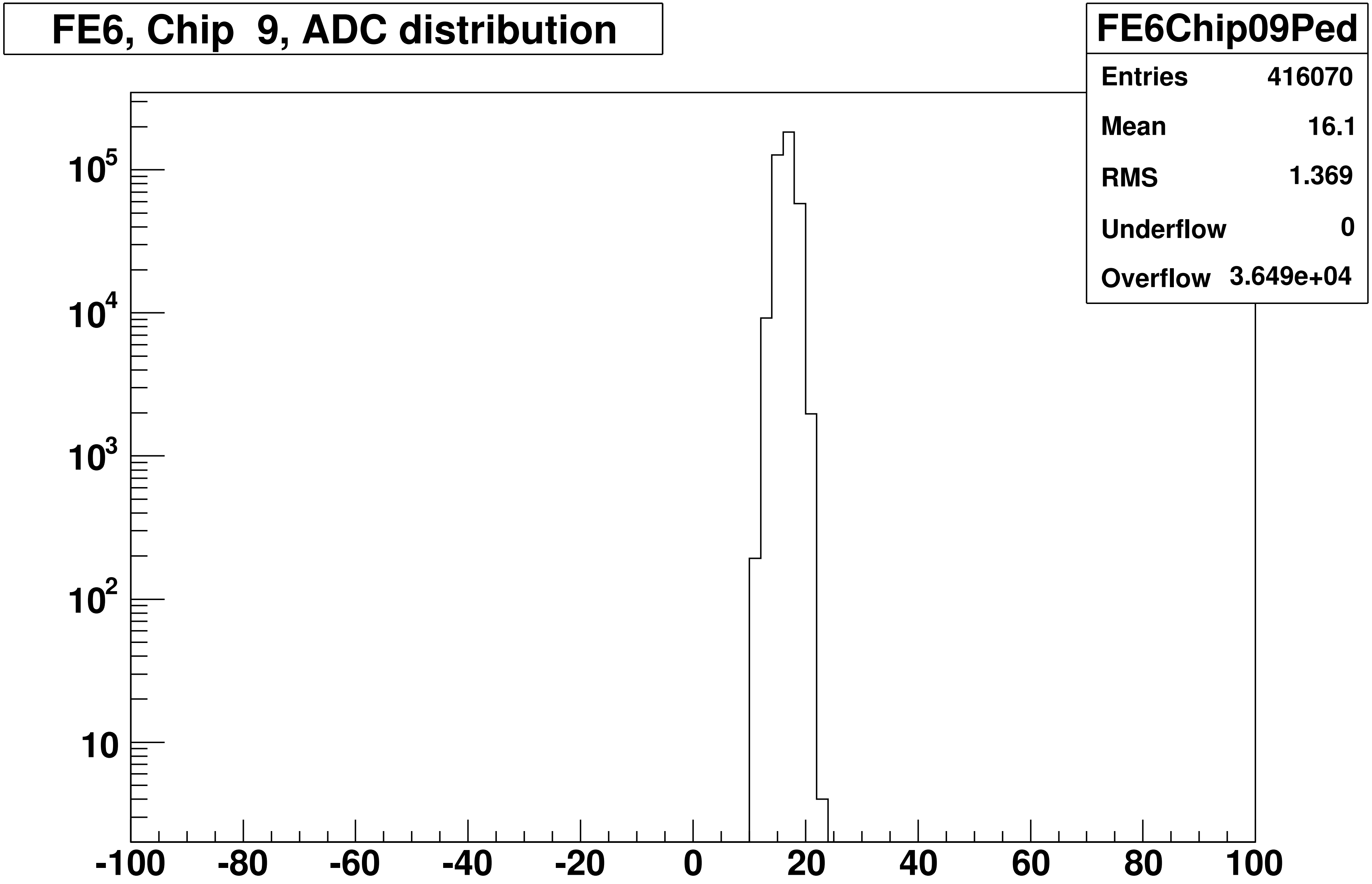

FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 416070
Mean 16.1
RMS 1.369
Underflow 0
Overflow 3.649e+04
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10^2
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-40
-20
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40
60
80
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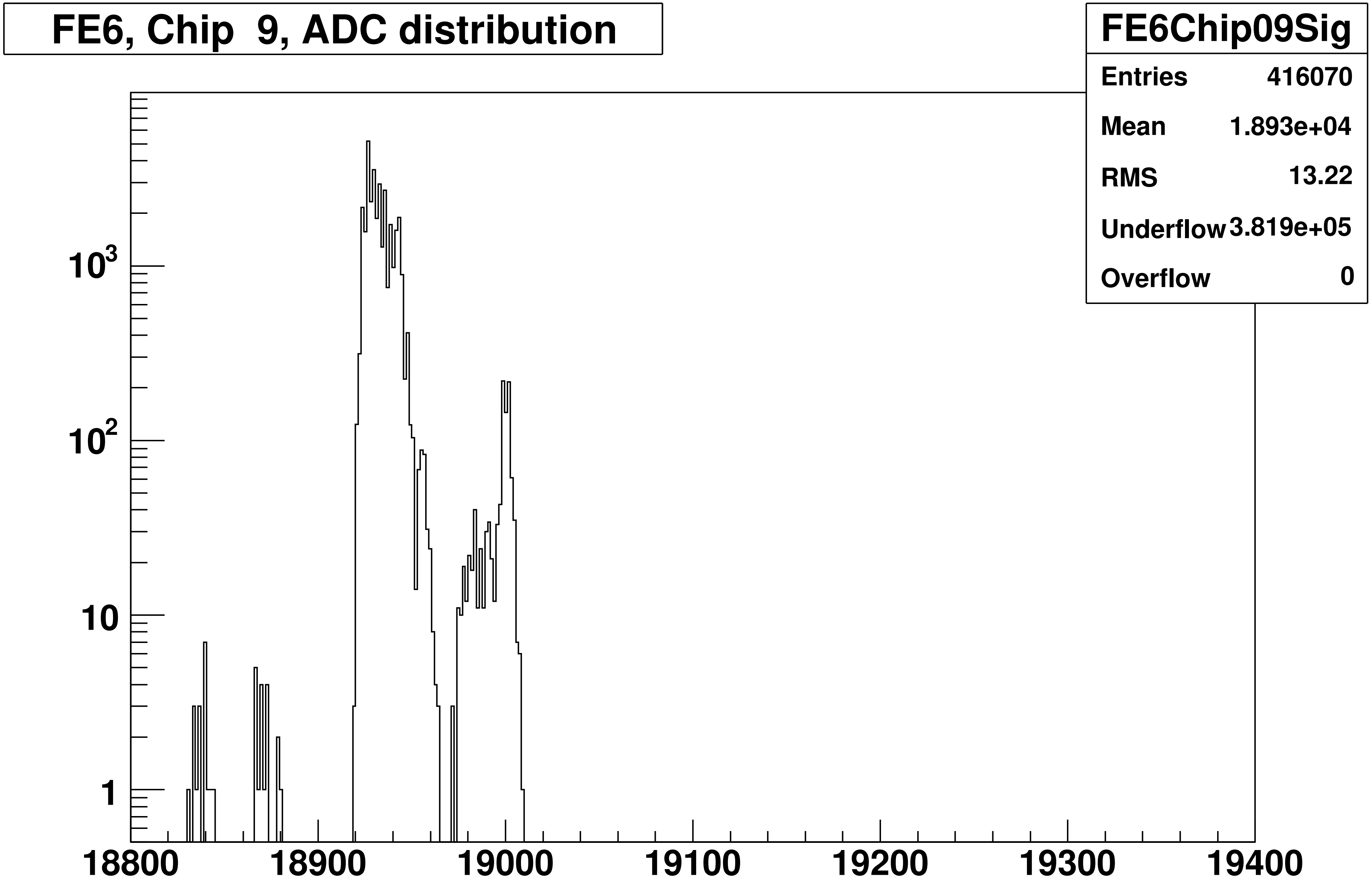
FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 416070
Mean 1.893e+04
RMS 13.22
Underflow 3.819e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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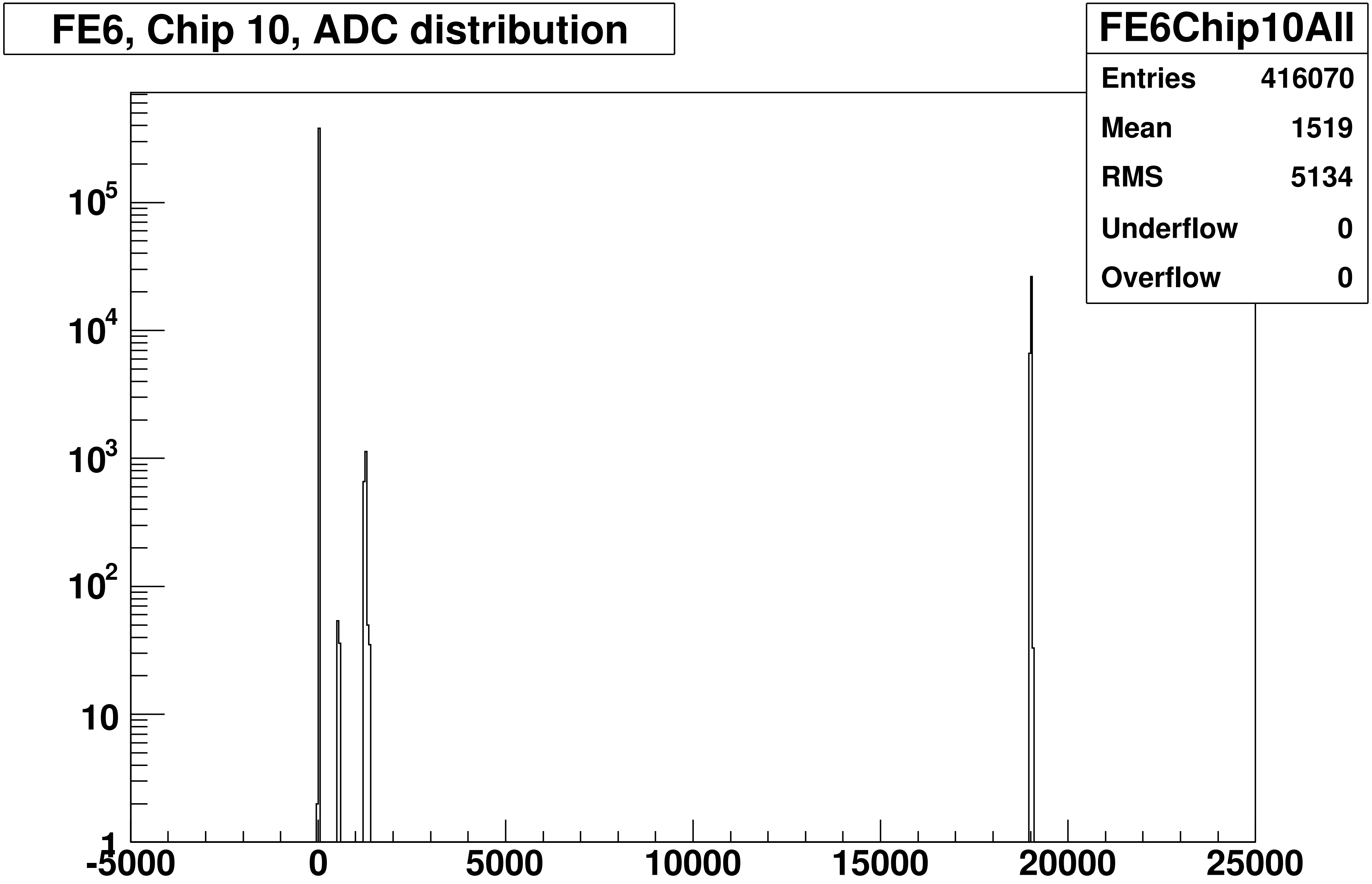
19100
19200
19300
19400



FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 416070
Mean 1519
RMS 5134
Underflow 0
Overflow 0
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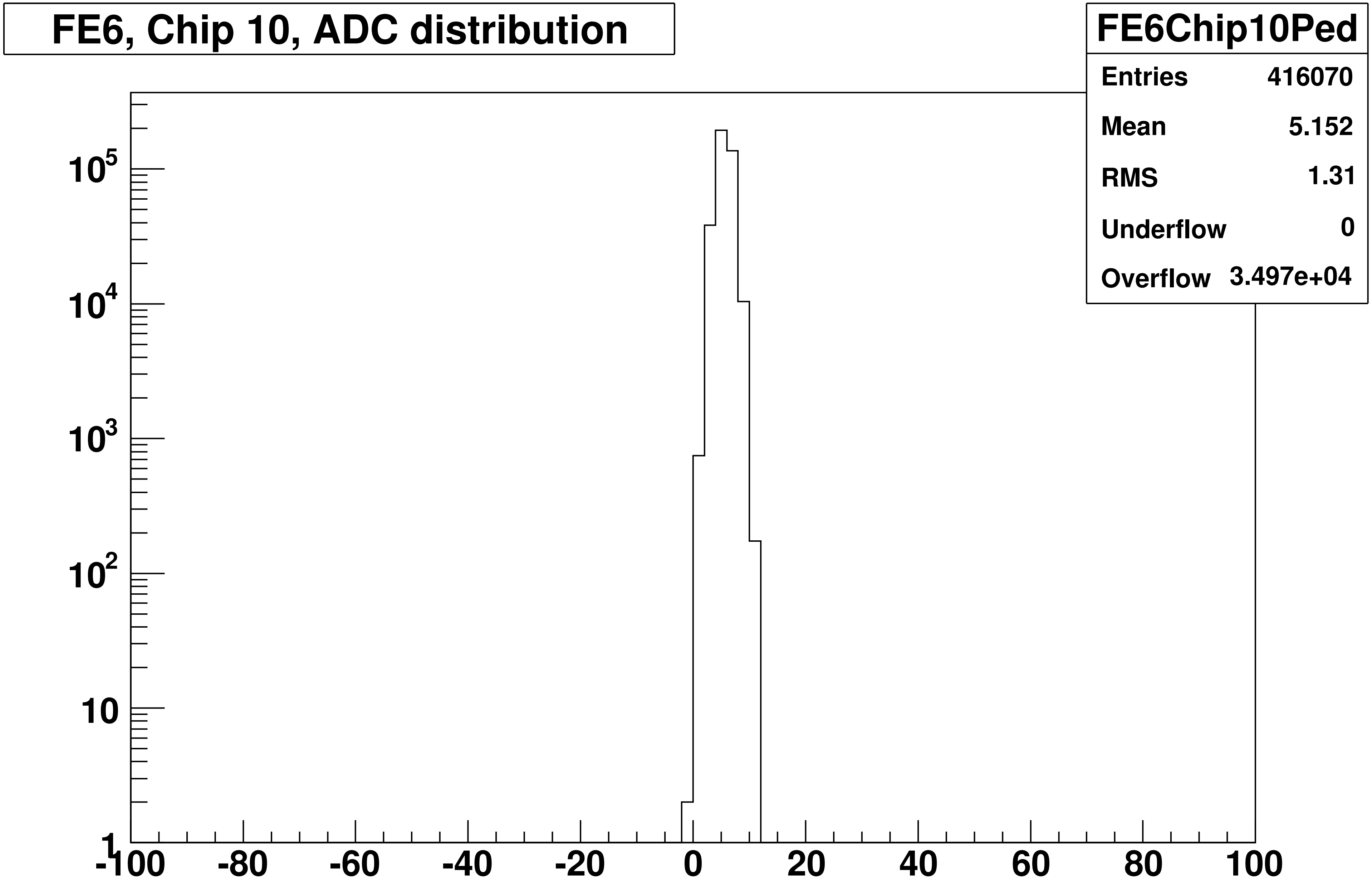
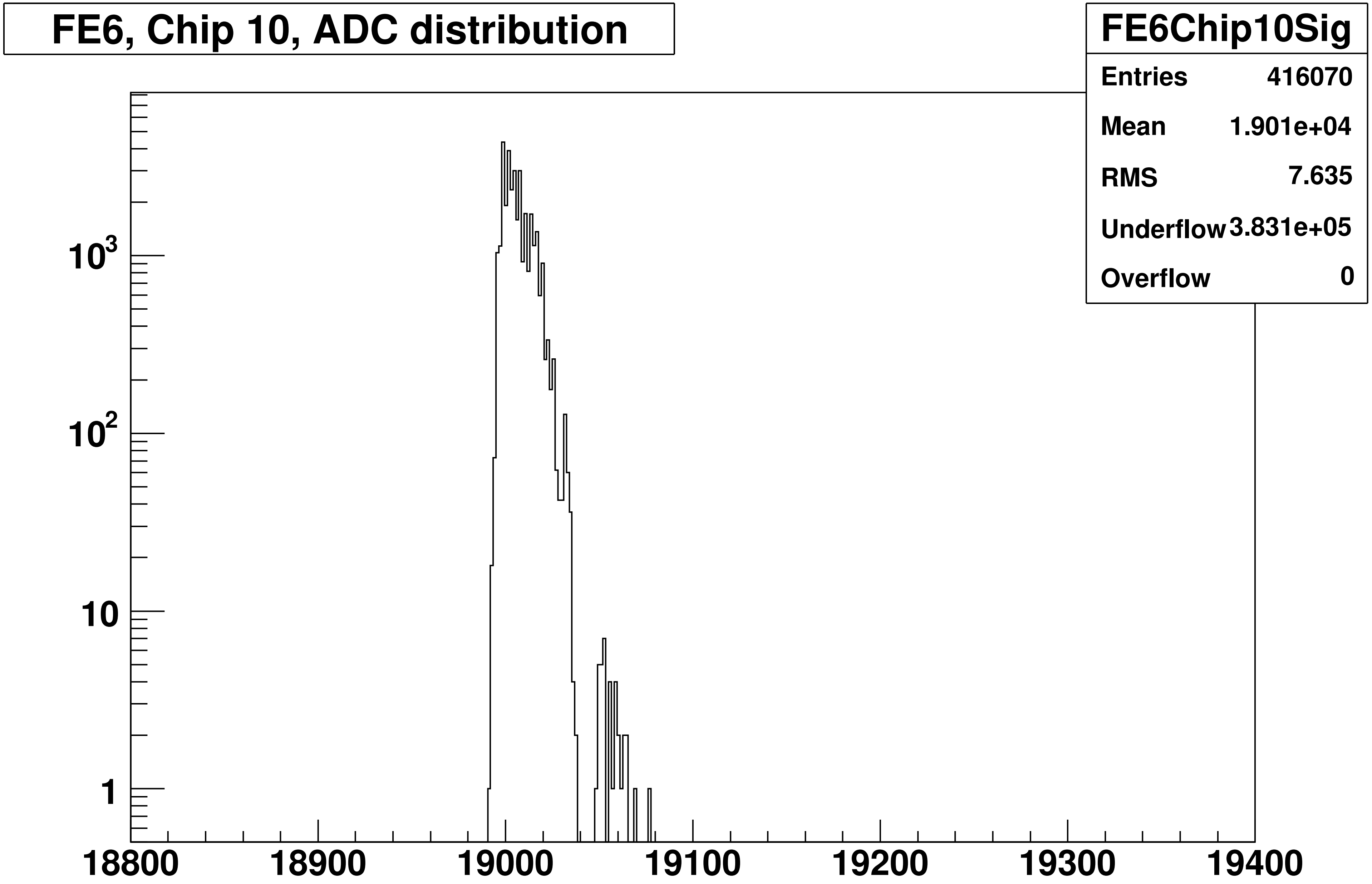
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 416070
Mean 5.152
RMS 1.31
Underflow 0
Overflow 3.497e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 416070
Mean 1.901e+04
RMS 7.635
Underflow 3.831e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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19100
19200
19300
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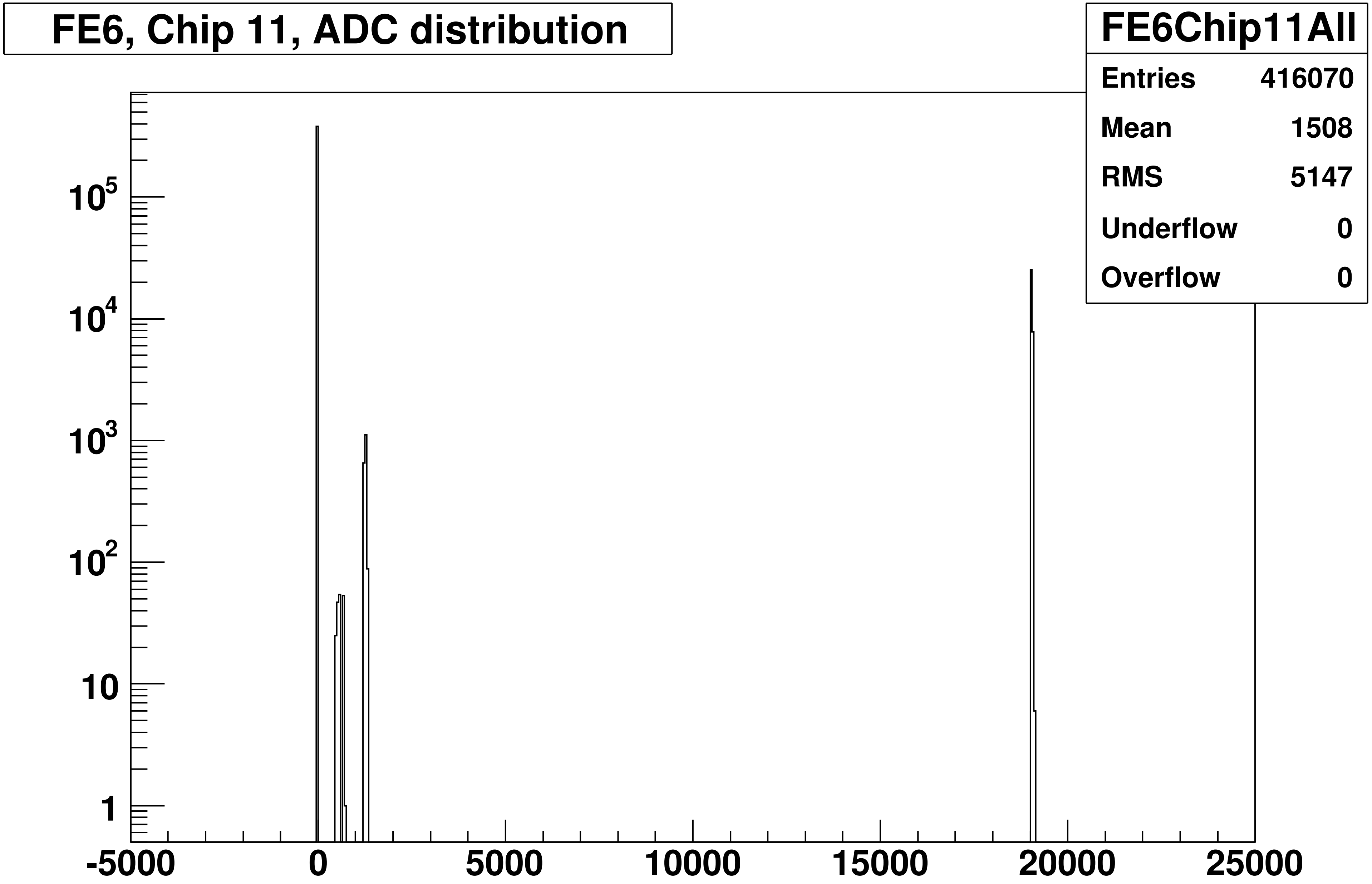
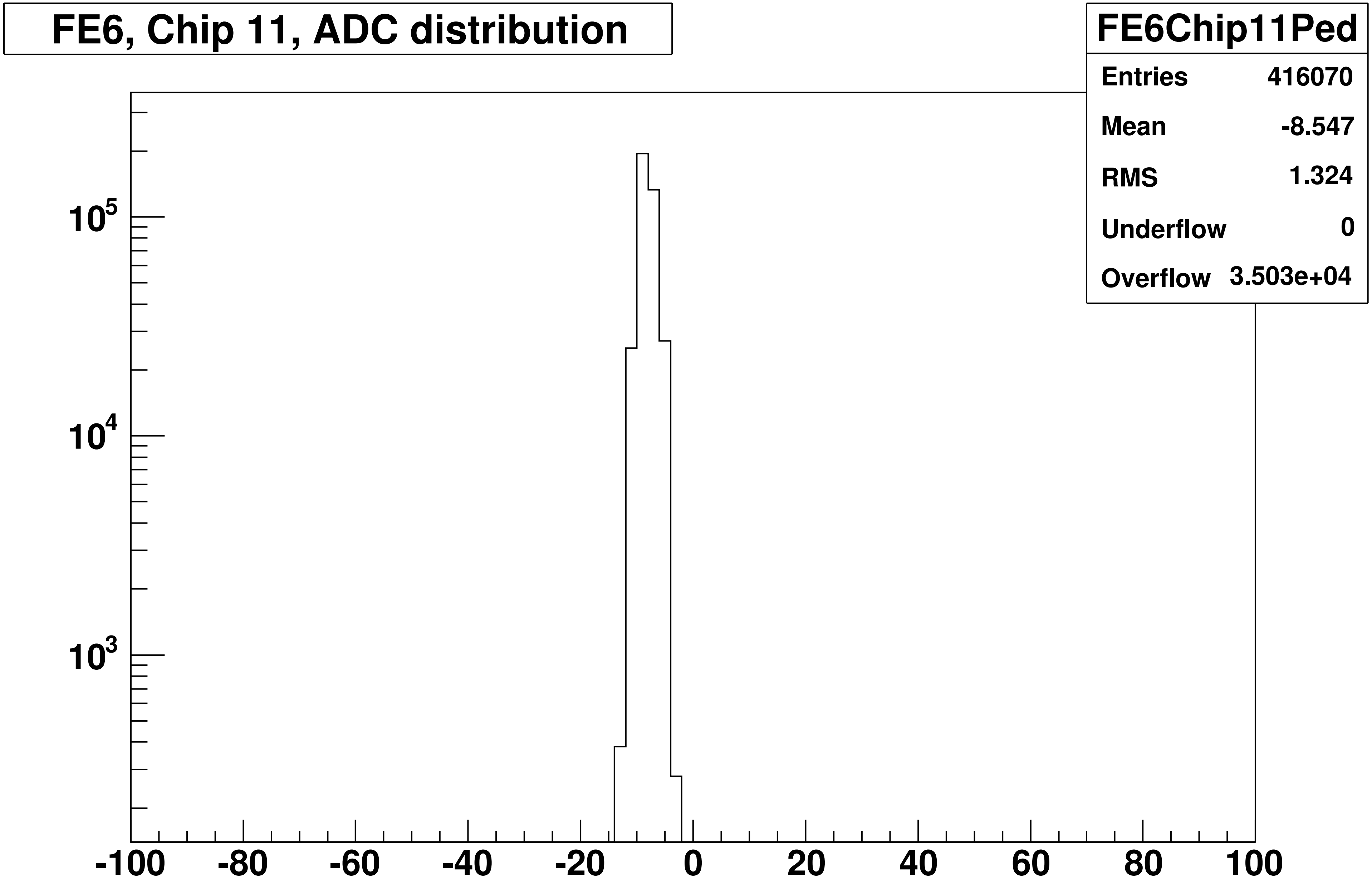


FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 416070
Mean 1508
RMS 5147
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 416070
Mean -8.547
RMS 1.324
Underflow 0
Overflow 3.503e+04
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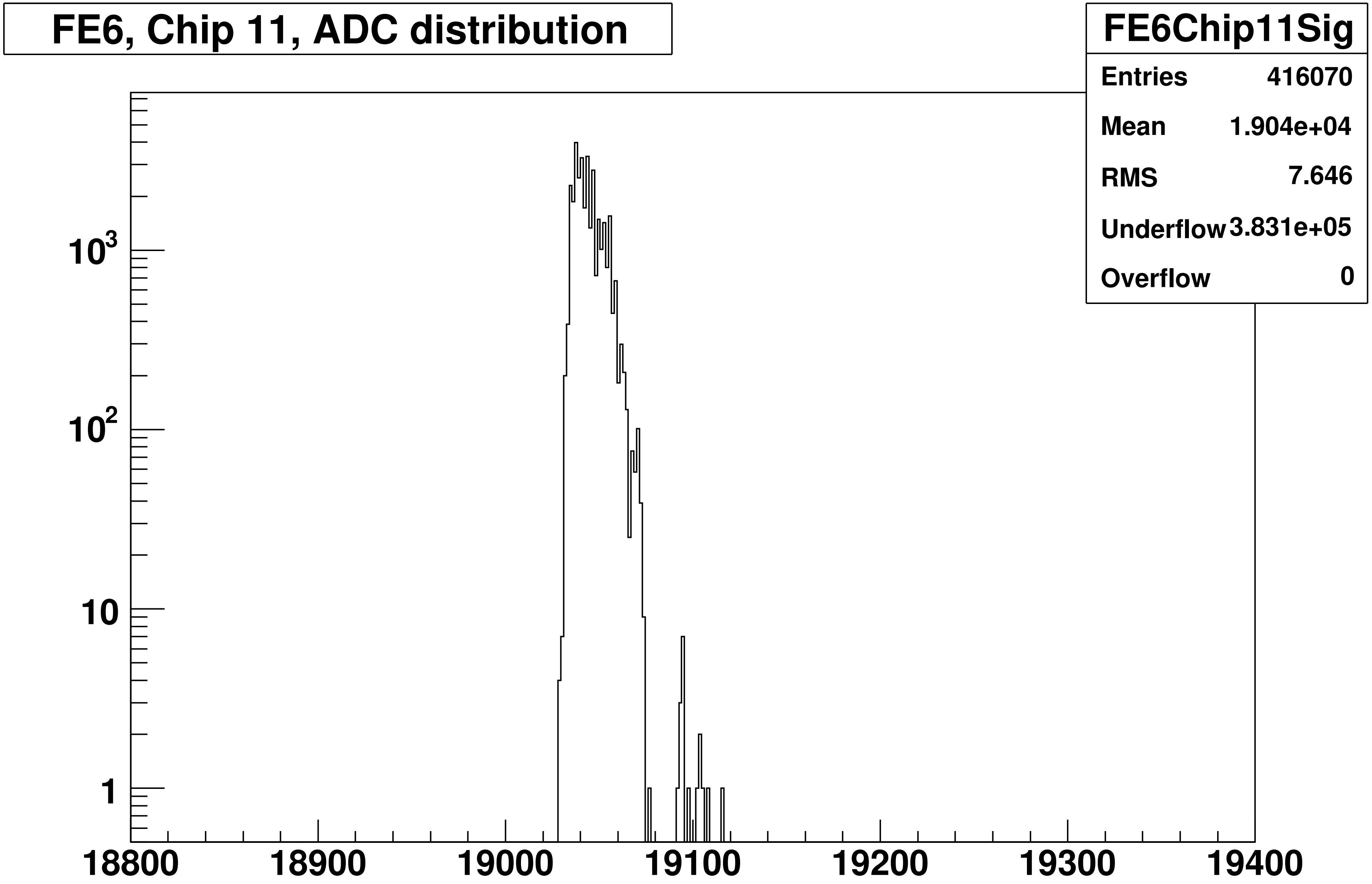
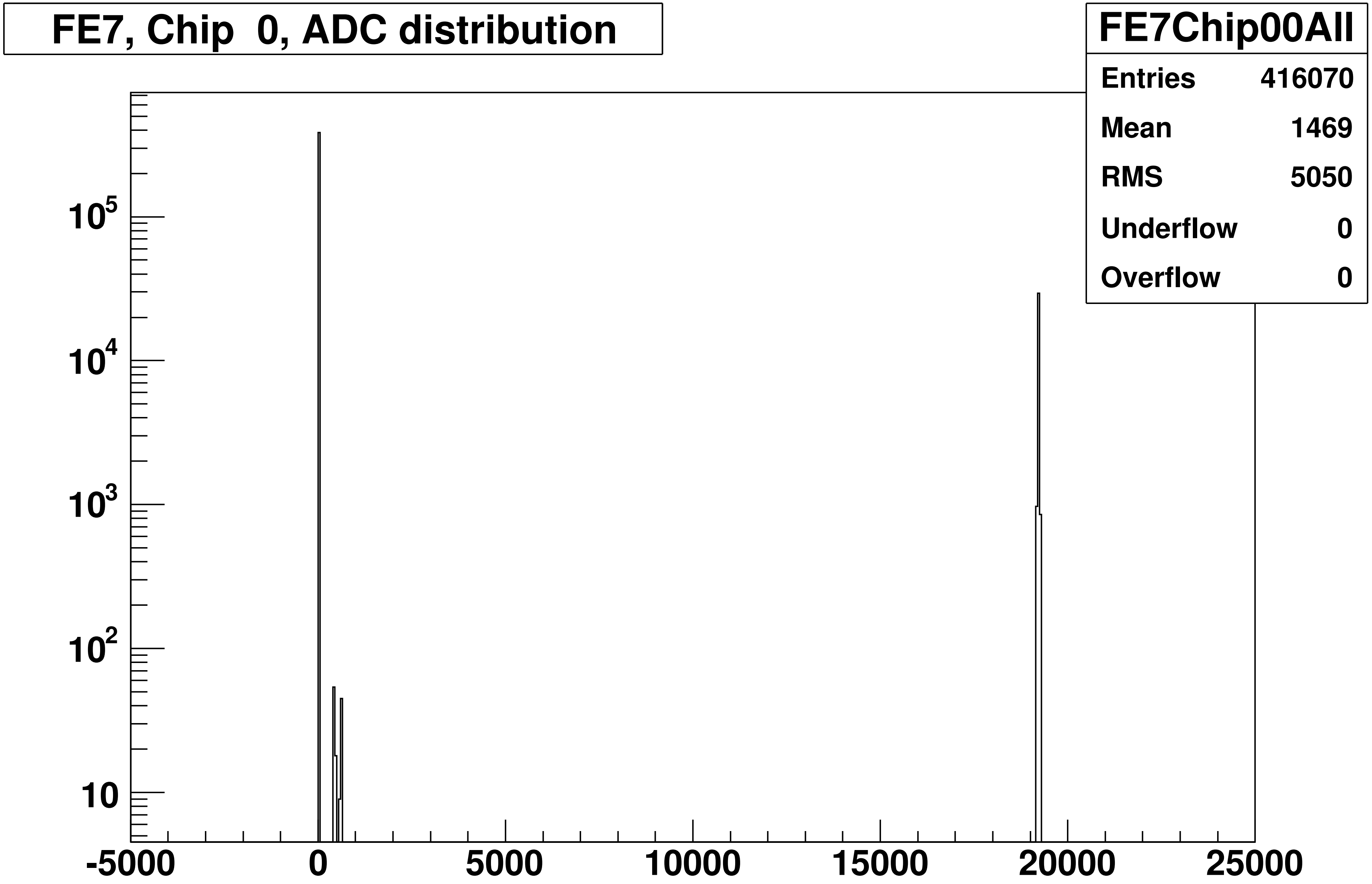
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 416070
Mean 1.904e+04
RMS 7.646
Underflow 3.831e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 416070
Mean 1469
RMS 5050
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
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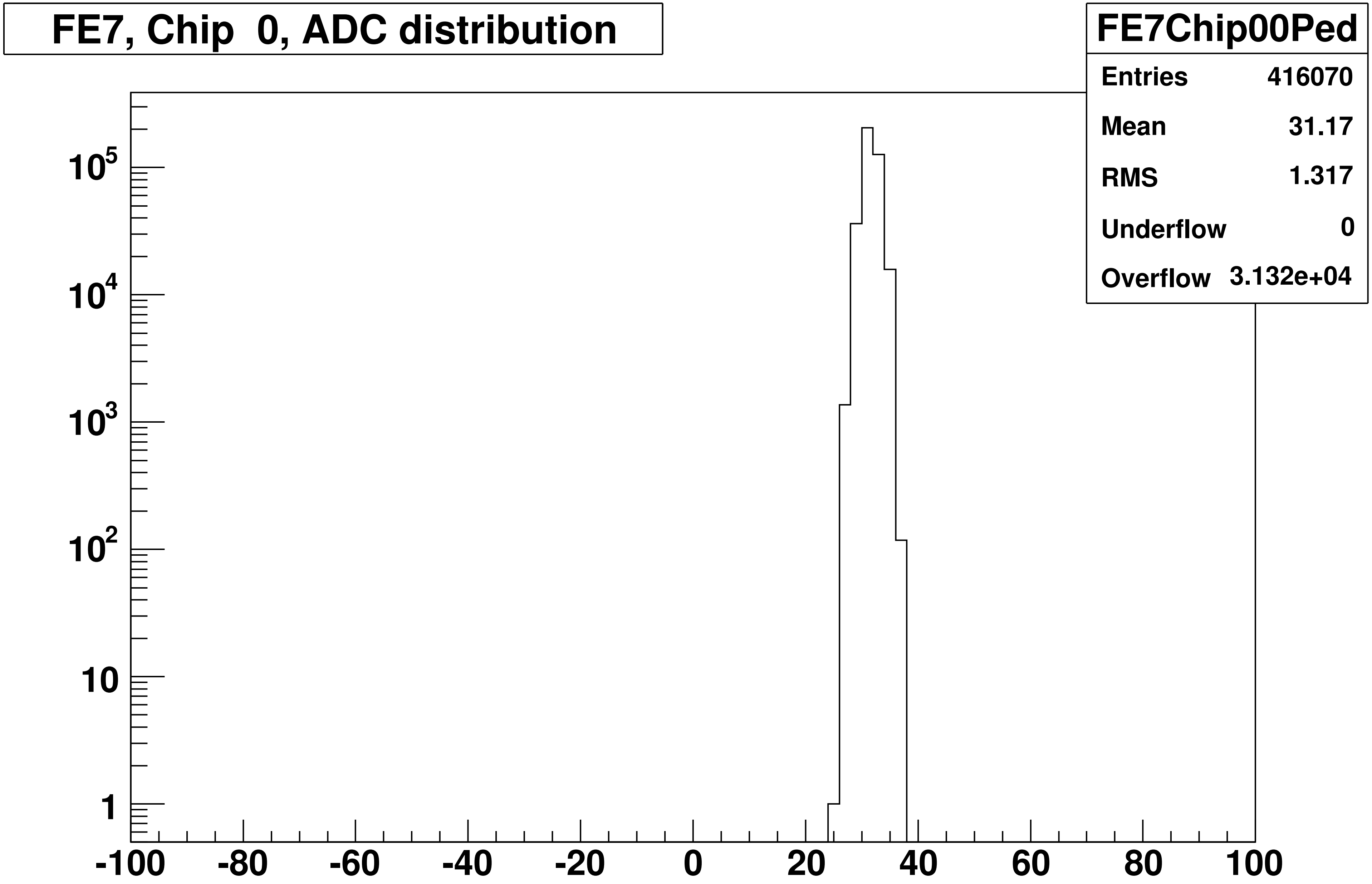
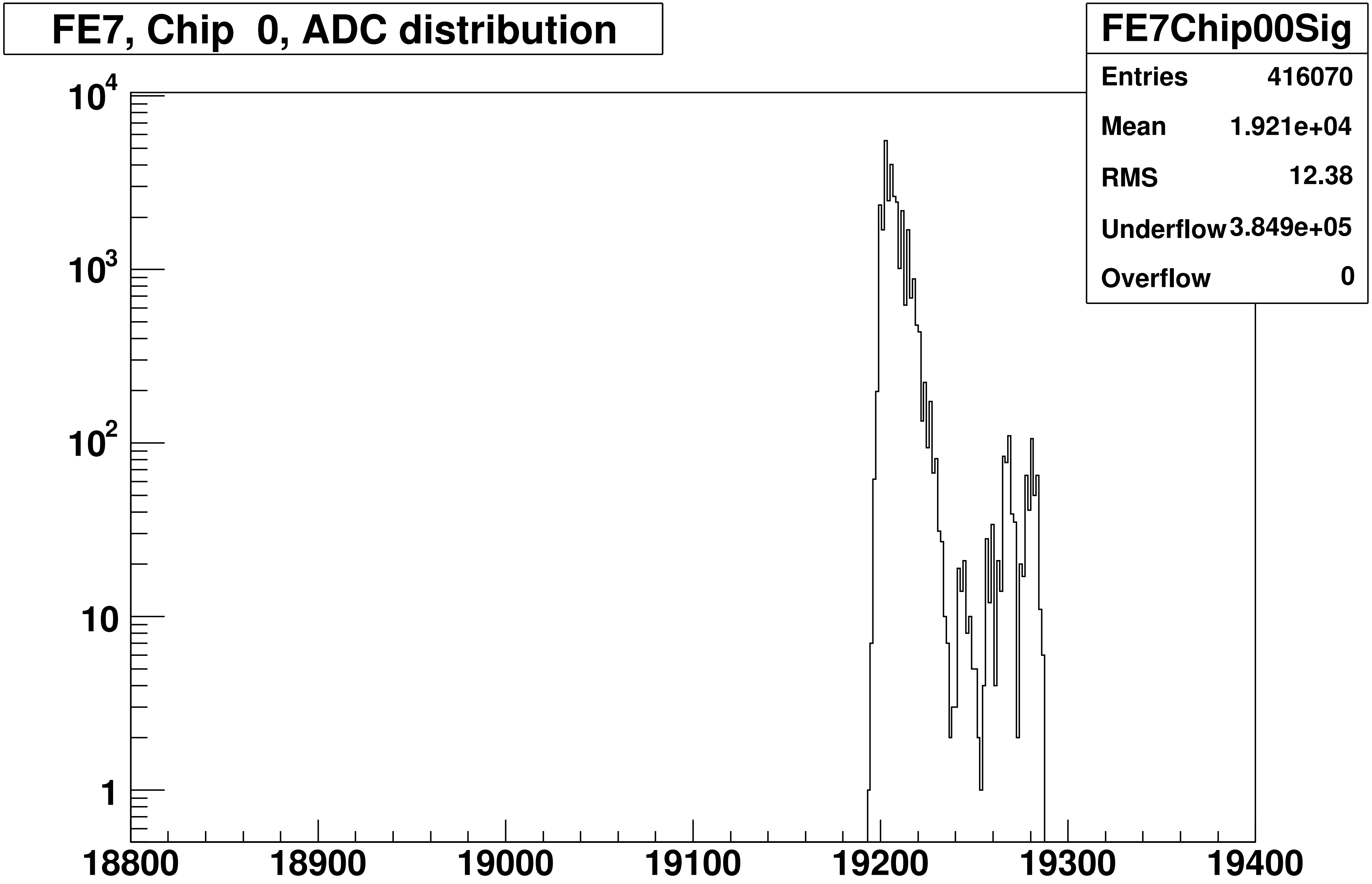
20000
25000



FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 416070
Mean 31.17
RMS 1.317
Underflow 0
Overflow 3.132e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 416070
Mean 1.921e+04
RMS 12.38
Underflow 3.849e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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19300
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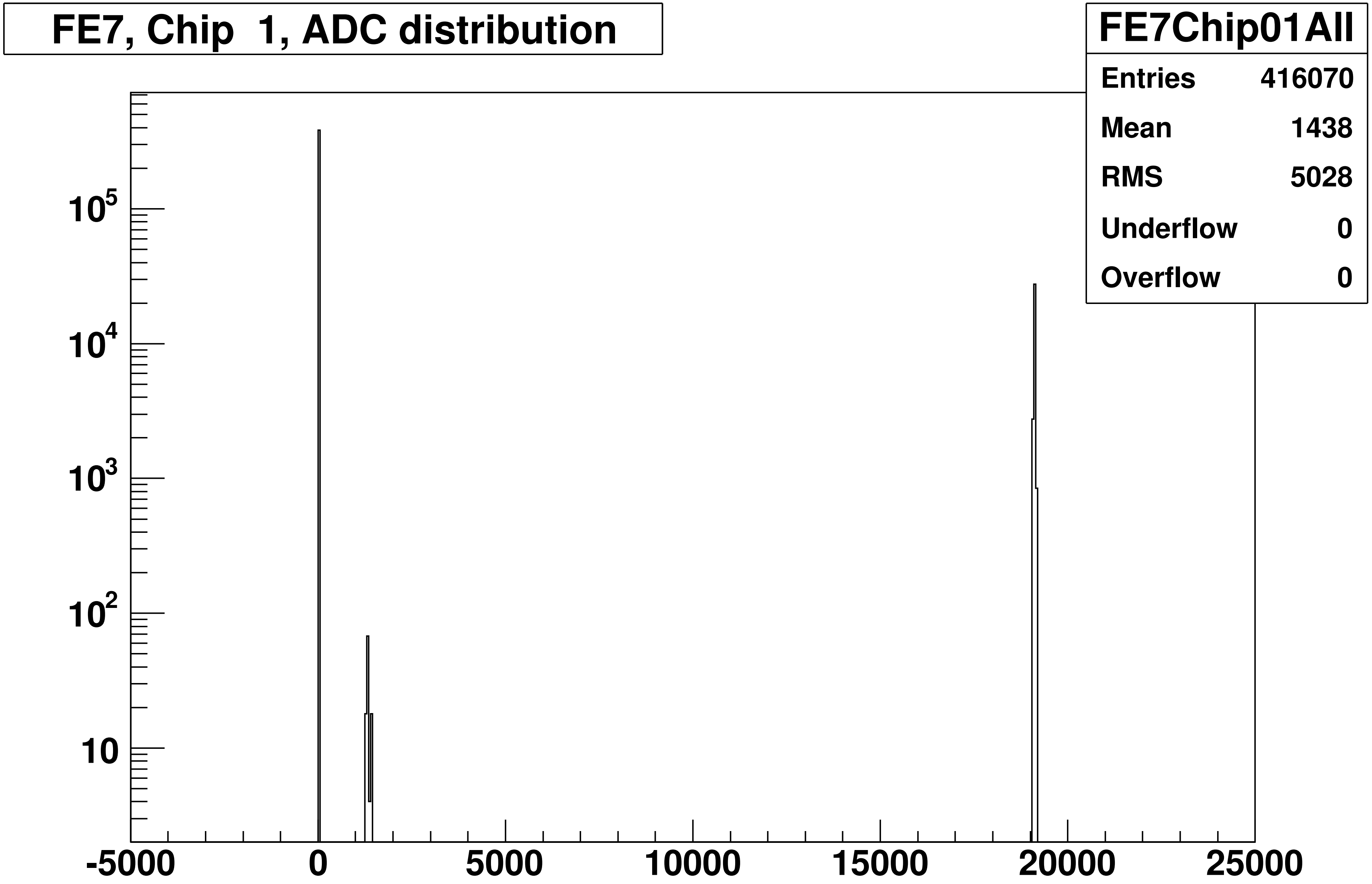
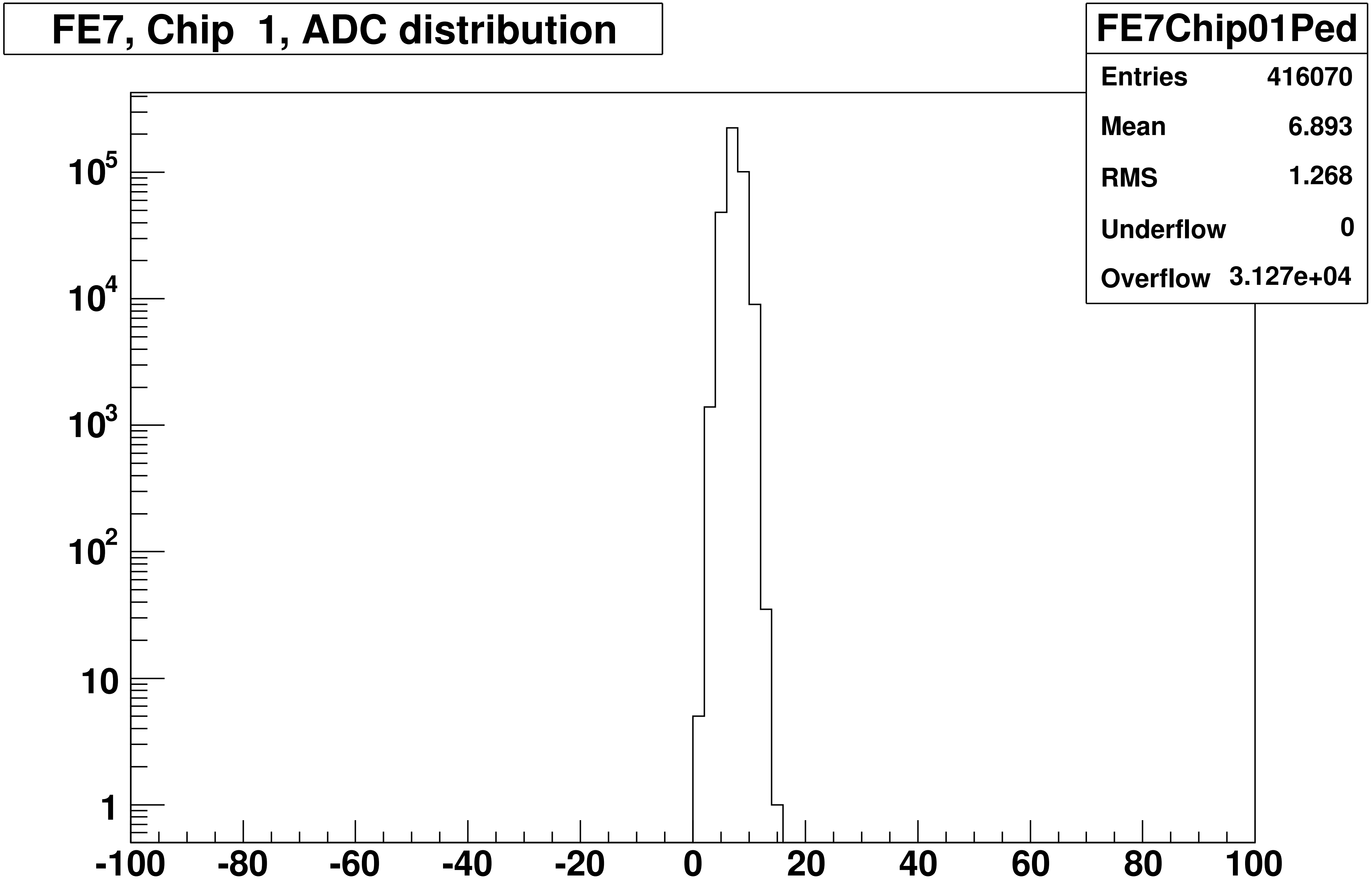
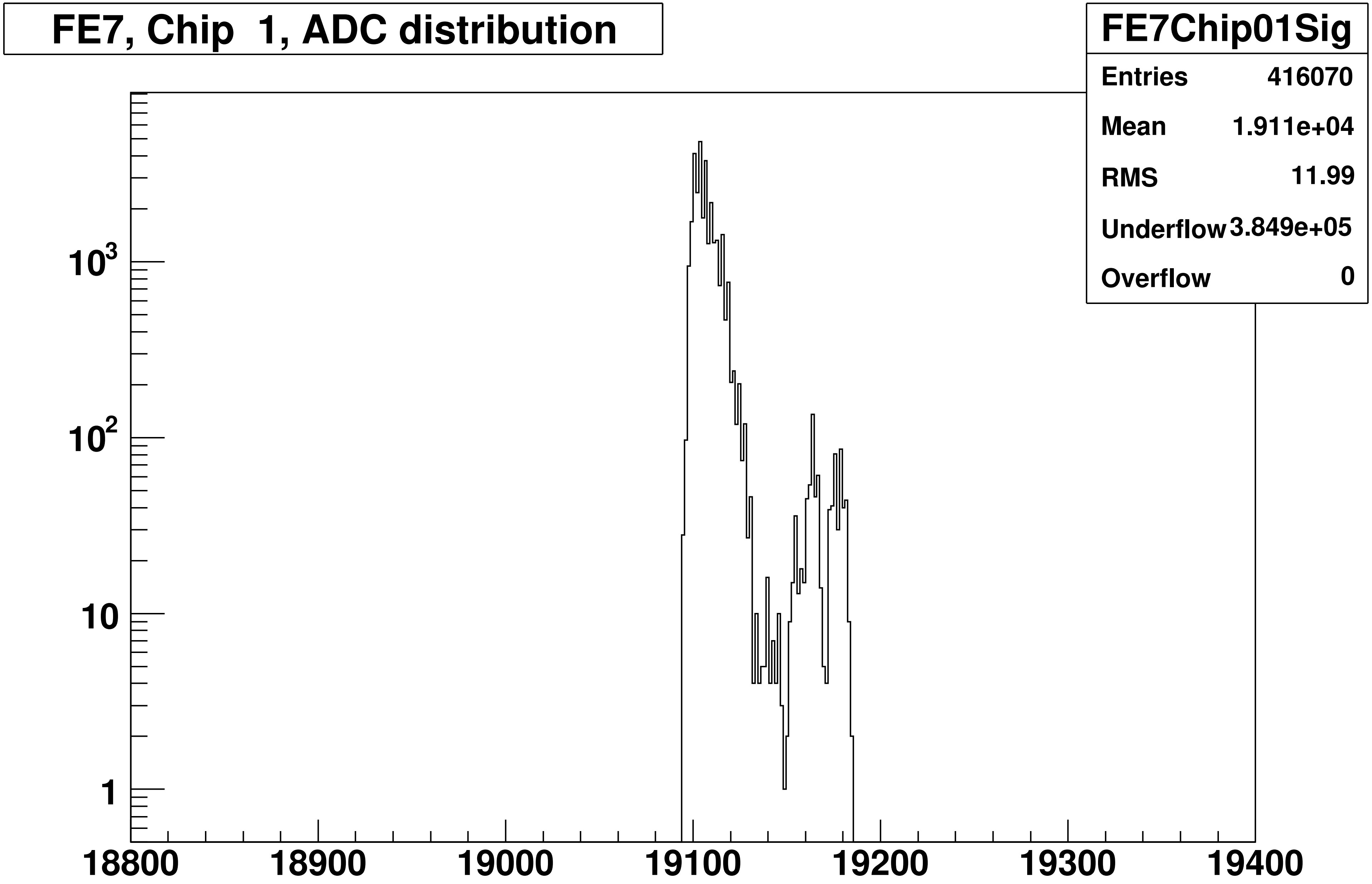

FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 416070
Mean 1438
RMS 5028
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 416070
Mean 6.893
RMS 1.268
Underflow 0
Overflow 3.127e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 416070
Mean 1.911e+04
RMS 11.99
Underflow 3.849e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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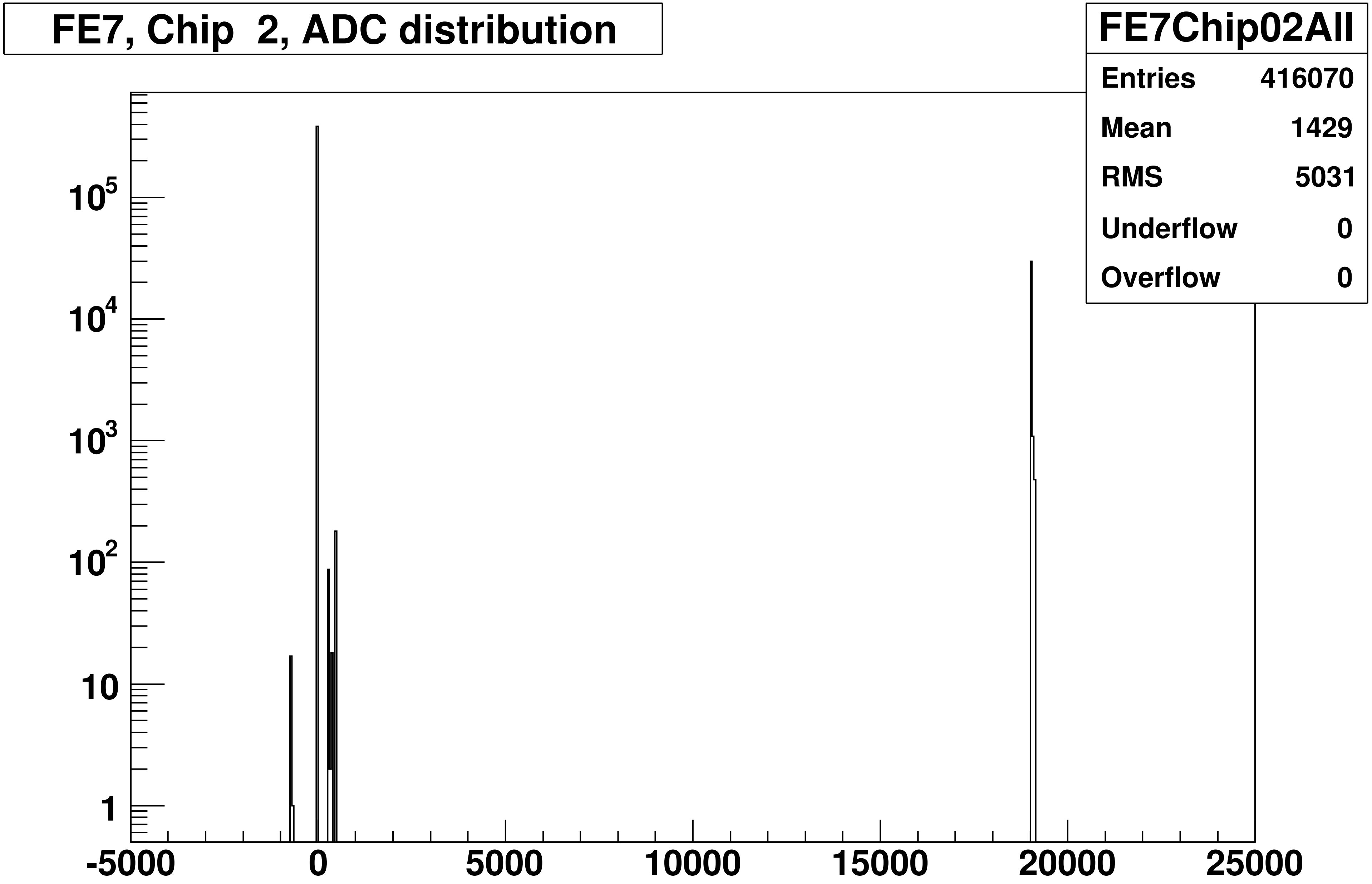
19200
19300
19400


FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 416070
Mean 1429
RMS 5031
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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-5000
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10000
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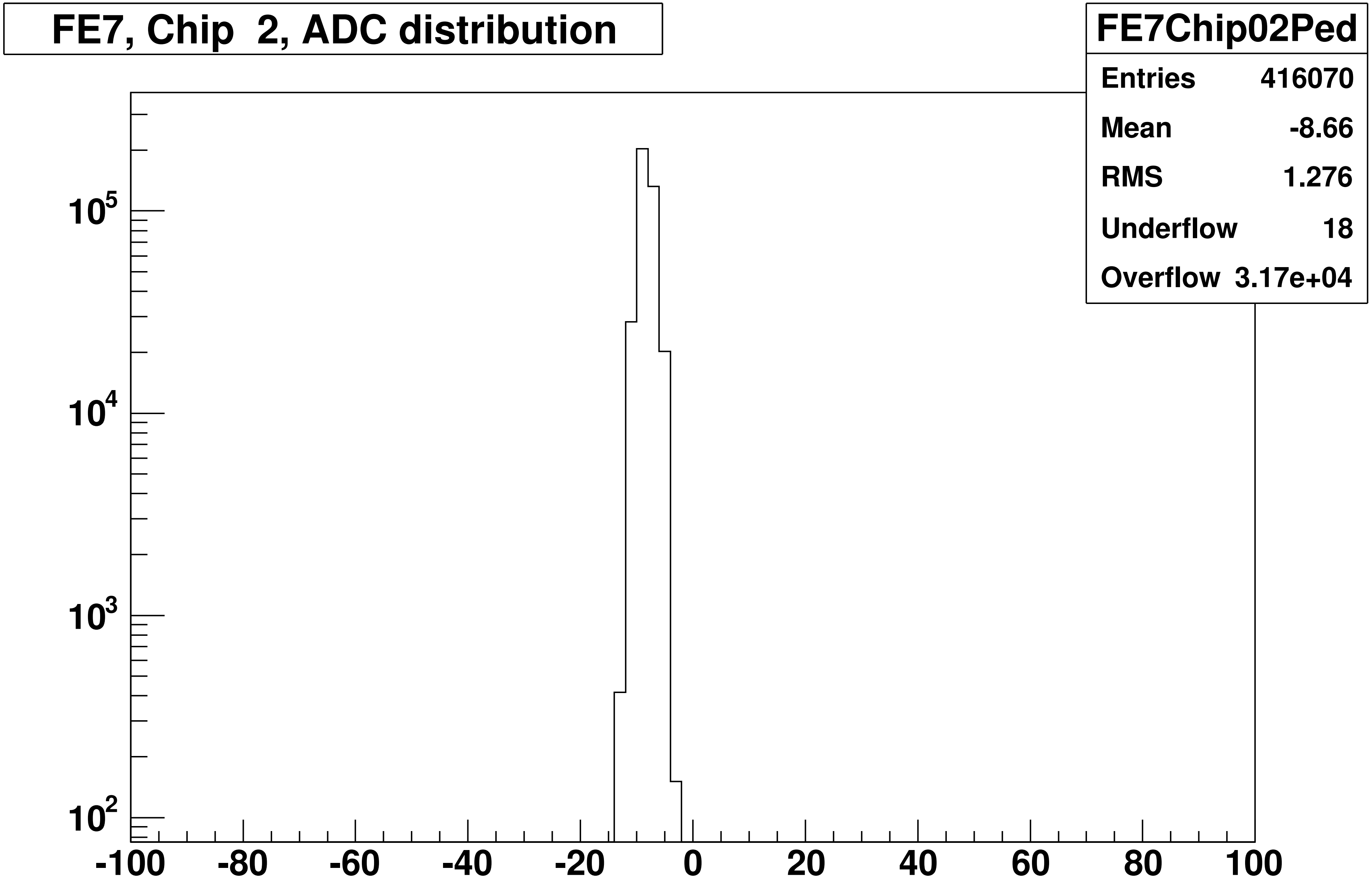
20000
25000


FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 416070
Mean -8.66
RMS 1.276
Underflow 18
Overflow 3.17e+04
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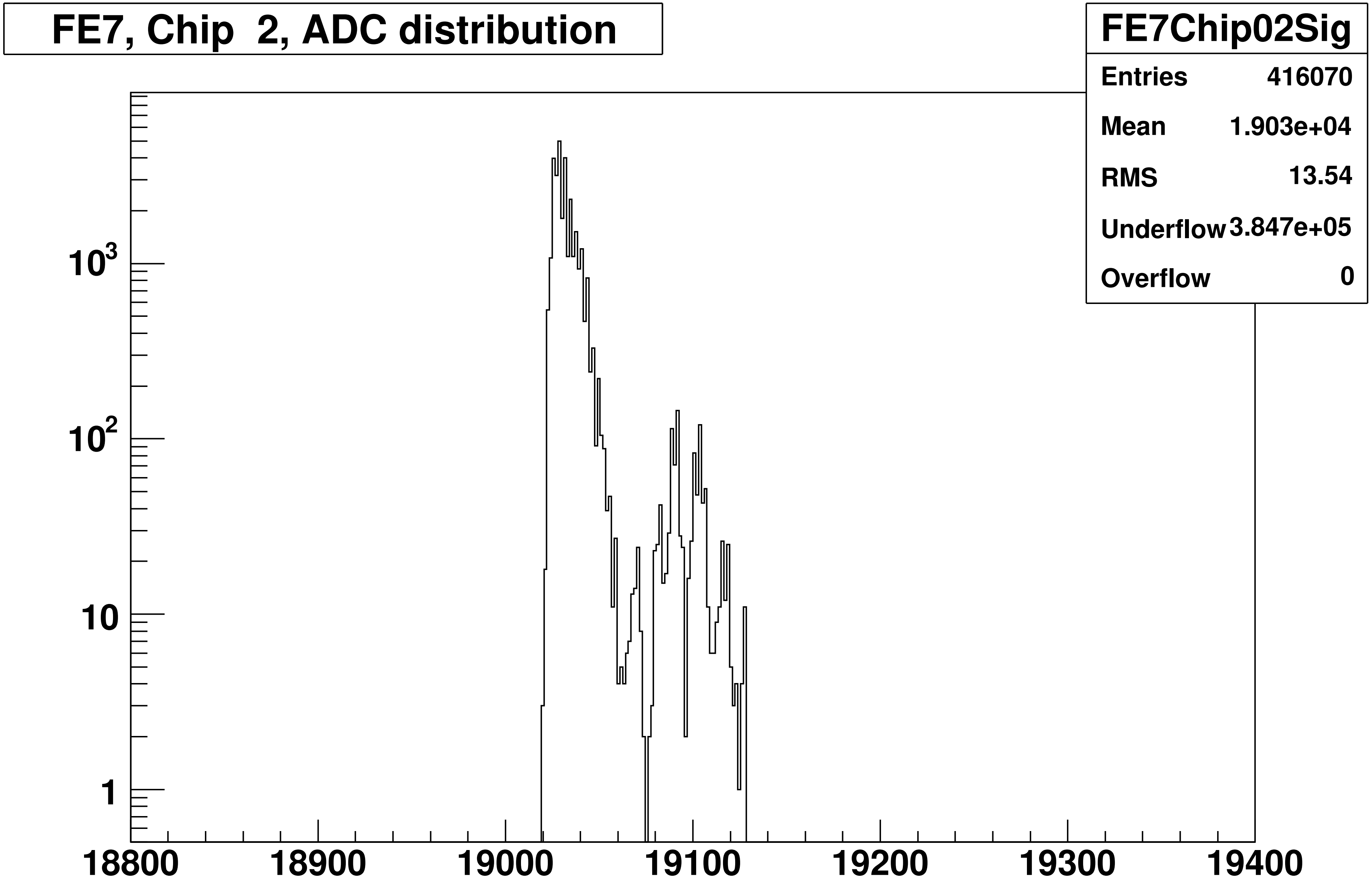
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 416070
Mean 1.903e+04
RMS 13.54
Underflow 3.847e+05
Overflow 0
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10^2
10
1
18800
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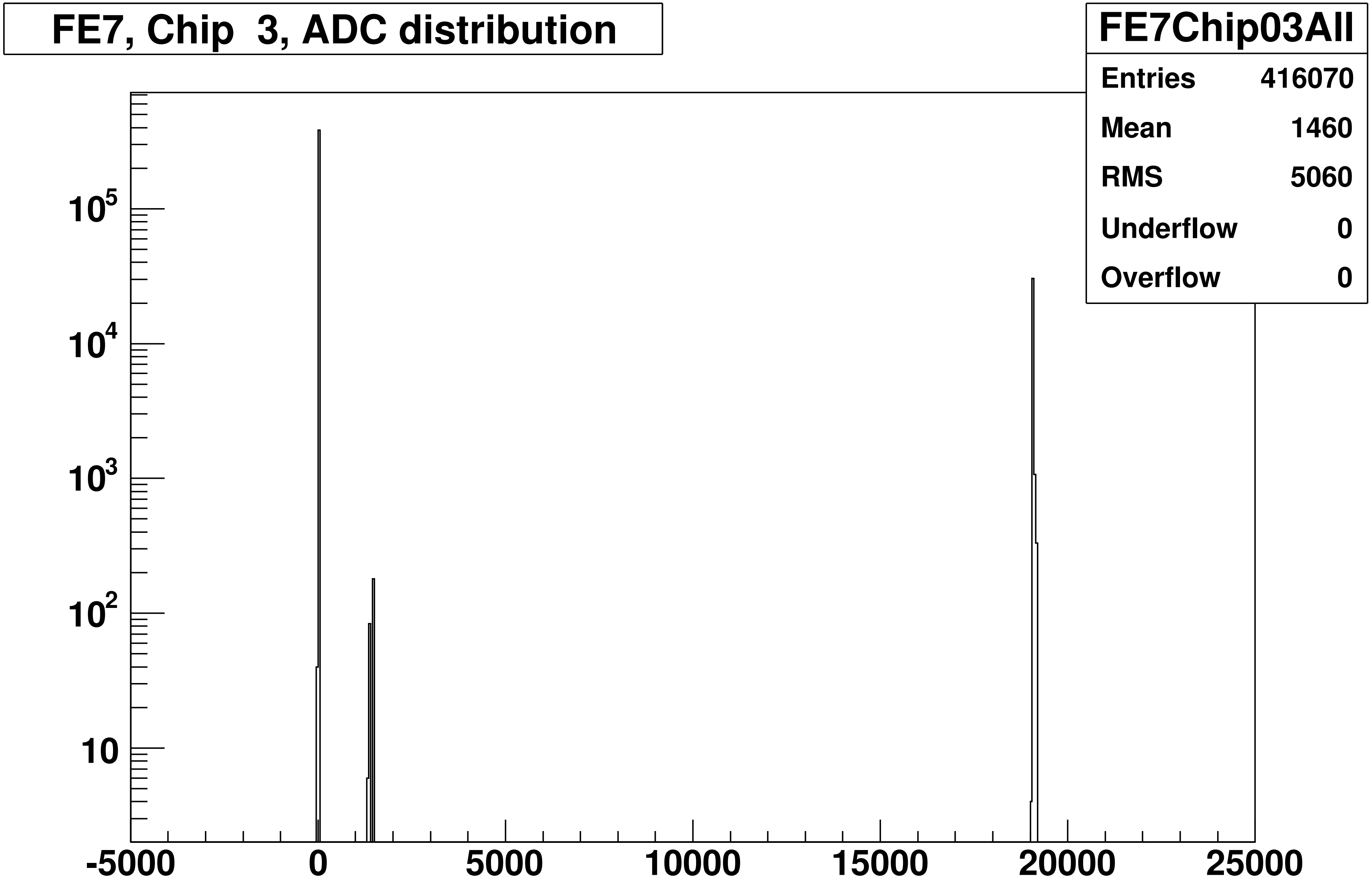
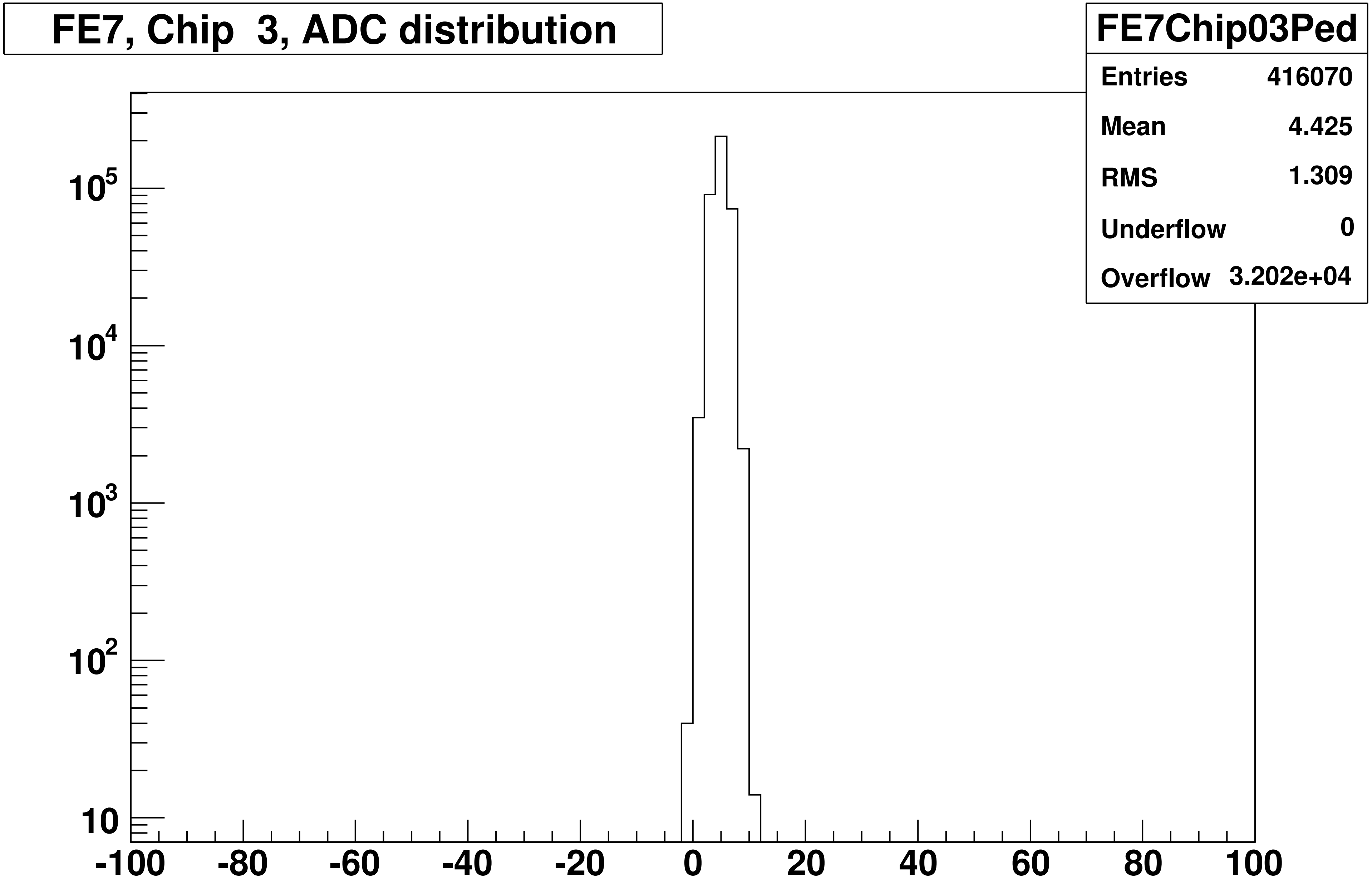
19400



FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 416070
Mean 1460
RMS 5060
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 416070
Mean 4.425
RMS 1.309
Underflow 0
Overflow 3.202e+04
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10^4
10^3
10^2
10
-100
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-40
-20
0
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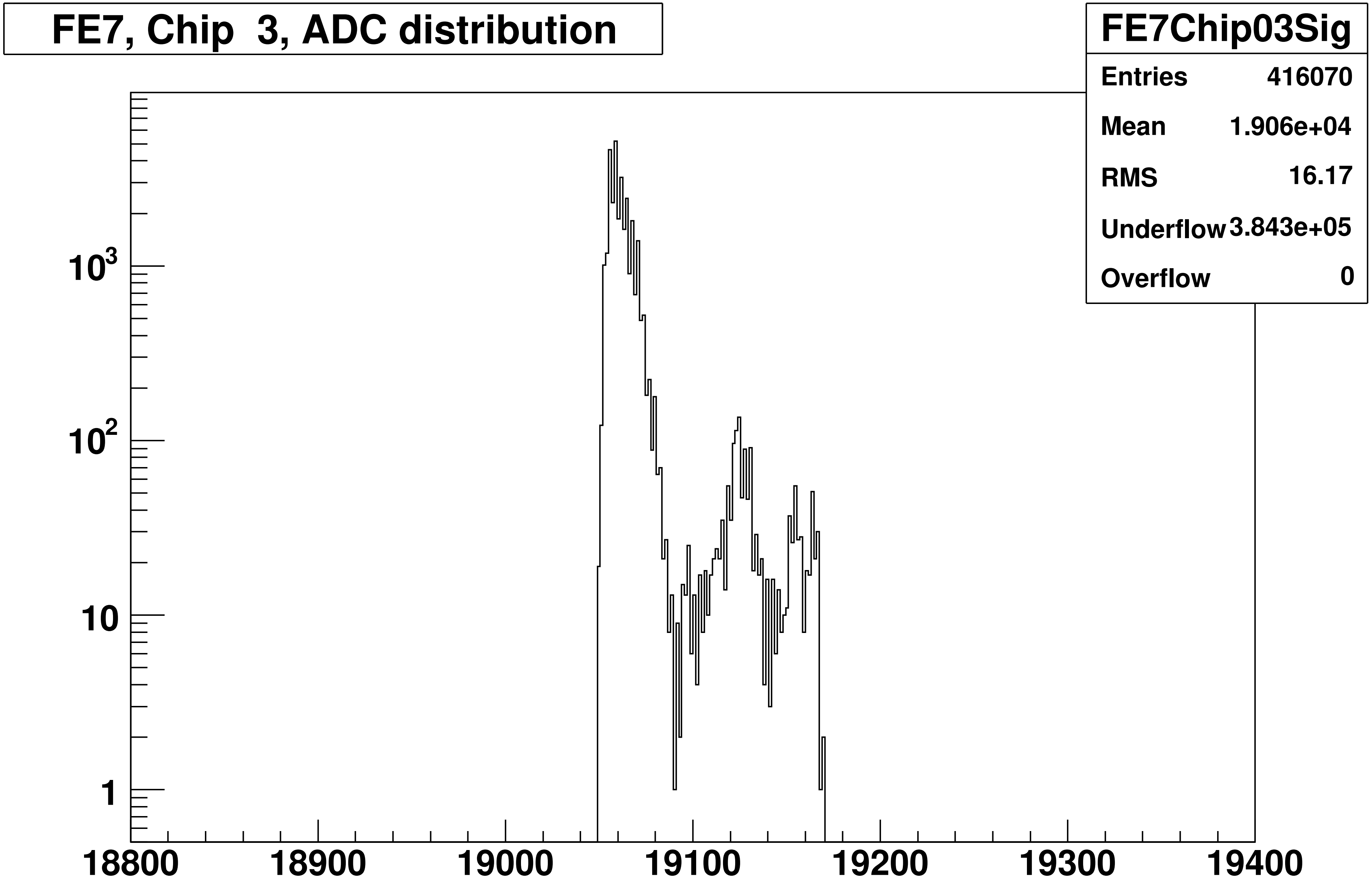
80
100



FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 416070
Mean 1.906e+04
RMS 16.17
Underflow 3.843e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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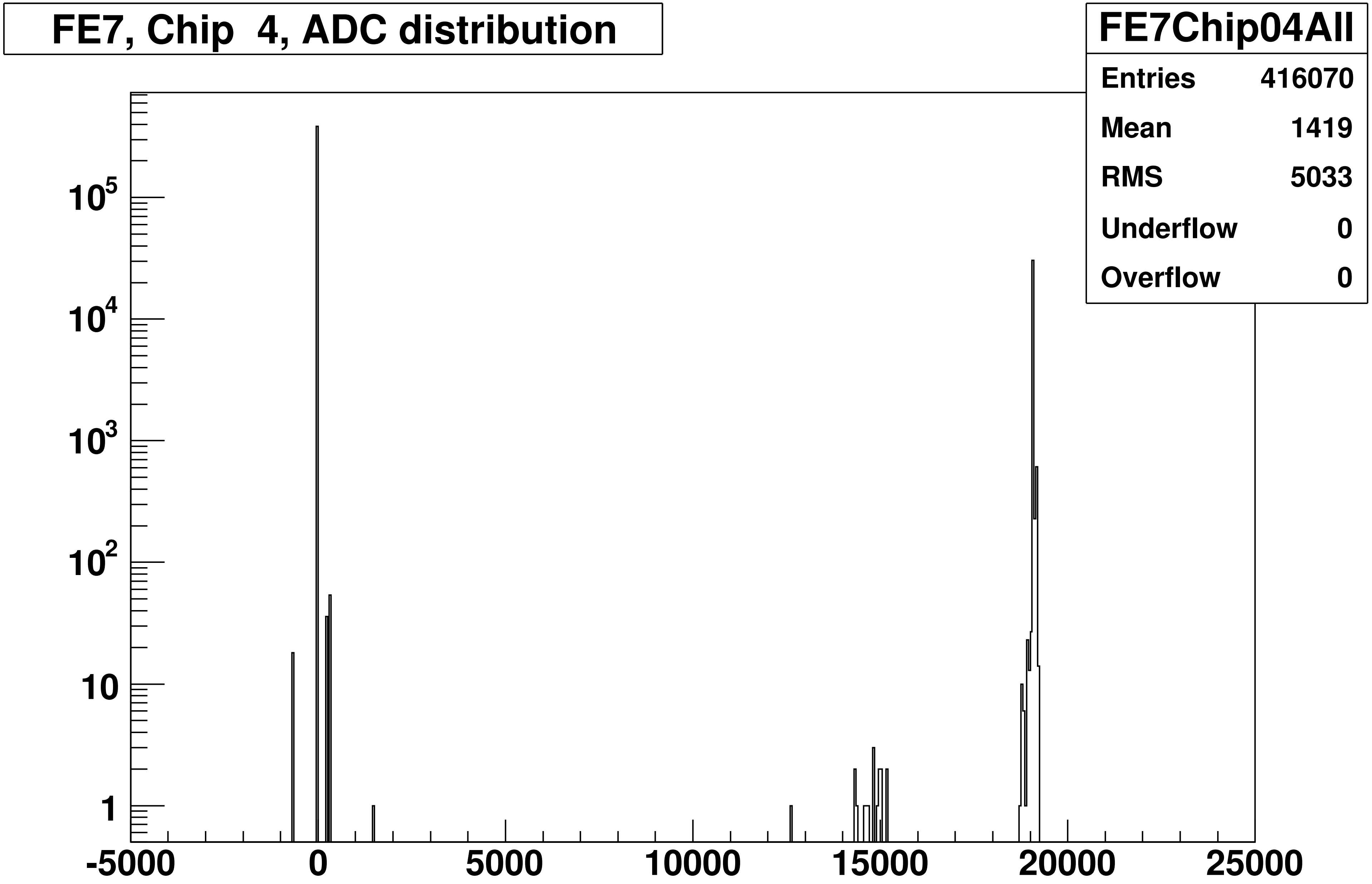

FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 416070
Mean 1419
RMS 5033
Underflow 0
Overflow 0
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10^4
10^3
10^2
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1
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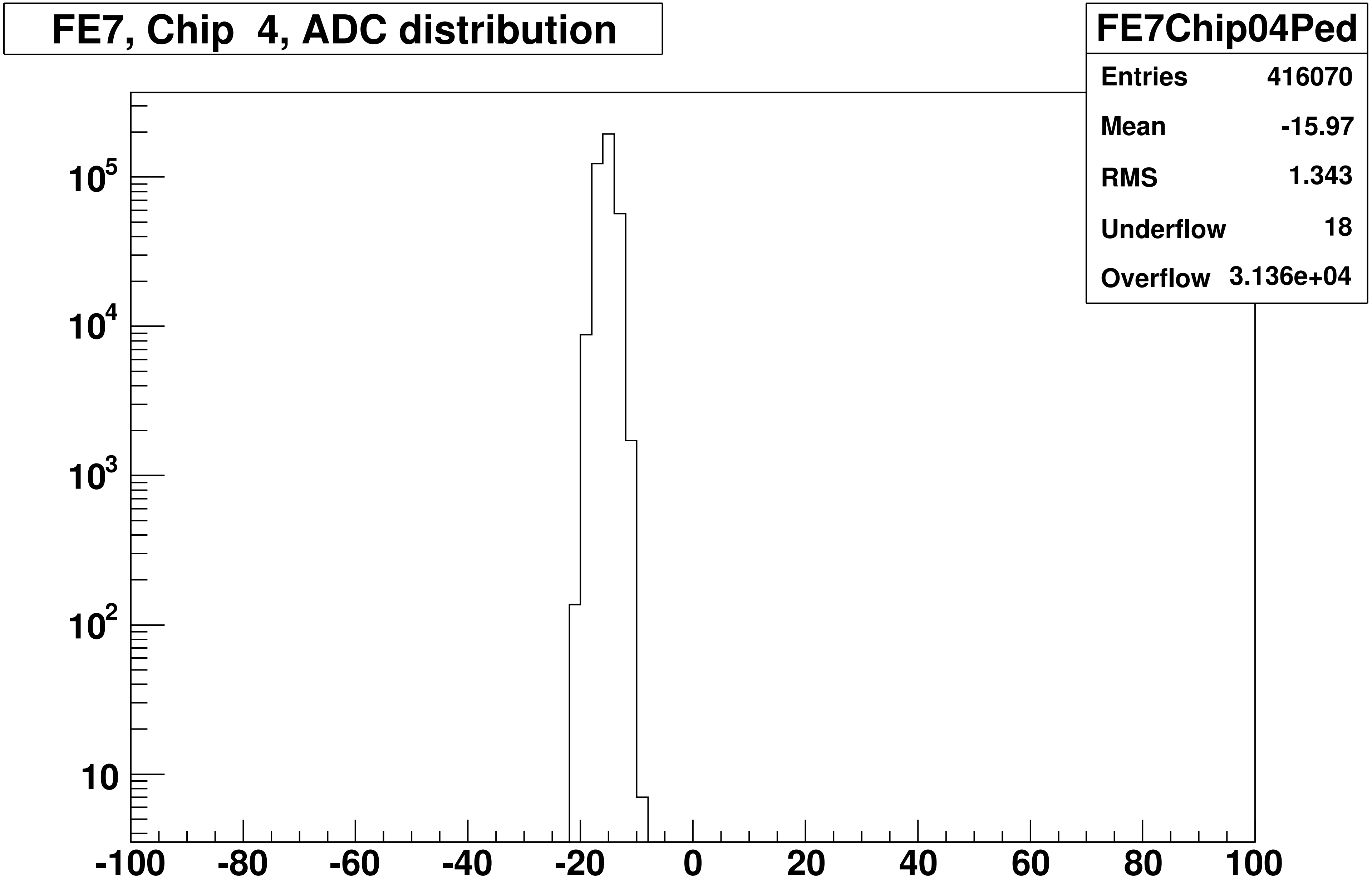

FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 416070
Mean -15.97
RMS 1.343
Underflow 18
Overflow 3.136e+04
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10^4
10^3
10^2
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-100
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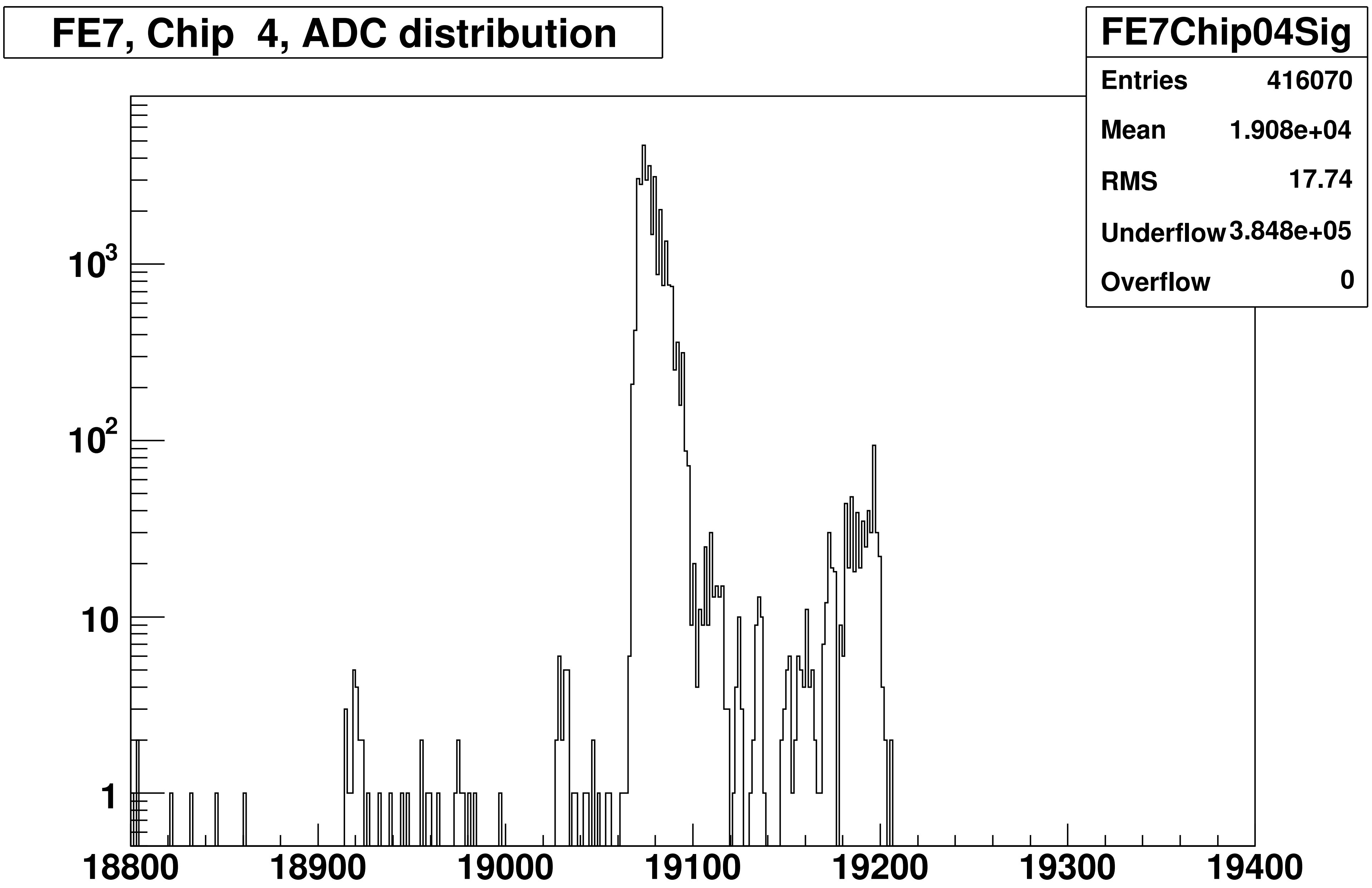

FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 416070
Mean 1.908e+04
RMS 17.74
Underflow 3.848e+05
Overflow 0
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10^2
10
1
18800
18900
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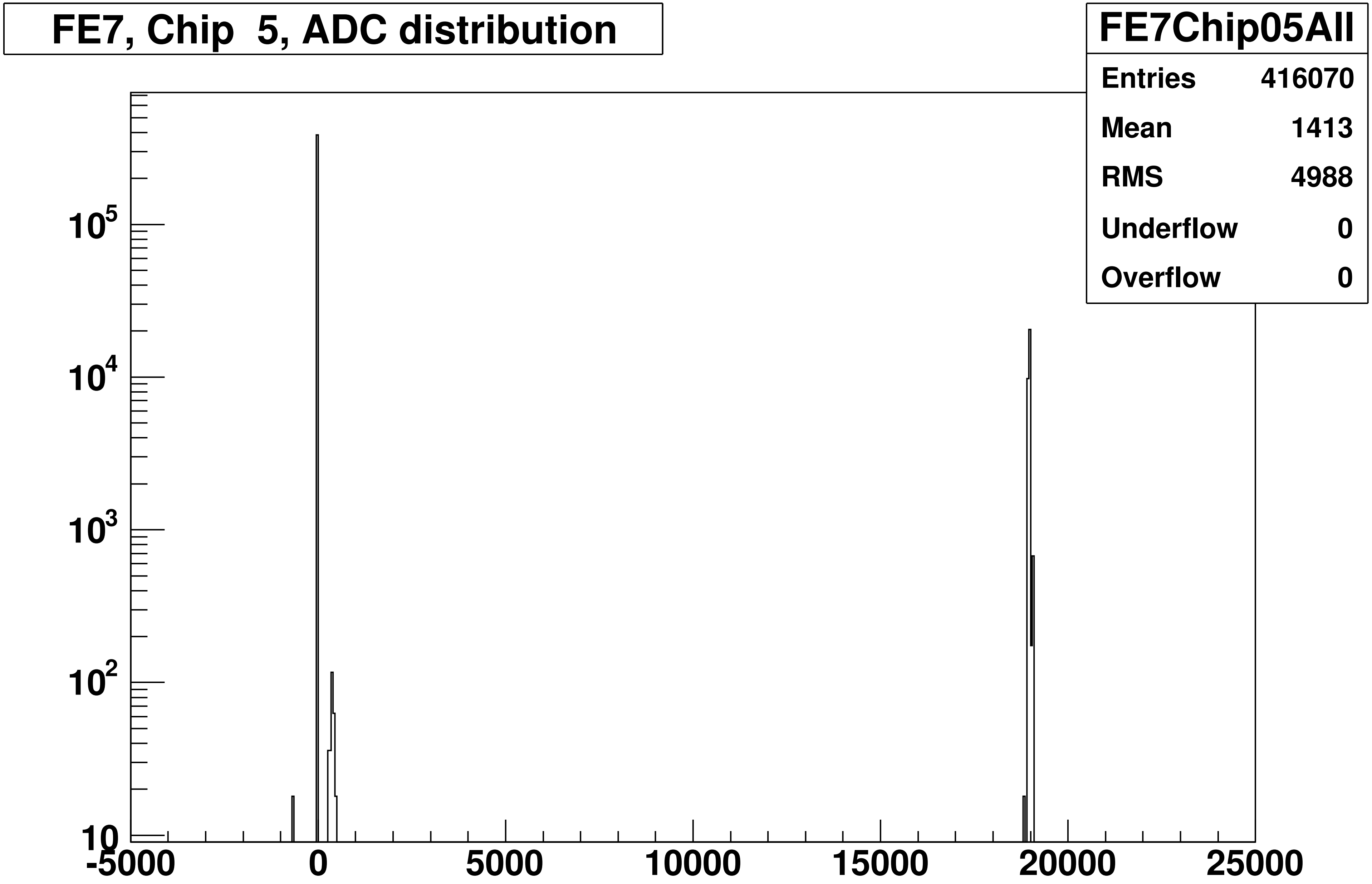
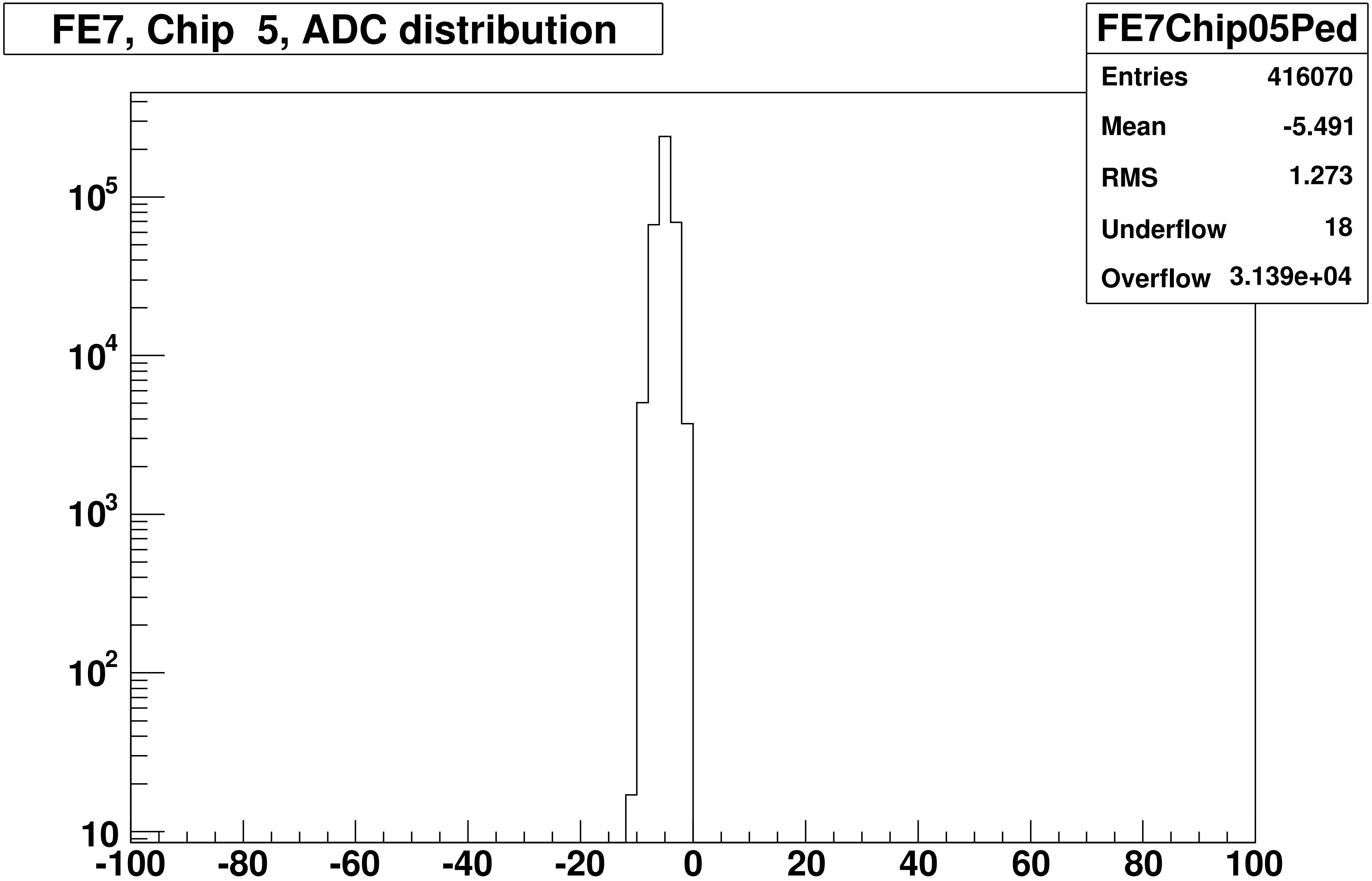


FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 416070
Mean 1413
RMS 4988
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 416070
Mean -5.491
RMS 1.273
Underflow 18
Overflow 3.139e+04
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10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
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40
60
80
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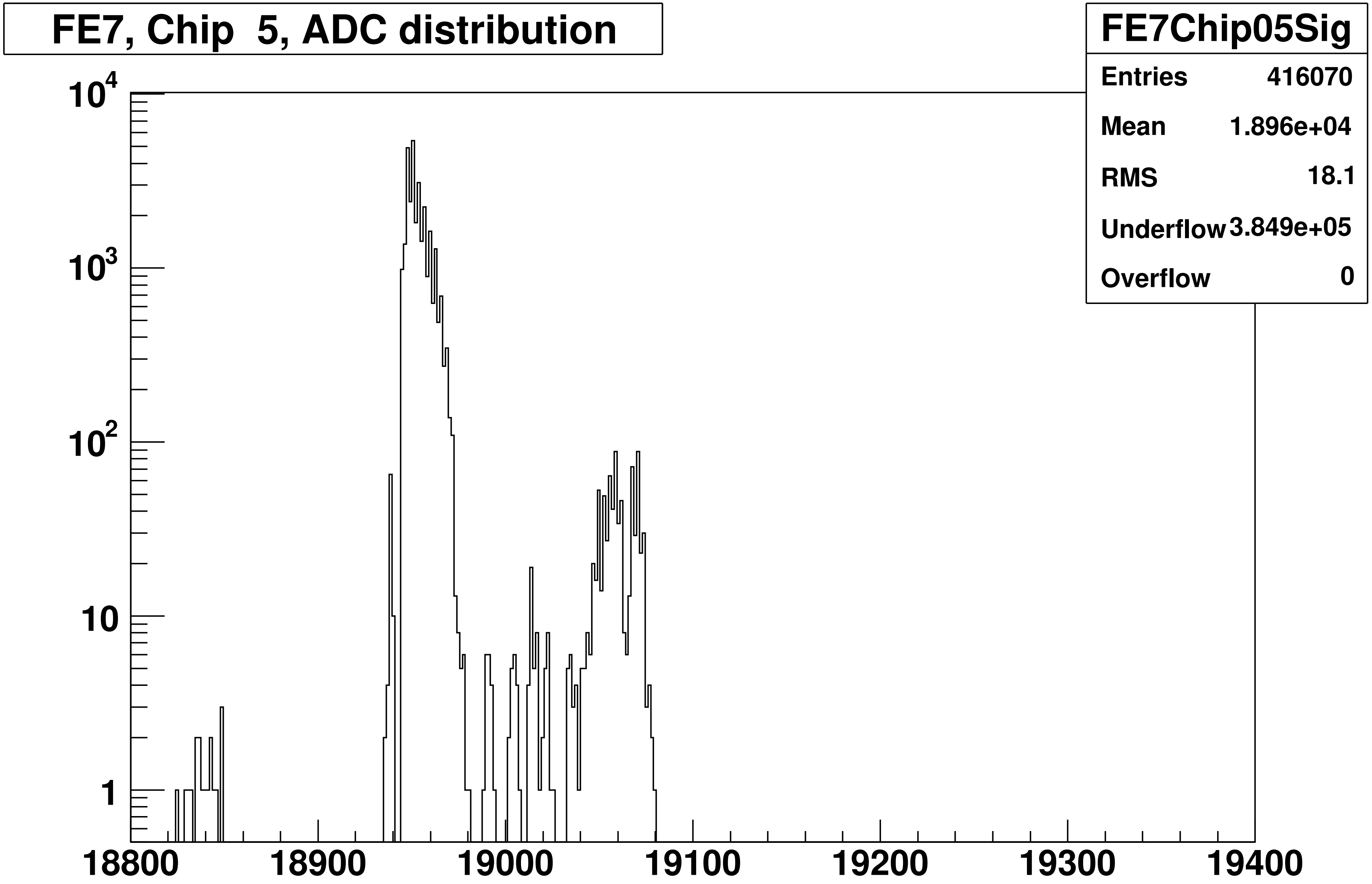
FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 416070
Mean 1.896e+04
RMS 18.1
Underflow 3.849e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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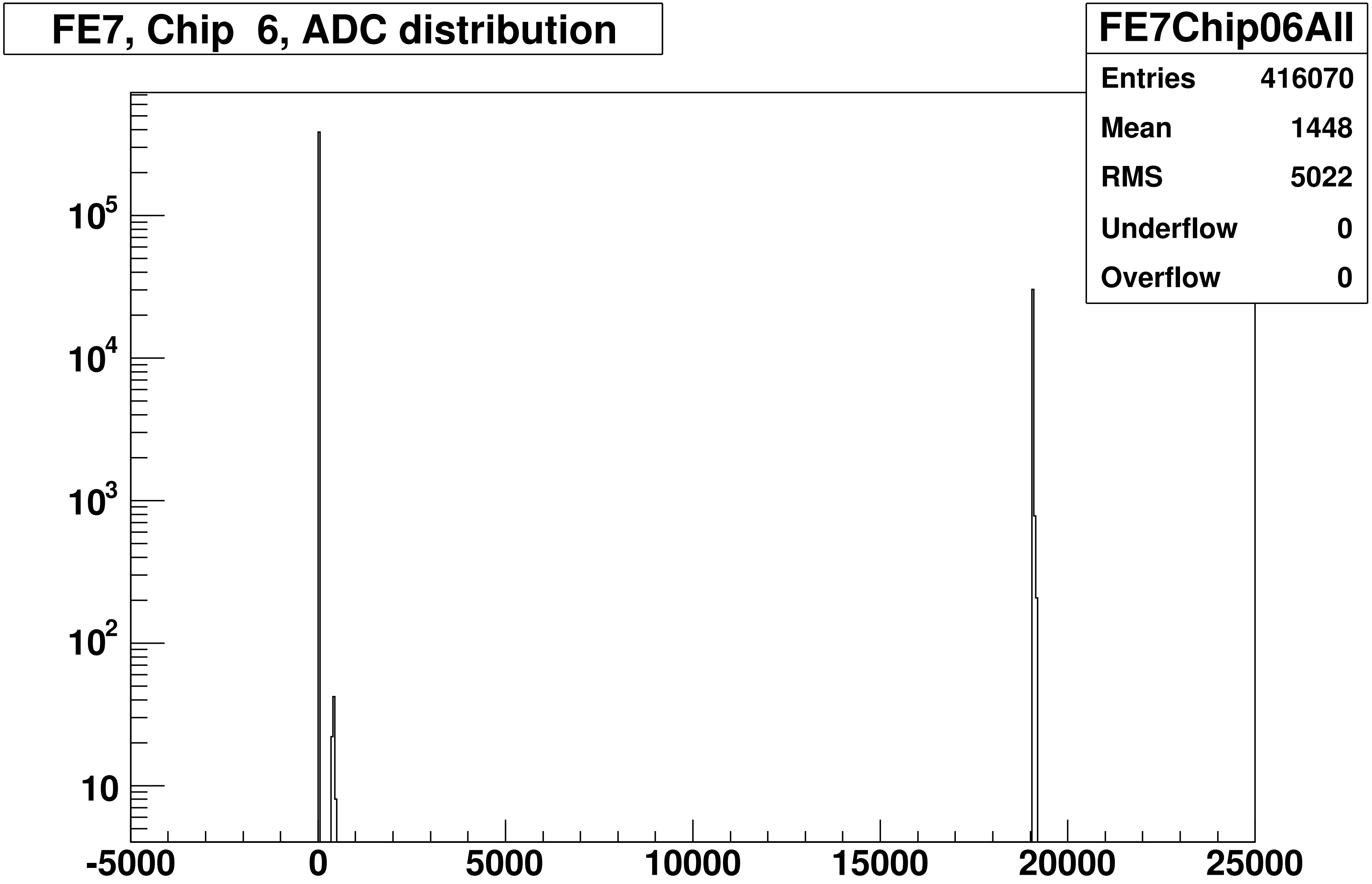
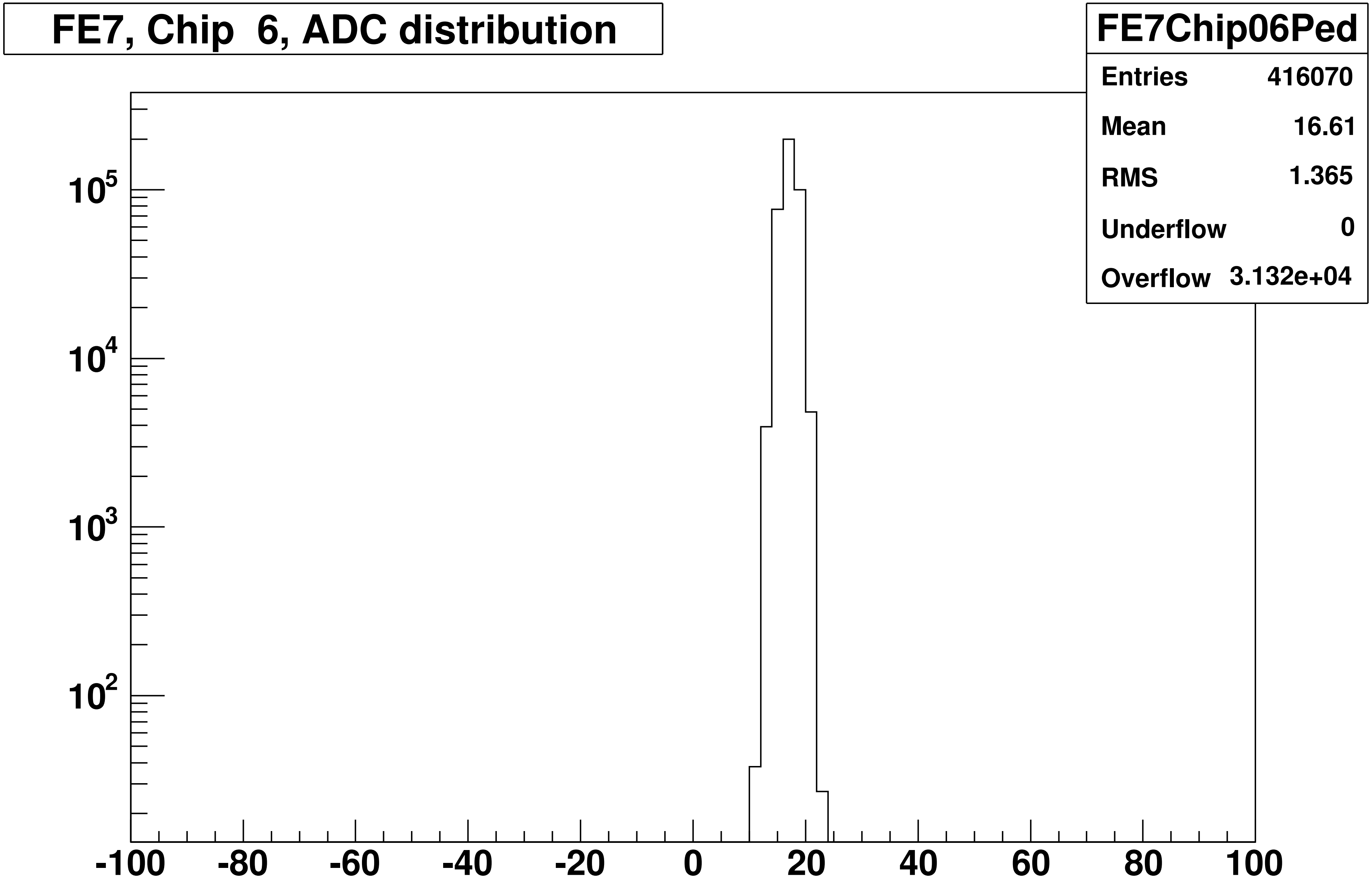
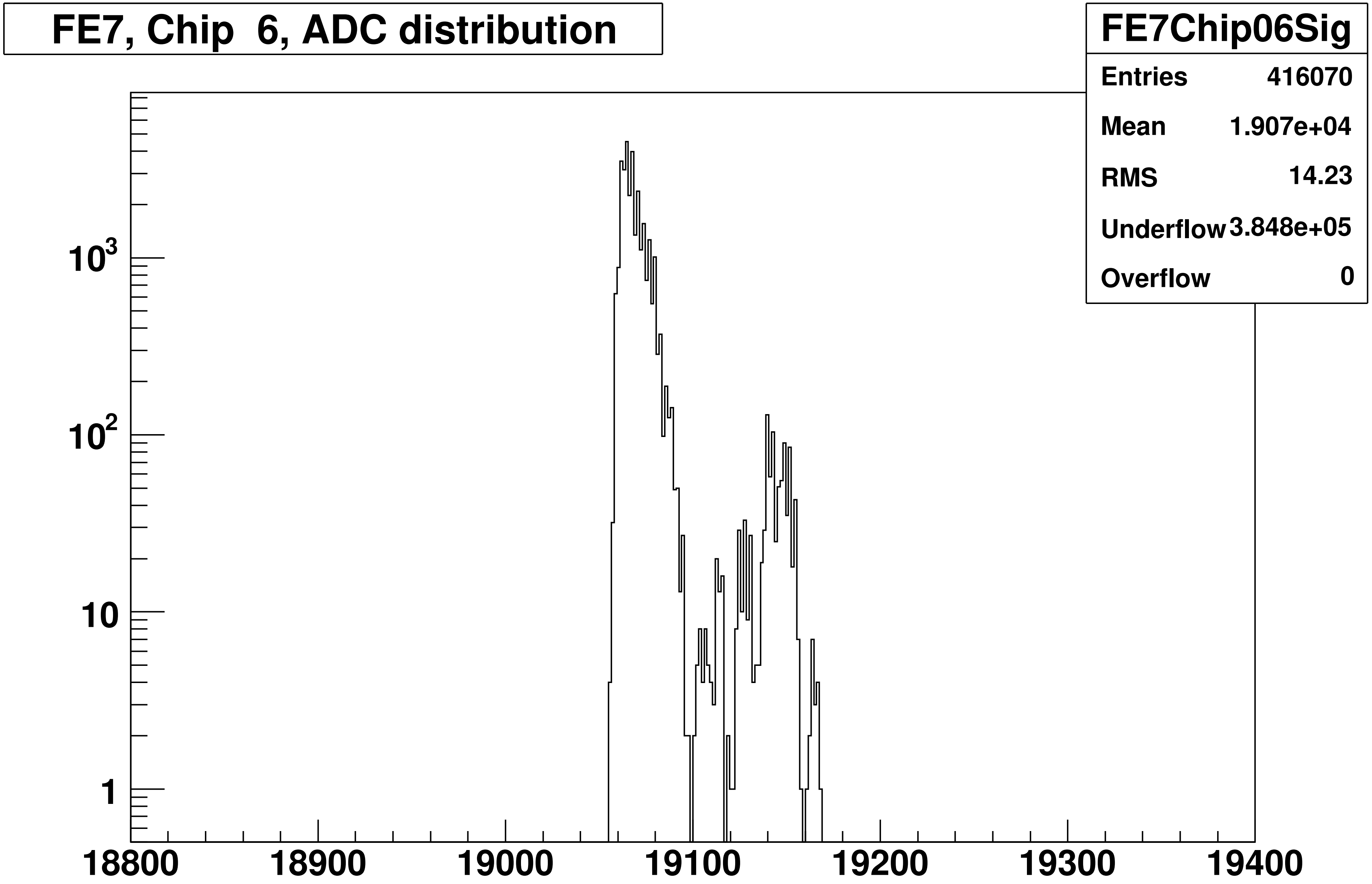
19100
19200
19300
19400


FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 416070
Mean 1448
RMS 5022
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 416070
Mean 16.61
RMS 1.365
Underflow 0
Overflow 3.132e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 416070
Mean 1.907e+04
RMS 14.23
Underflow 3.848e+05
Overflow 0
1
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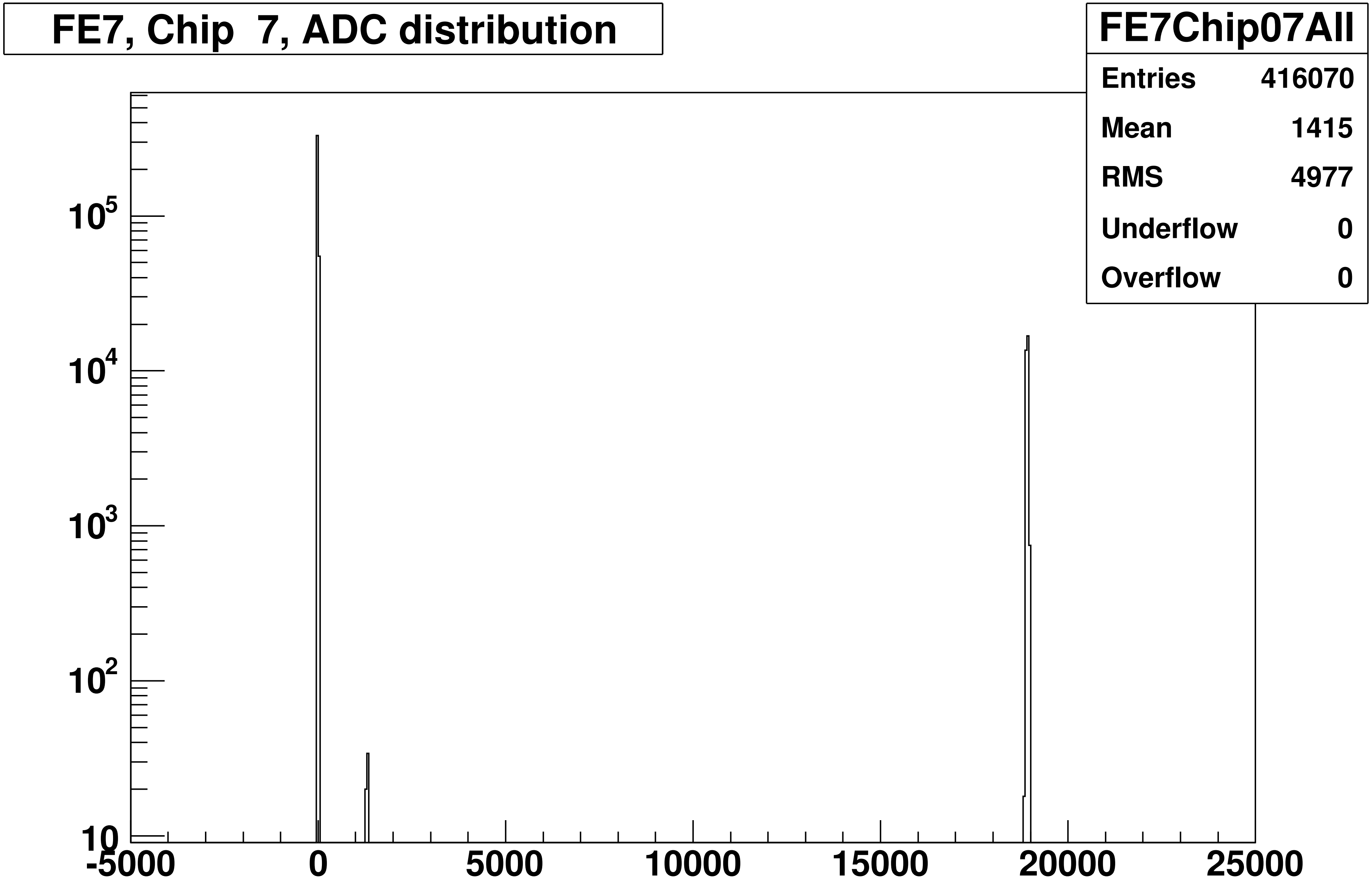
19400



FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 416070
Mean 1415
RMS 4977
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
-5000
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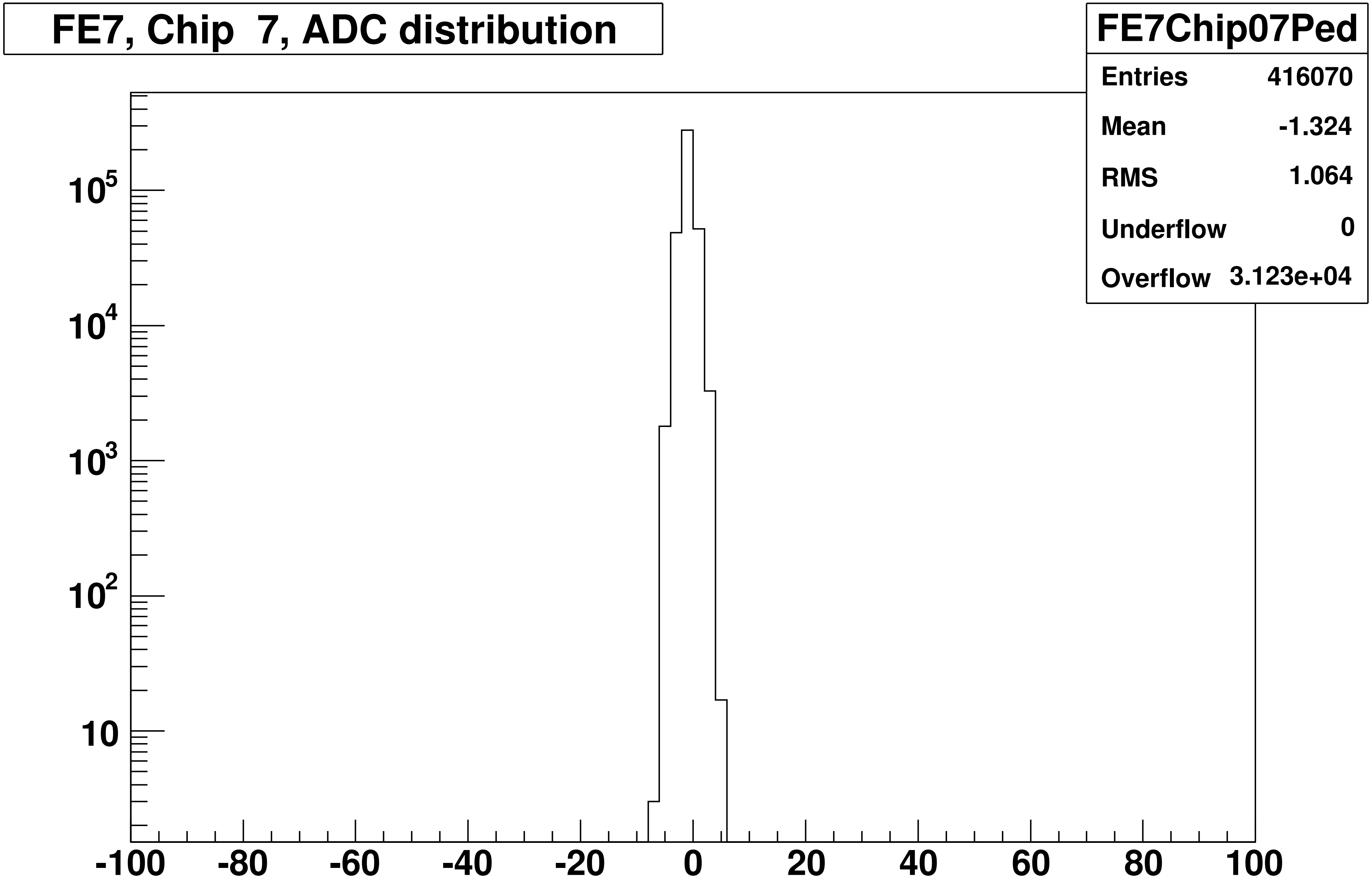
5000
10000
15000
20000
25000


FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 416070
Mean -1.324
RMS 1.064
Underflow 0
Overflow 3.123e+04
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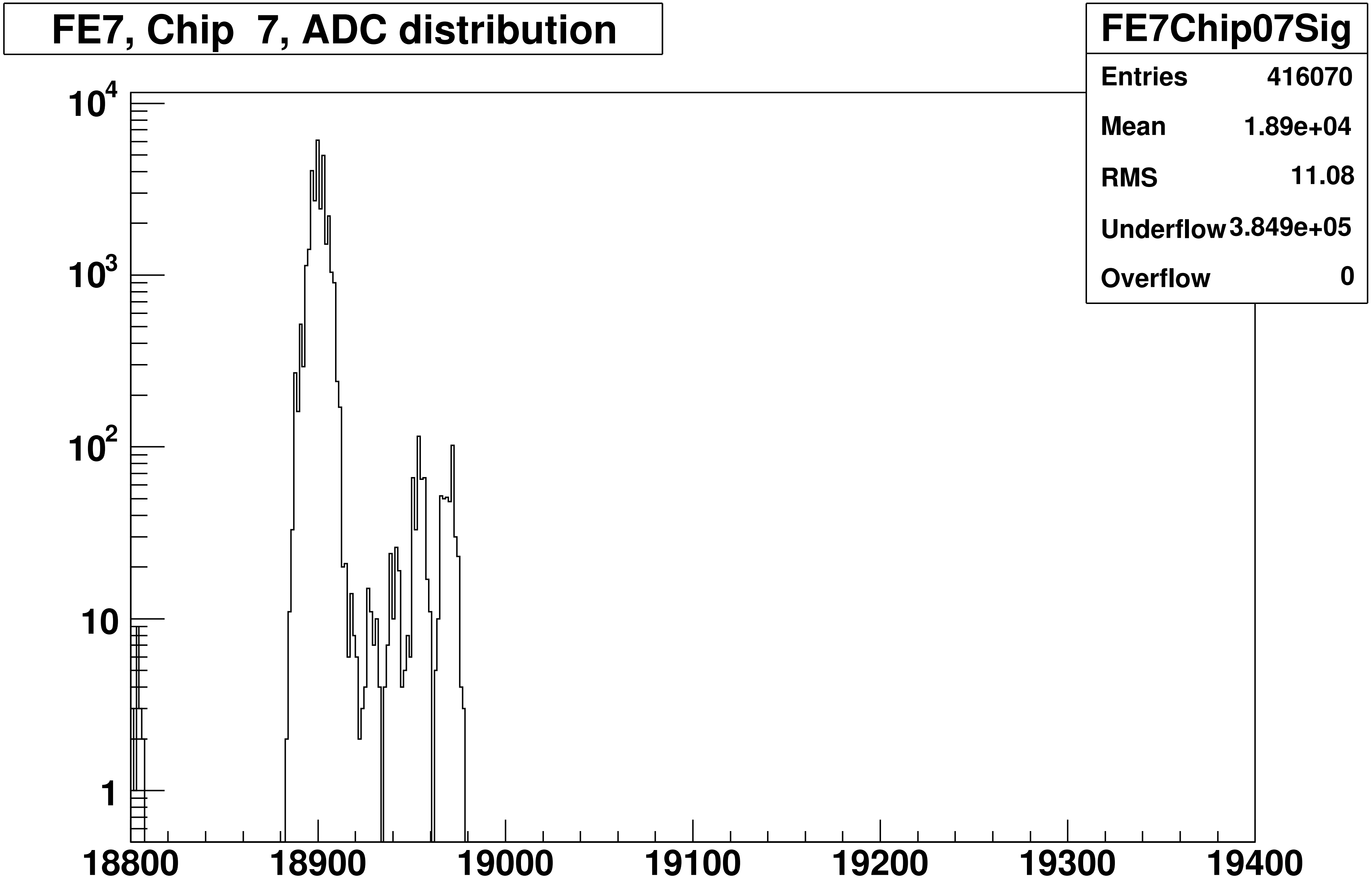
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 416070
Mean 1.89e+04
RMS 11.08
Underflow 3.849e+05
Overflow 0
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10^3
10^2
10
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18900
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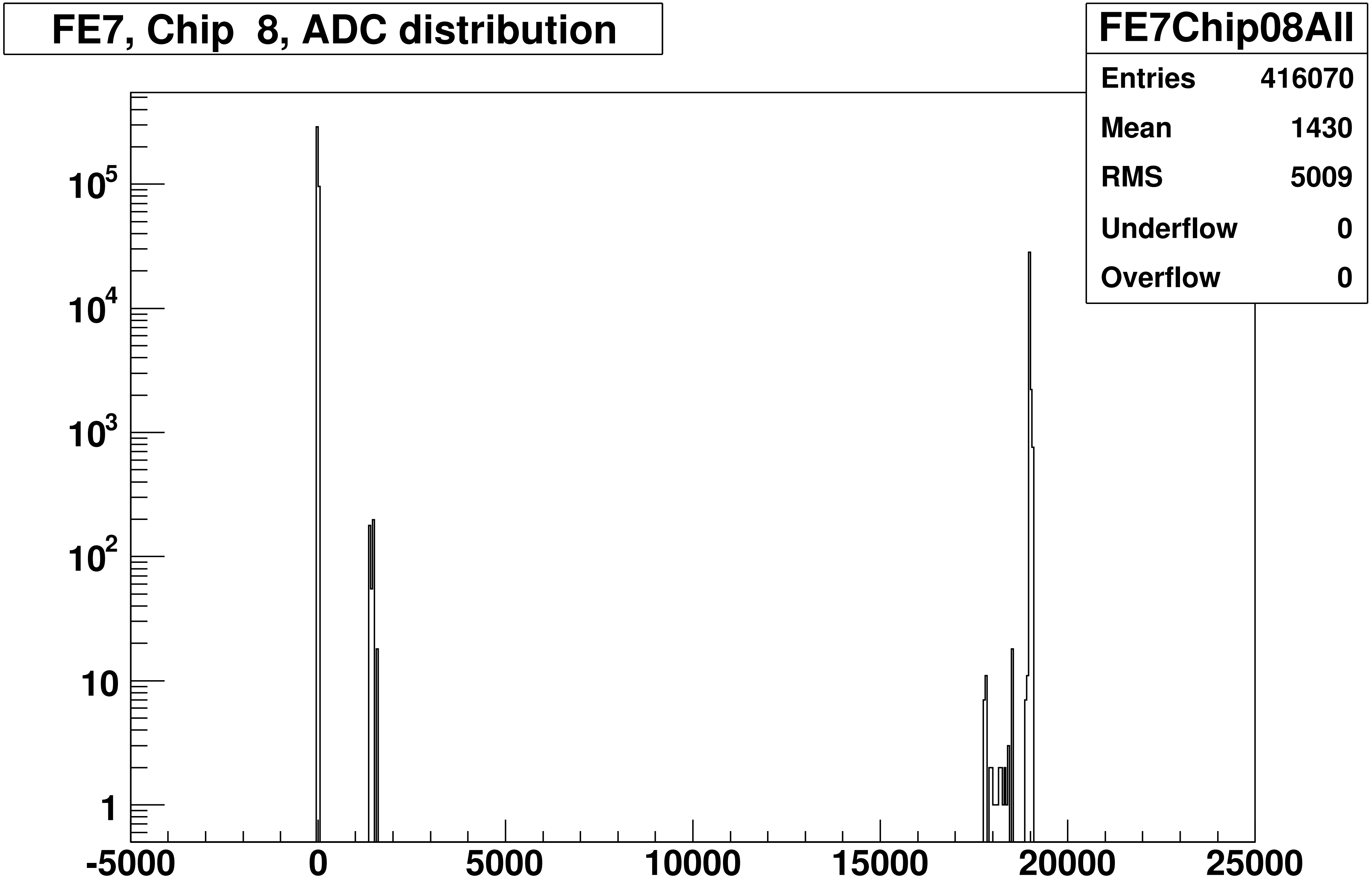
19400


FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 416070
Mean 1430
RMS 5009
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
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5000
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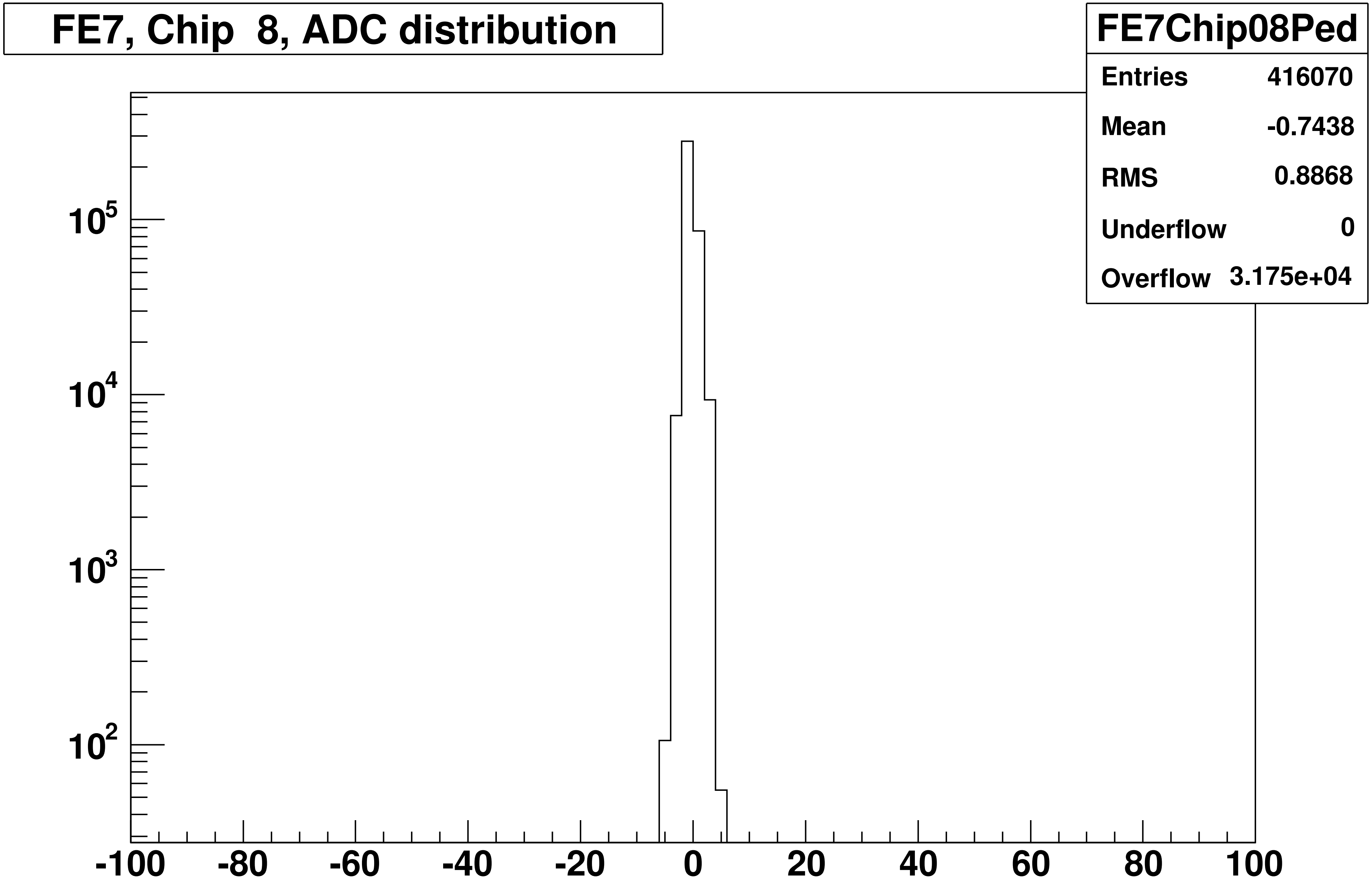

FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 416070
Mean -0.7438
RMS 0.8868
Underflow 0
Overflow 3.175e+04
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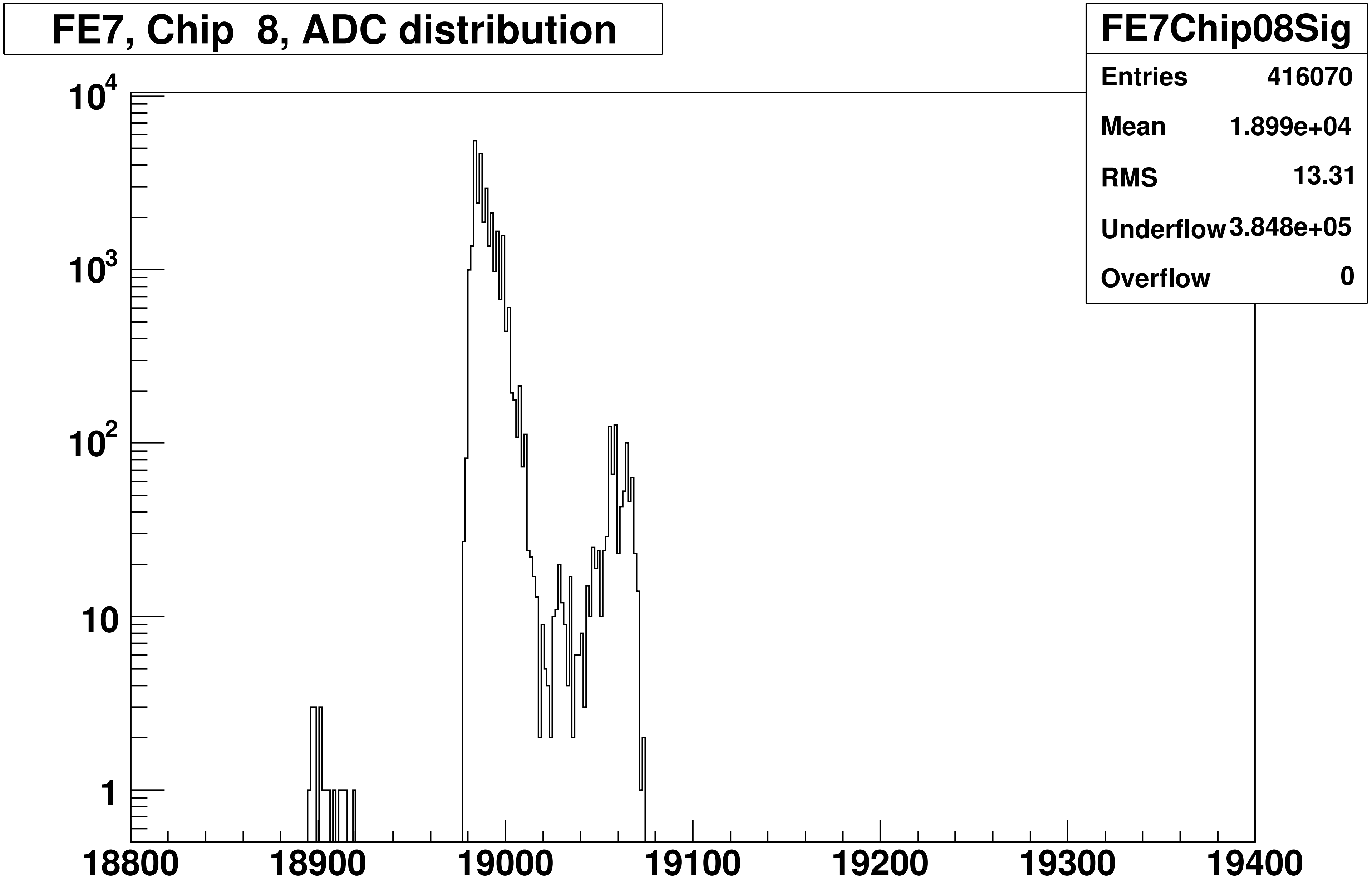
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 416070
Mean 1.899e+04
RMS 13.31
Underflow 3.848e+05
Overflow 0
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10^3
10^2
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18800
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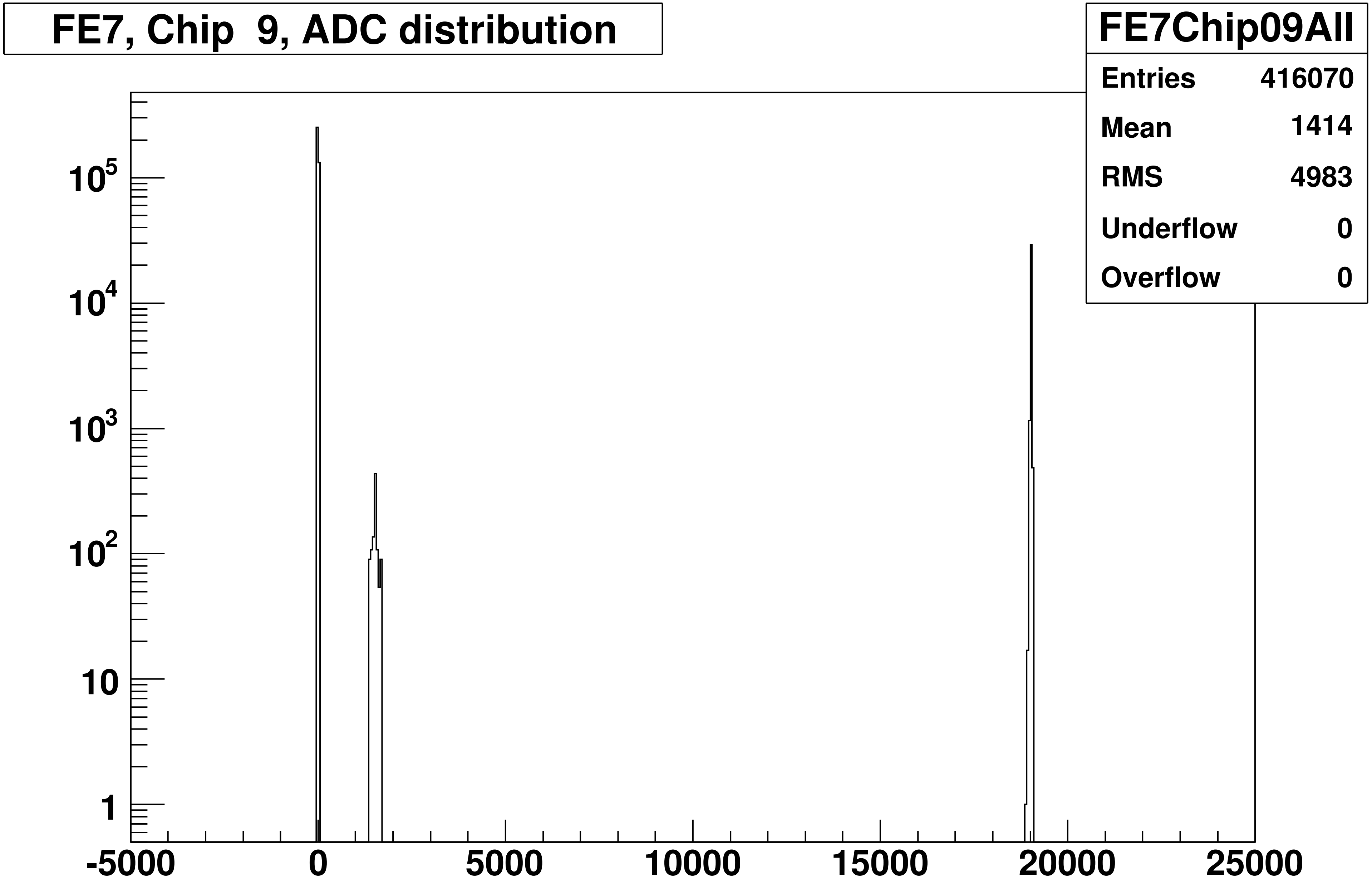
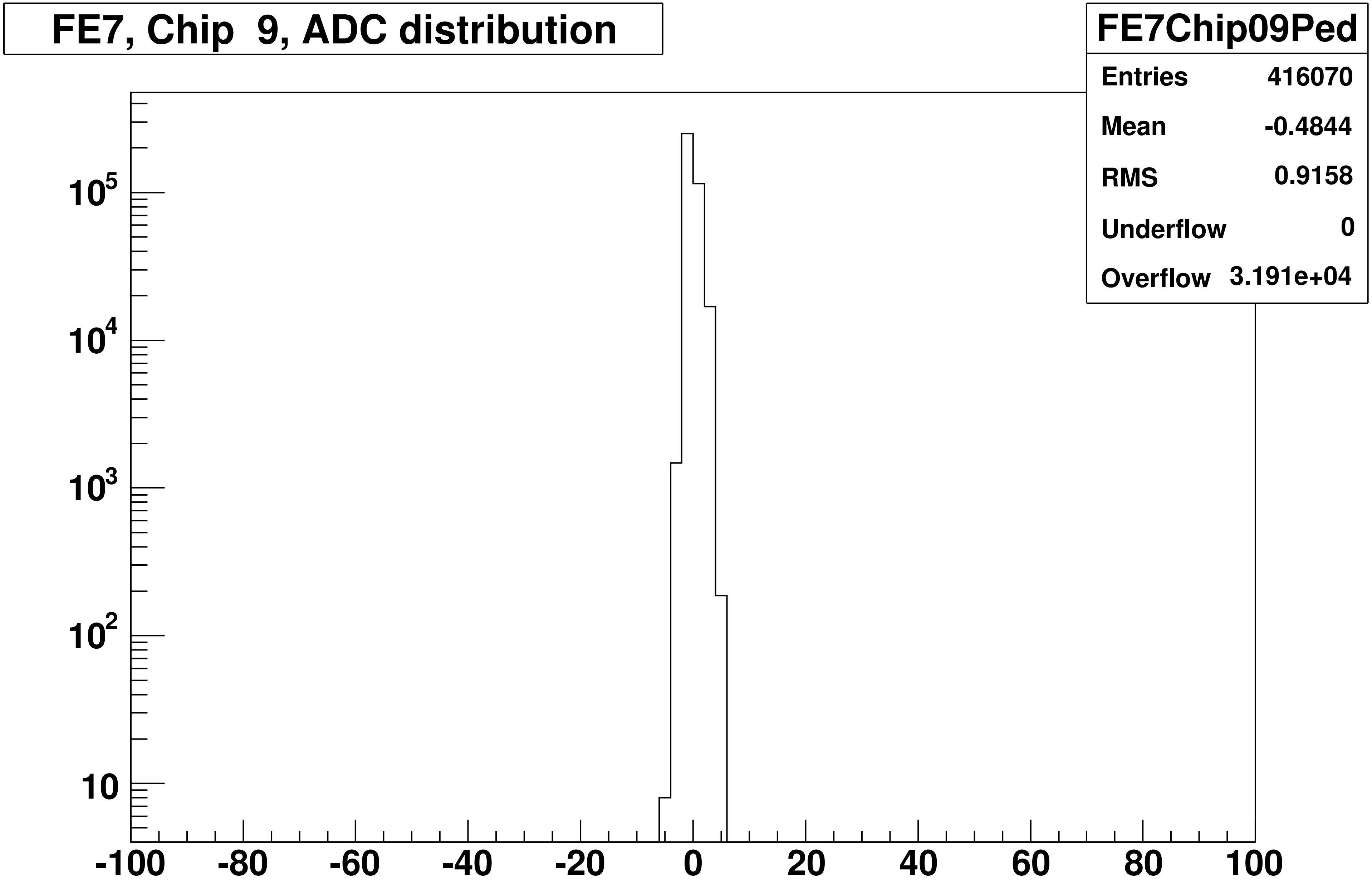
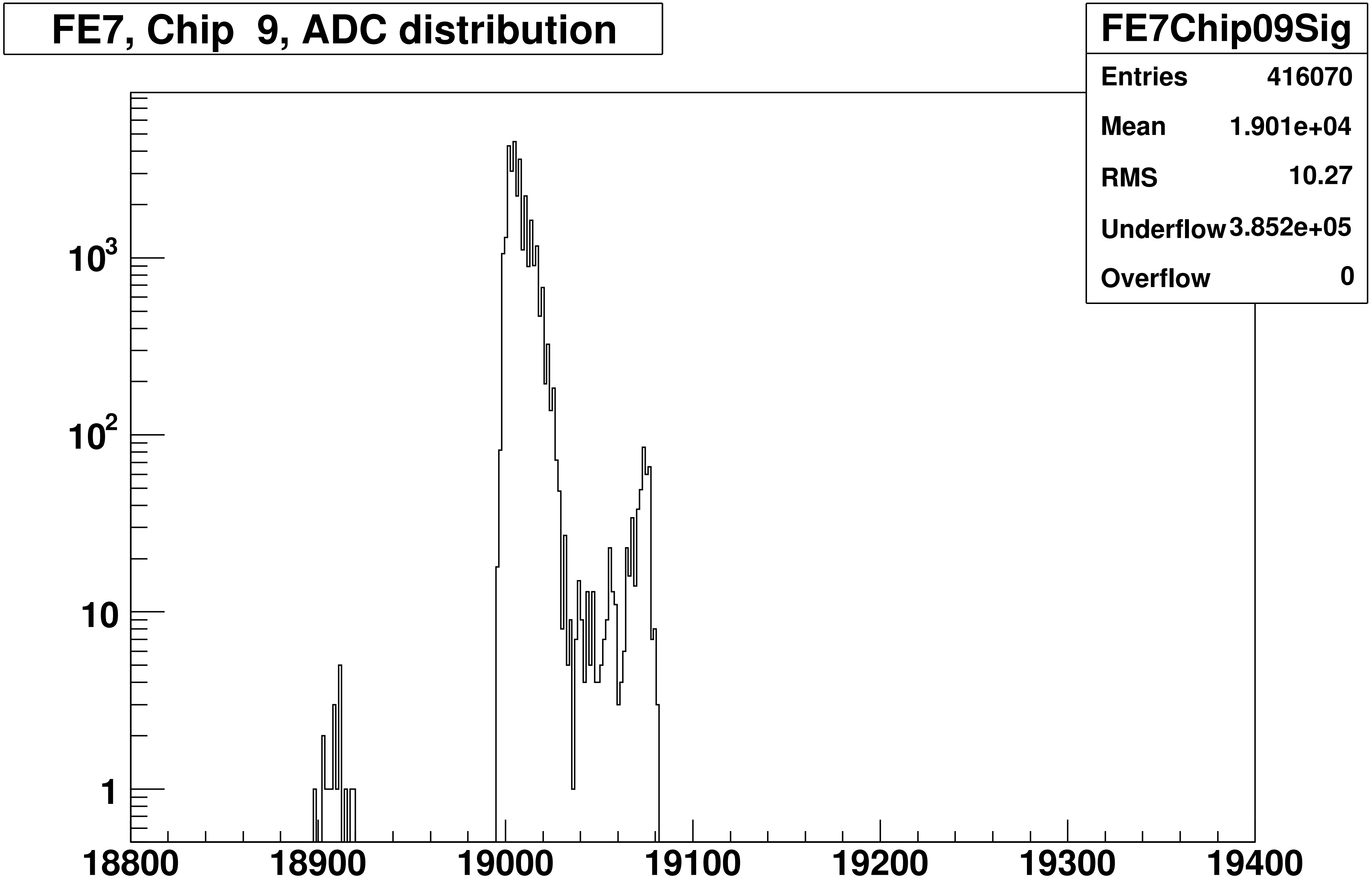
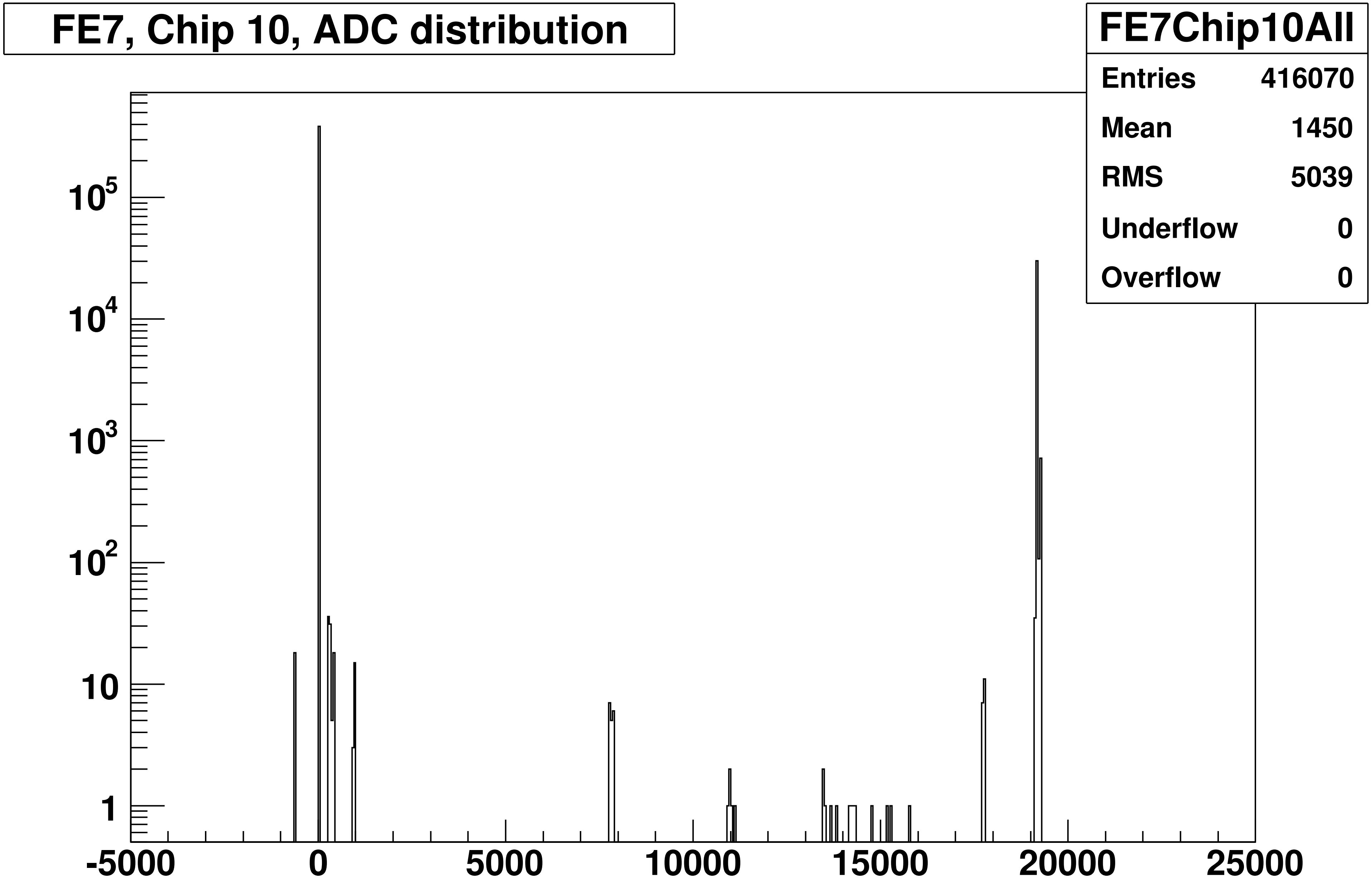
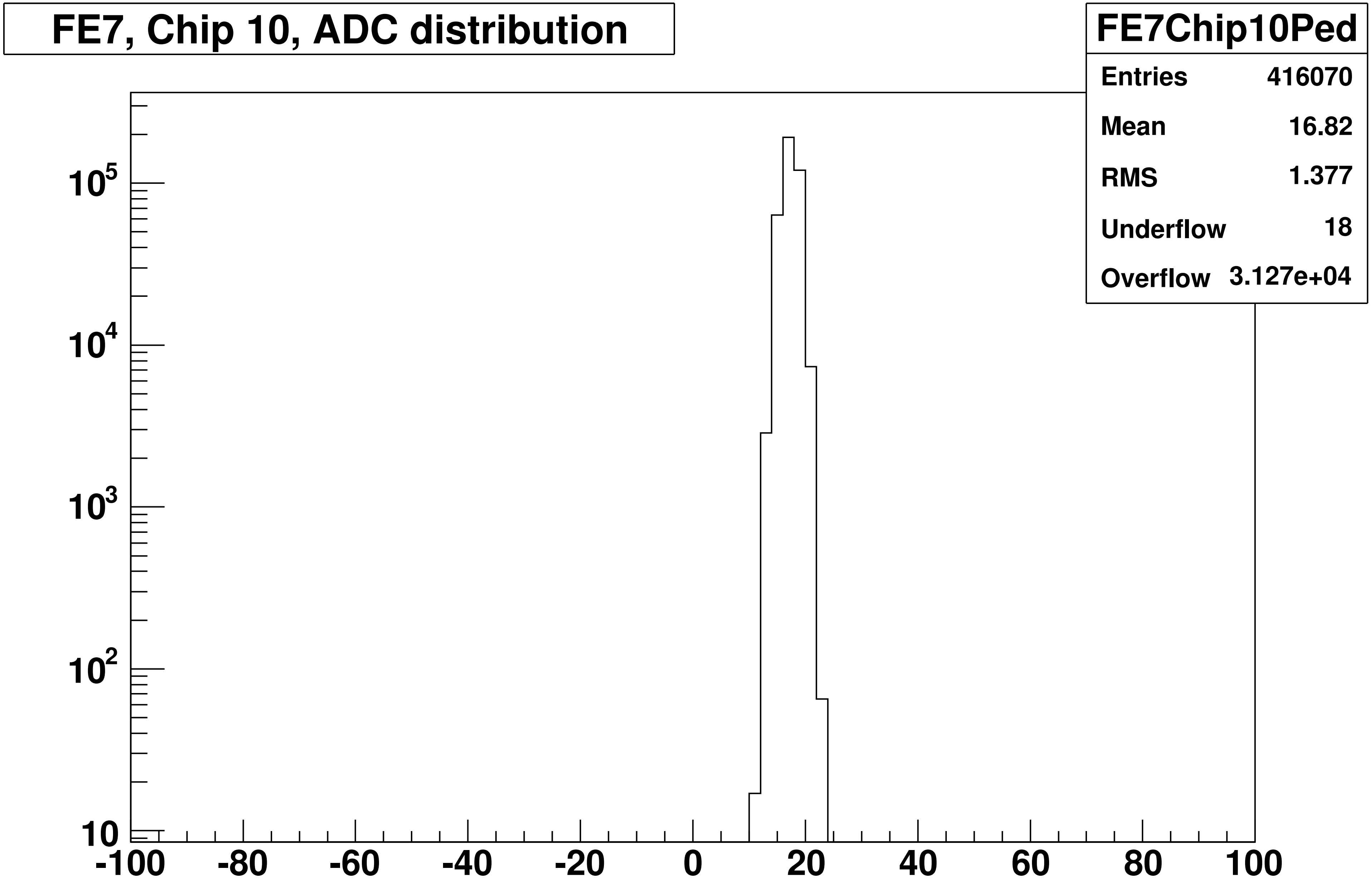
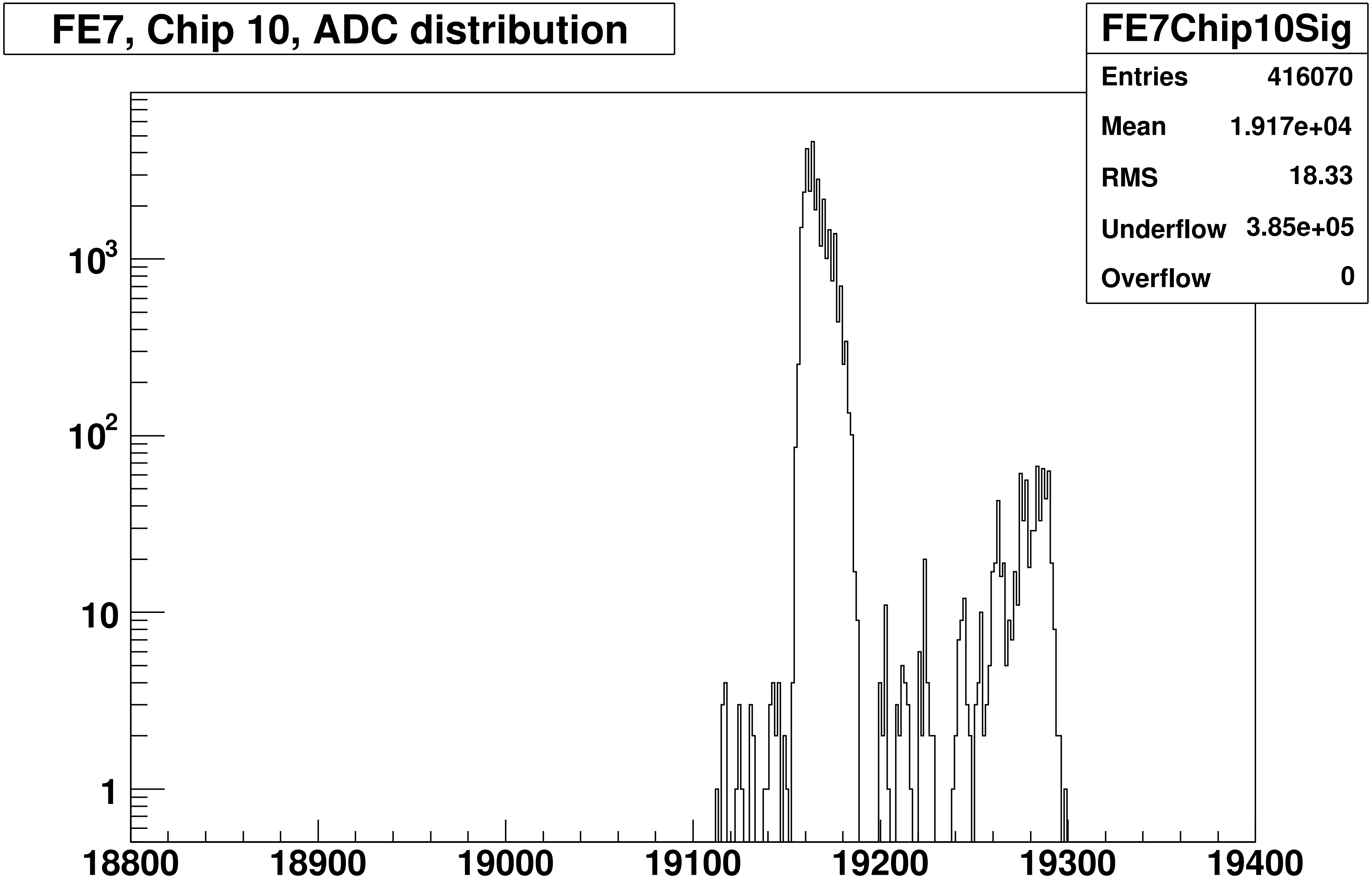

FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 416070
Mean 1414
RMS 4983
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 416070
Mean -0.4844
RMS 0.9158
Underflow 0
Overflow 3.191e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

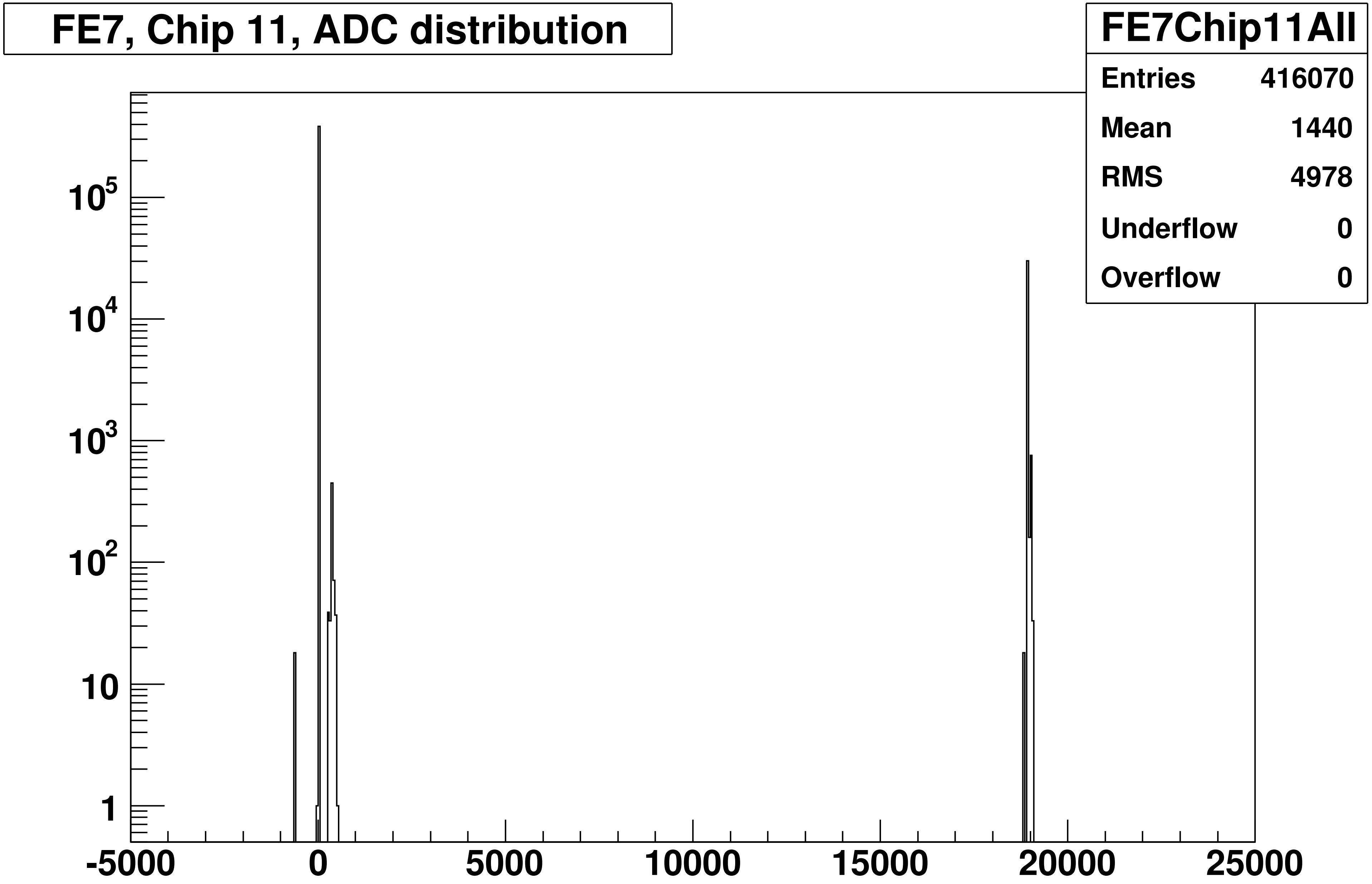

FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 416070
Mean 1.901e+04
RMS 10.27
Underflow 3.852e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400

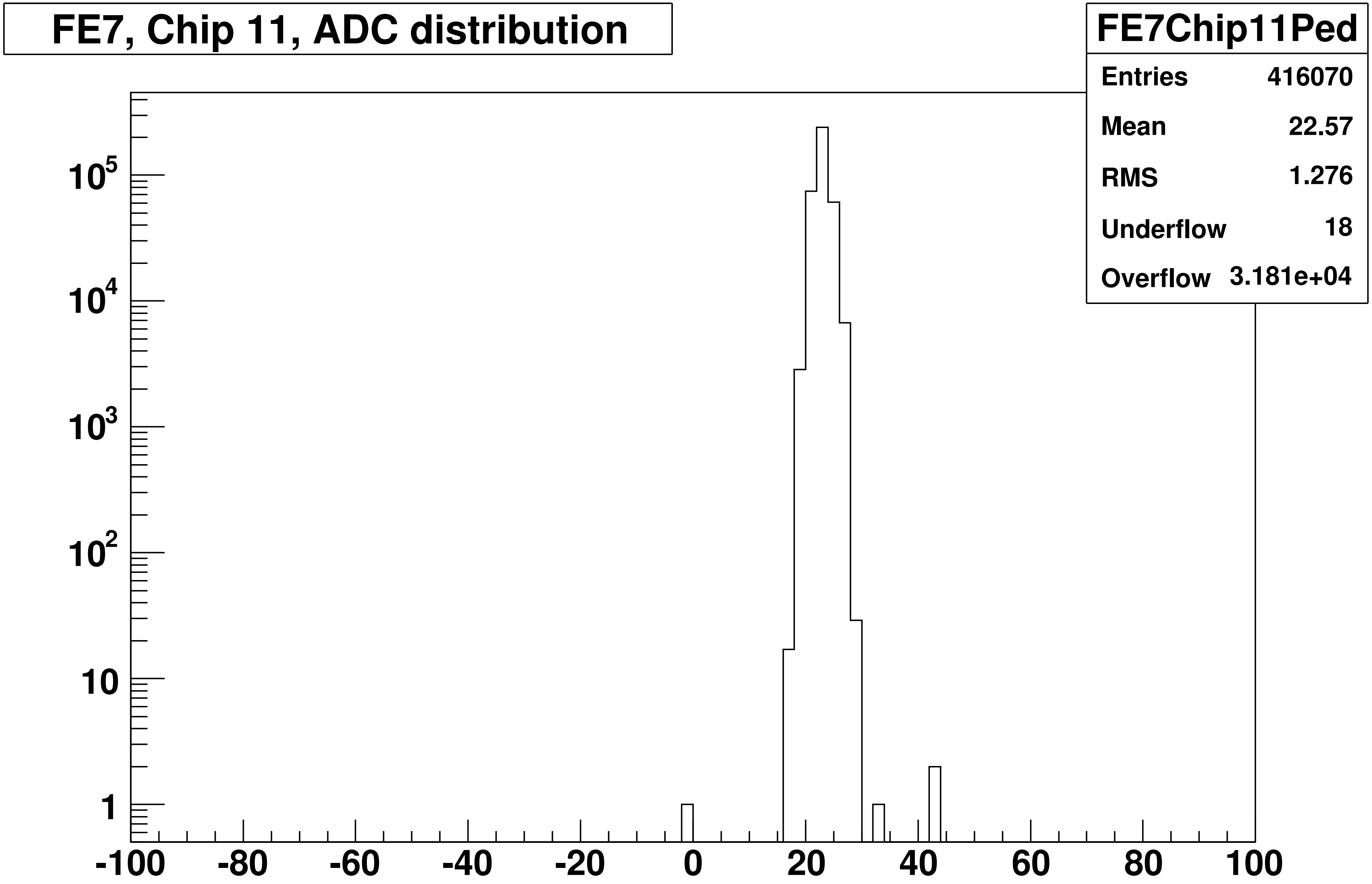

FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 416070
Mean 1450
RMS 5039
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

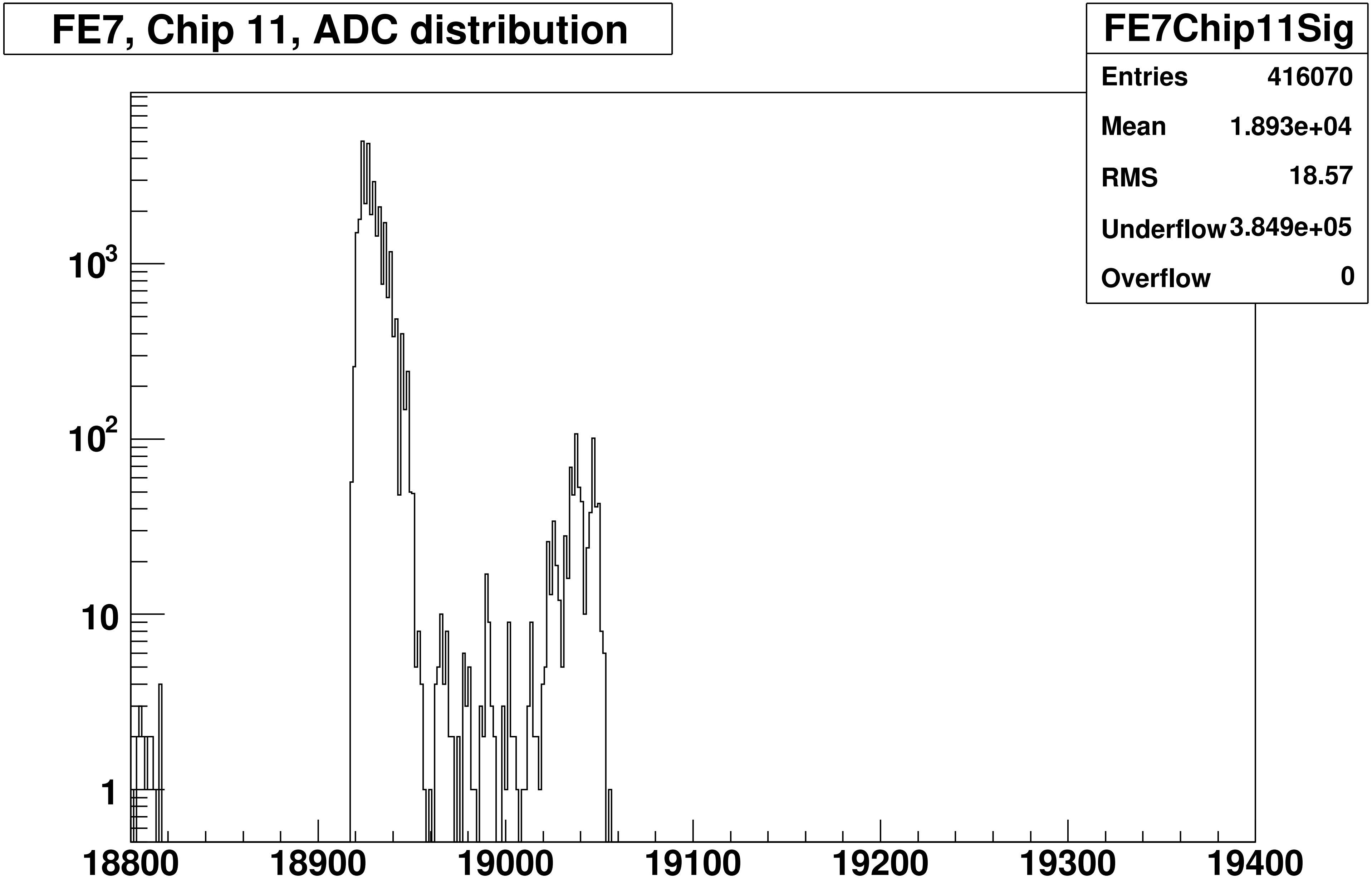

FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 416070
Mean 16.82
RMS 1.377
Underflow 18
Overflow 3.127e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



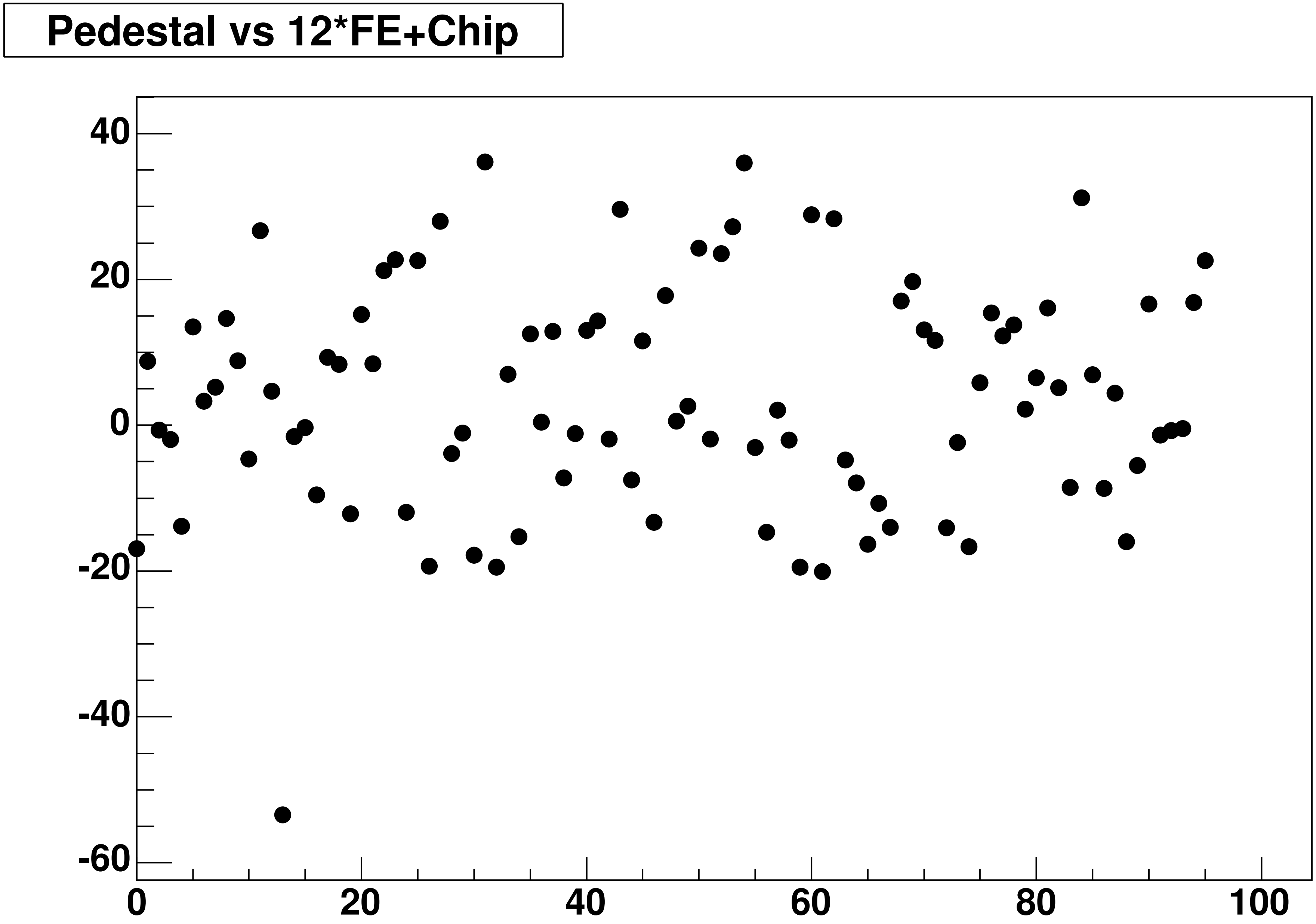
FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 416070
Mean 1.917e+04
RMS 18.33
Underflow 3.85e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

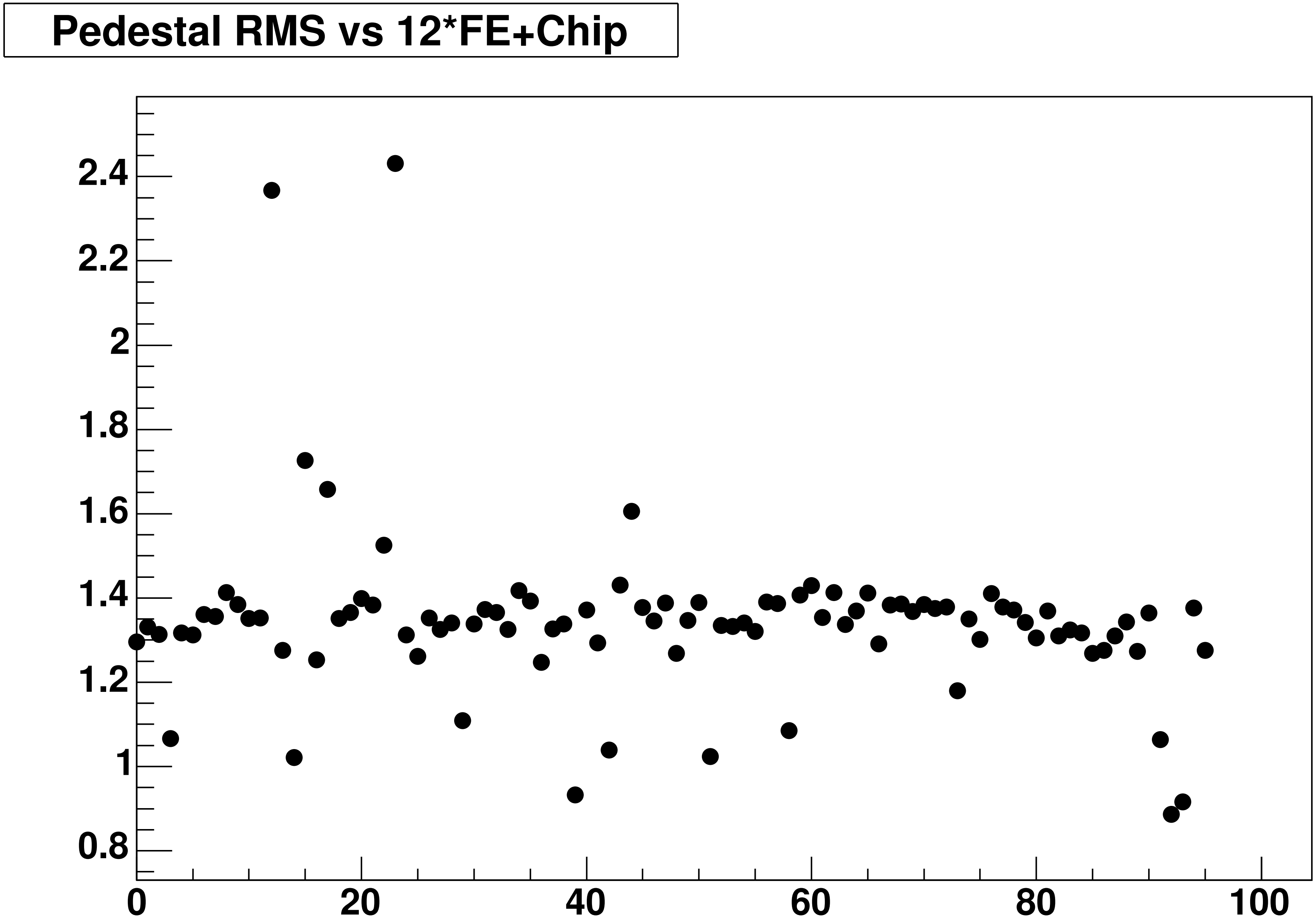

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 416070
Mean 1440
RMS 4978
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

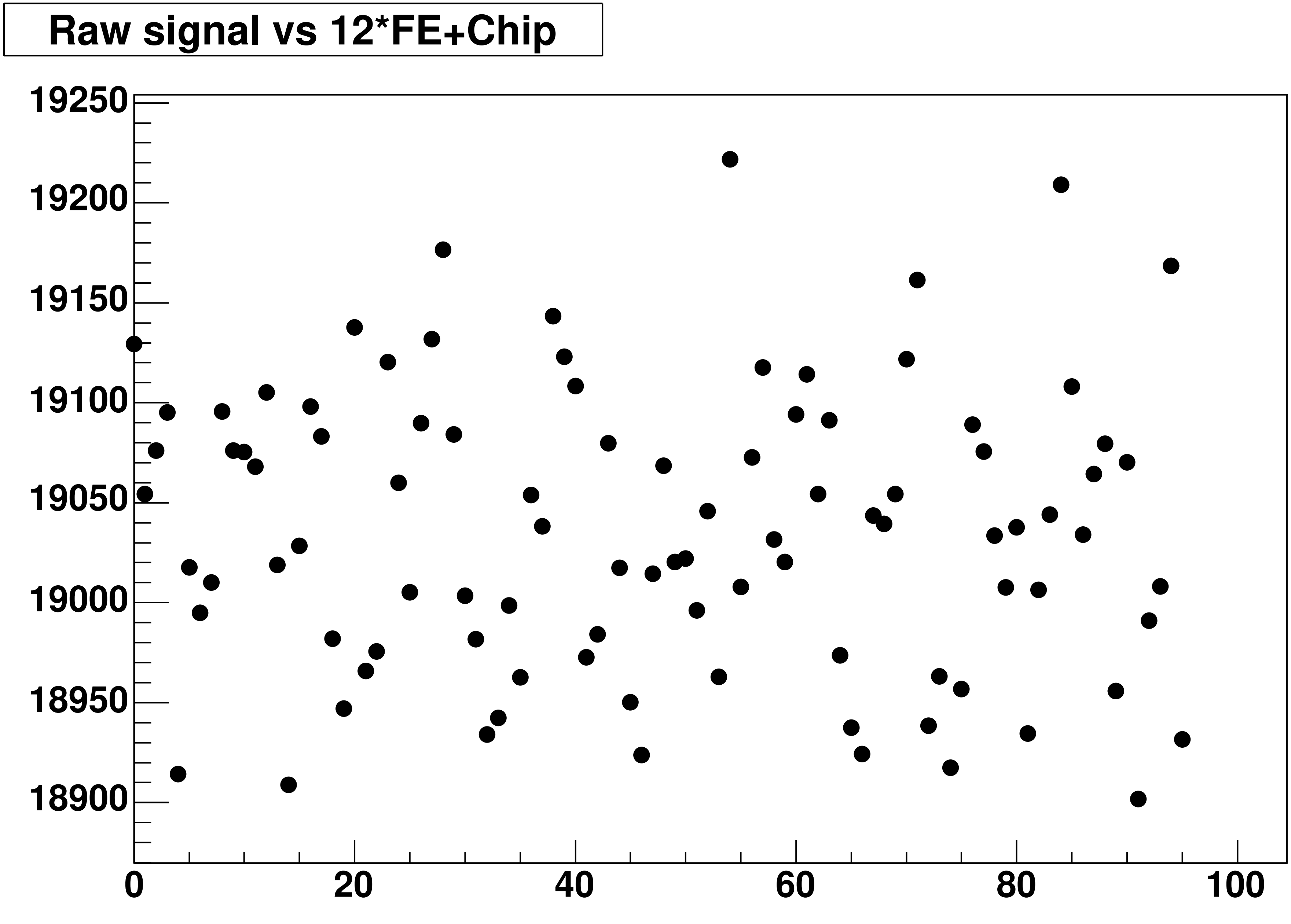

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 416070
Mean 22.57
RMS 1.276
Underflow 18
Overflow 3.181e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



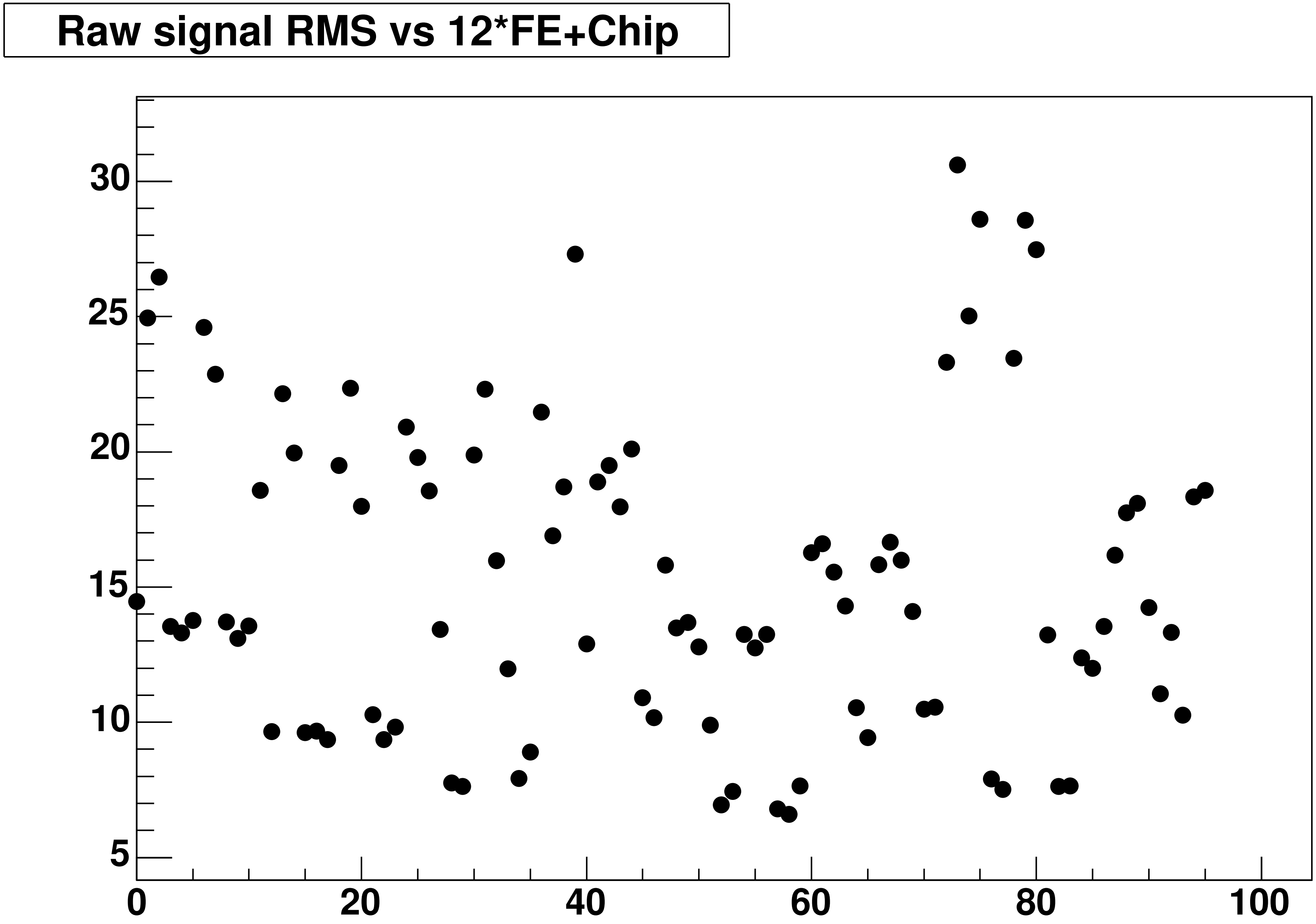
FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 416070
Mean 1.893e+04
RMS 18.57
Underflow 3.849e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

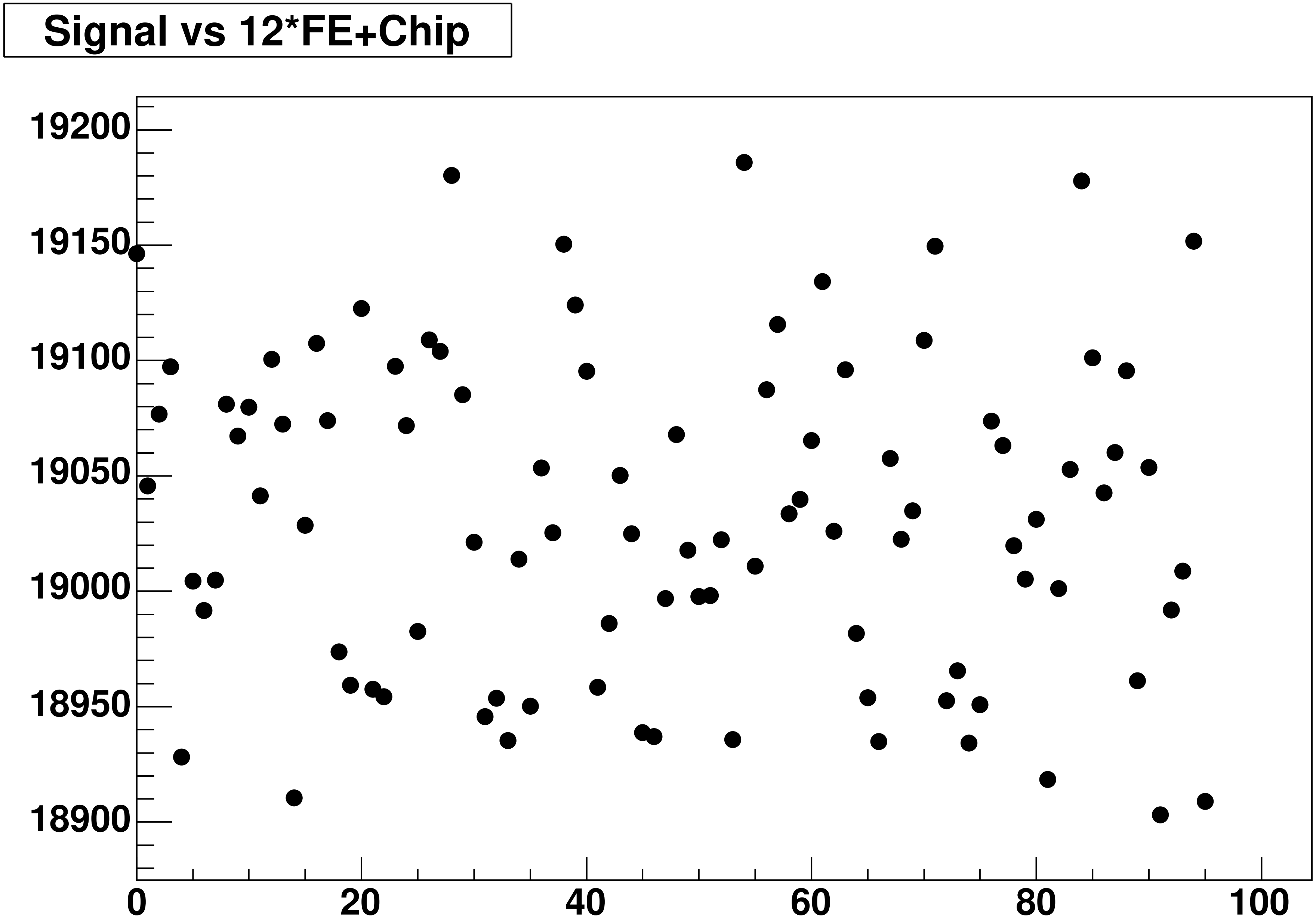

Pedestal vs 12*FE+Chip
40
20
0
-20
-40
-60
0
20
40
60
80
100


Pedestal RMS vs 12*FE+Chip
2.4
2.2
2
1.8
1.6
1.4
1.2
1
0.8
0
20
40
60
80
100

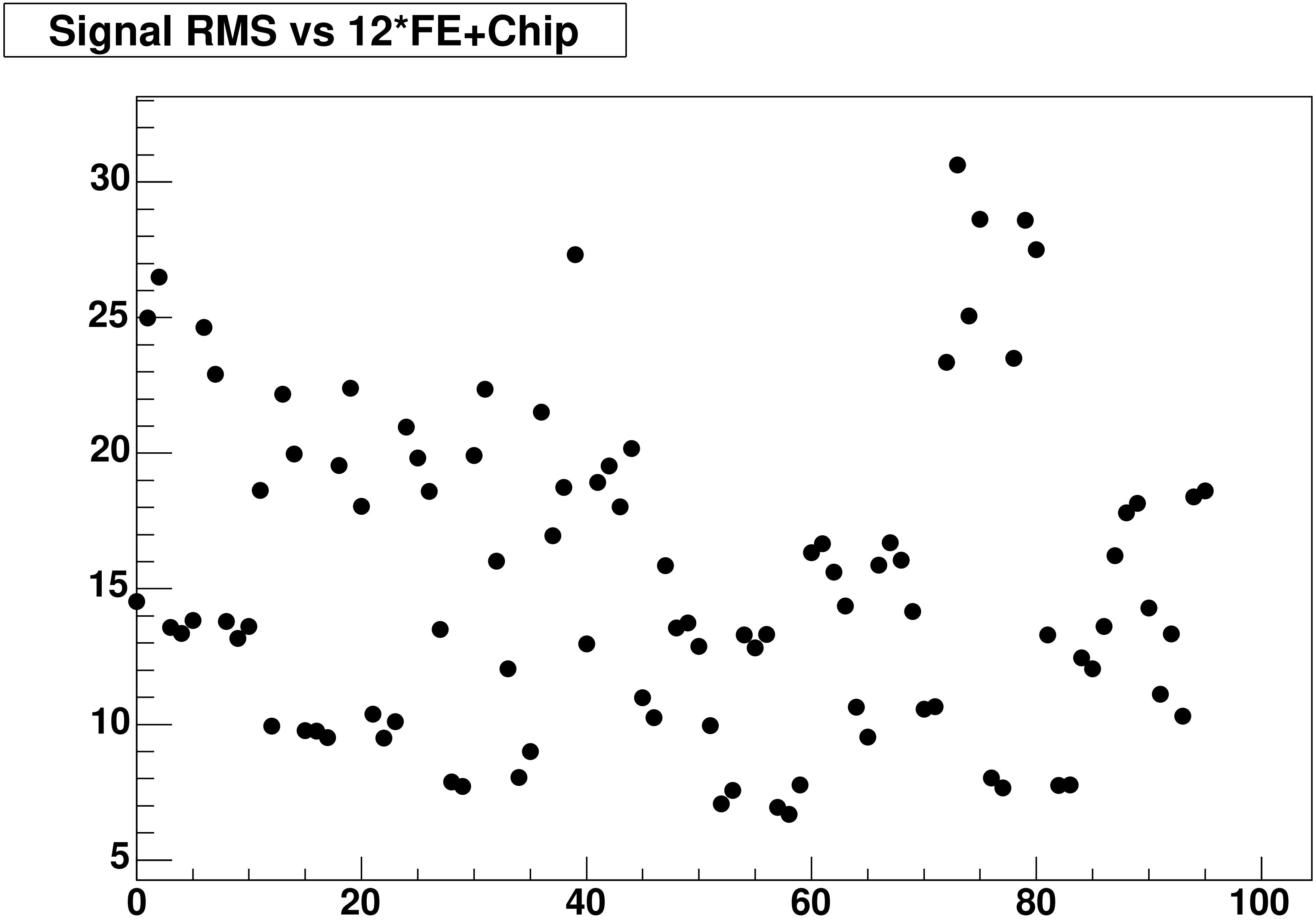


Raw signal vs 12*FE+Chip
19250
19200
19150
19100
19050
19000
18950
18900
0
20
40
60
80
100


Raw signal RMS vs 12*FE+Chip
30
25
20
15
10
5
0
20
40
60
80
100



Signal vs 12*FE+Chip
19200
19150
19100
19050
19000
18950
18900
0
20
40
60
80
100



Signal RMS vs 12*FE+Chip
30
25
20
15
10
5
0
20
40
60
80
100