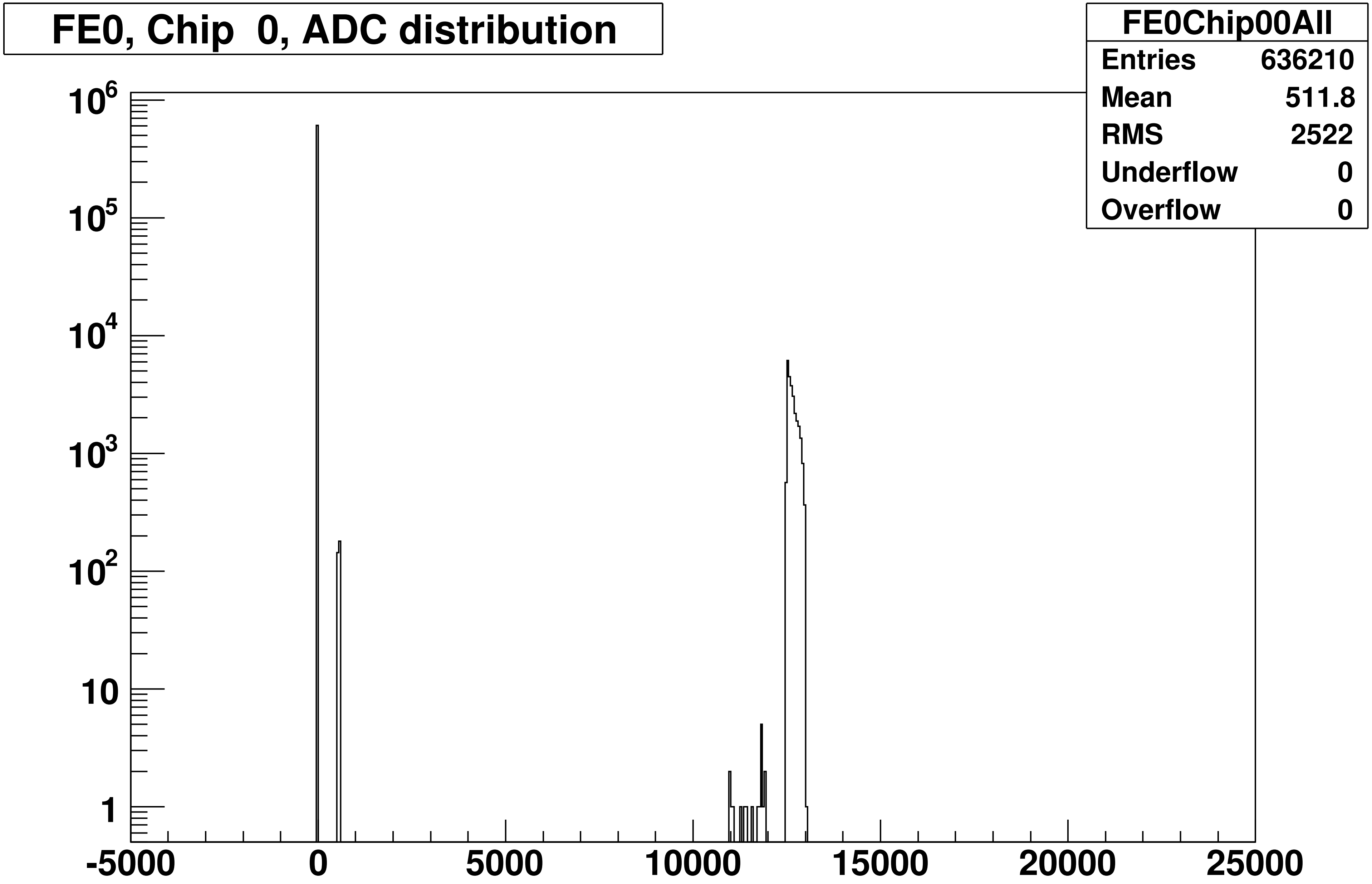
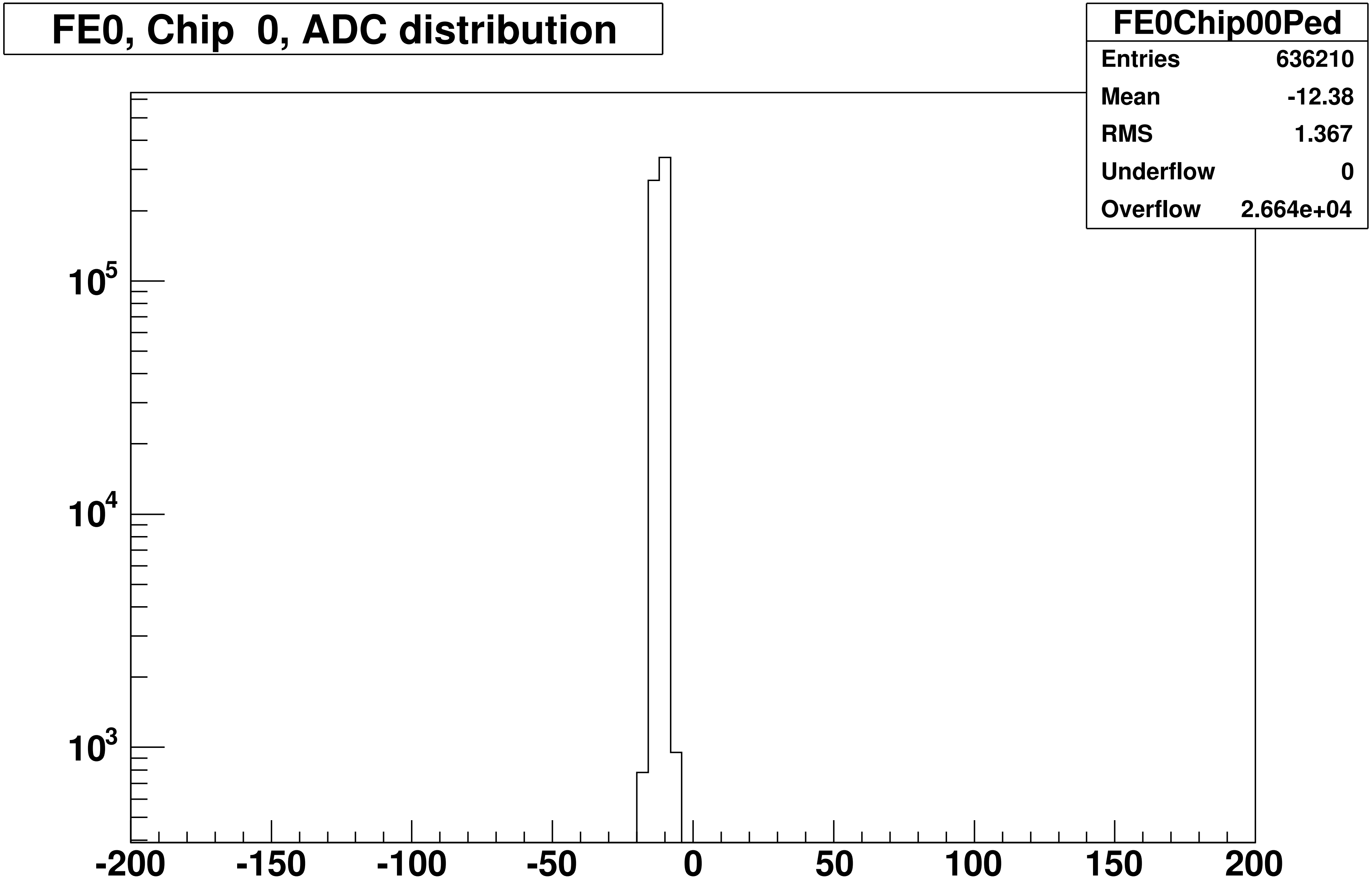
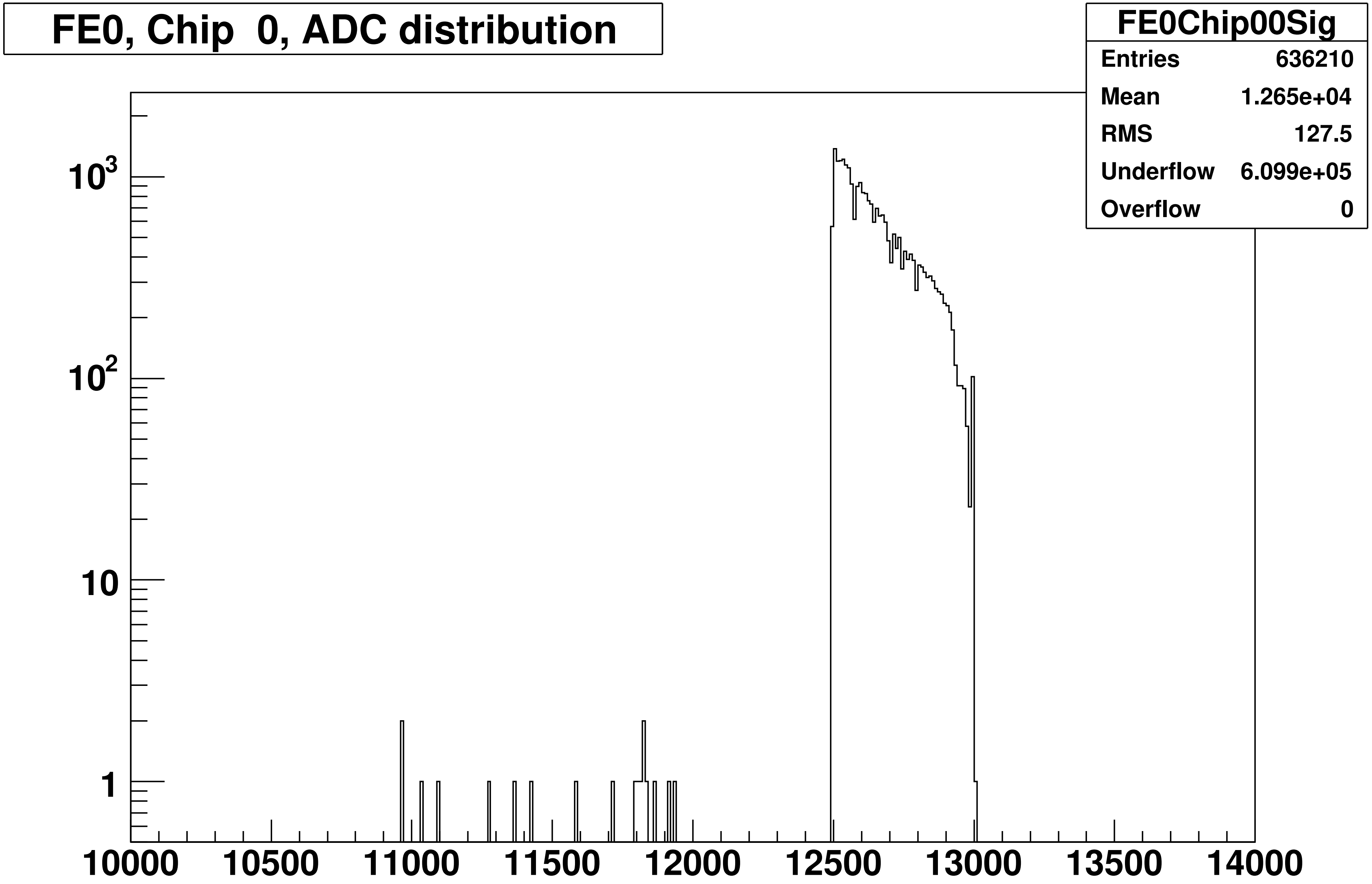
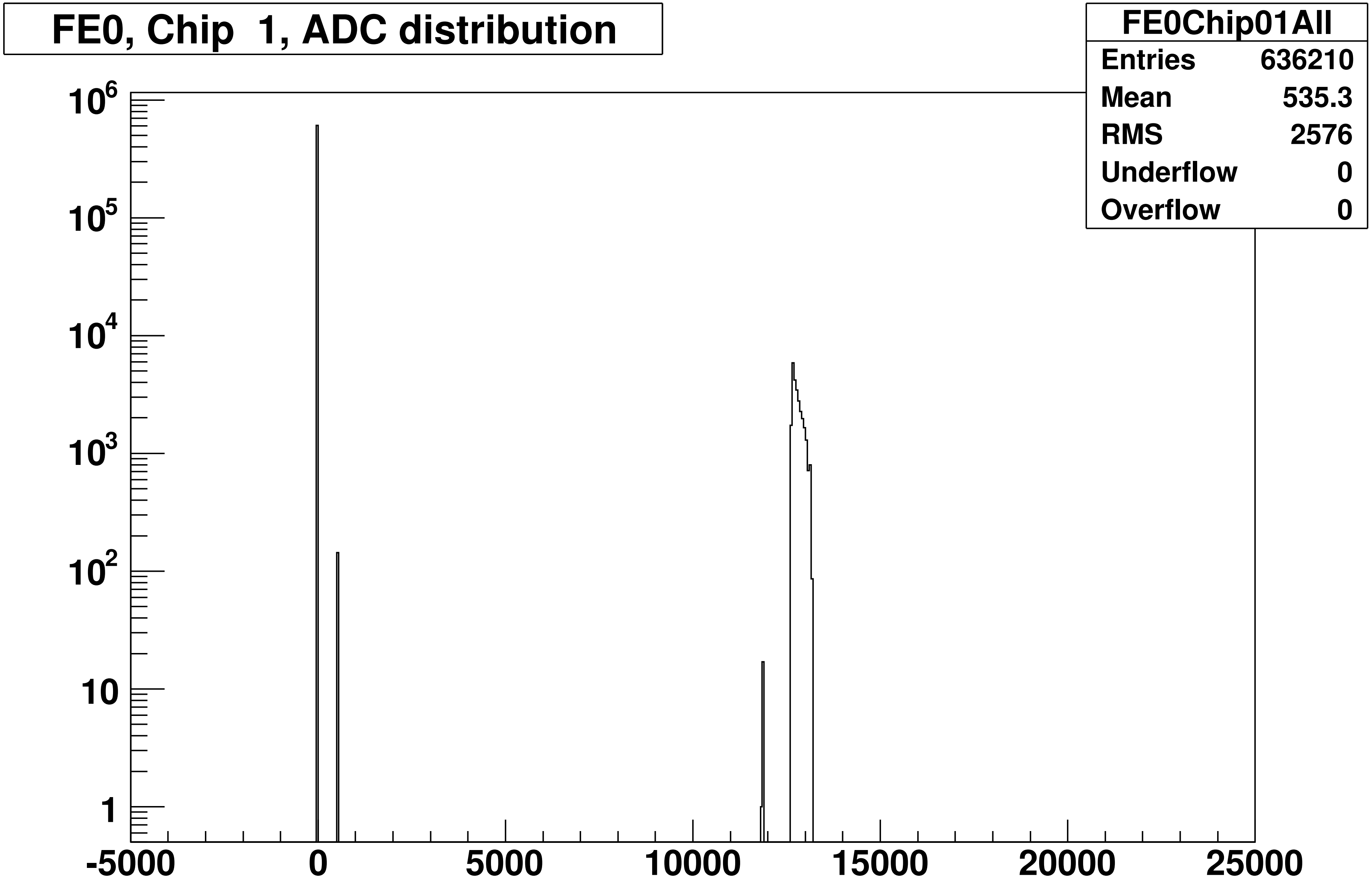
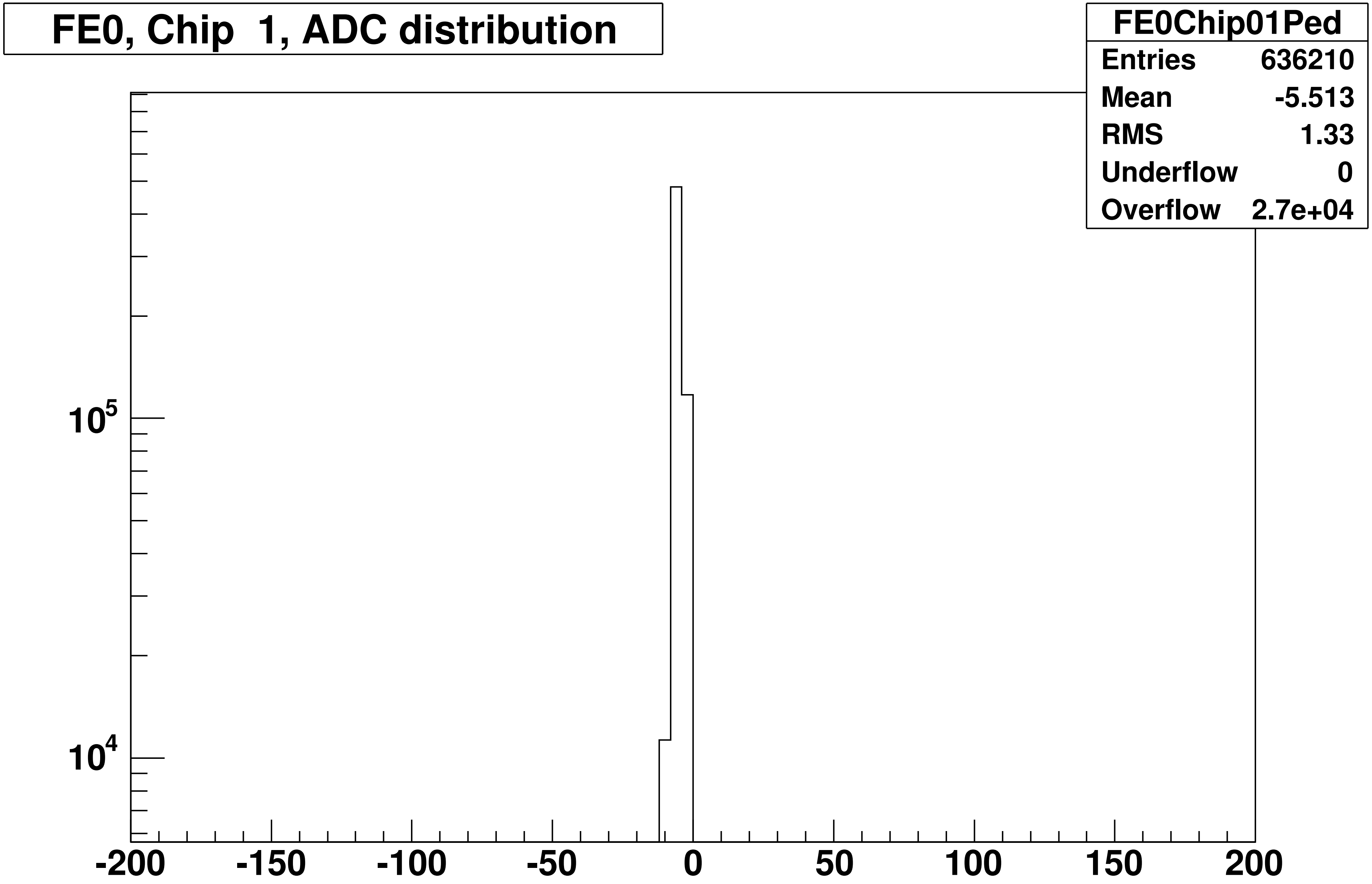

FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 636210
Mean 511.8
RMS 2522
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

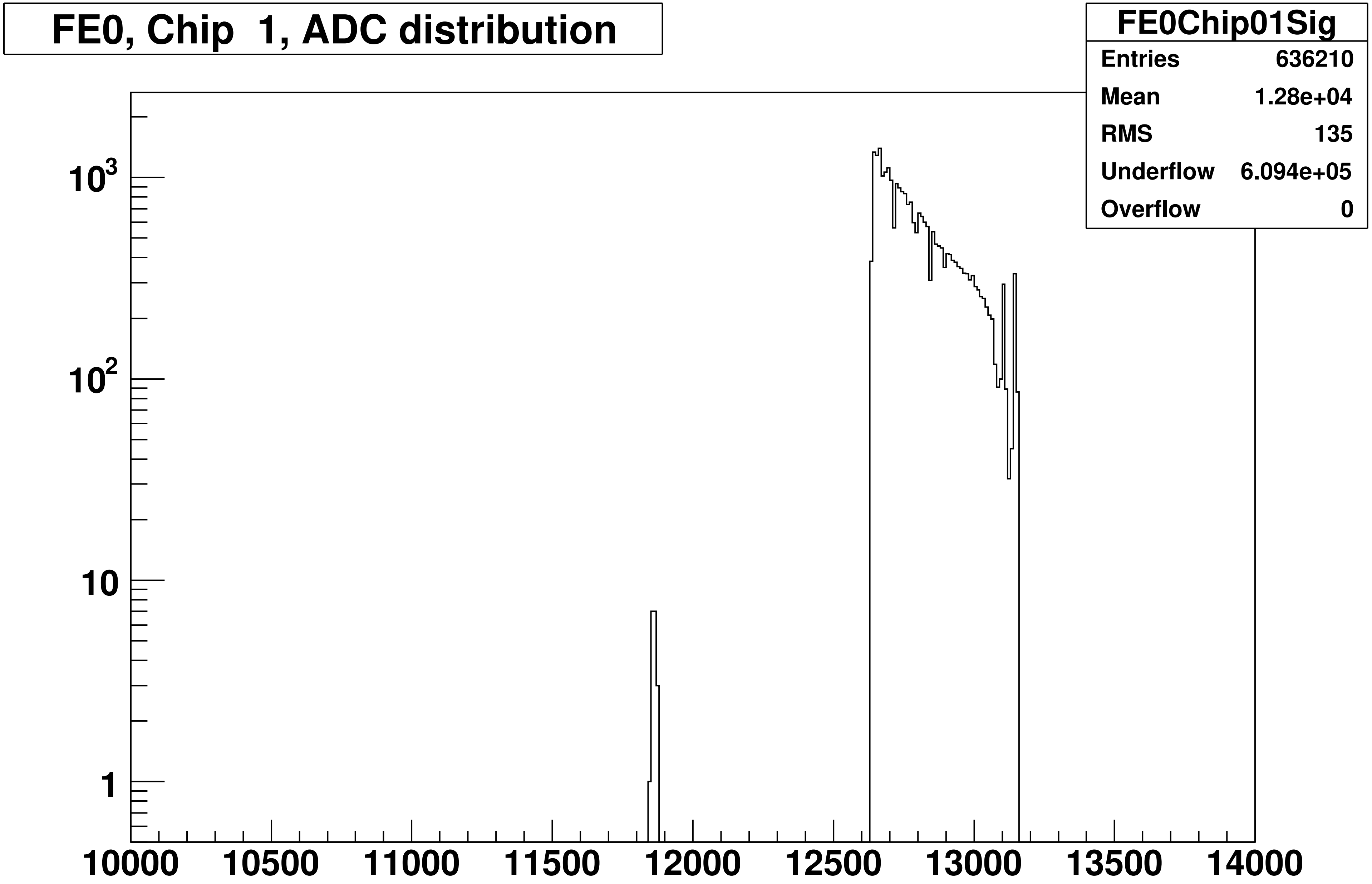


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 636210
Mean -12.38
RMS 1.367
Underflow 0
Overflow 2.664e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

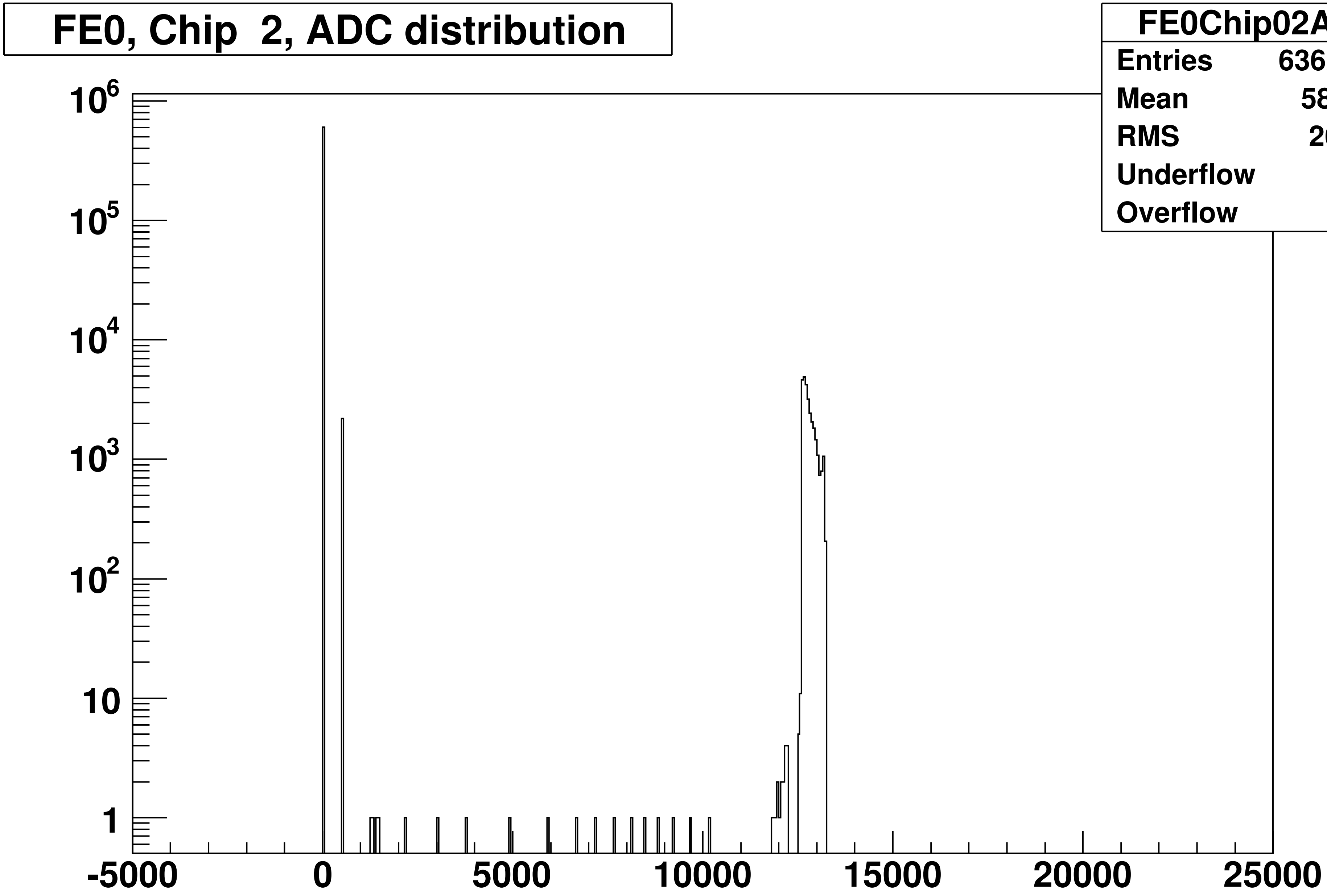

FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 636210
Mean 1.265e+04
RMS 127.5
Underflow 6.099e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

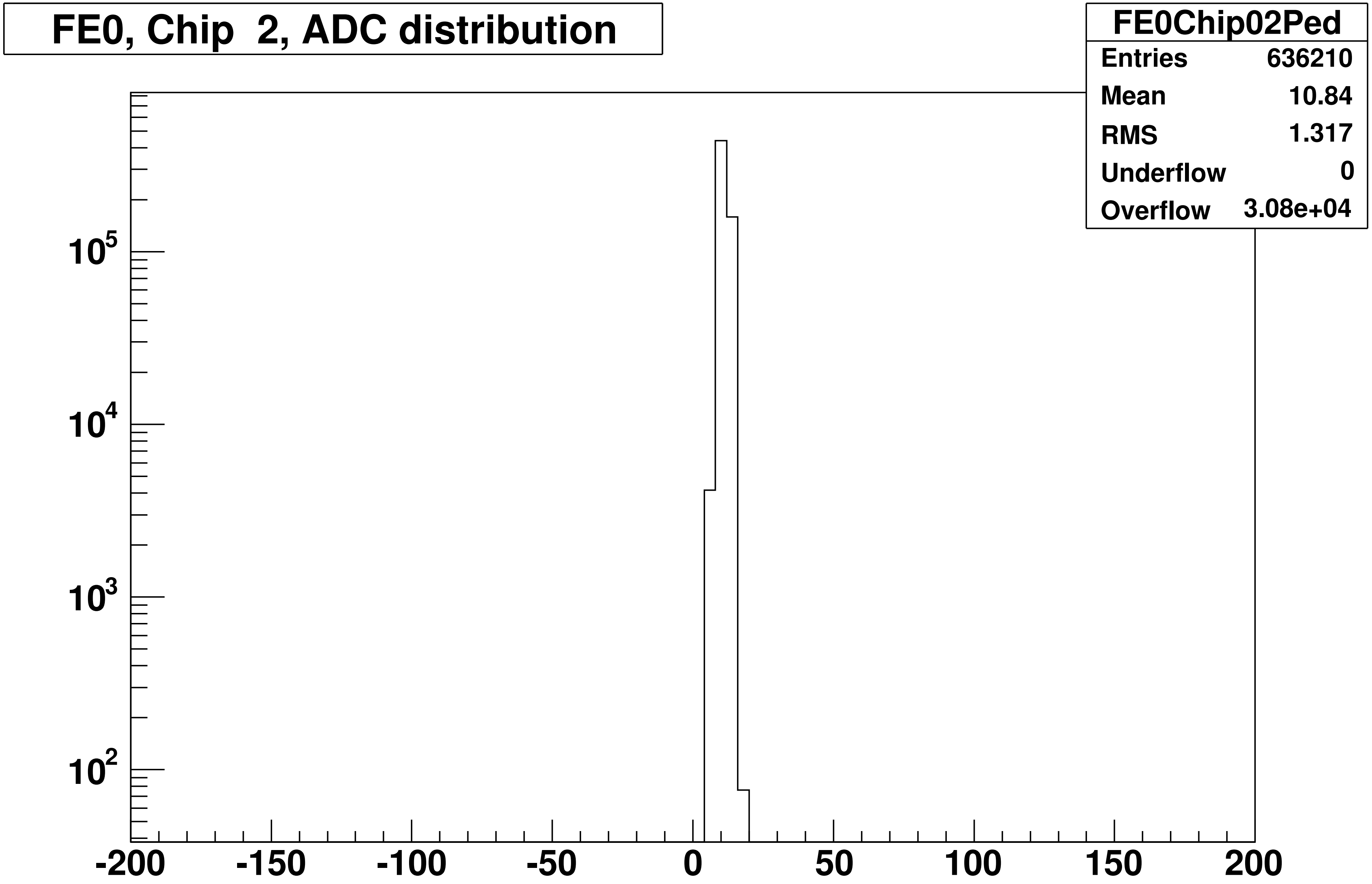

FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 636210
Mean 535.3
RMS 2576
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

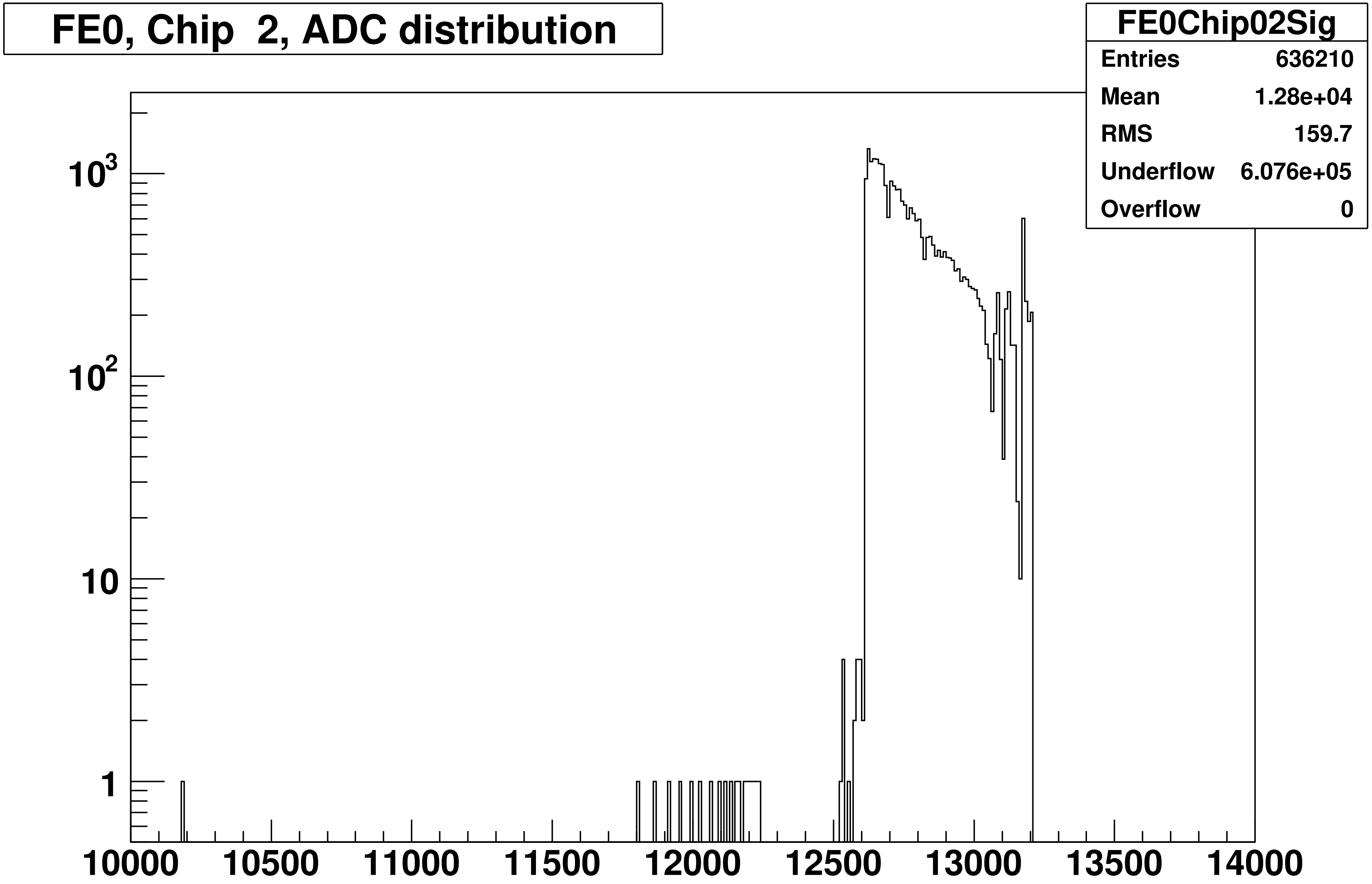

FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 636210
Mean -5.513
RMS 1.33
Underflow 0
Overflow 2.7e+04
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200

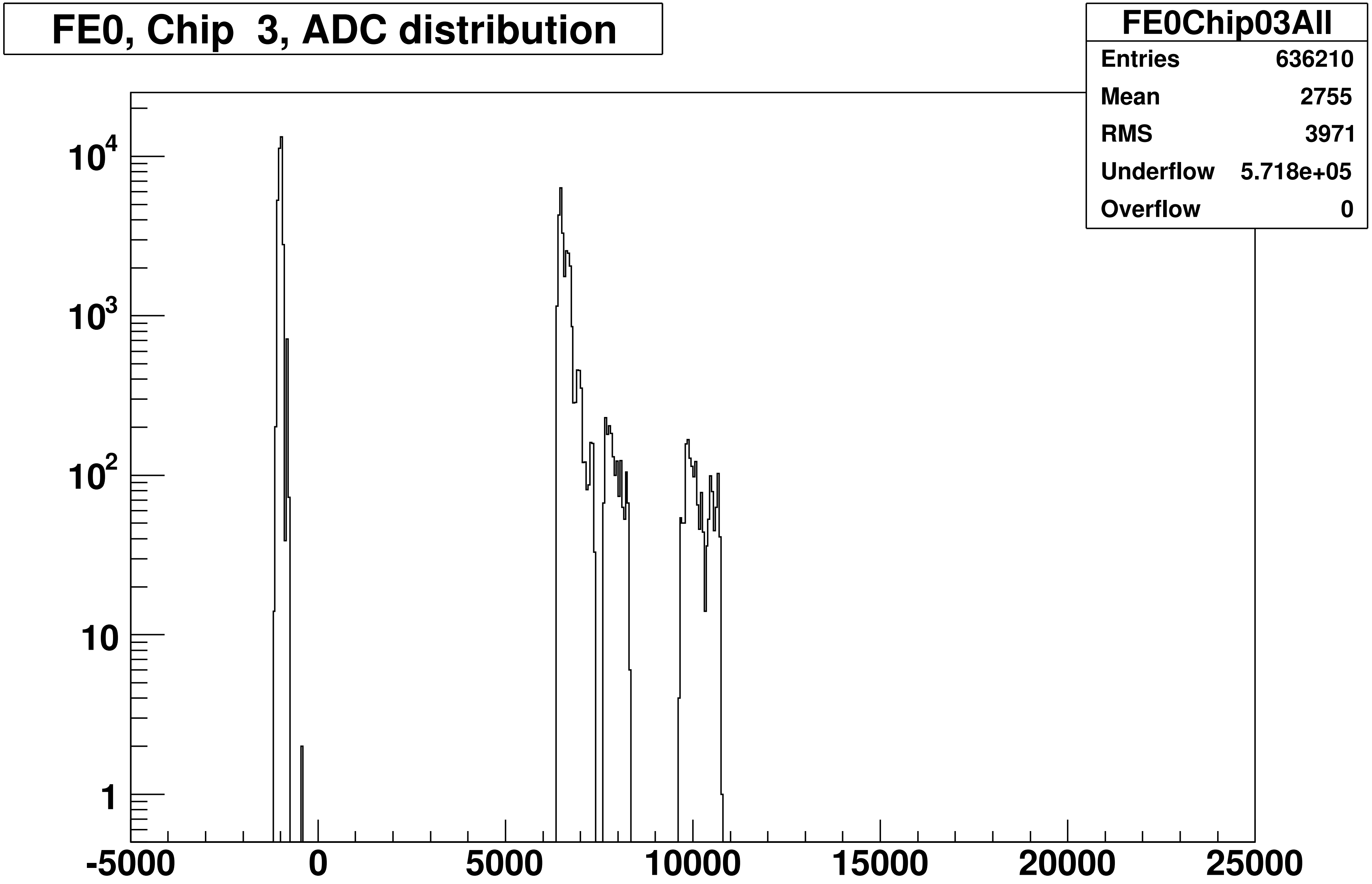


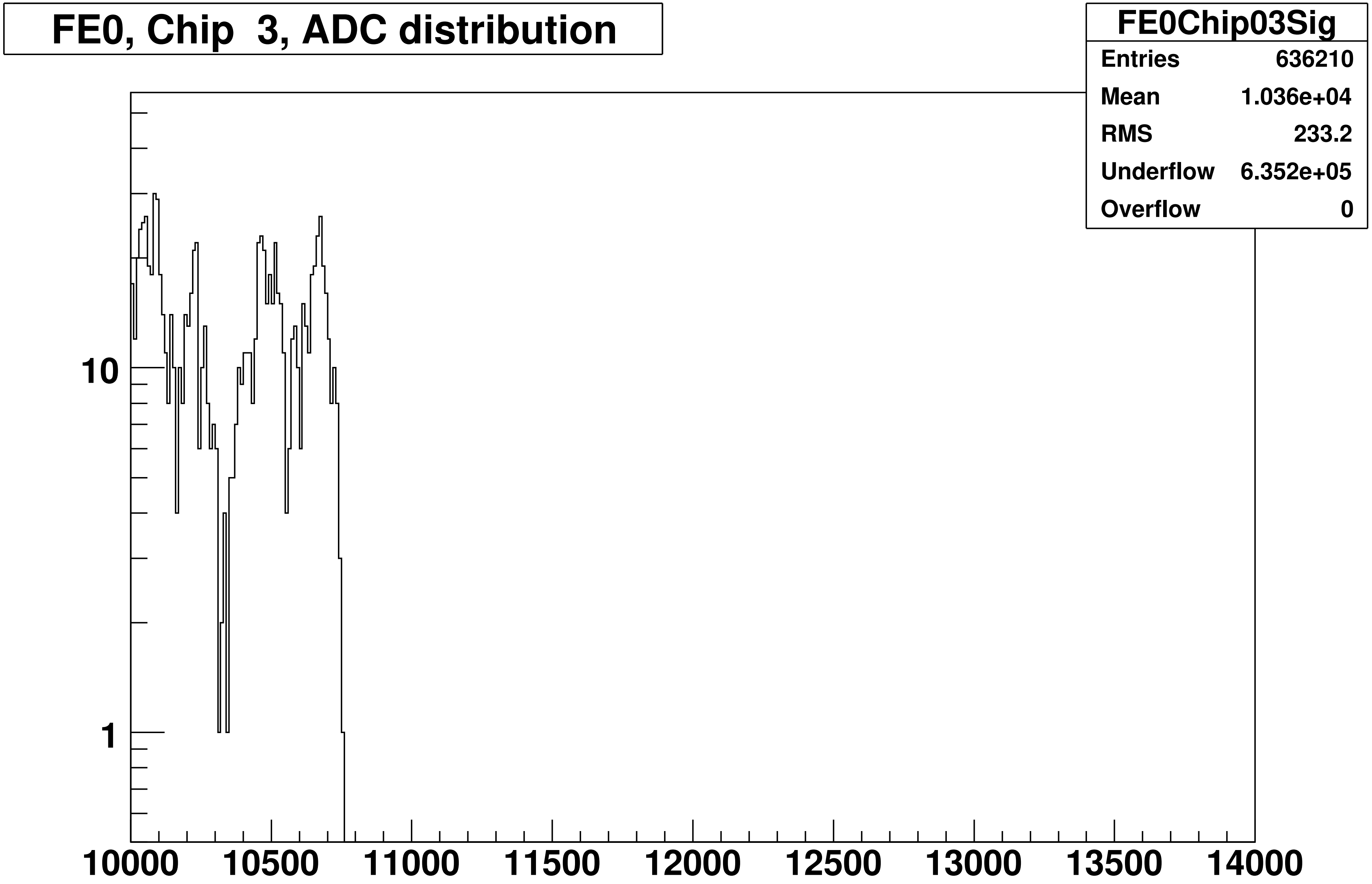
FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 636210
Mean 1.28e+04
RMS 135
Underflow 6.094e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 636210
Mean 587.5
RMS 2650
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

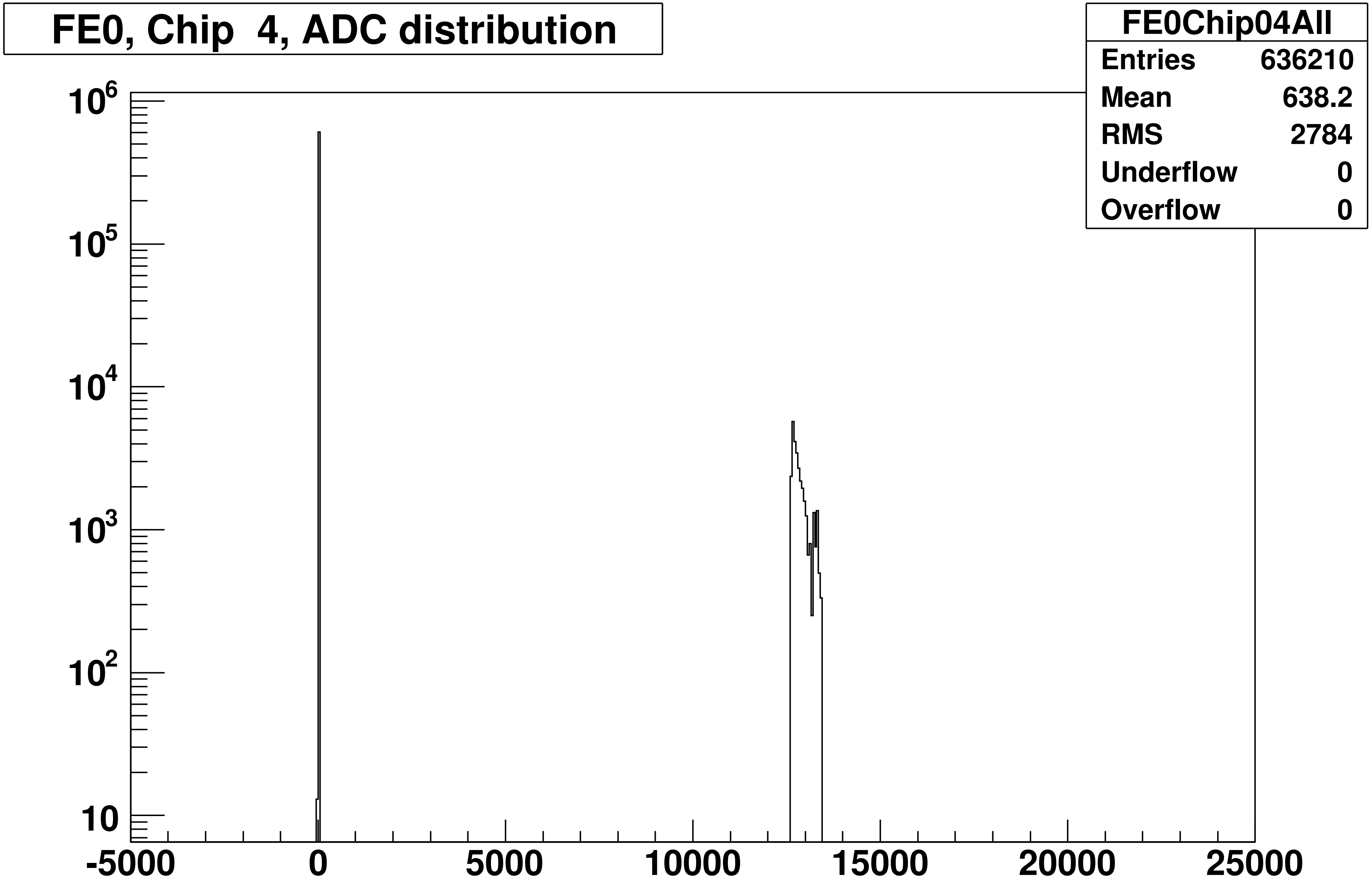

FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 636210
Mean 10.84
RMS 1.317
Underflow 0
Overflow 3.08e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

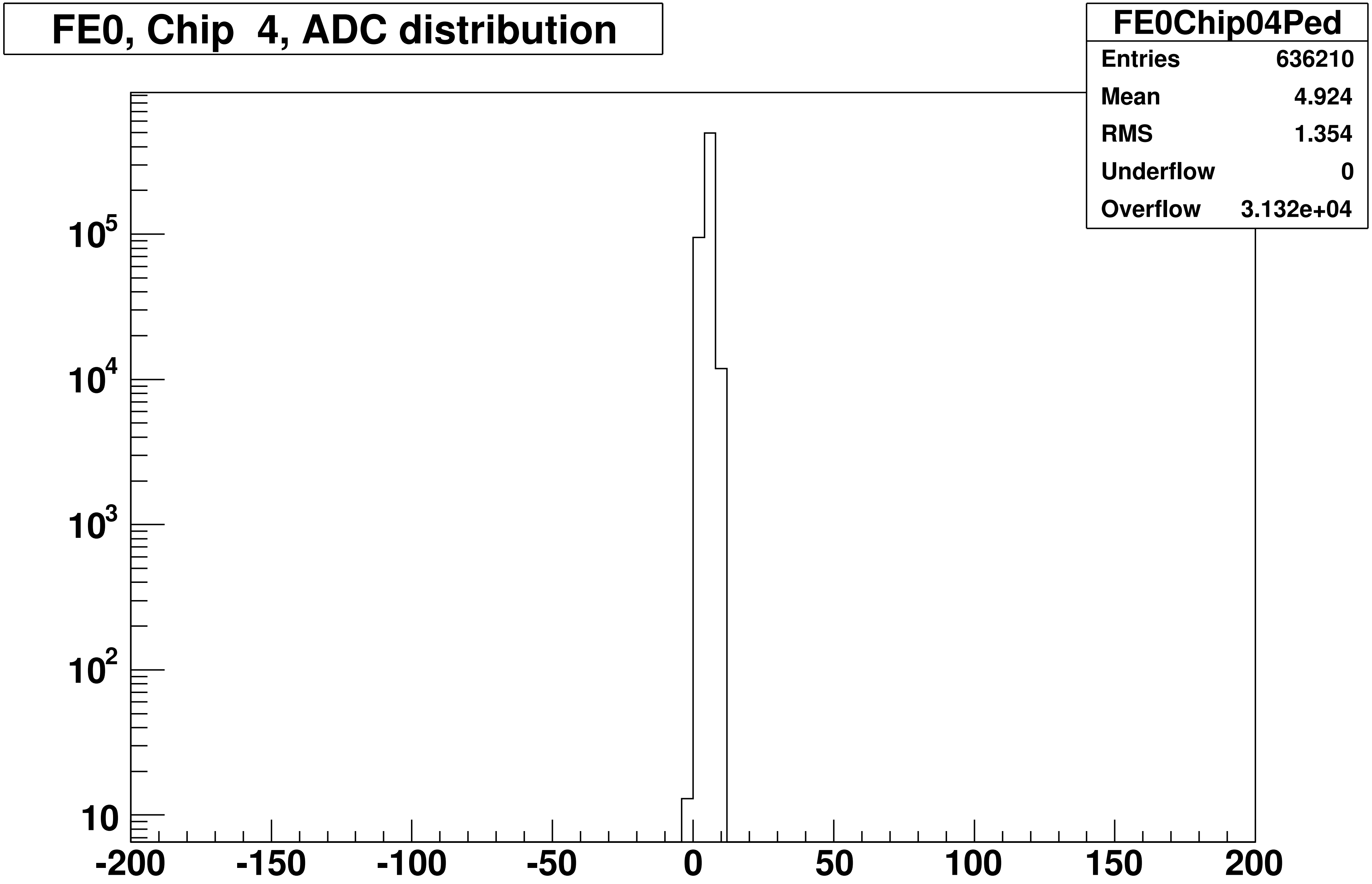

FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 636210
Mean 1.28e+04
RMS 159.7
Underflow 6.076e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

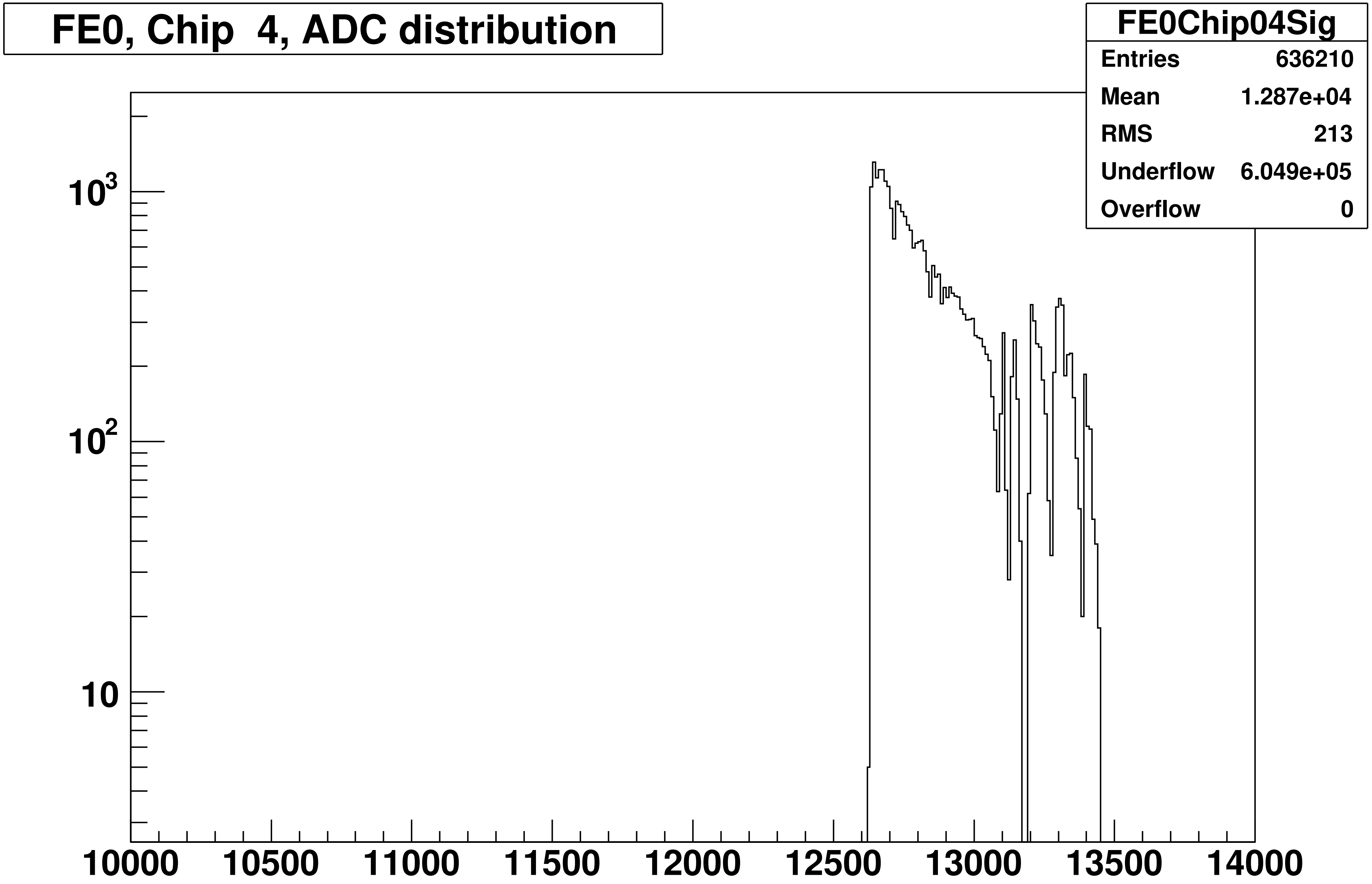

FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 636210
Mean 2755
RMS 3971
Underflow 5.718e+05
Overflow 0
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

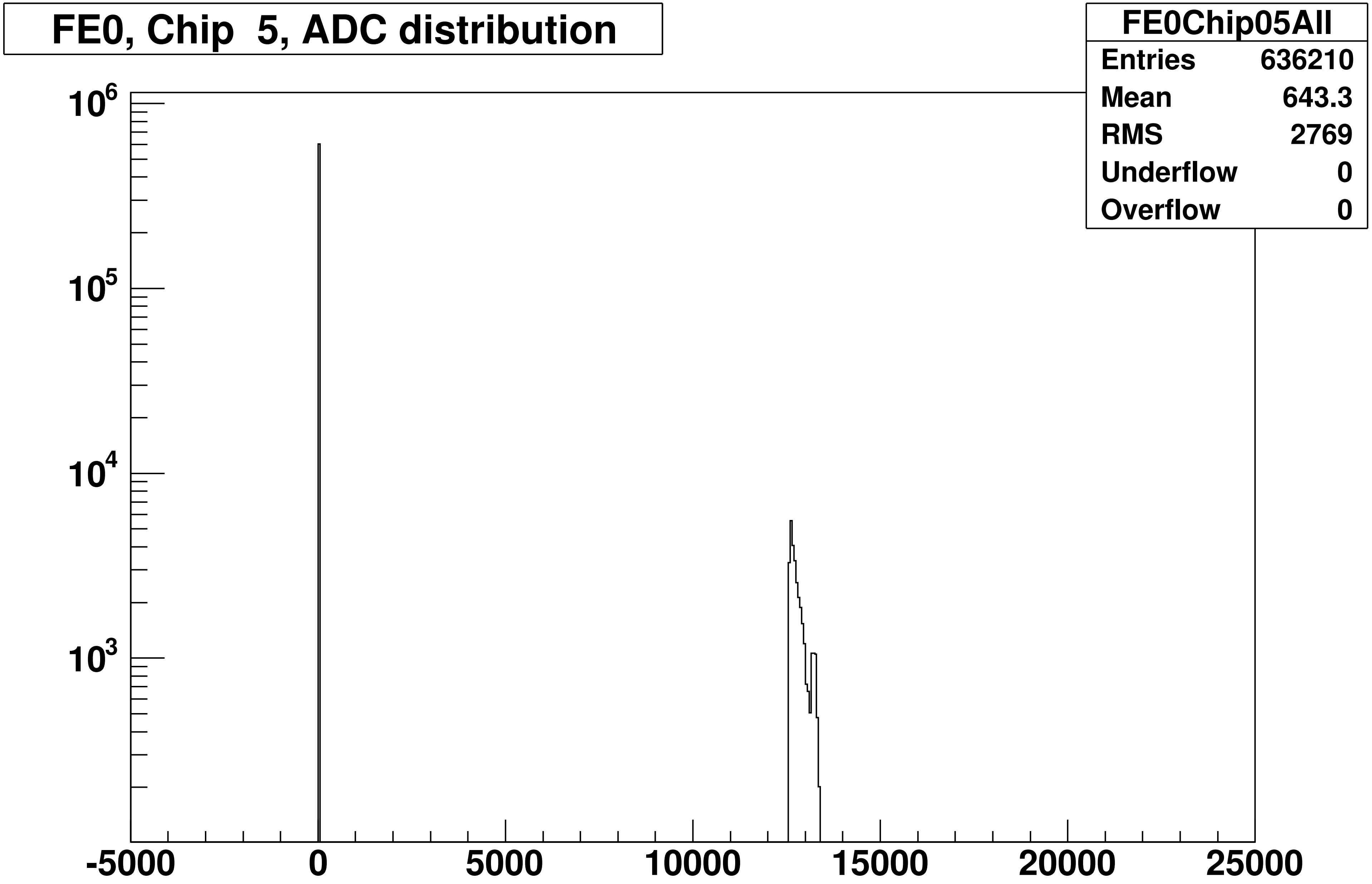


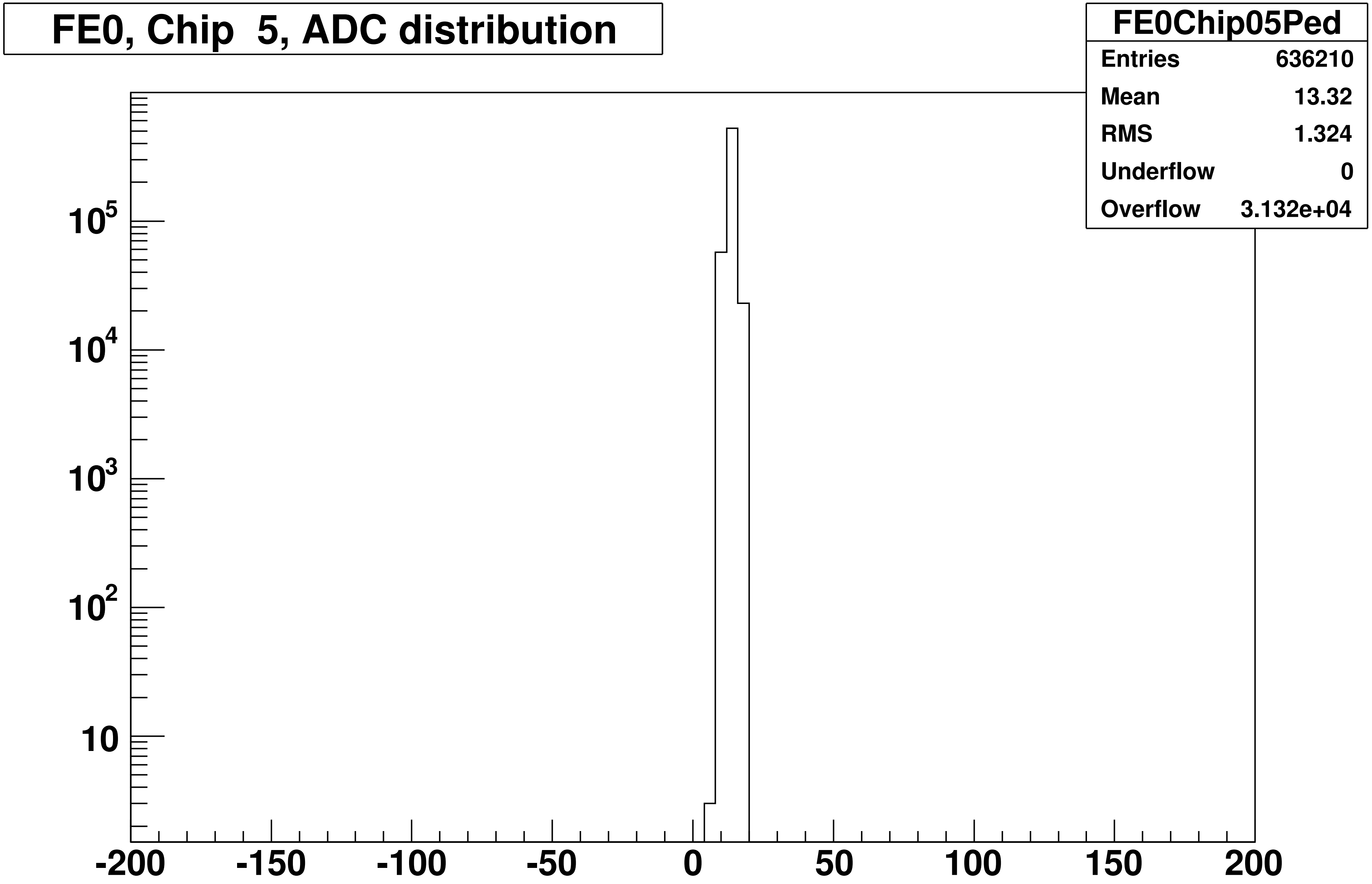
FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 636210
Mean 1.036e+04
RMS 233.2
Underflow 6.352e+05
Overflow 0
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

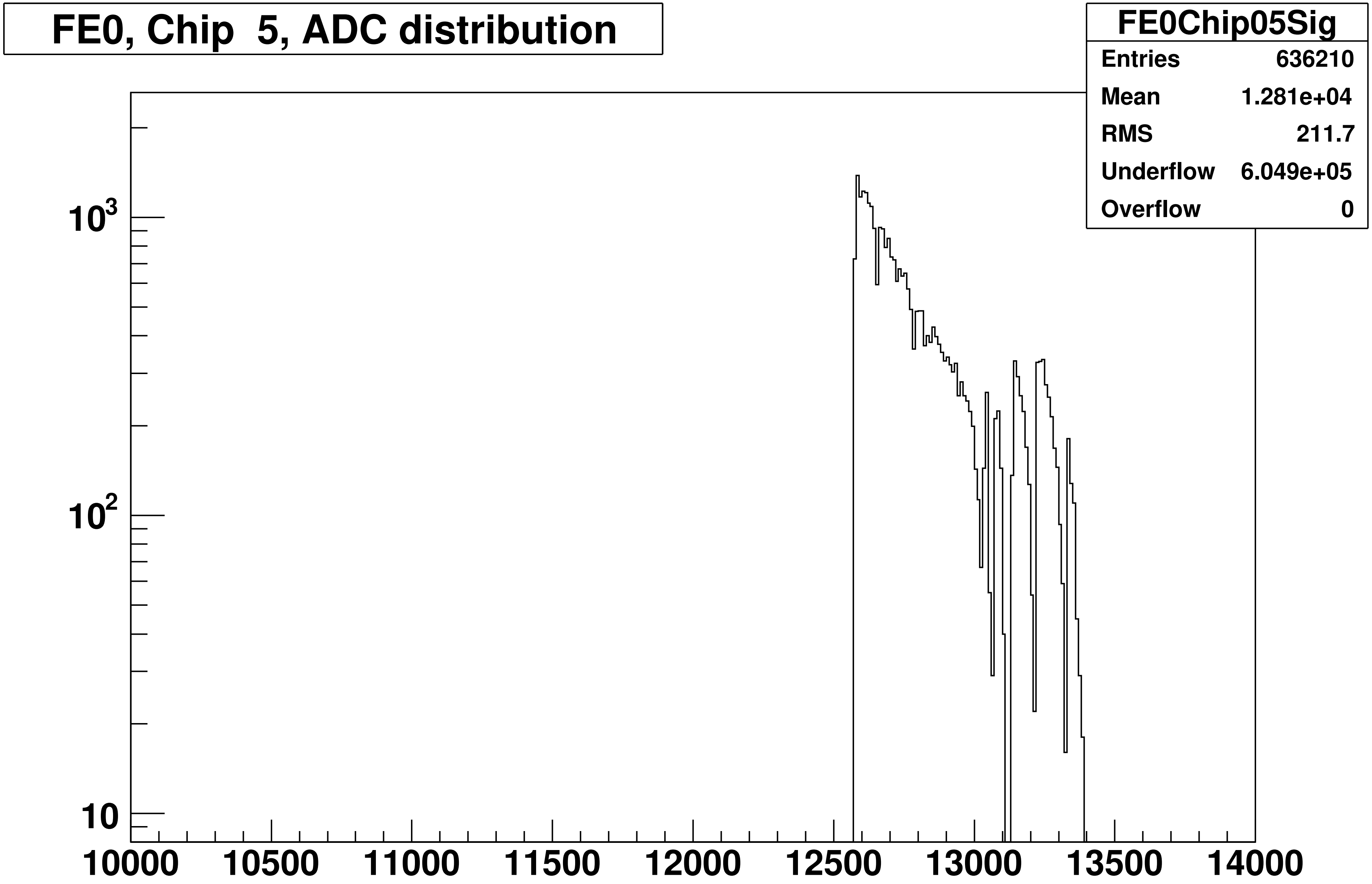

FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 636210
Mean 638.2
RMS 2784
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 636210
Mean 4.924
RMS 1.354
Underflow 0
Overflow 3.132e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

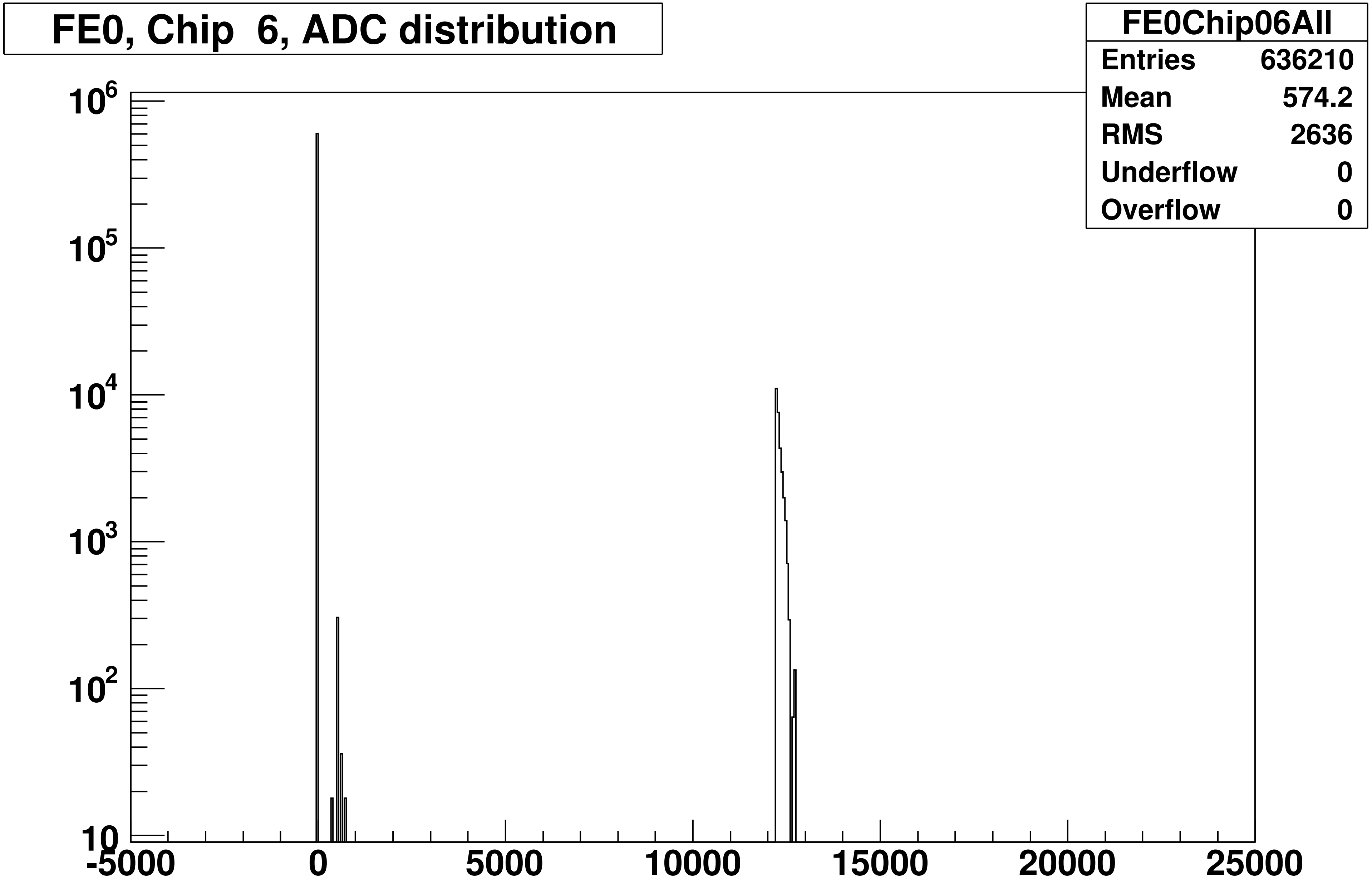

FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 636210
Mean 1.287e+04
RMS 213
Underflow 6.049e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000

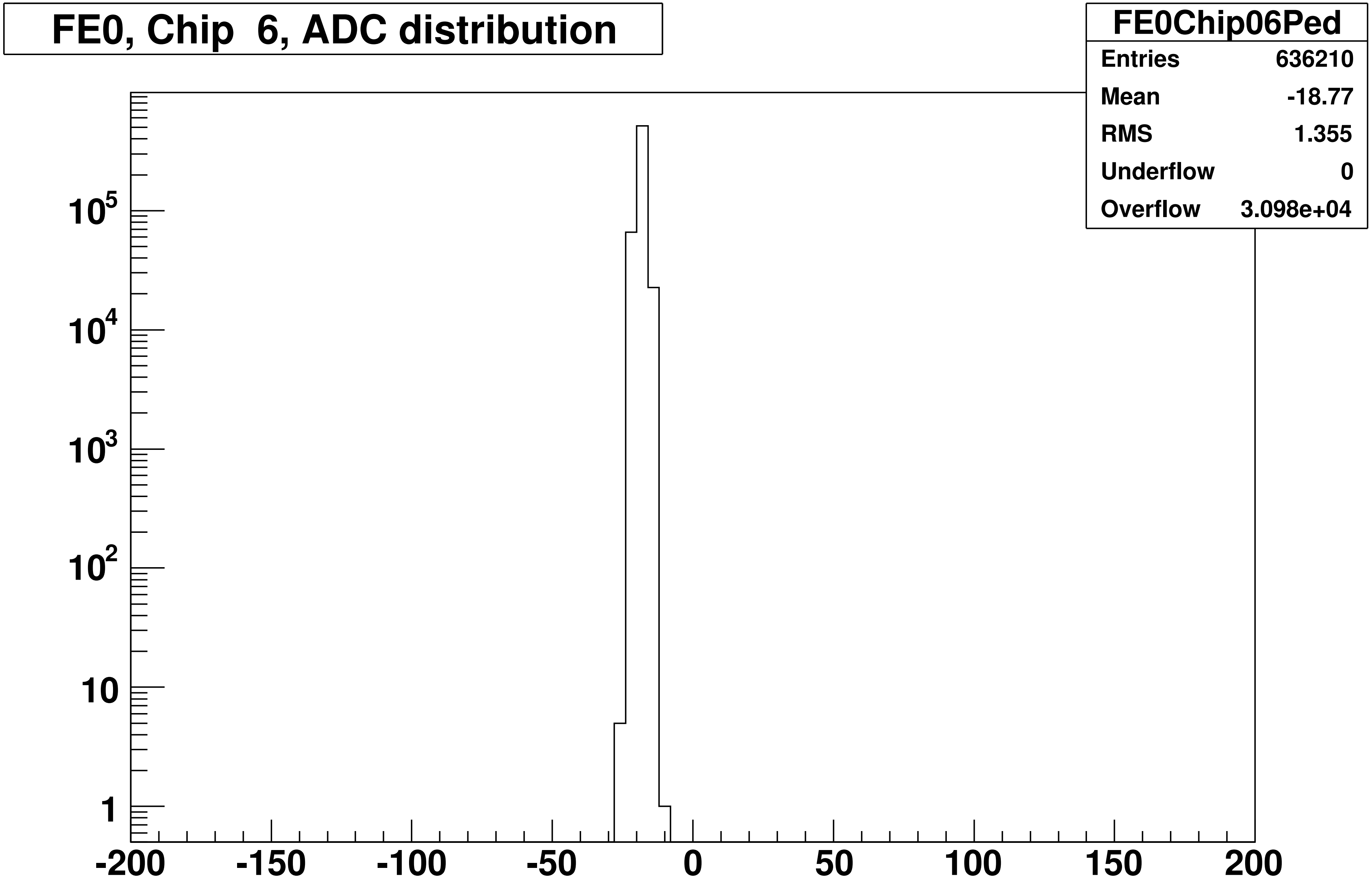

FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 636210
Mean 643.3
RMS 2769
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

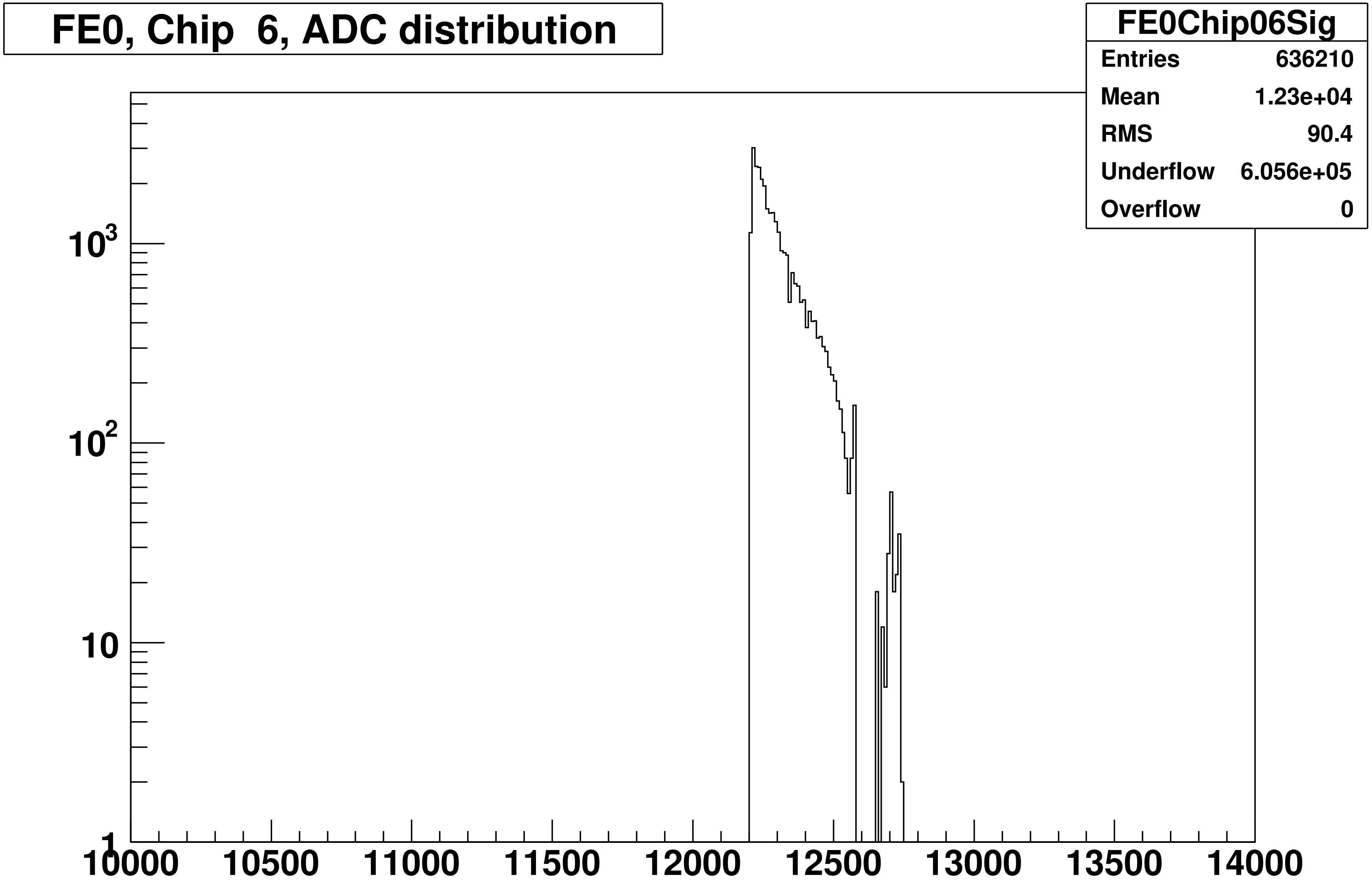

FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 636210
Mean 13.32
RMS 1.324
Underflow 0
Overflow 3.132e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

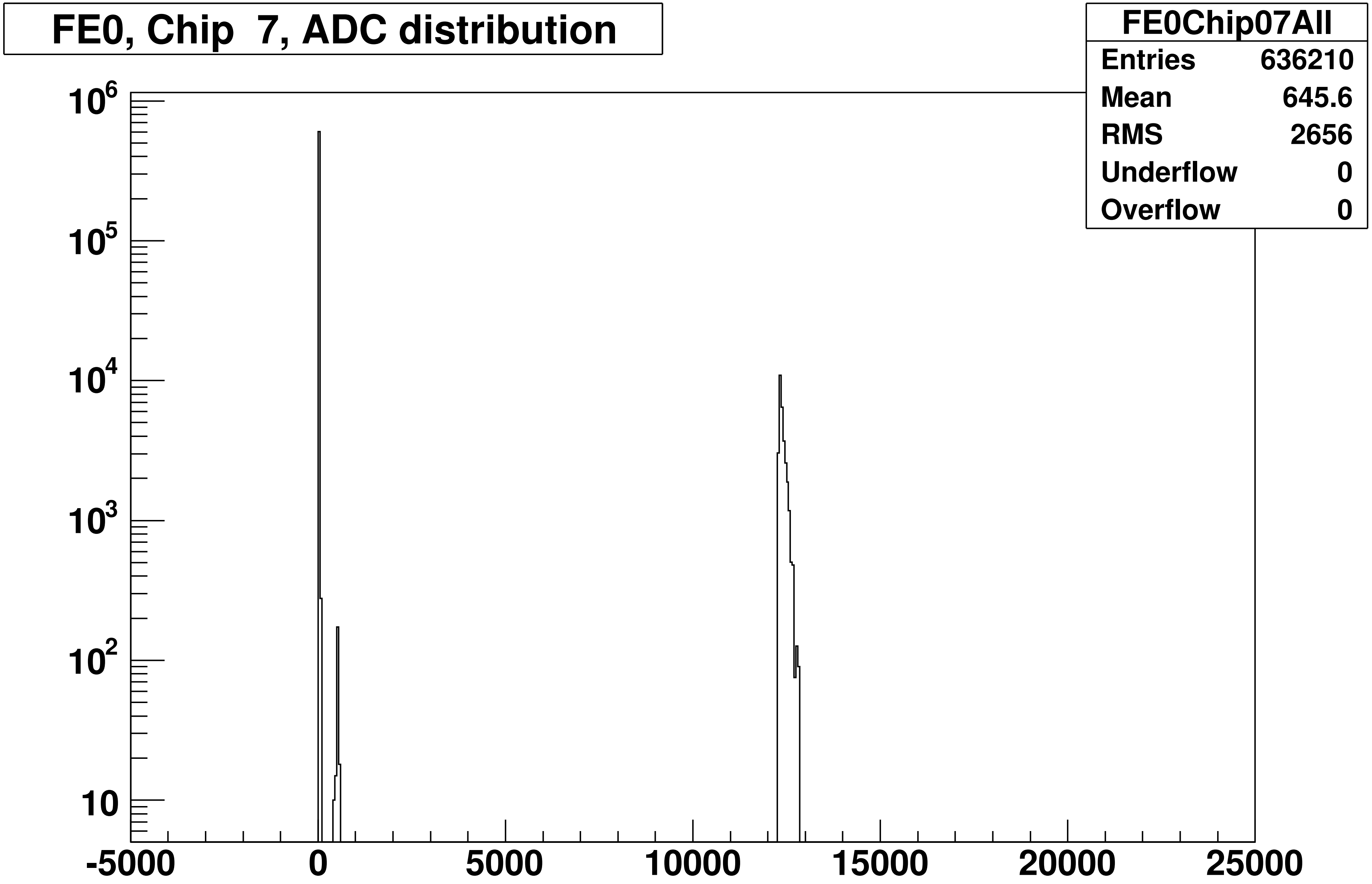


FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 636210
Mean 1.281e+04
RMS 211.7
Underflow 6.049e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000

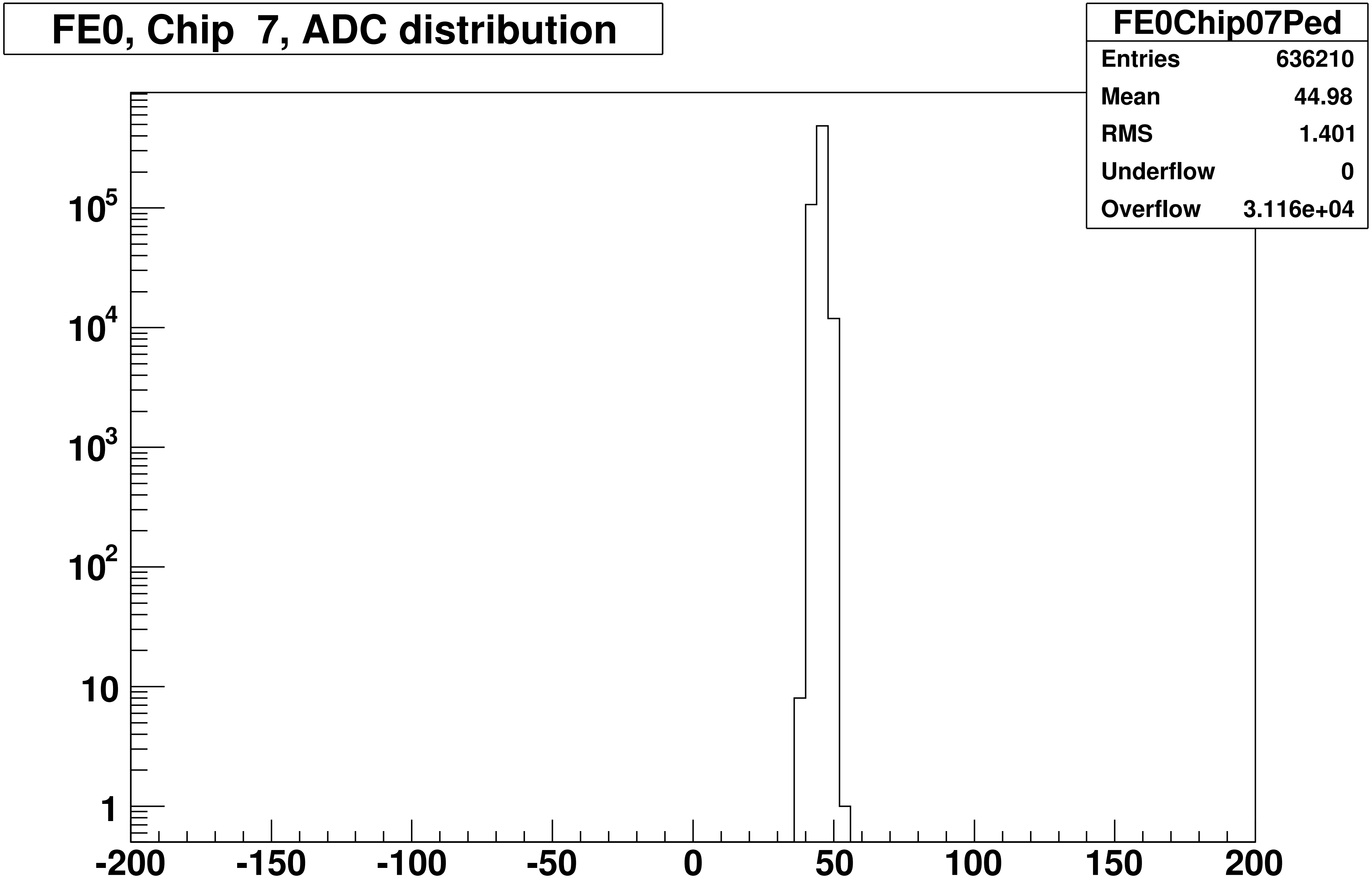

FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 636210
Mean 574.2
RMS 2636
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

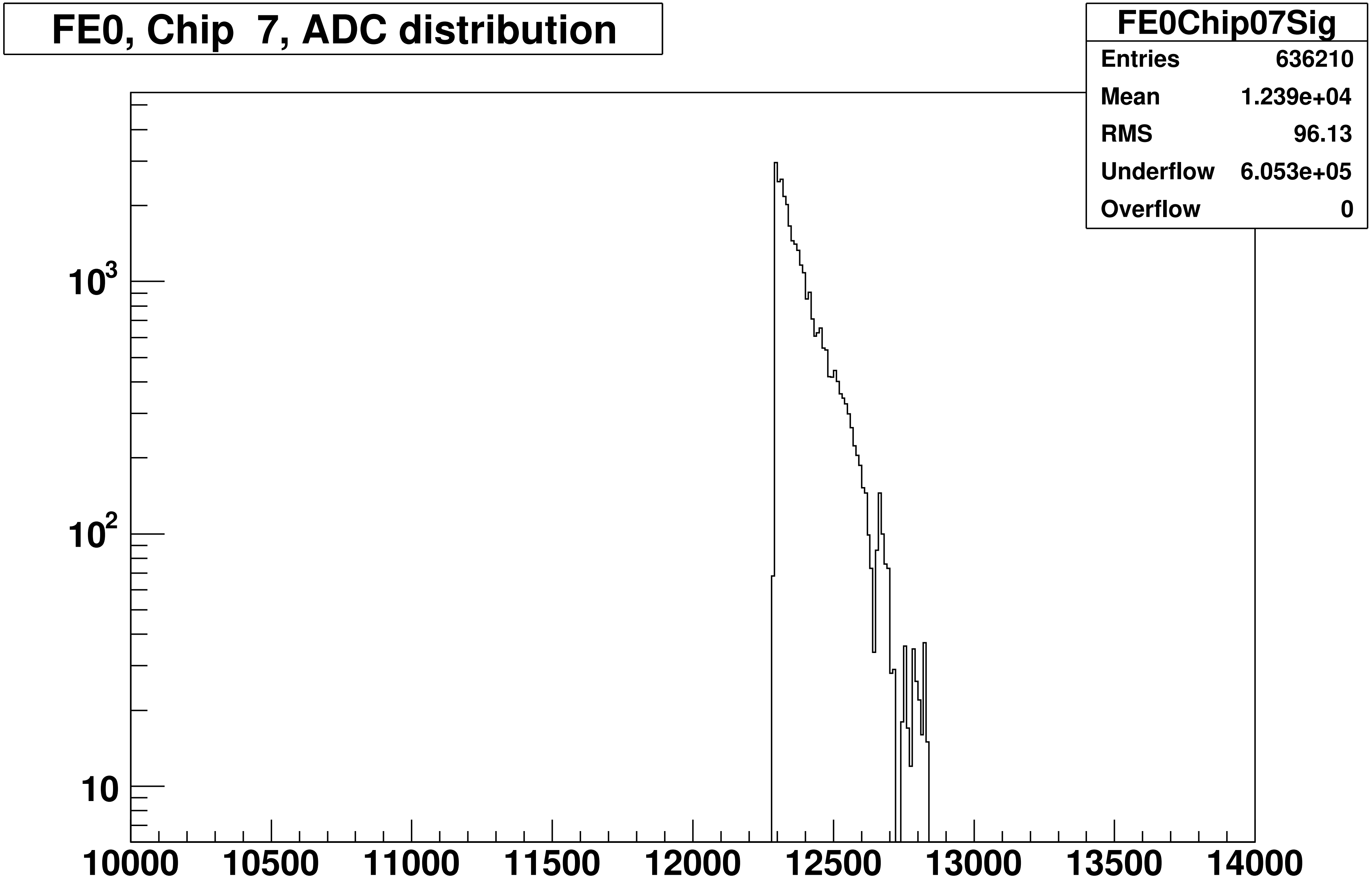

FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 636210
Mean -18.77
RMS 1.355
Underflow 0
Overflow 3.098e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 636210
Mean 1.23e+04
RMS 90.4
Underflow 6.056e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

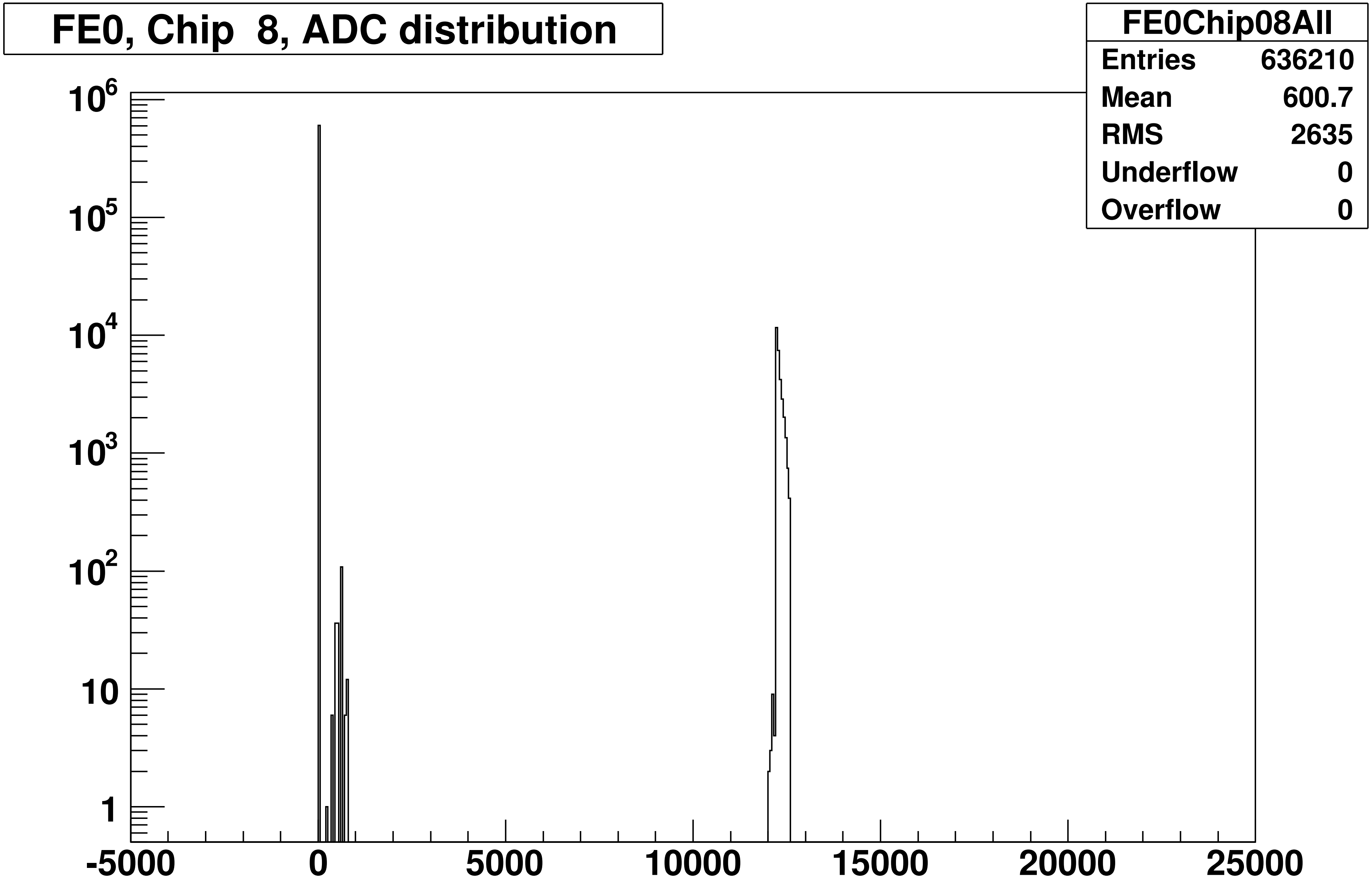


FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 636210
Mean 645.6
RMS 2656
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

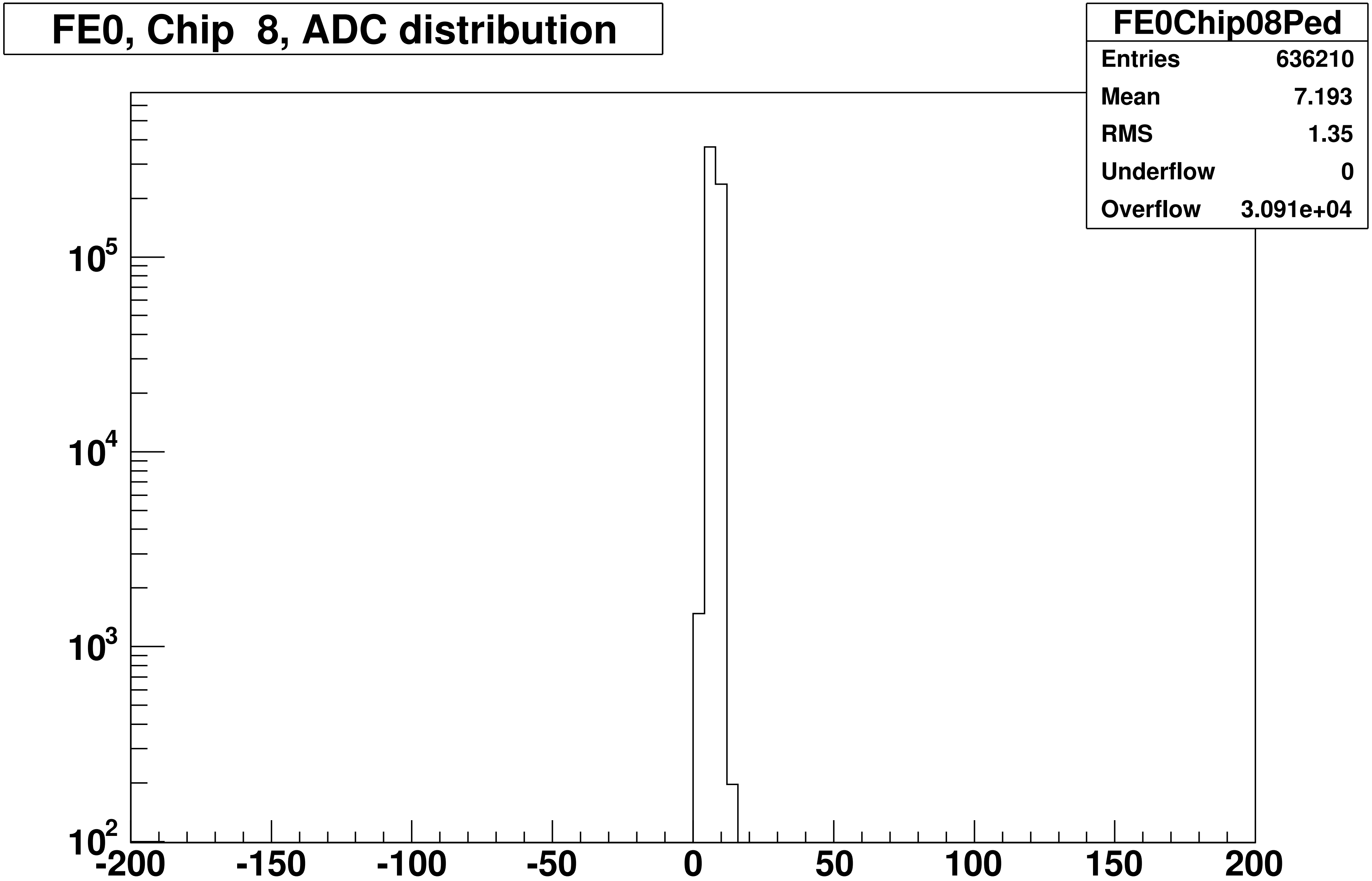

FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 636210
Mean 44.98
RMS 1.401
Underflow 0
Overflow 3.116e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



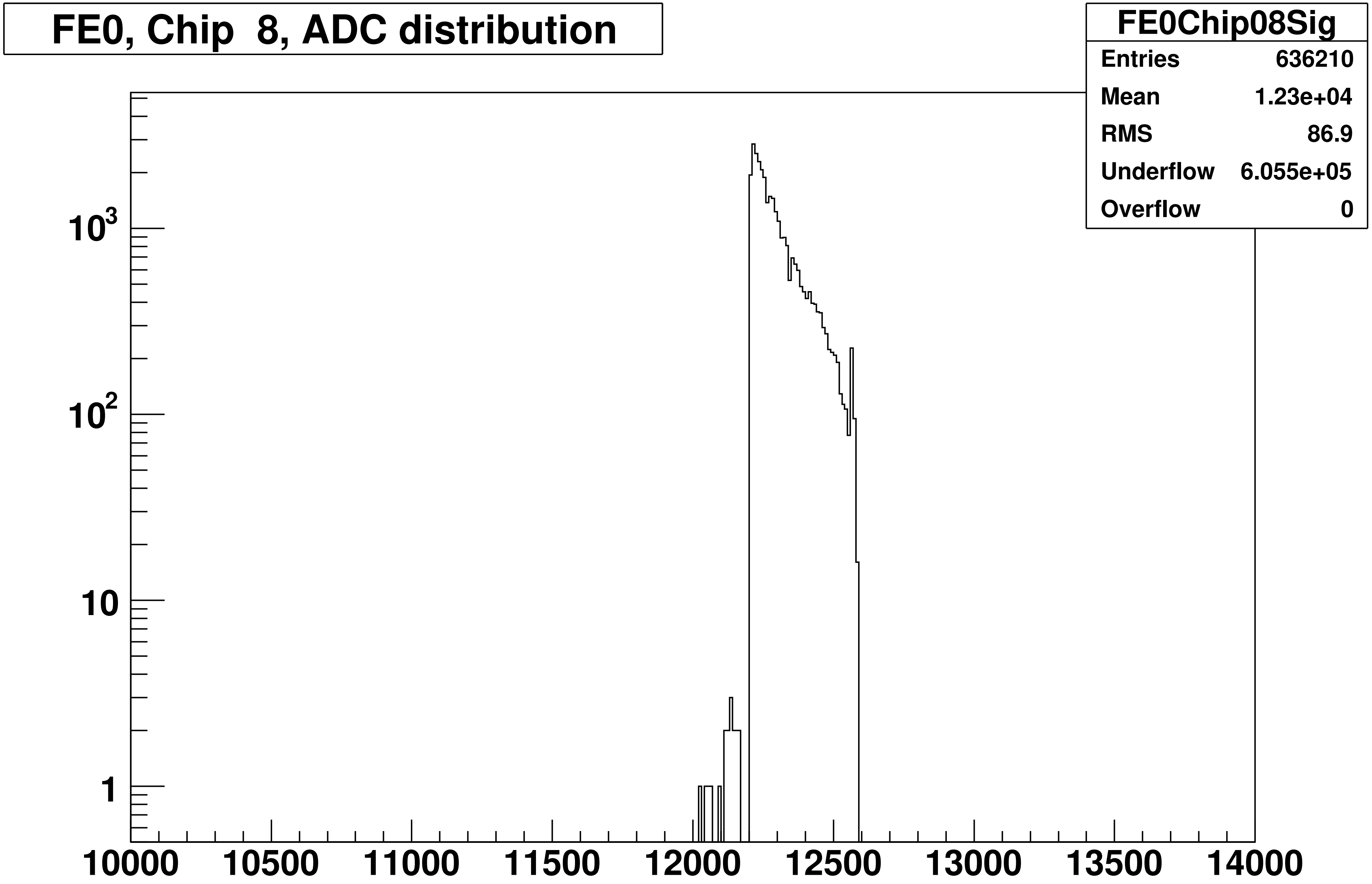
FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 636210
Mean 1.239e+04
RMS 96.13
Underflow 6.053e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000



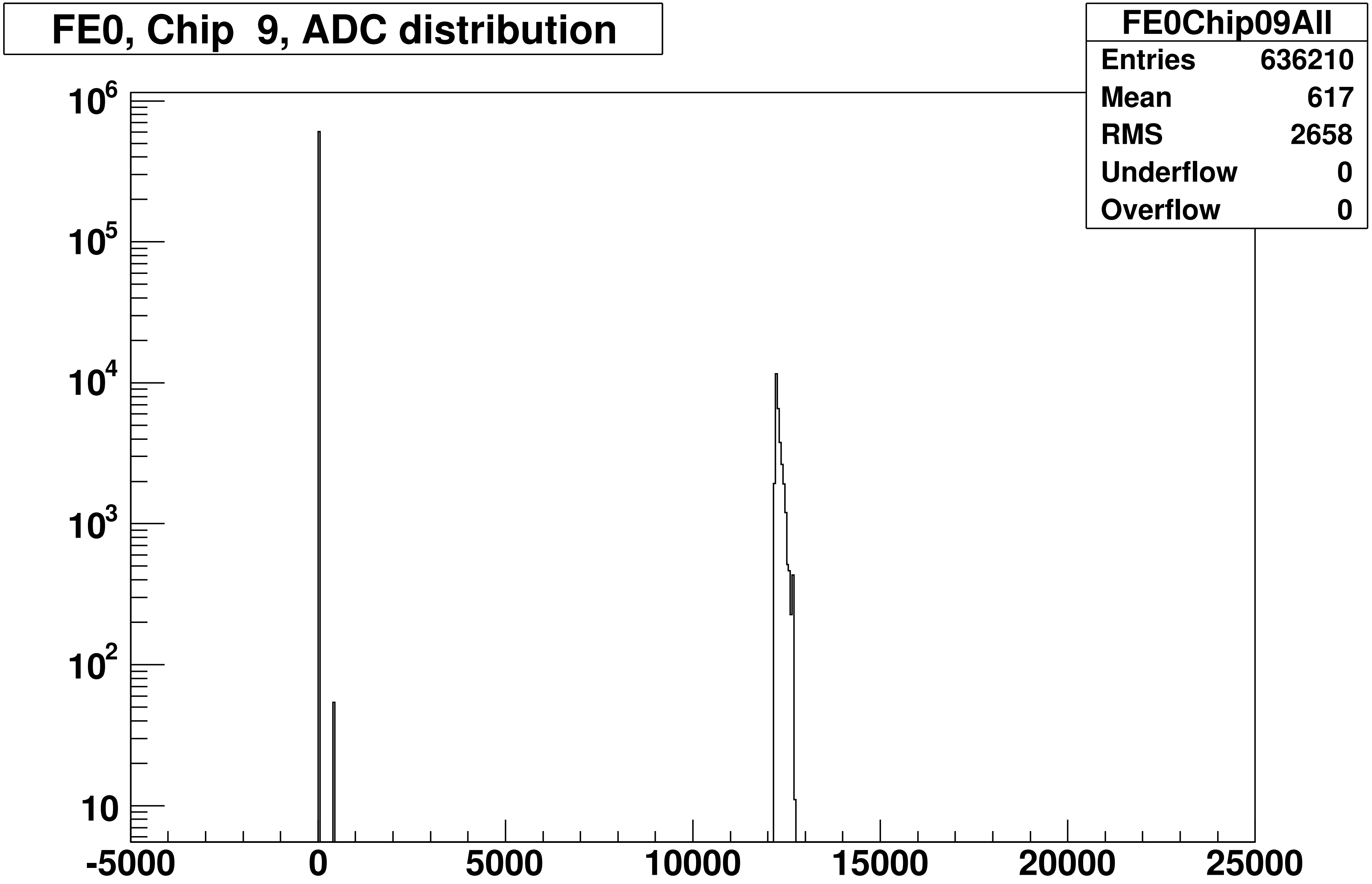
FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 636210
Mean 600.7
RMS 2635
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



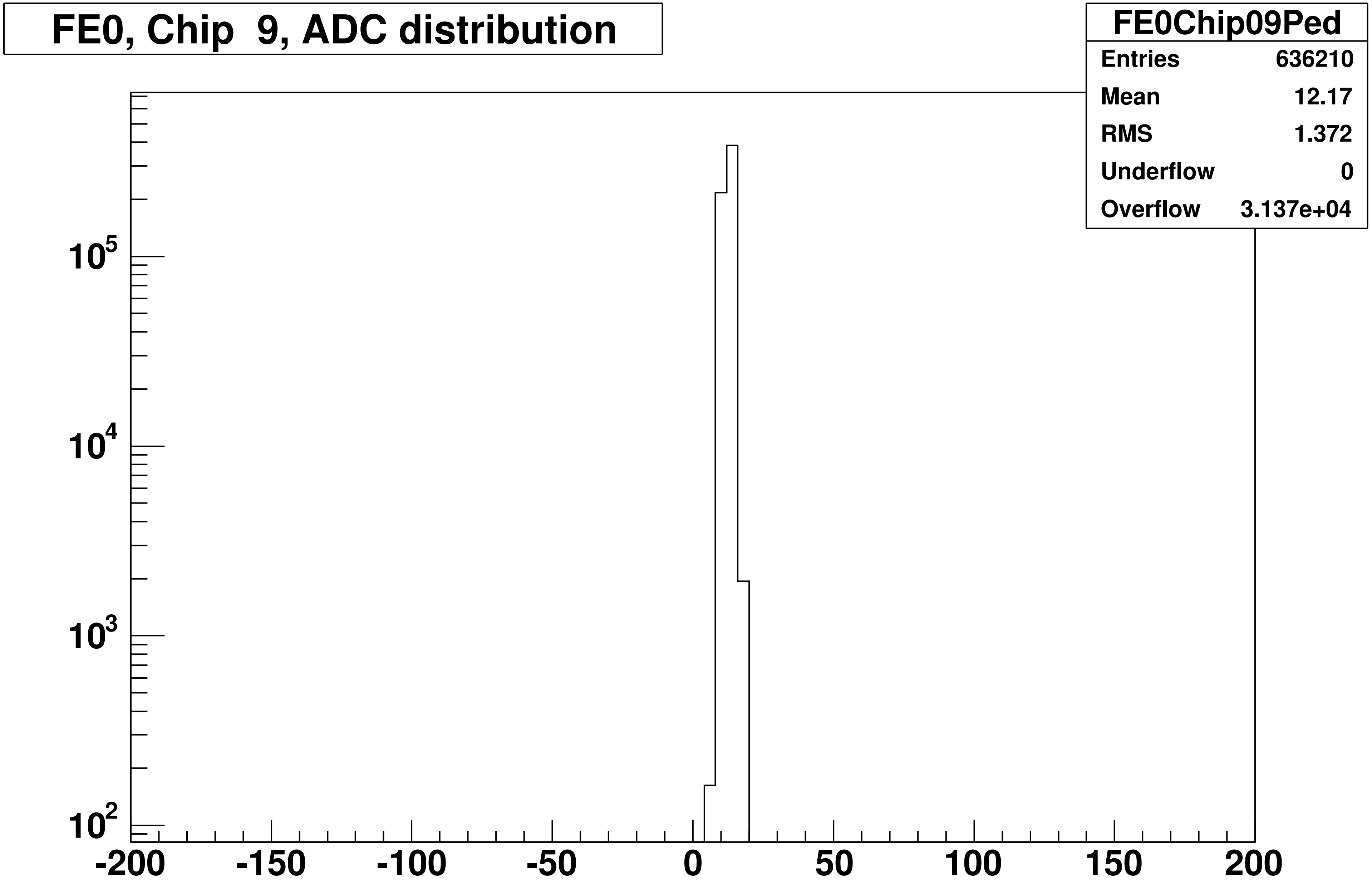
FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 636210
Mean 7.193
RMS 1.35
Underflow 0
Overflow 3.091e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

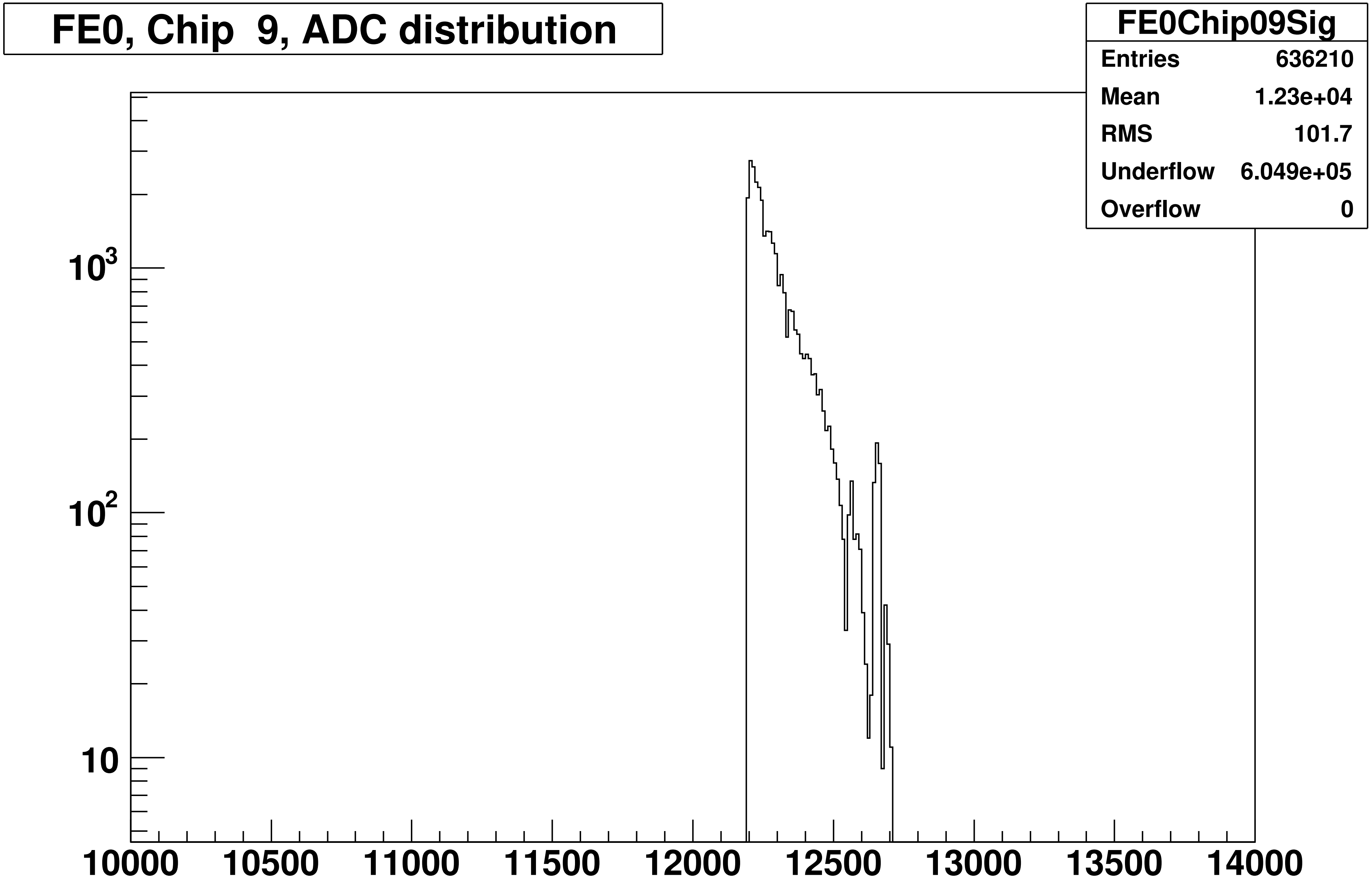


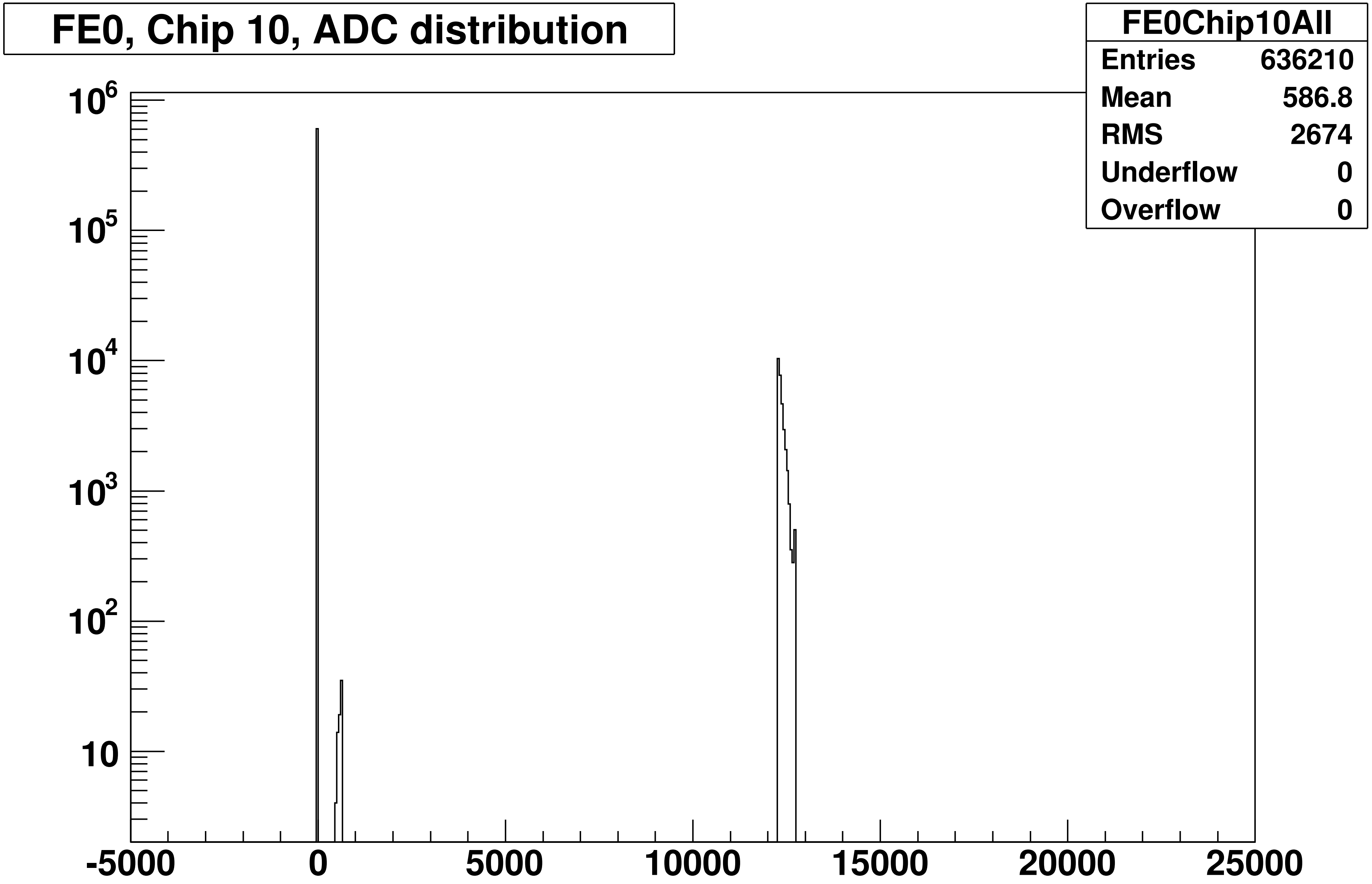
FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 636210
Mean 1.23e+04
RMS 86.9
Underflow 6.055e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

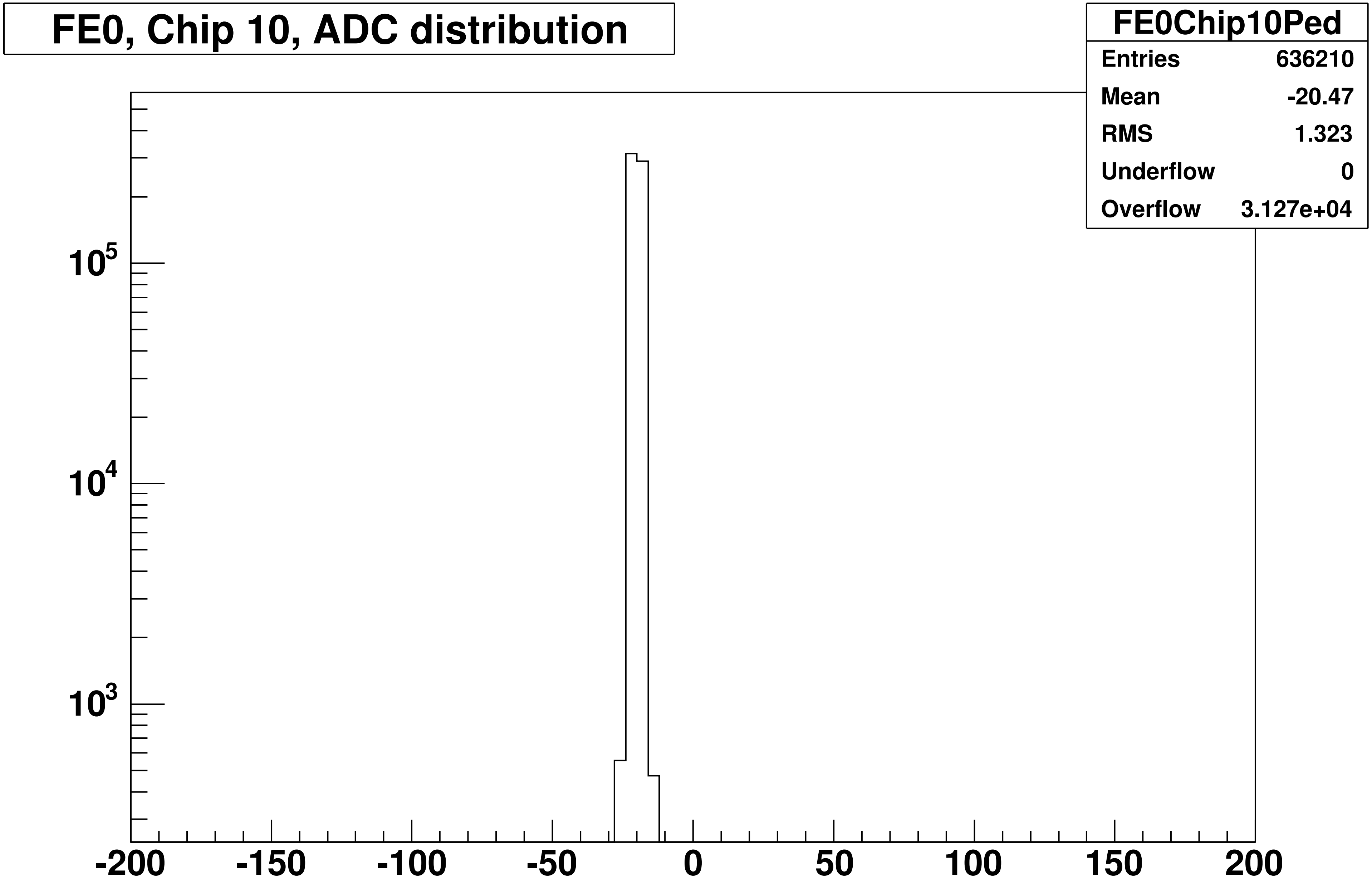


FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 636210
Mean 617
RMS 2658
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

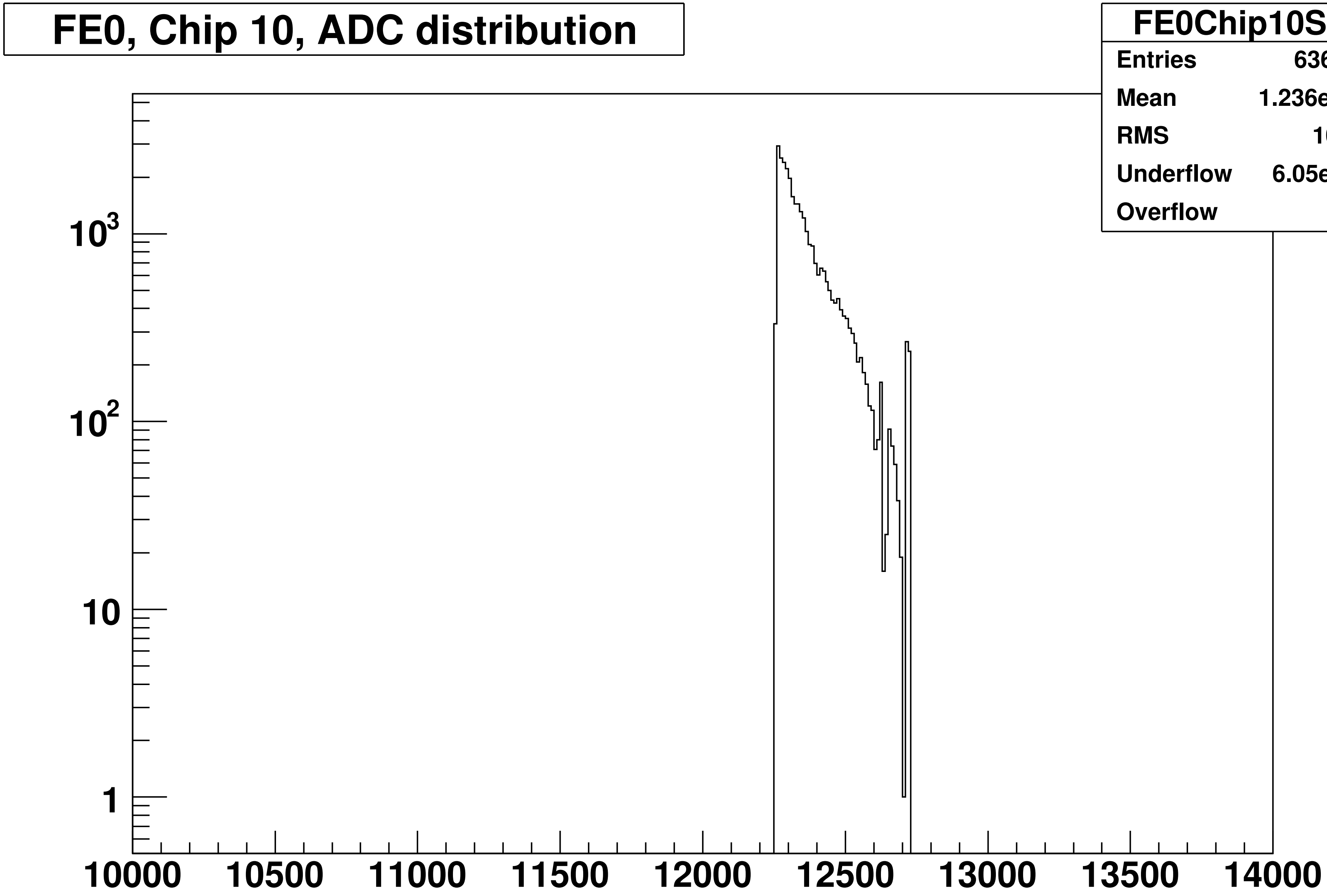

FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 636210
Mean 12.17
RMS 1.372
Underflow 0
Overflow 3.137e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

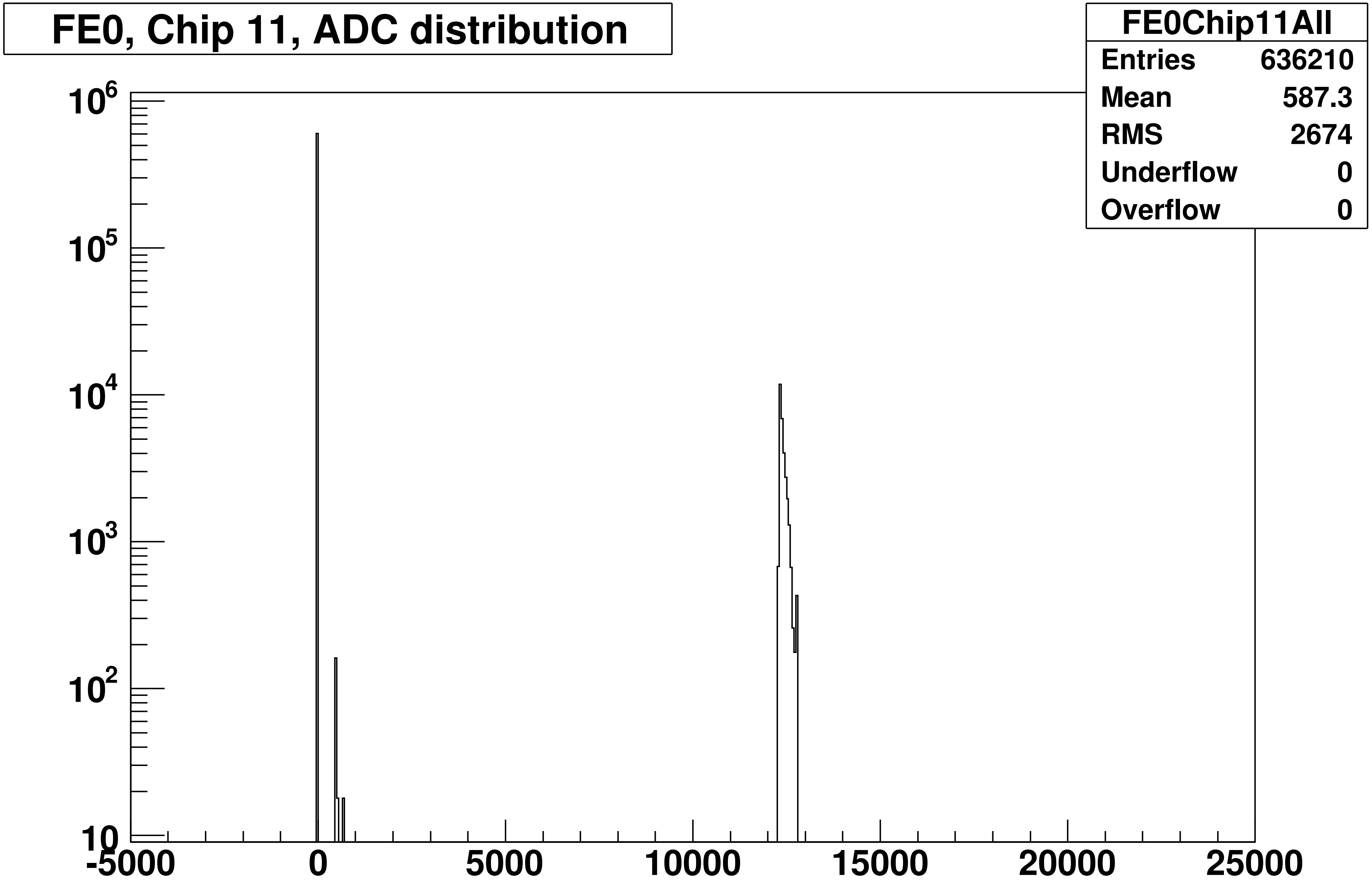

FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 636210
Mean 1.23e+04
RMS 101.7
Underflow 6.049e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

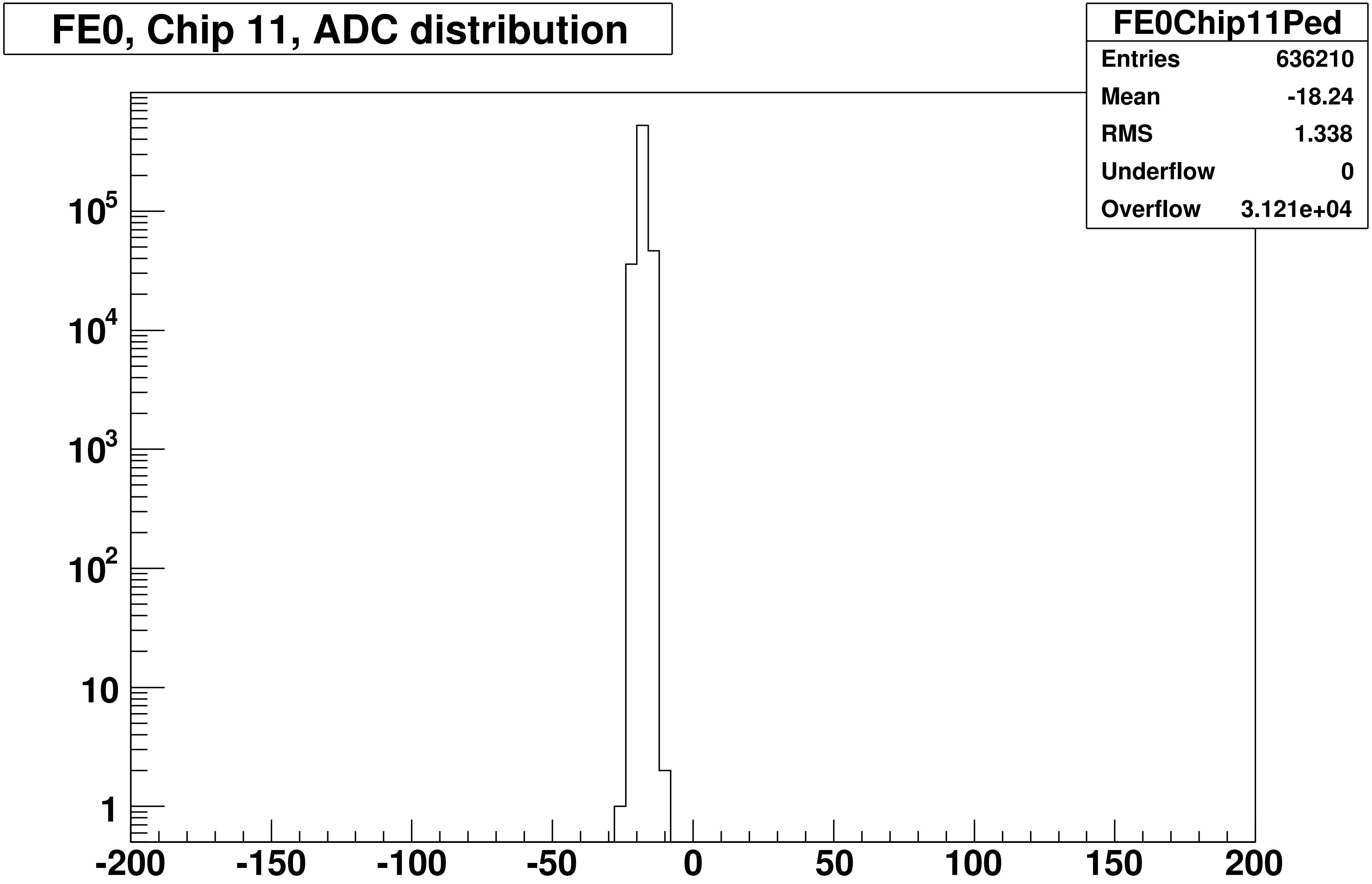

FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 636210
Mean 586.8
RMS 2674
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



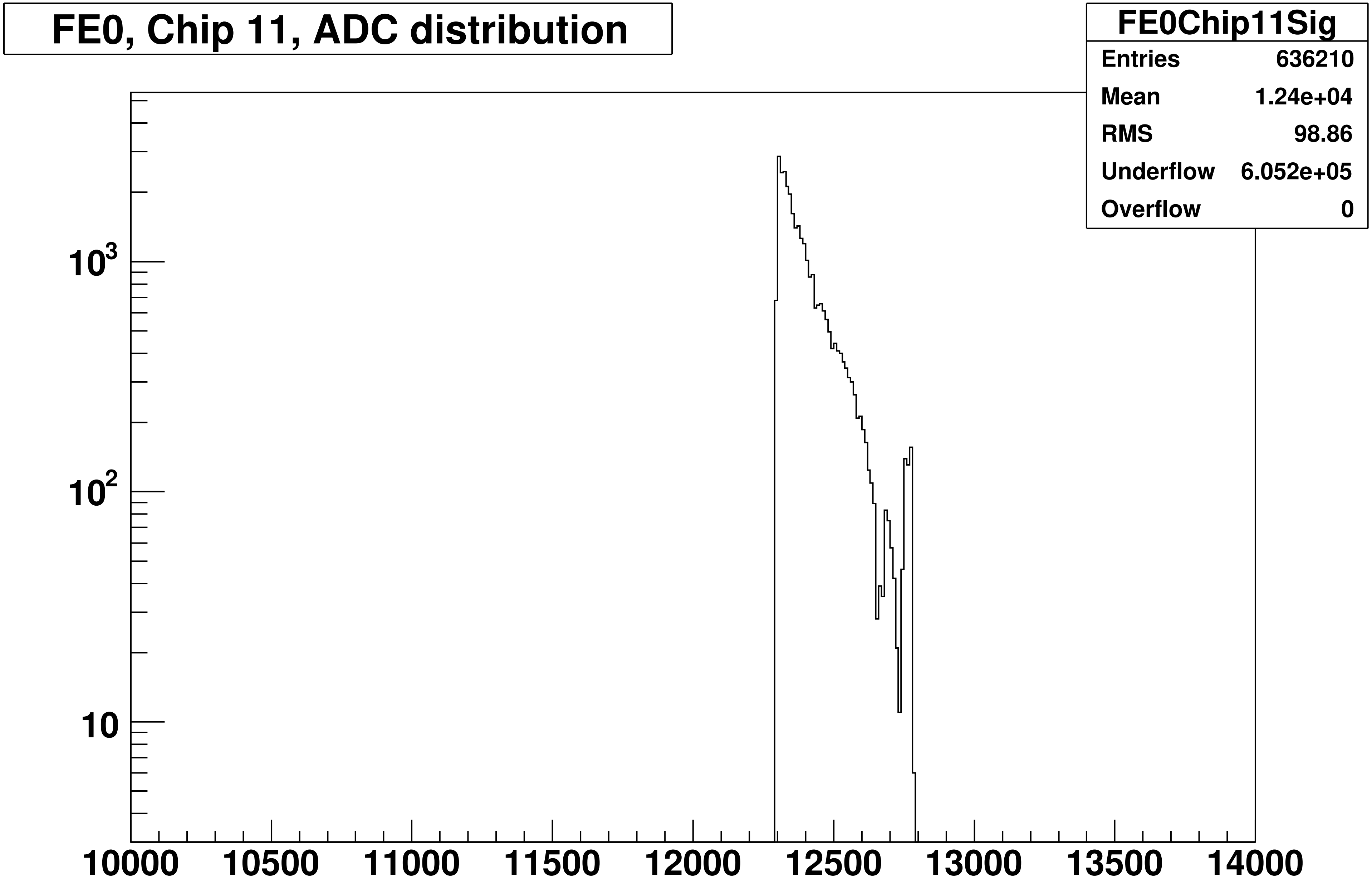
FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 636210
Mean -20.47
RMS 1.323
Underflow 0
Overflow 3.127e+04
10^5
10^4
10^3
-200 -150 -100 -50 0 50 100 150 200

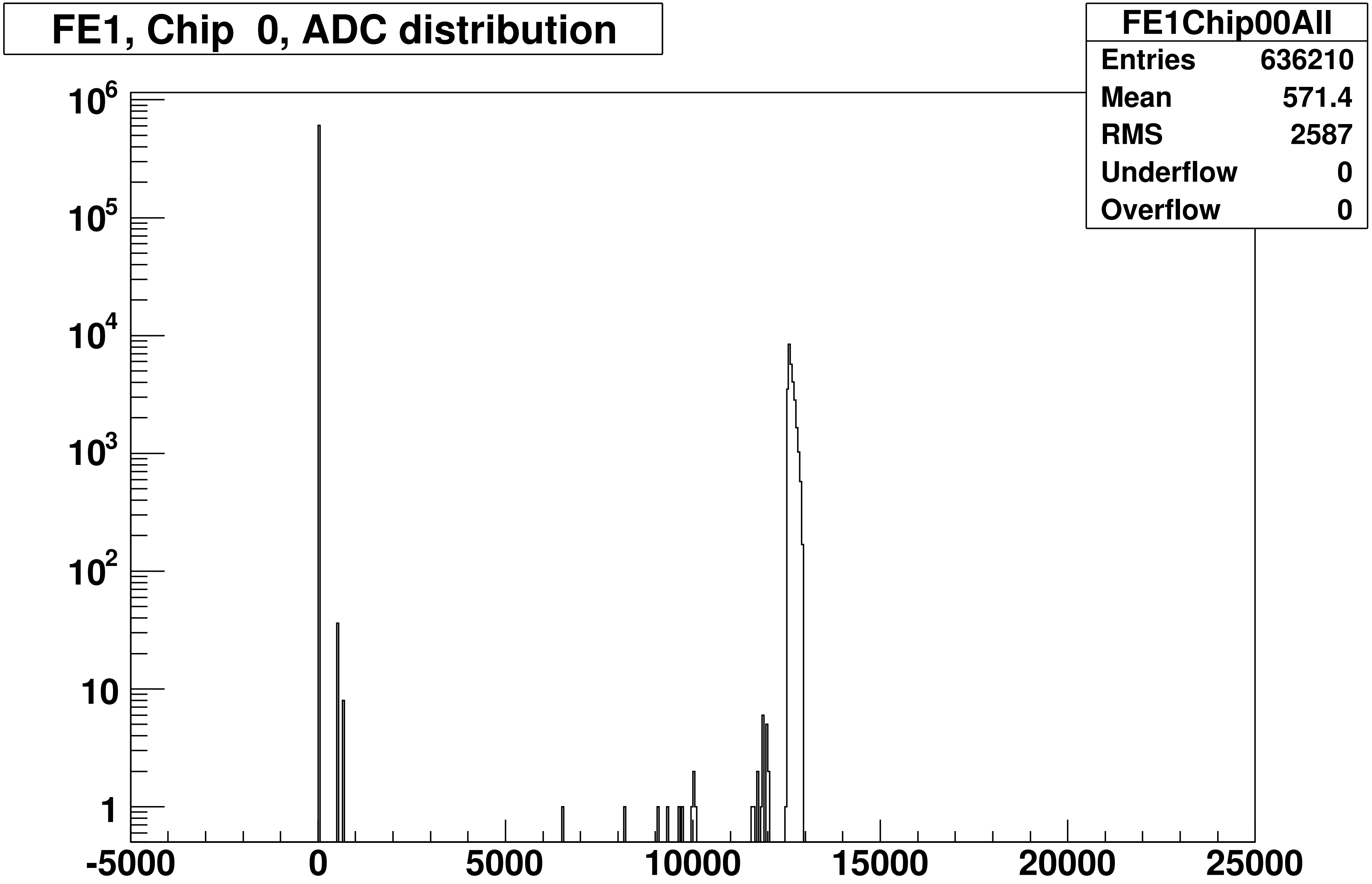

FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 636210
Mean 1.236e+04
RMS 100.4
Underflow 6.05e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 636210
Mean 587.3
RMS 2674
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

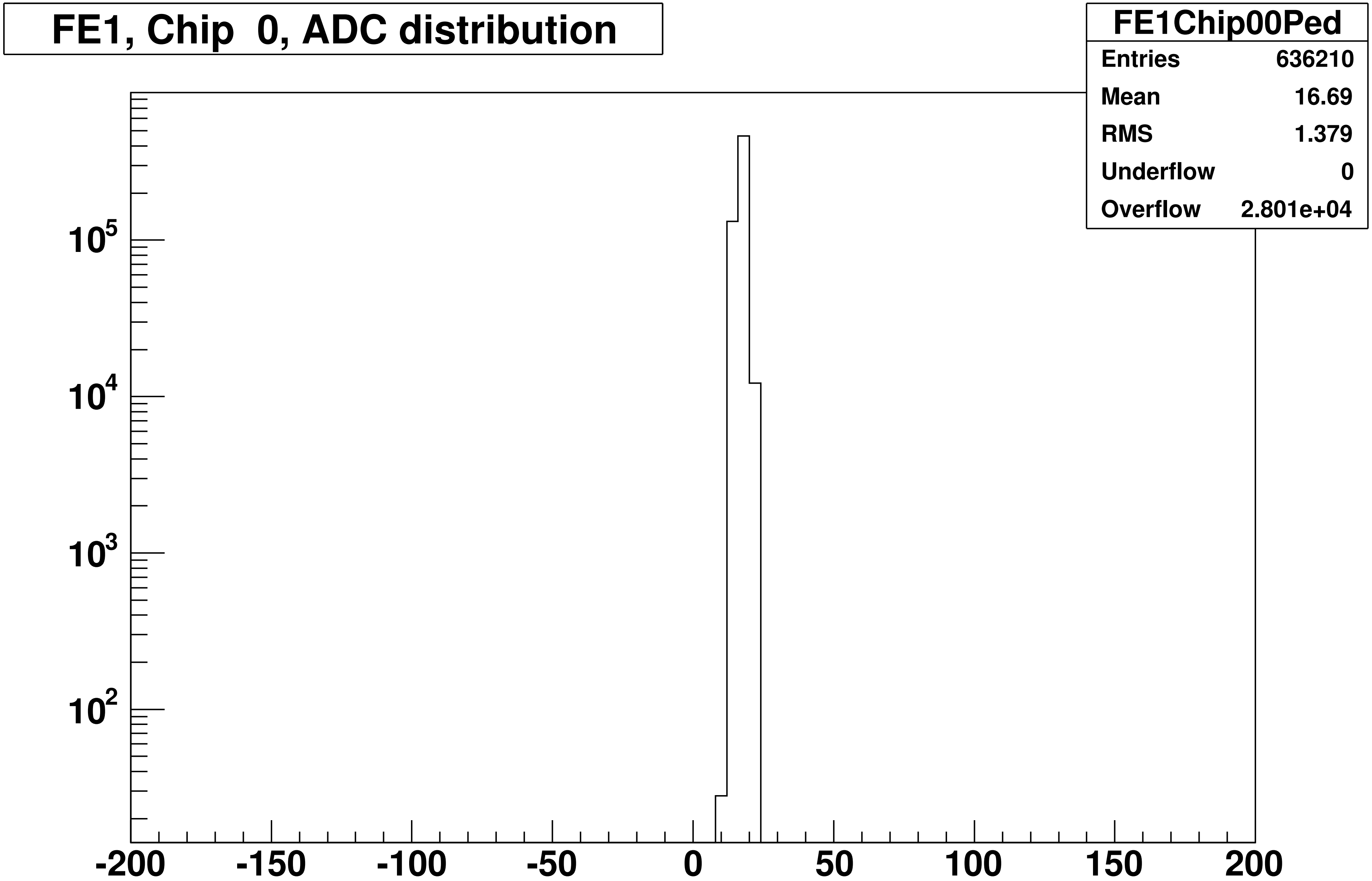


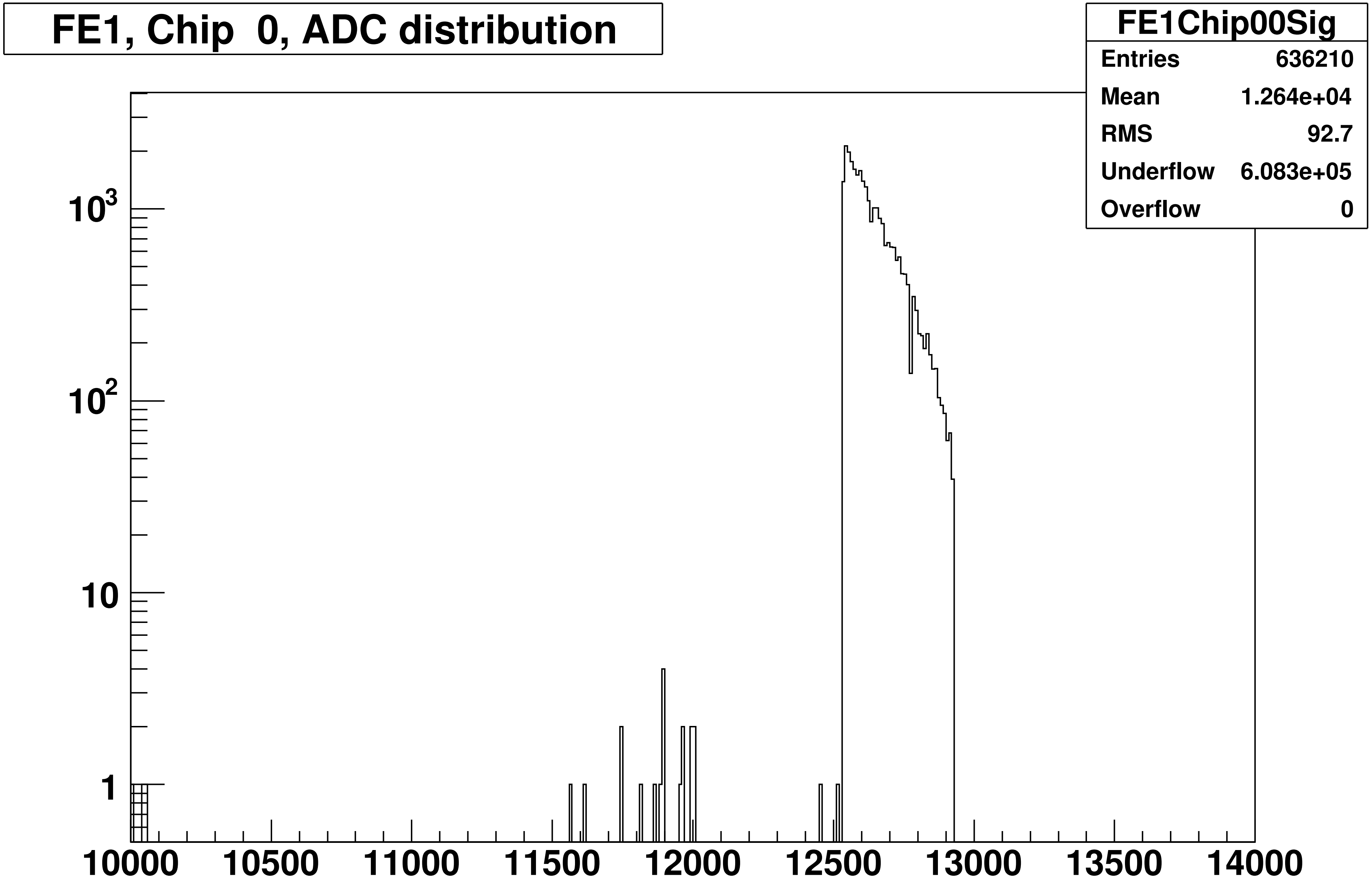
FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 636210
Mean -18.24
RMS 1.338
Underflow 0
Overflow 3.121e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

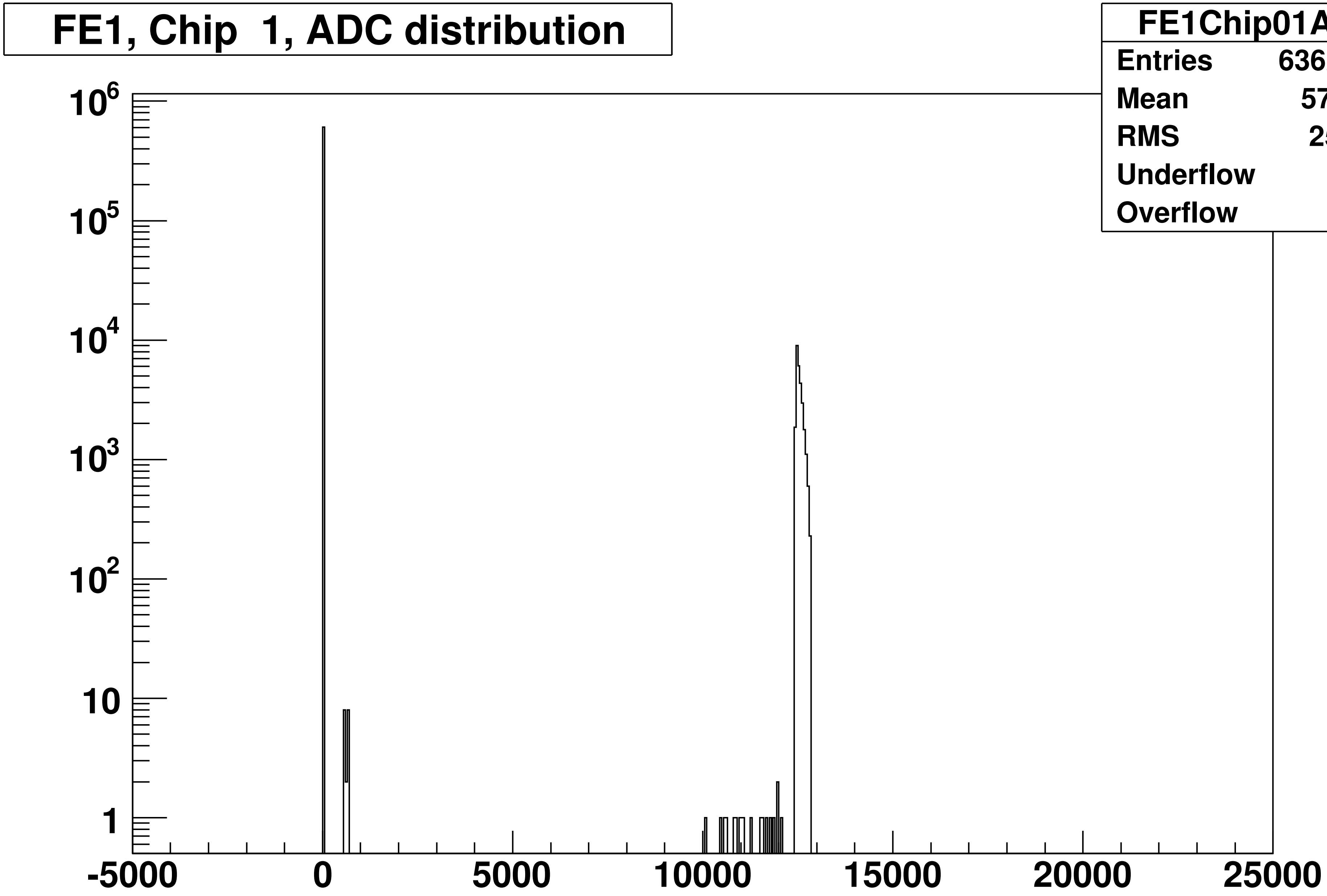

FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 636210
Mean 1.24e+04
RMS 98.86
Underflow 6.052e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000



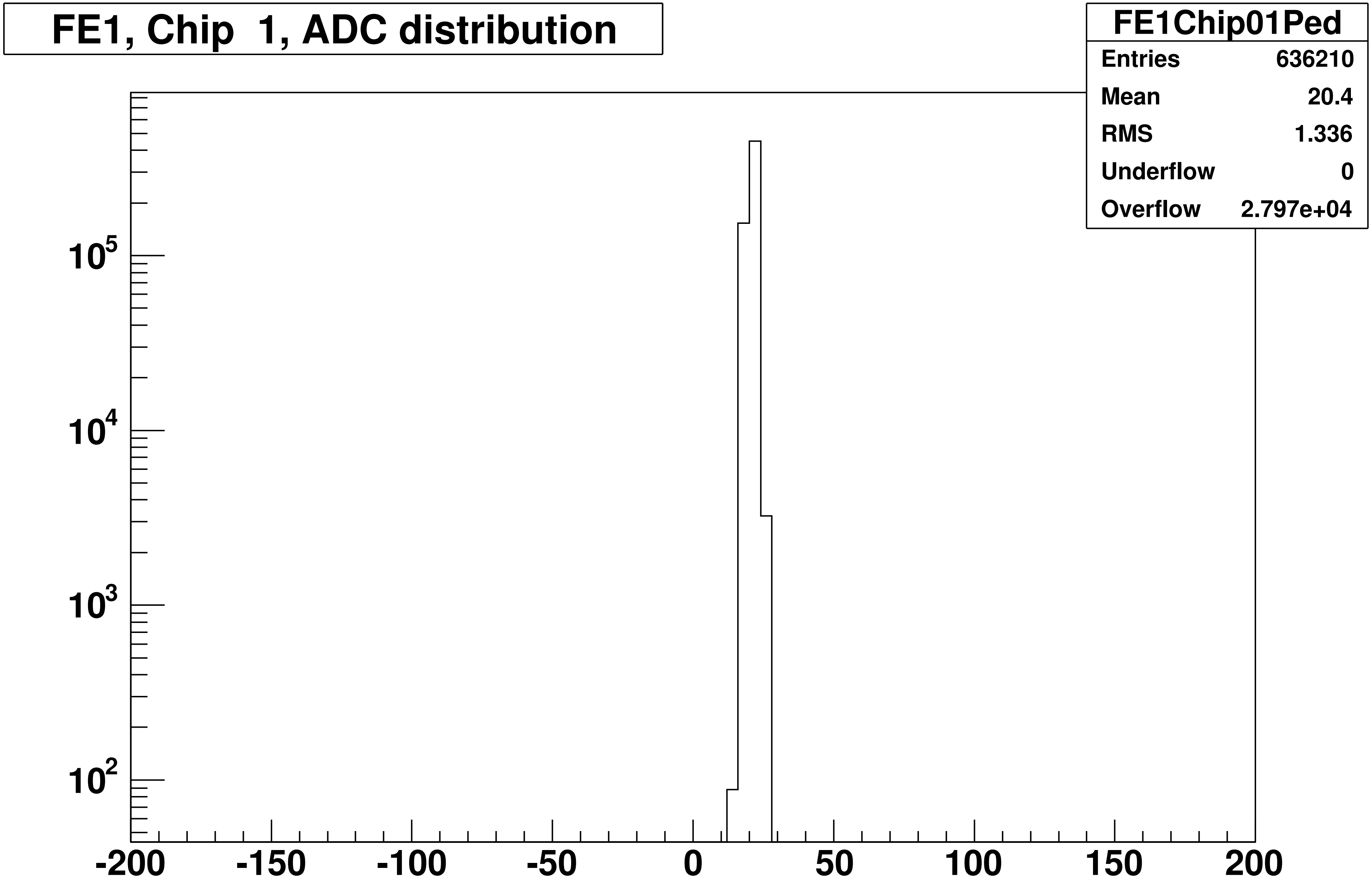
FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 636210
Mean 571.4
RMS 2587
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 636210
Mean 16.69
RMS 1.379
Underflow 0
Overflow 2.801e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

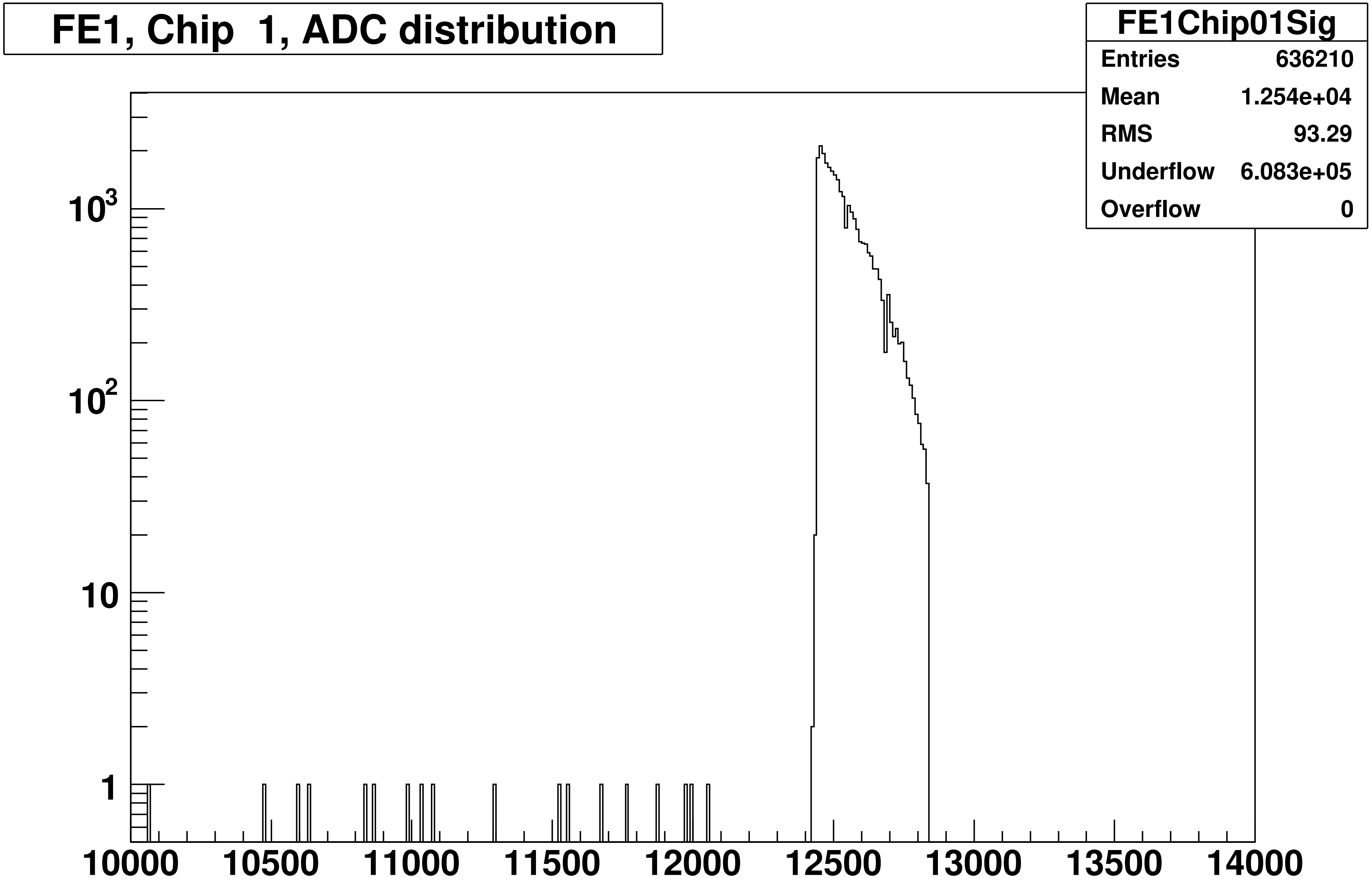

FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 636210
Mean 1.264e+04
RMS 92.7
Underflow 6.083e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

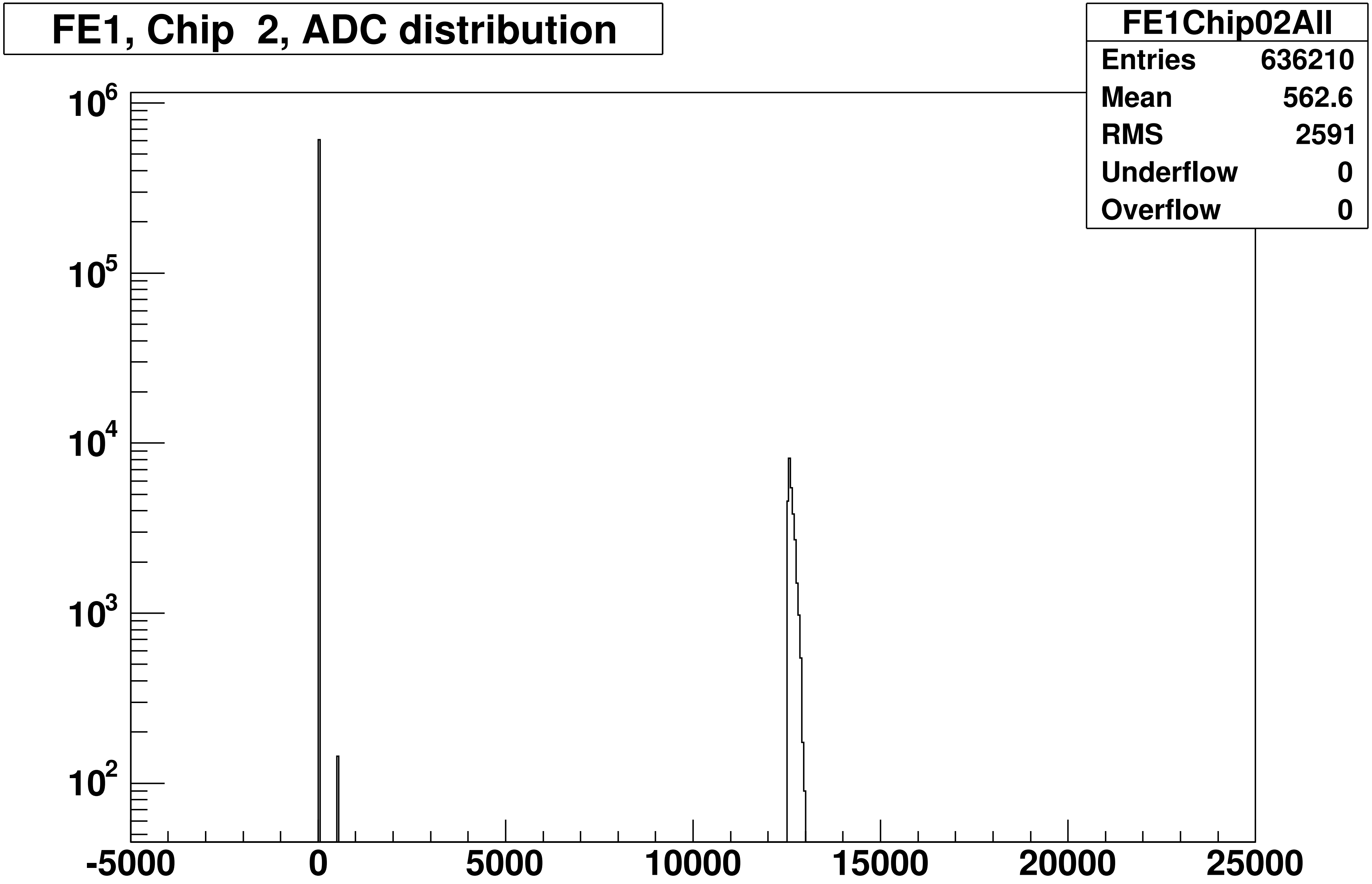


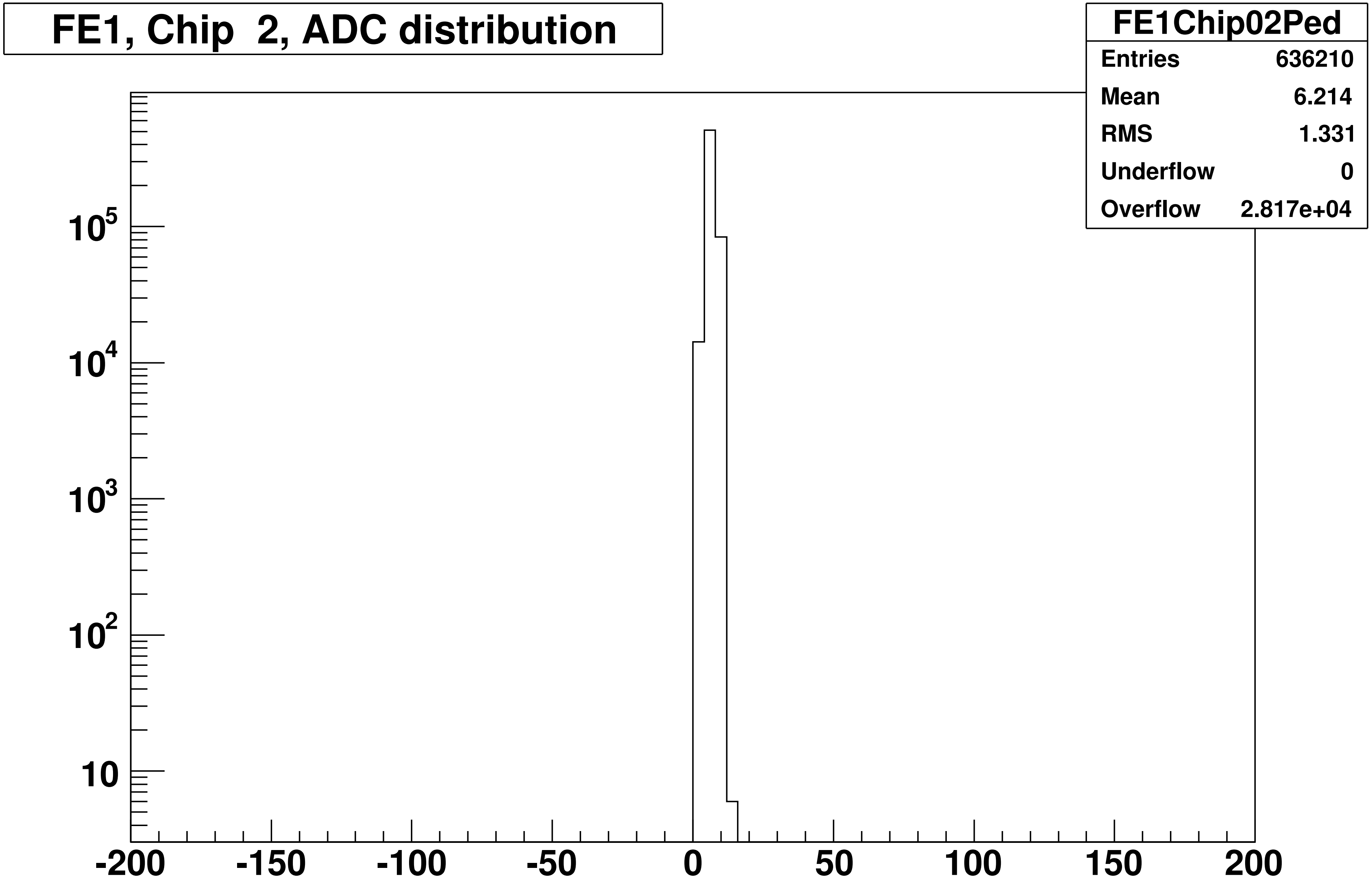
FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 636210
Mean 570.7
RMS 2567
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



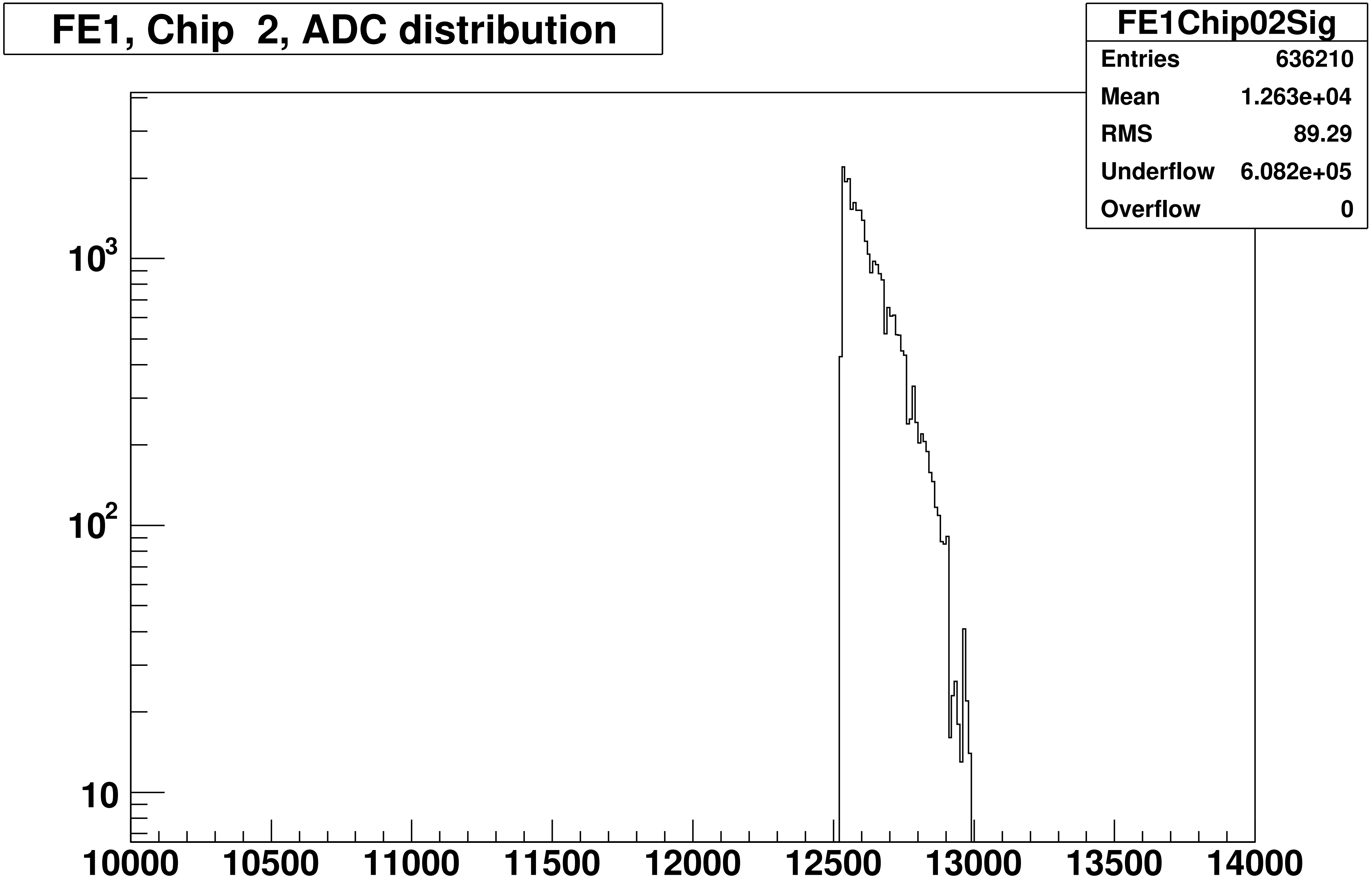
FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 636210
Mean 20.4
RMS 1.336
Underflow 0
Overflow 2.797e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

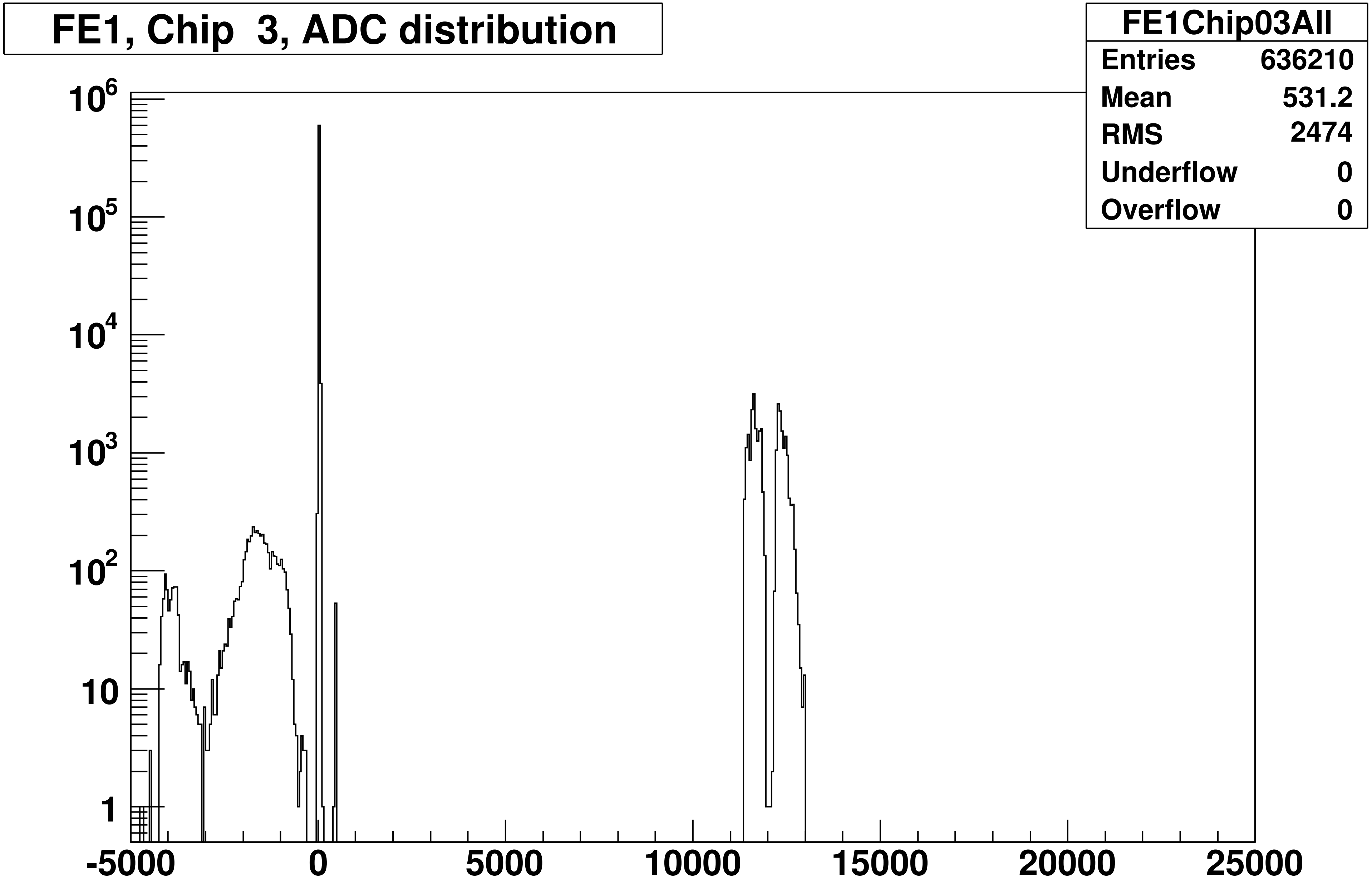

FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 636210
Mean 1.254e+04
RMS 93.29
Underflow 6.083e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

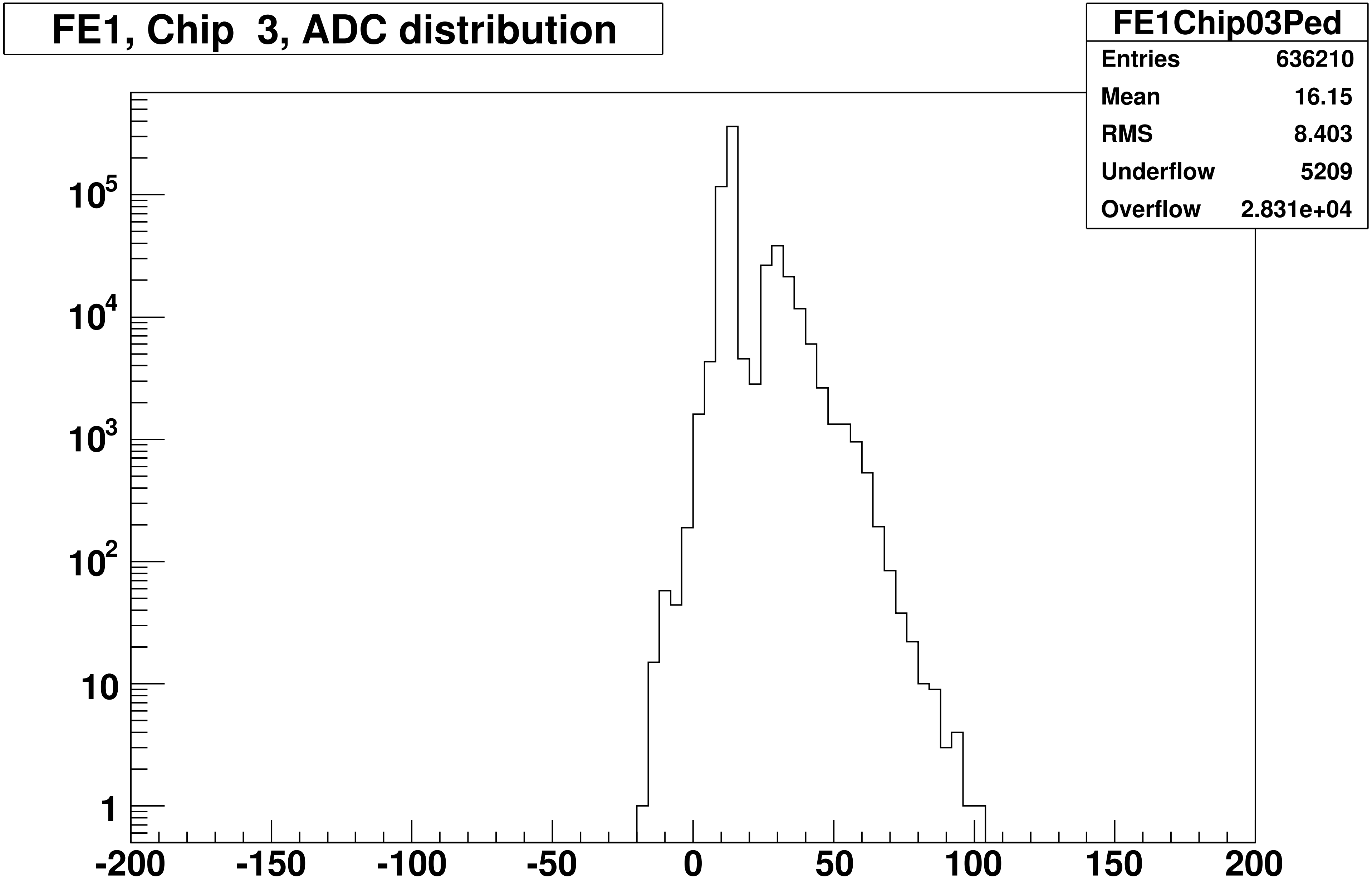

FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 636210
Mean 562.6
RMS 2591
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



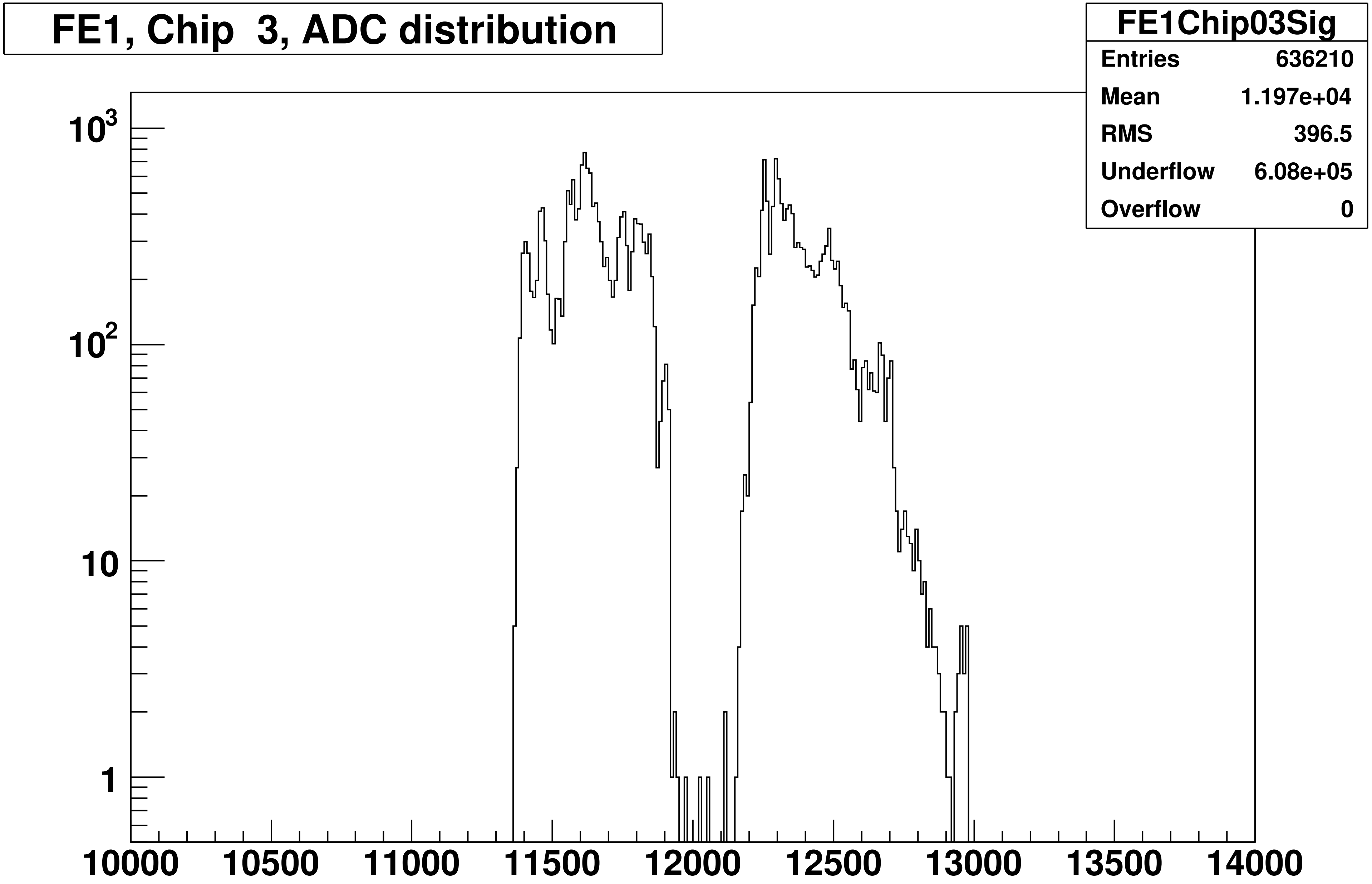
FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 636210
Mean 6.214
RMS 1.331
Underflow 0
Overflow 2.817e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 636210
Mean 1.263e+04
RMS 89.29
Underflow 6.082e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

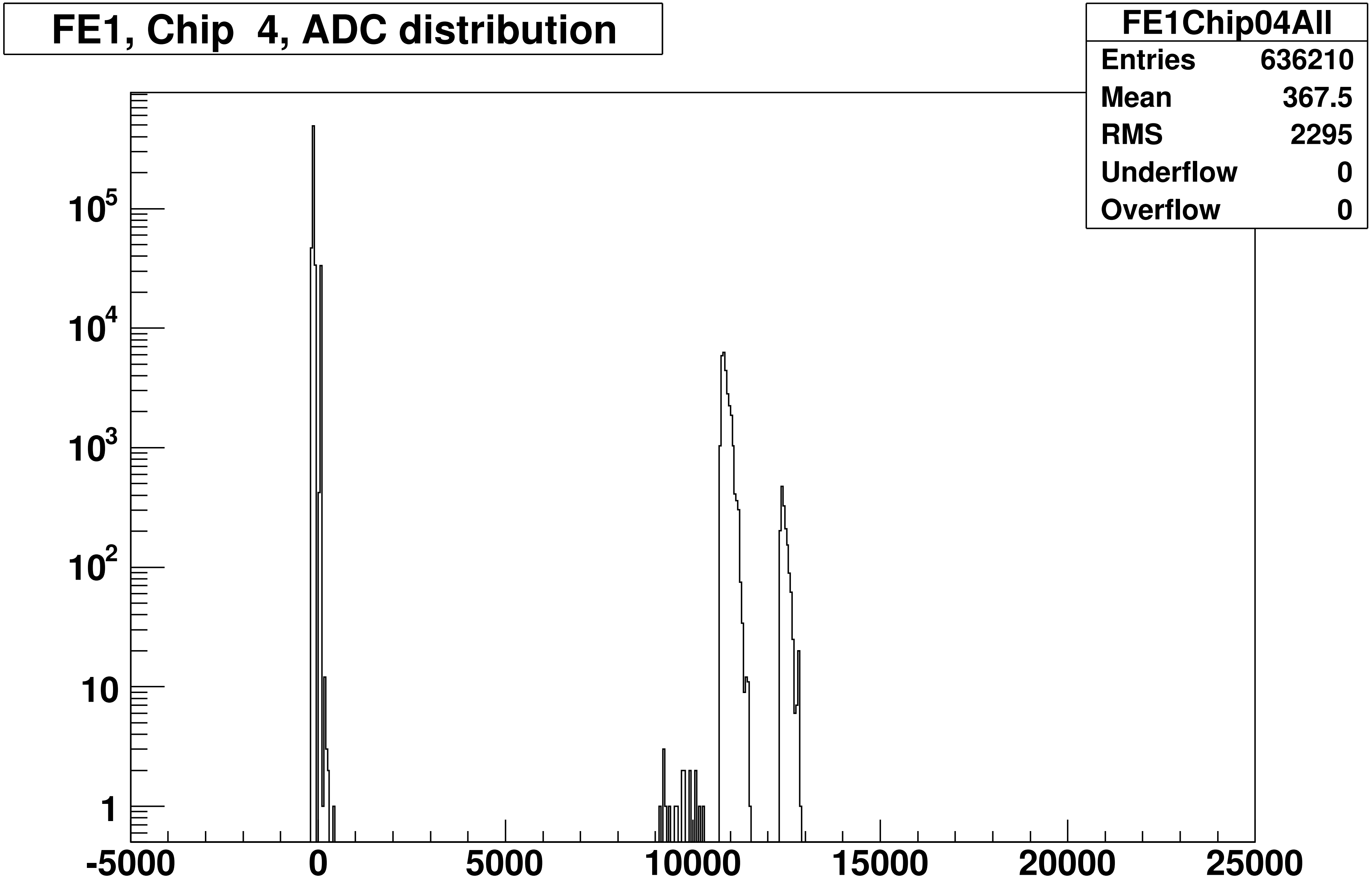

FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 636210
Mean 531.2
RMS 2474
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

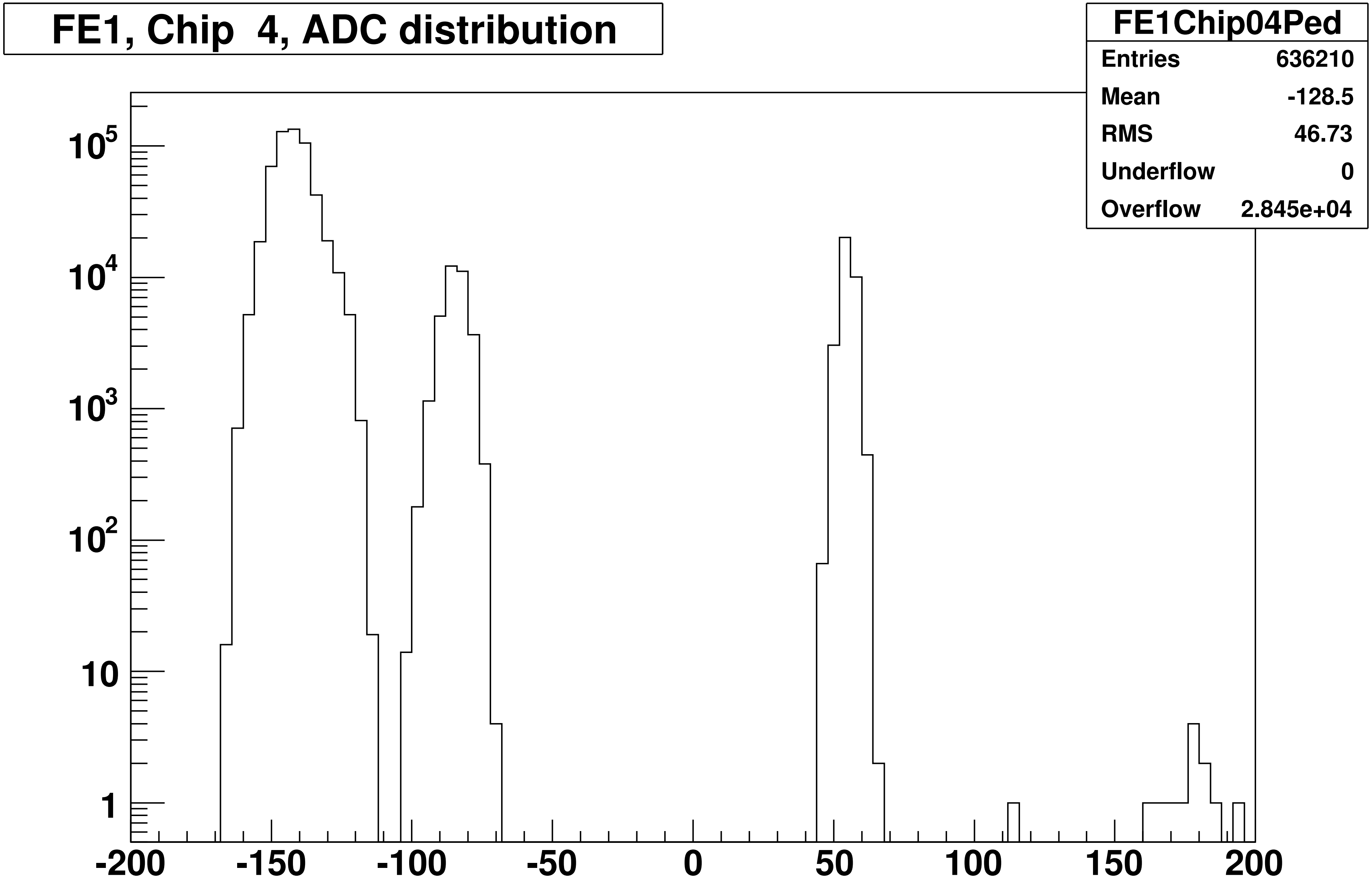


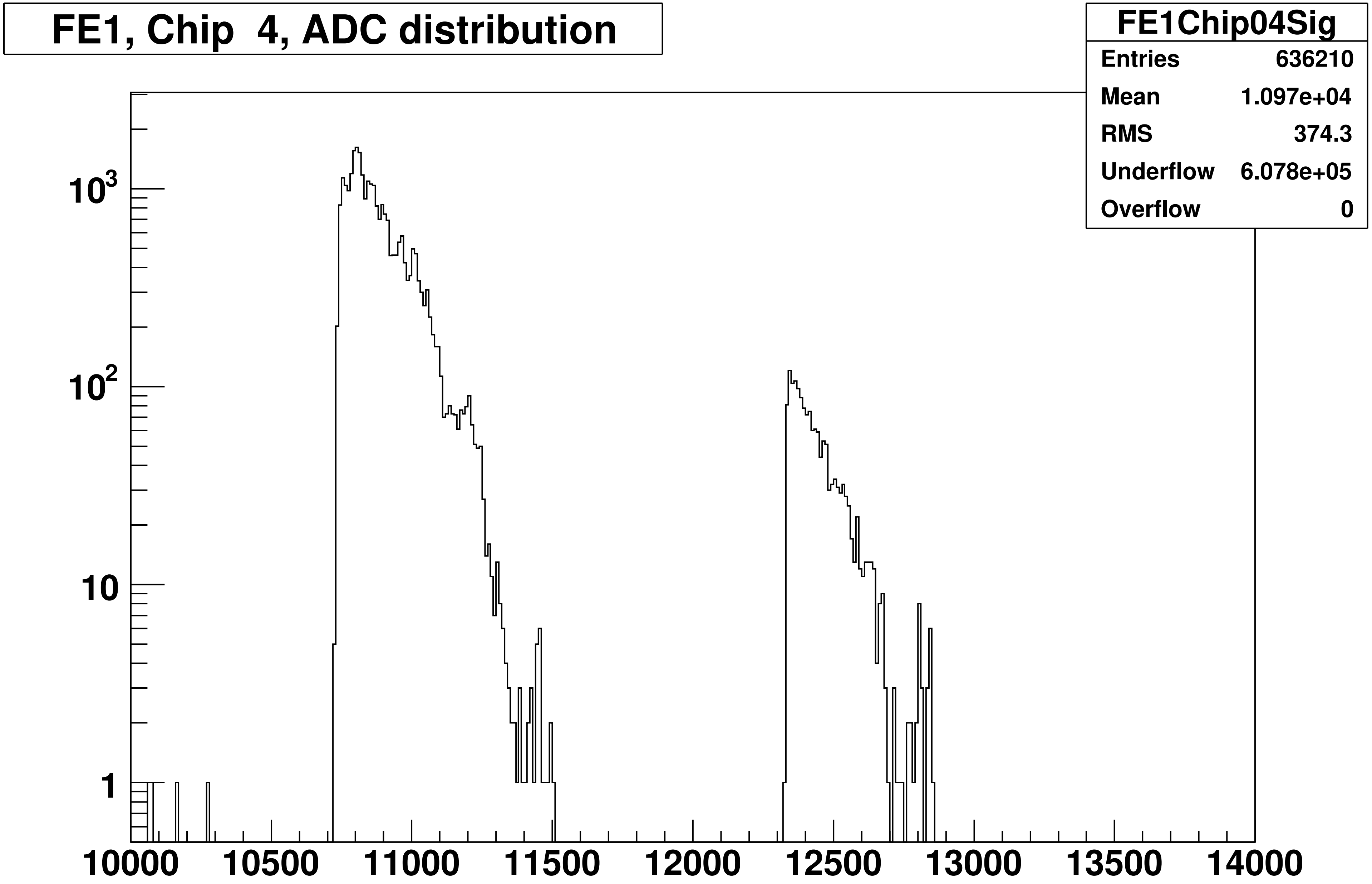
FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 636210
Mean 16.15
RMS 8.403
Underflow 5209
Overflow 2.831e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

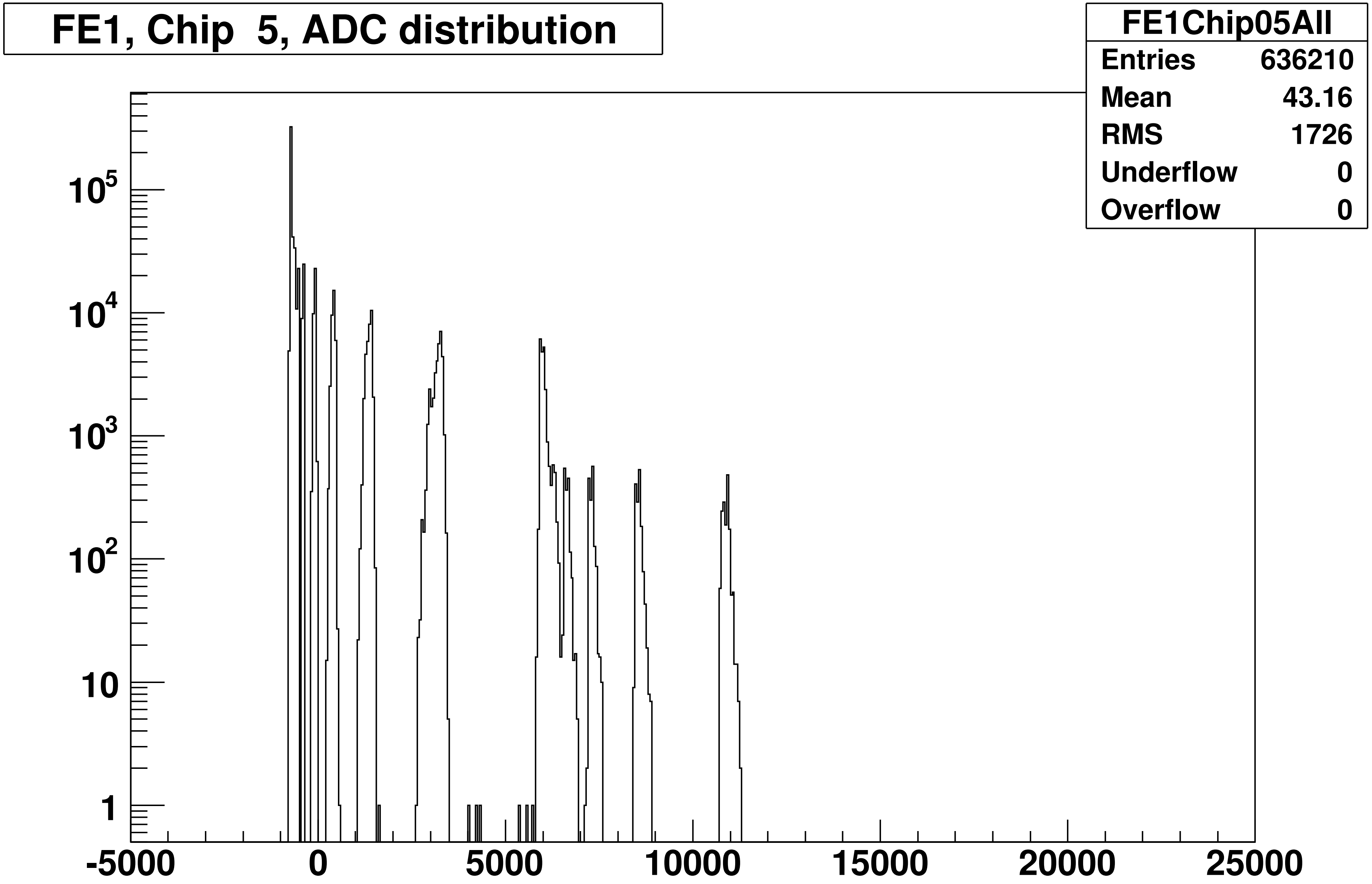


FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 636210
Mean 1.197e+04
RMS 396.5
Underflow 6.08e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

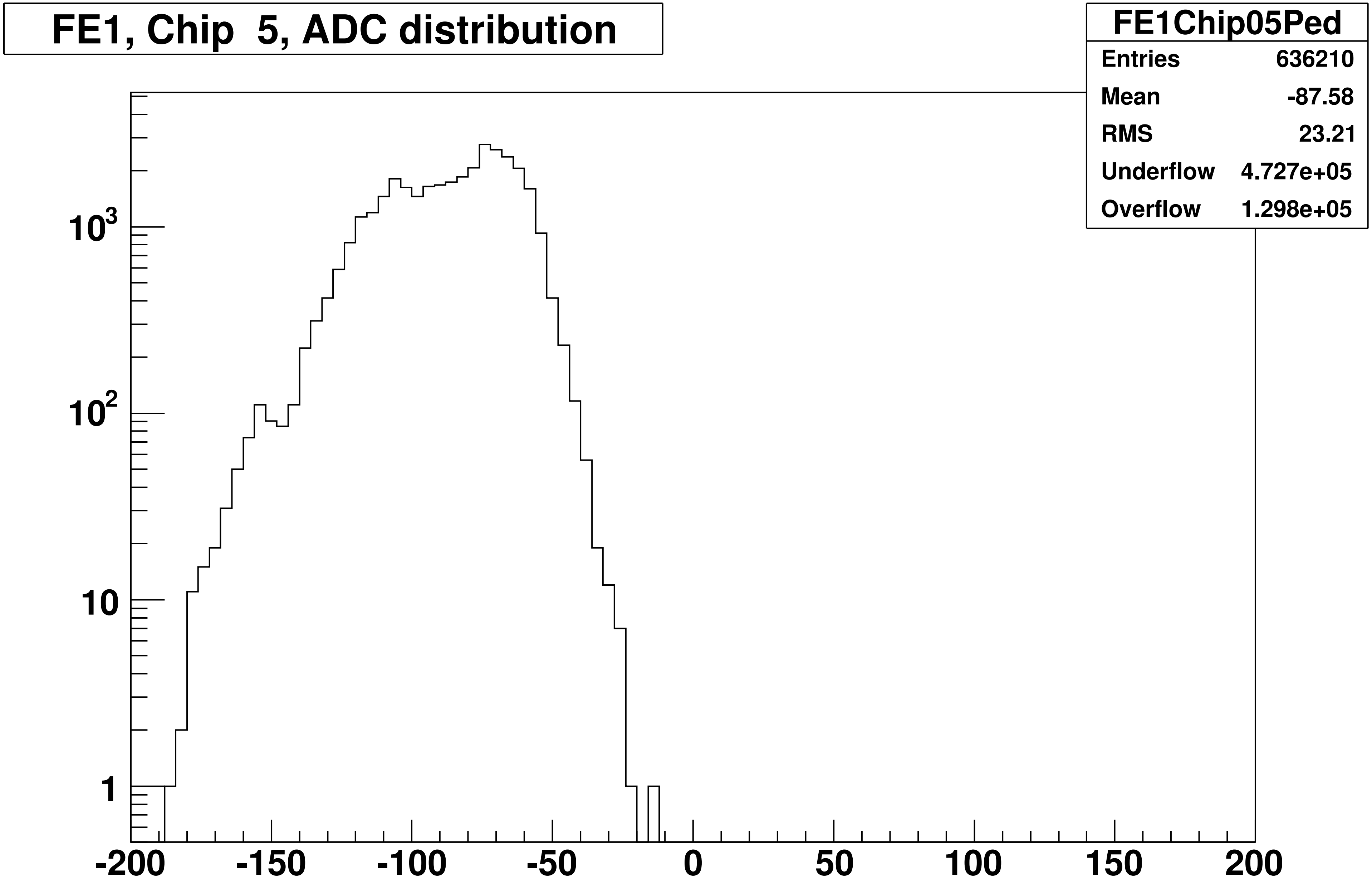

FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 636210
Mean 367.5
RMS 2295
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

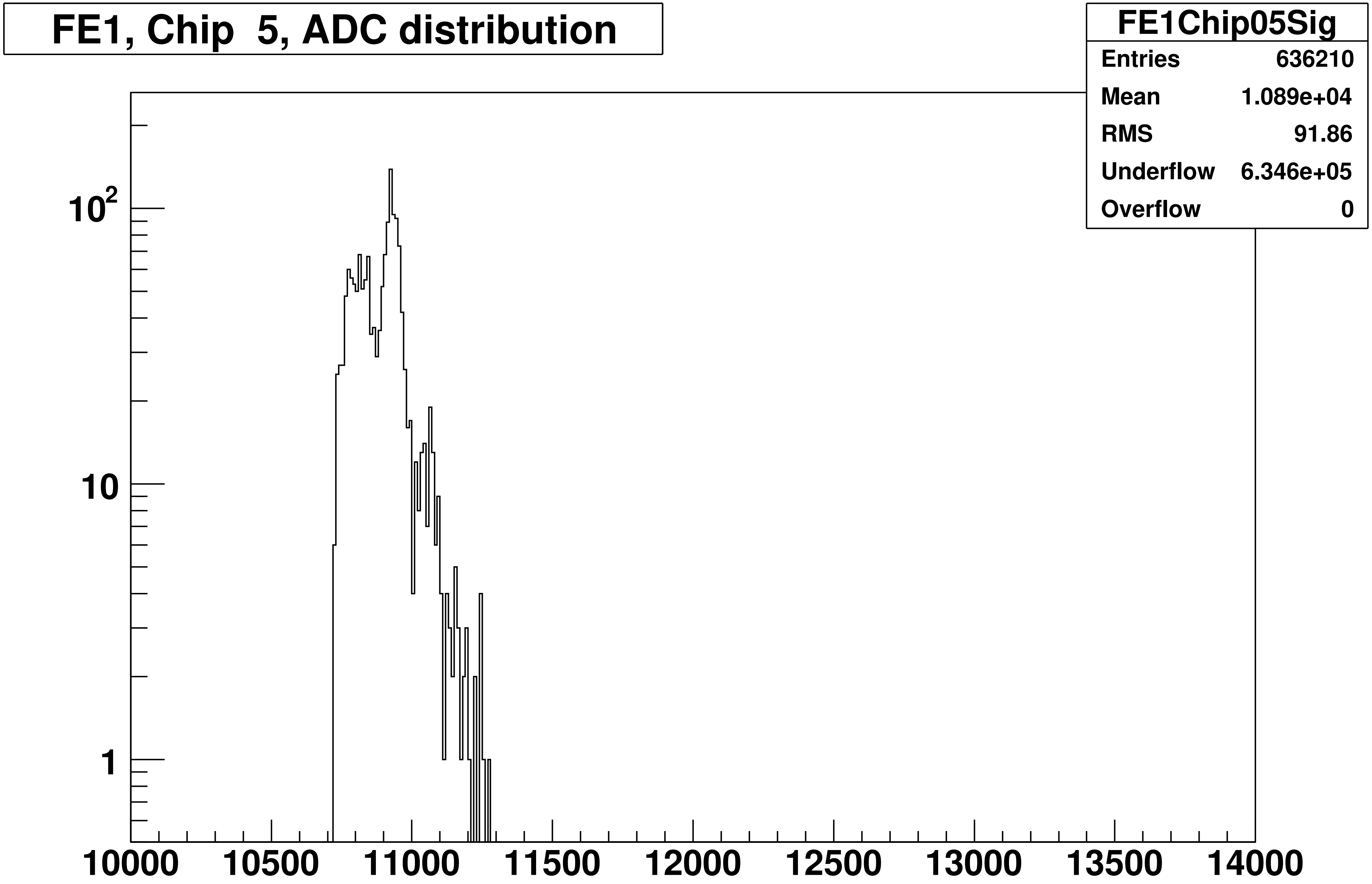

FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 636210
Mean -128.5
RMS 46.73
Underflow 0
Overflow 2.845e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 636210
Mean 1.097e+04
RMS 374.3
Underflow 6.078e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

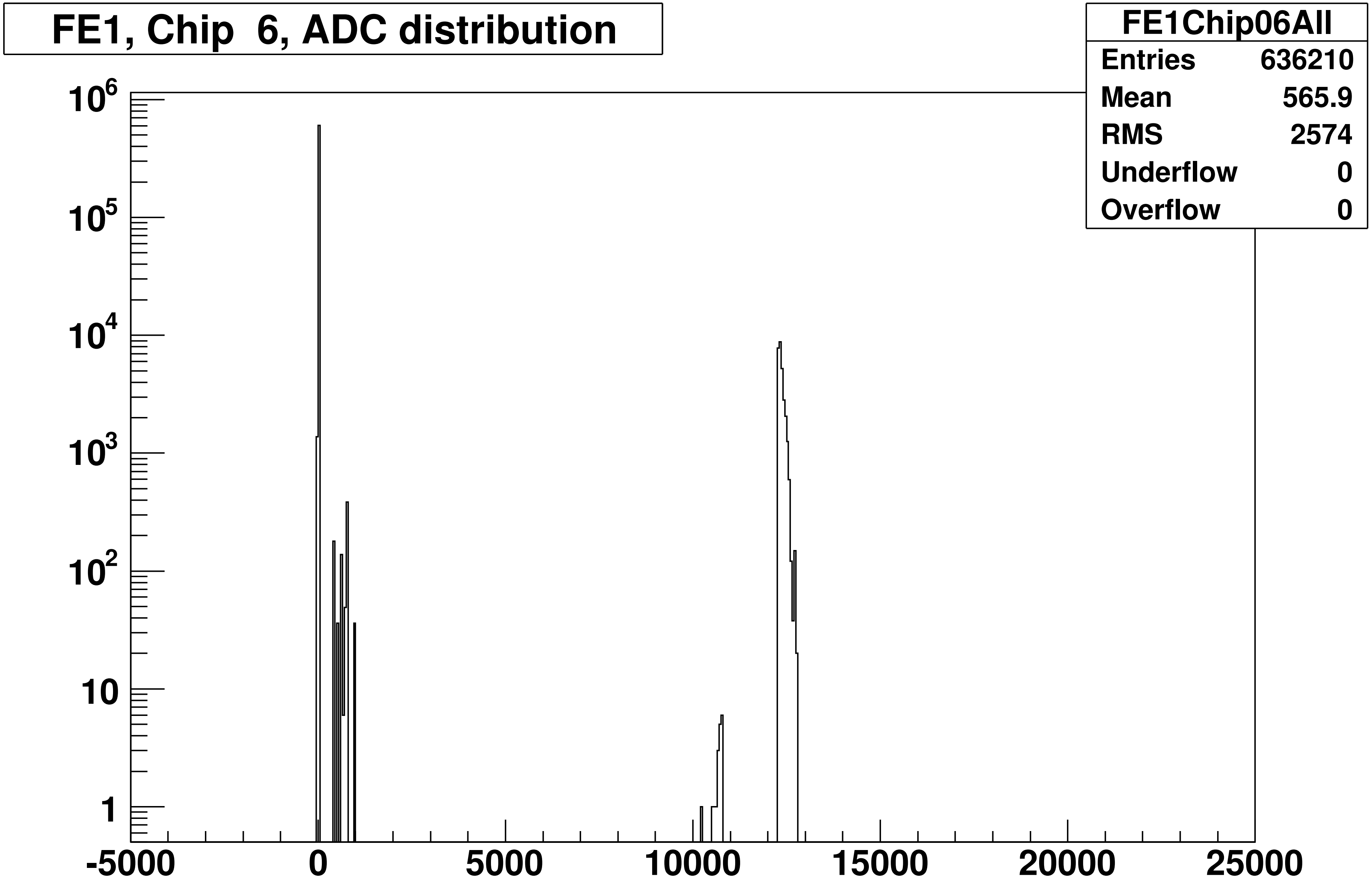


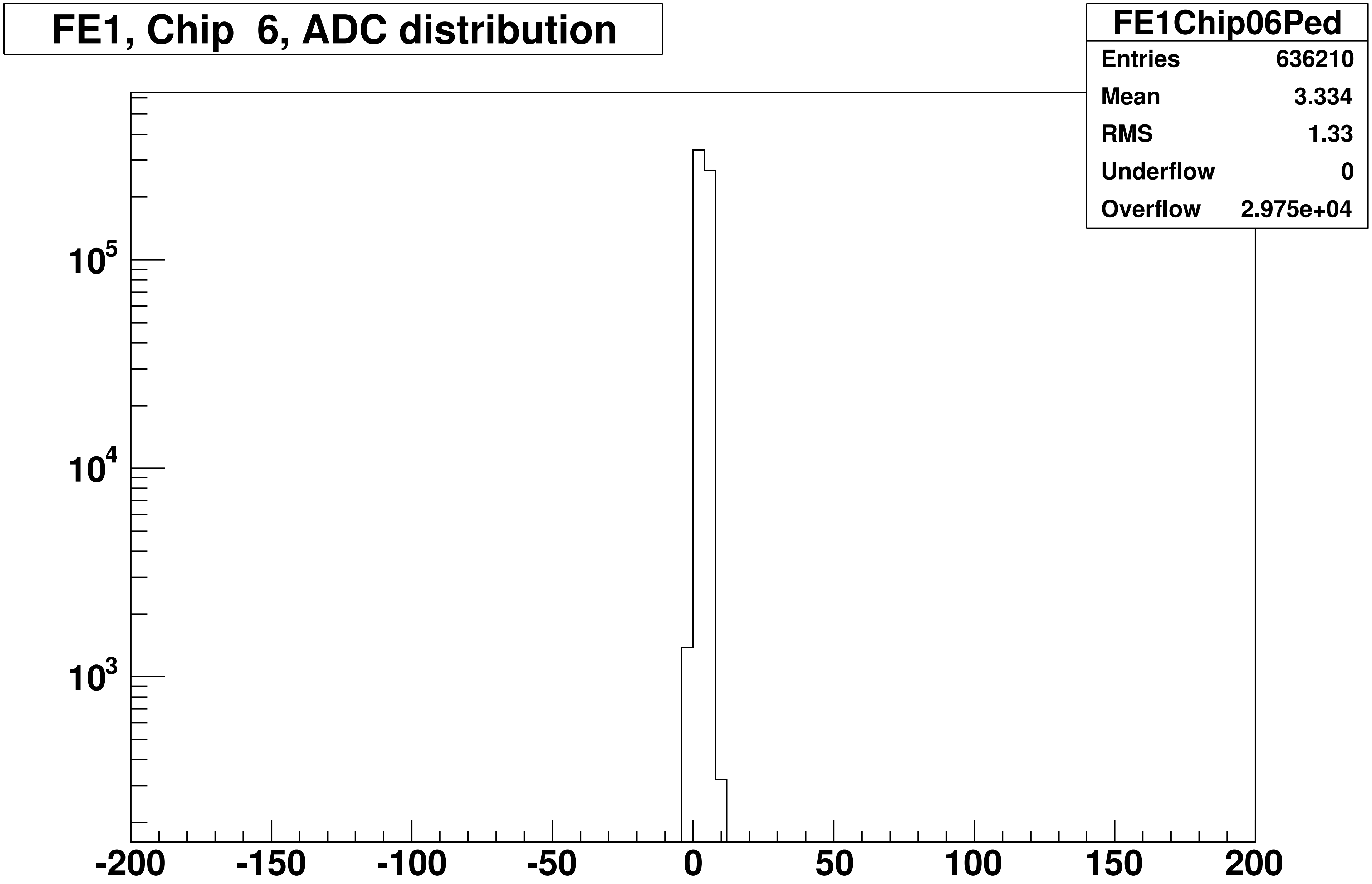
FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 636210
Mean 43.16
RMS 1726
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

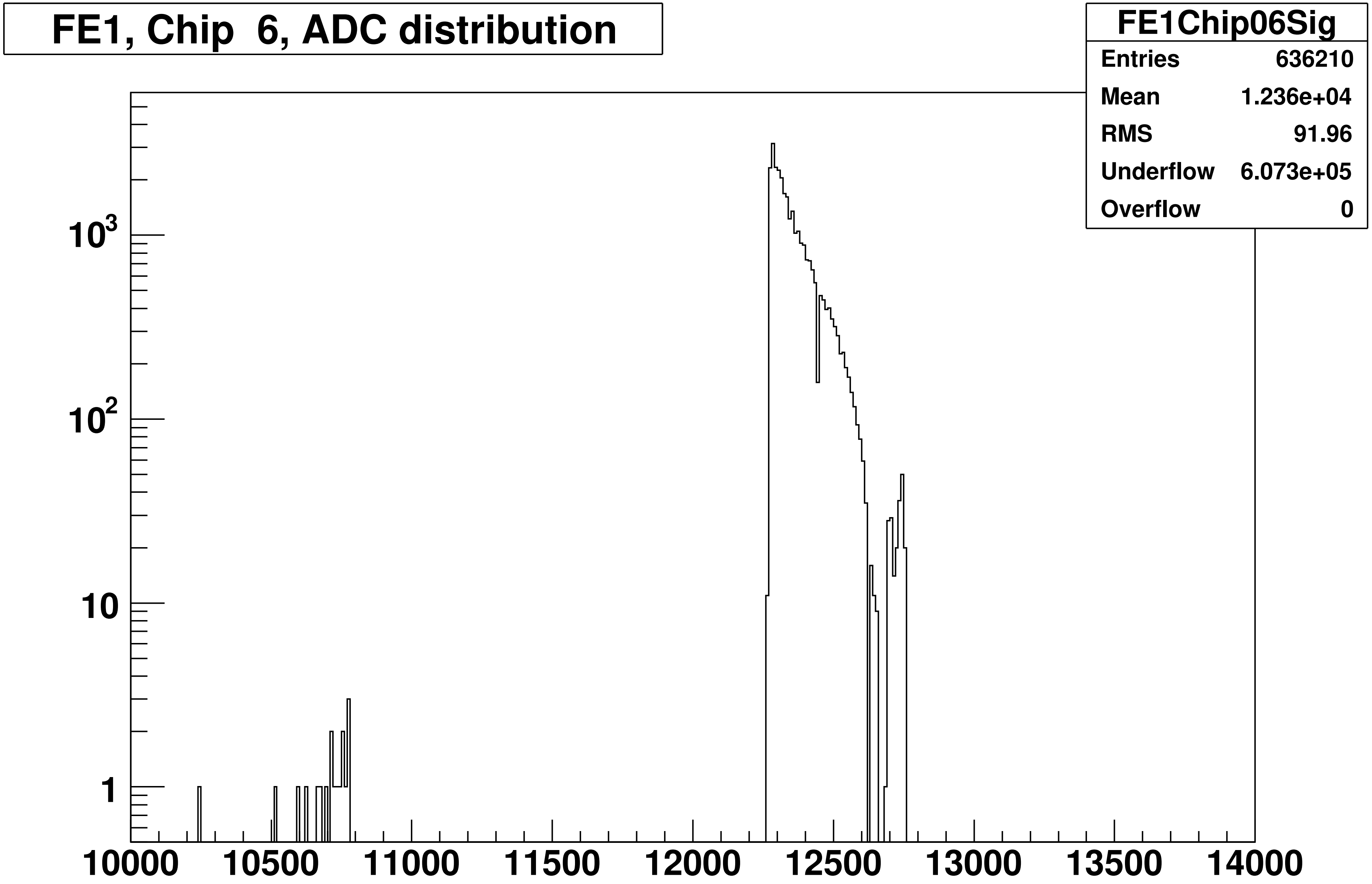

FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 636210
Mean -87.58
RMS 23.21
Underflow 4.727e+05
Overflow 1.298e+05
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



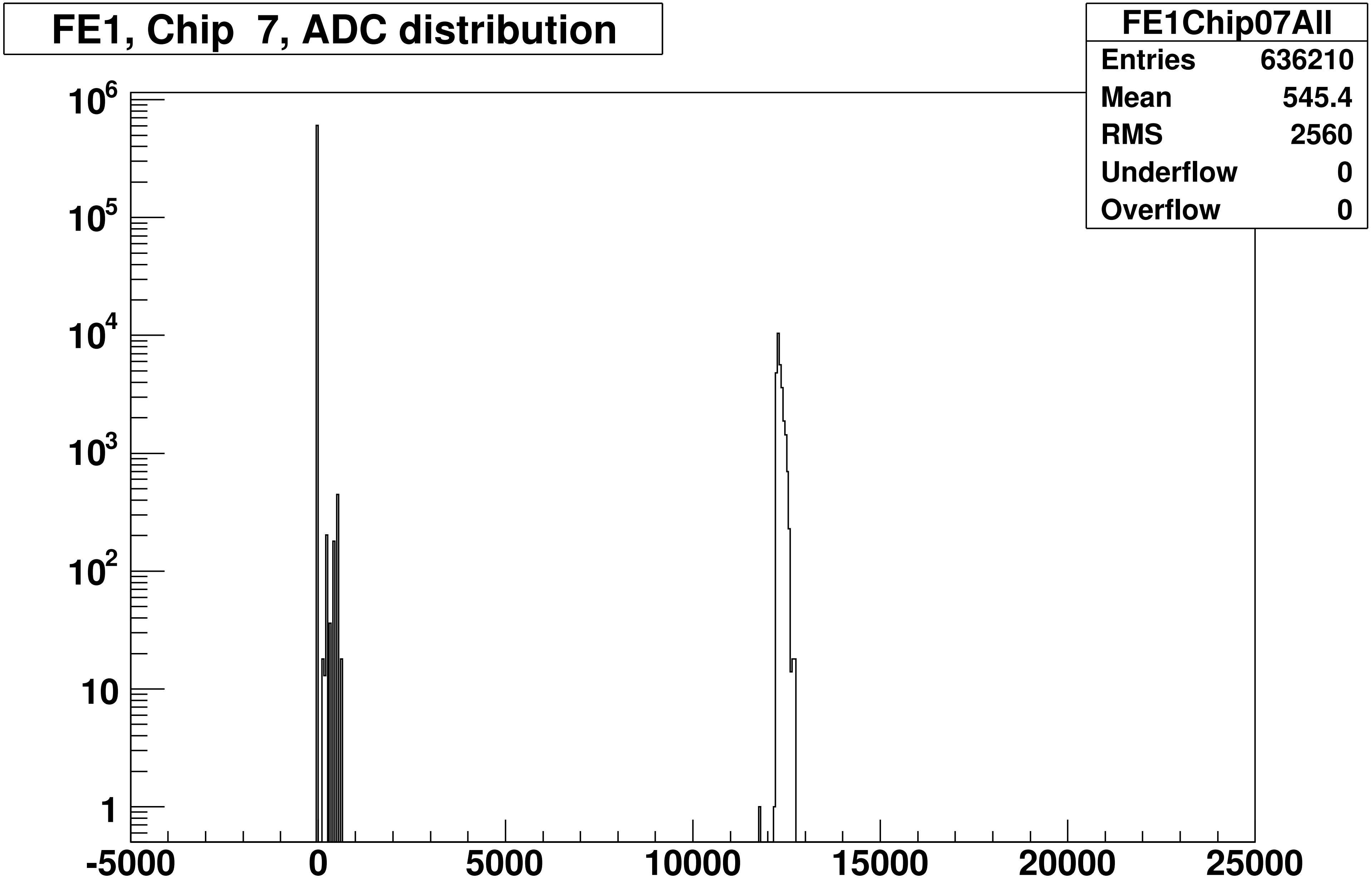
FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 636210
Mean 1.089e+04
RMS 91.86
Underflow 6.346e+05
Overflow 0
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

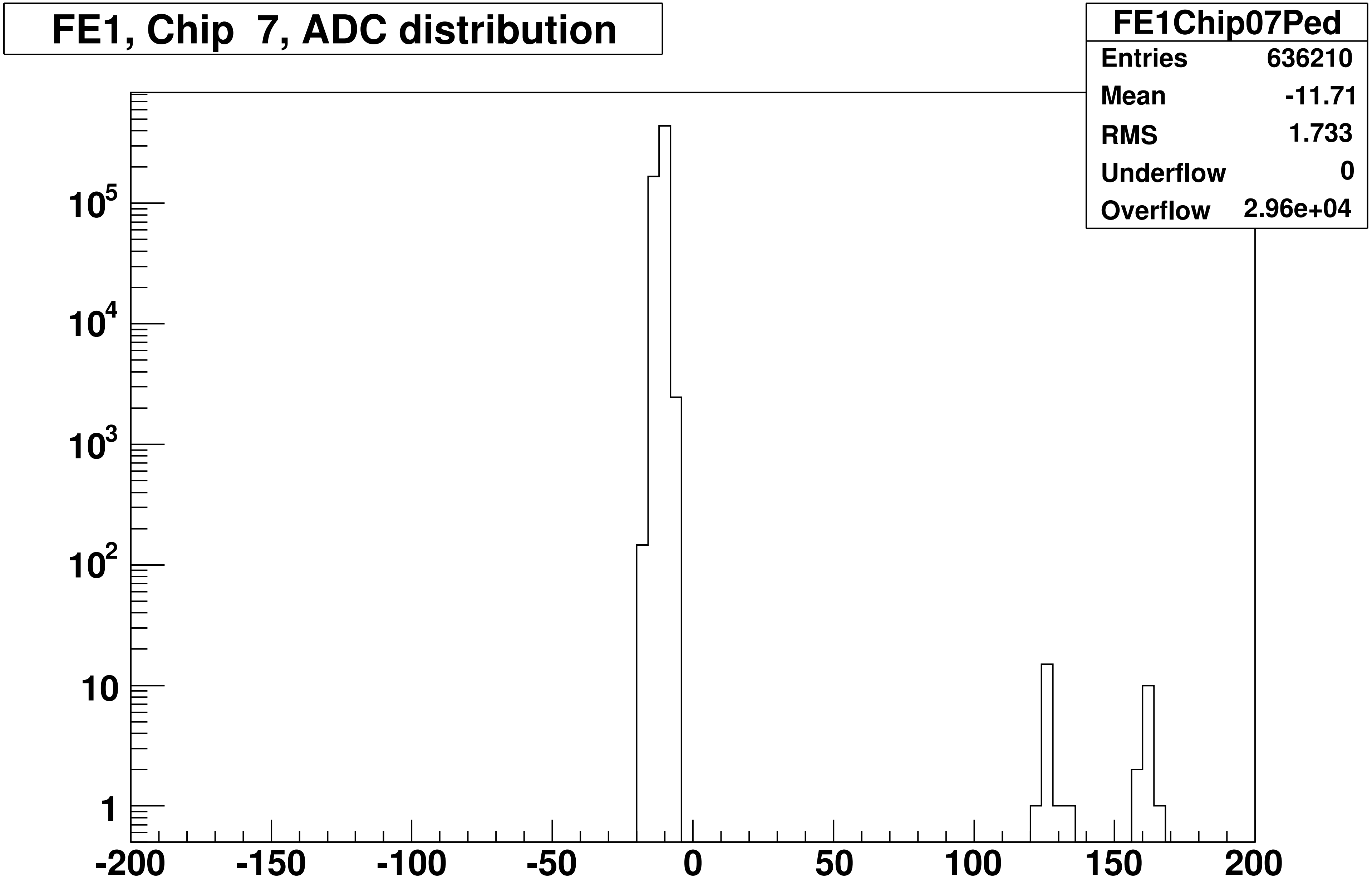

FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 636210
Mean 565.9
RMS 2574
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

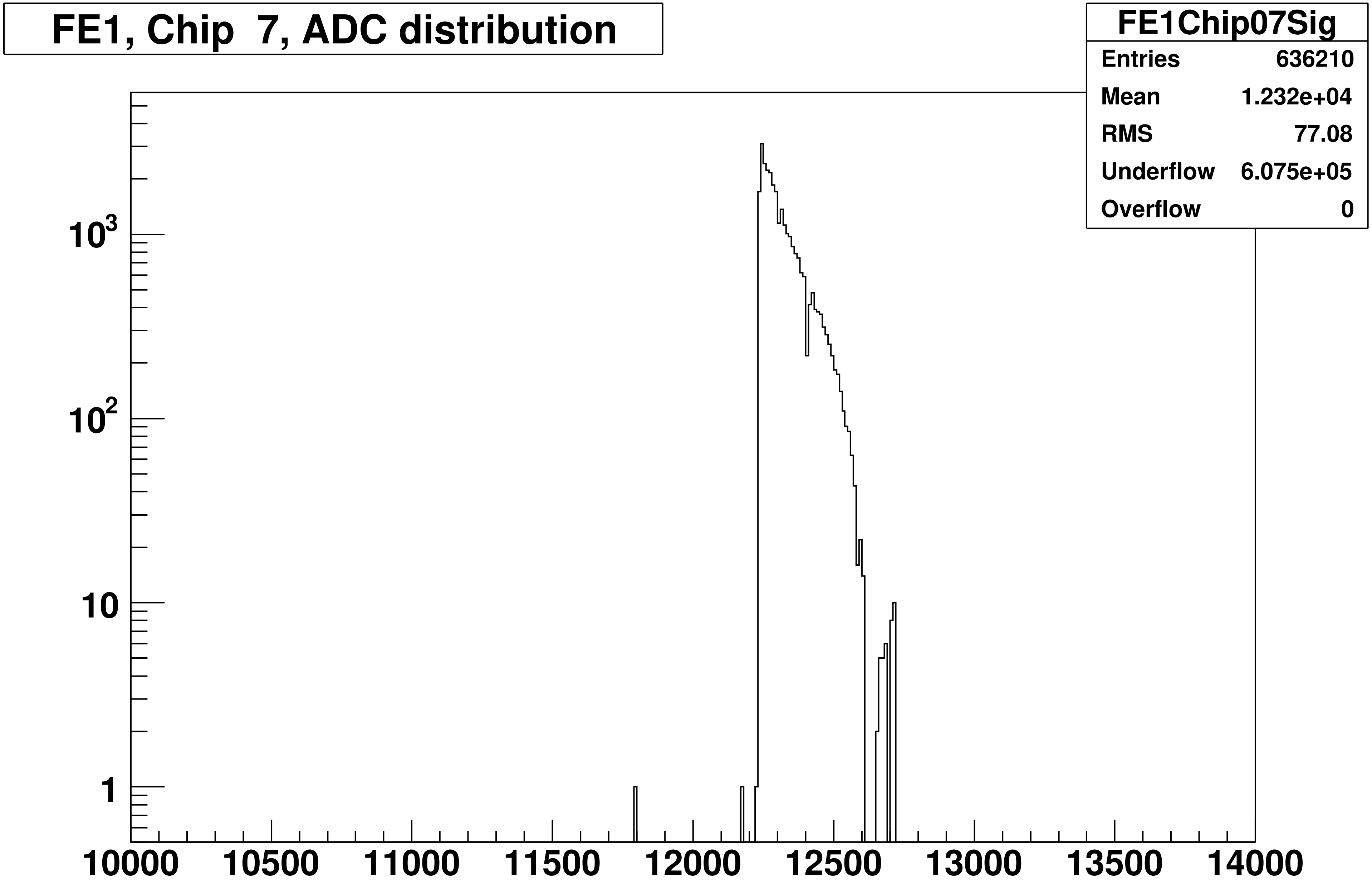

FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 636210
Mean 3.334
RMS 1.33
Underflow 0
Overflow 2.975e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200



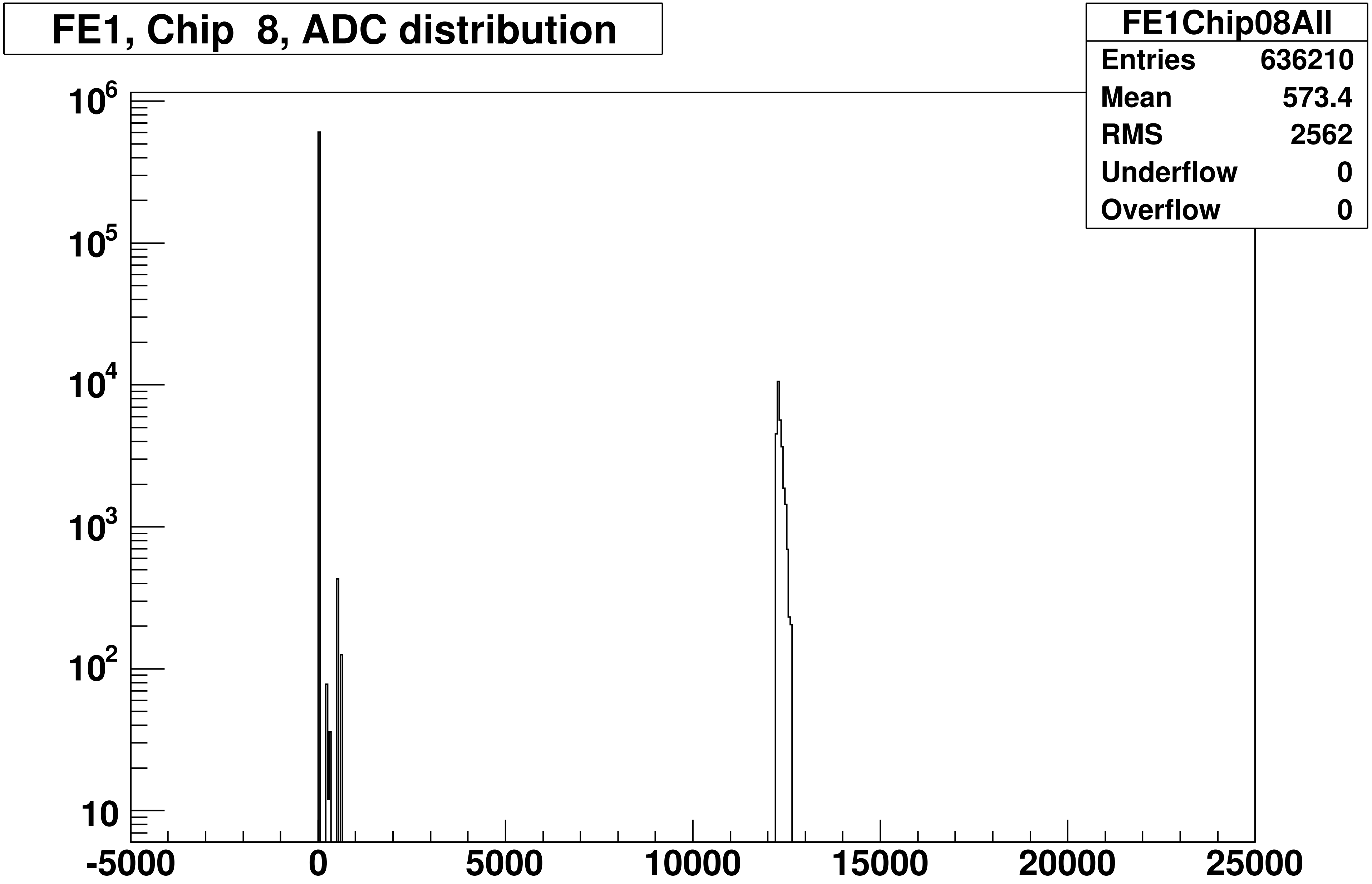
FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 636210
Mean 1.236e+04
RMS 91.96
Underflow 6.073e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

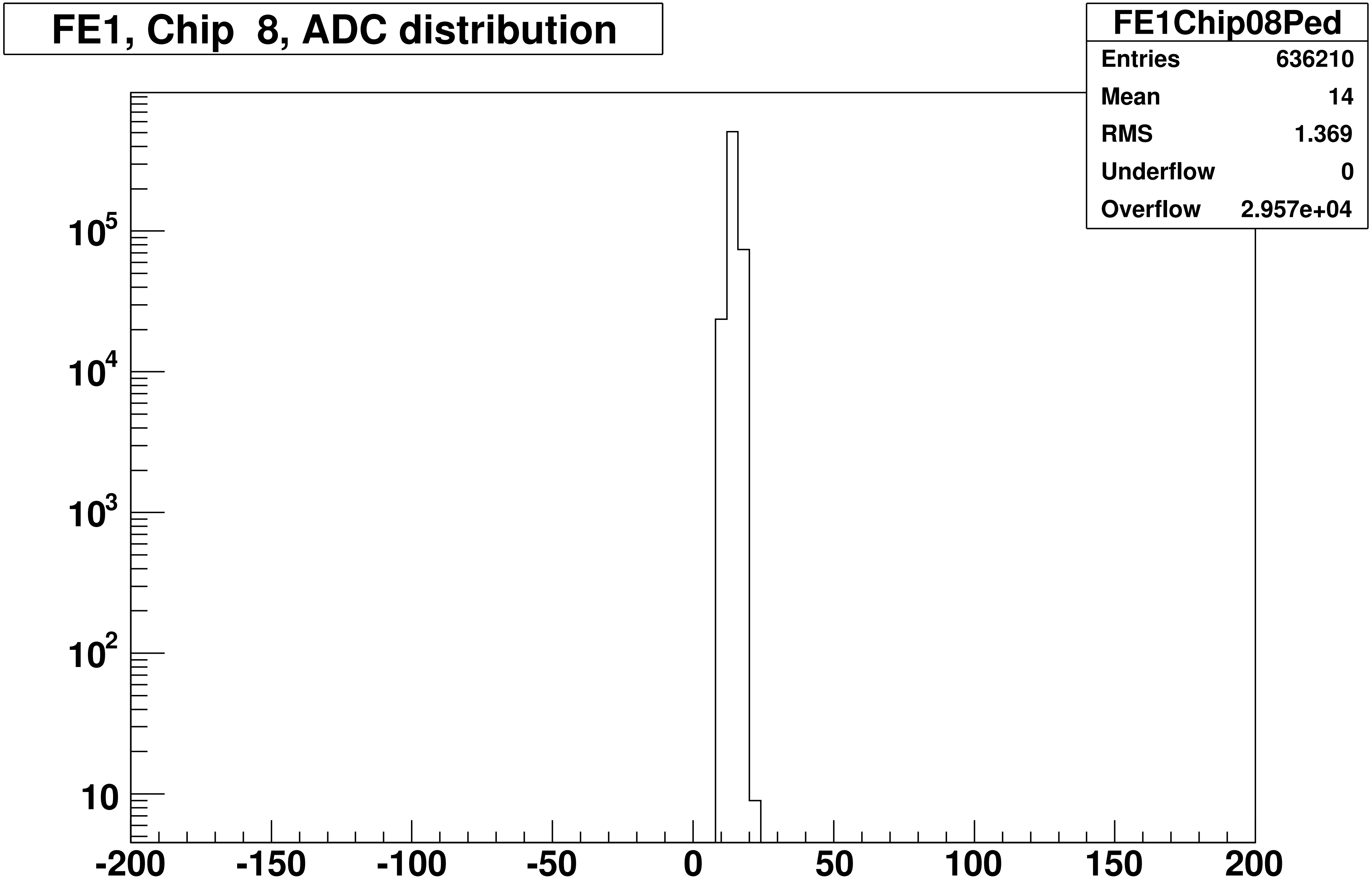

FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 636210
Mean 545.4
RMS 2560
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 636210
Mean -11.71
RMS 1.733
Underflow 0
Overflow 2.96e+04
10^5
10^4
10^3
10^2
10
1
-200 -150 -100 -50 0 50 100 150 200

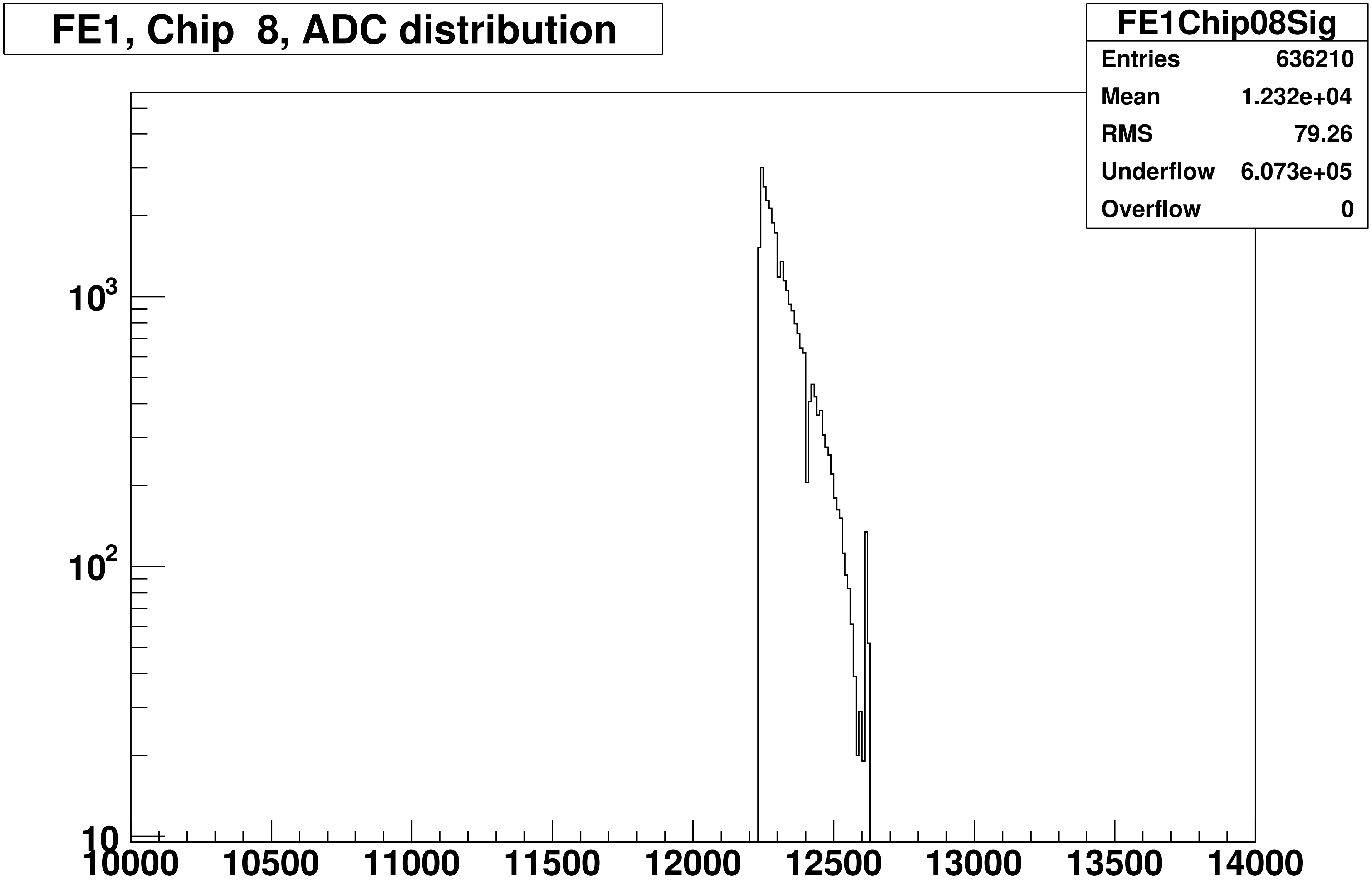


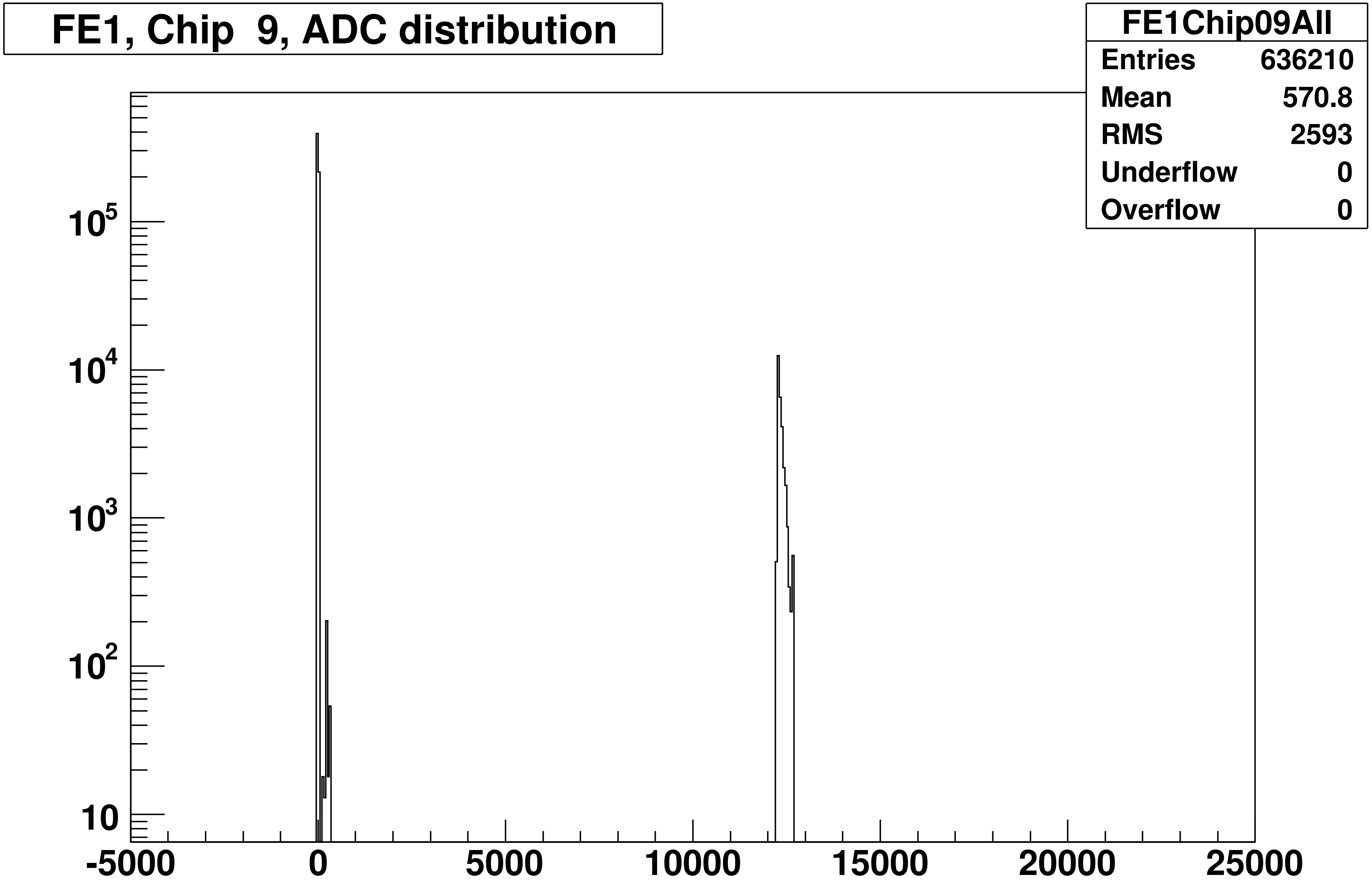
FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 636210
Mean 1.232e+04
RMS 77.08
Underflow 6.075e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 636210
Mean 573.4
RMS 2562
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

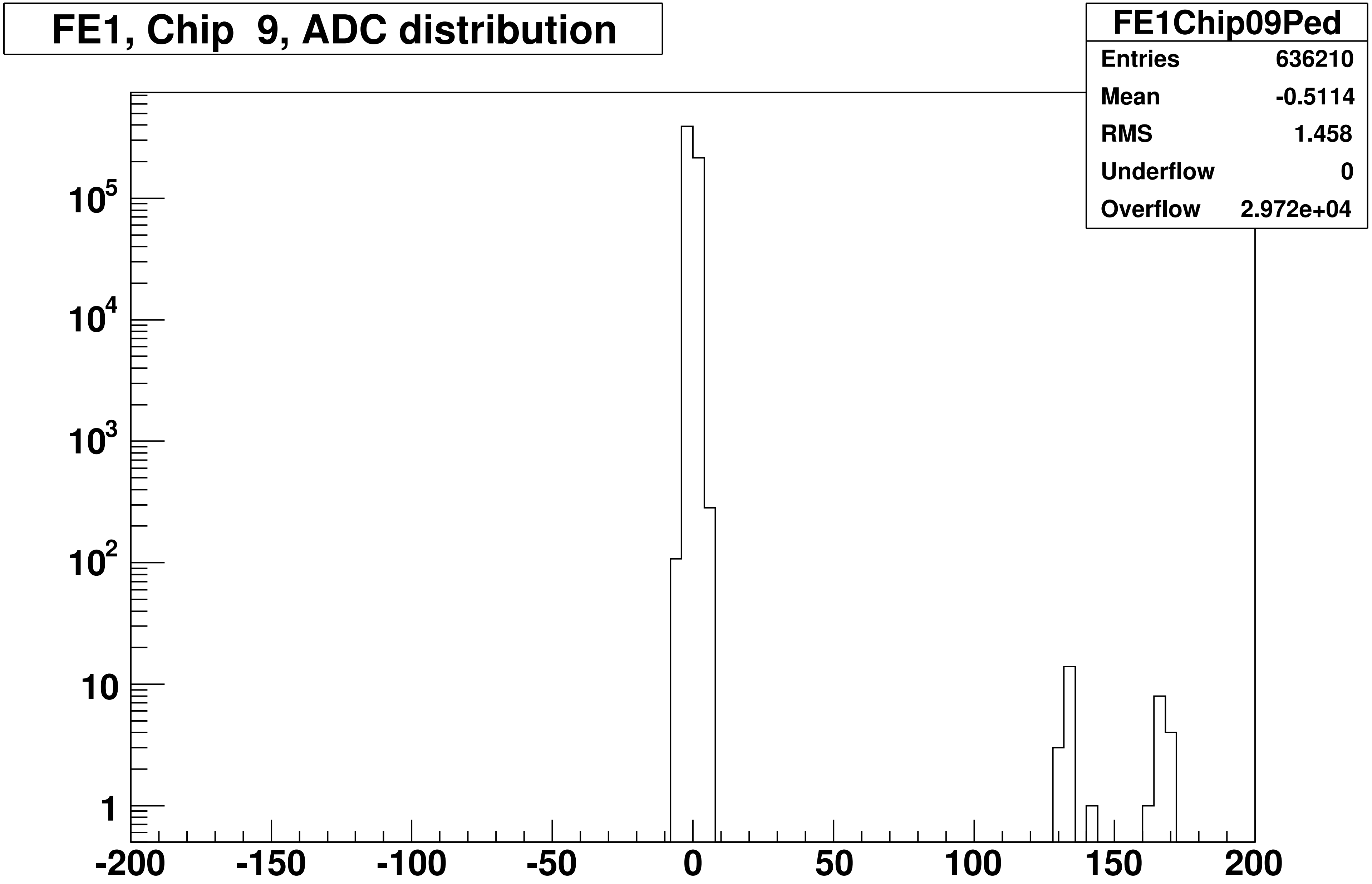


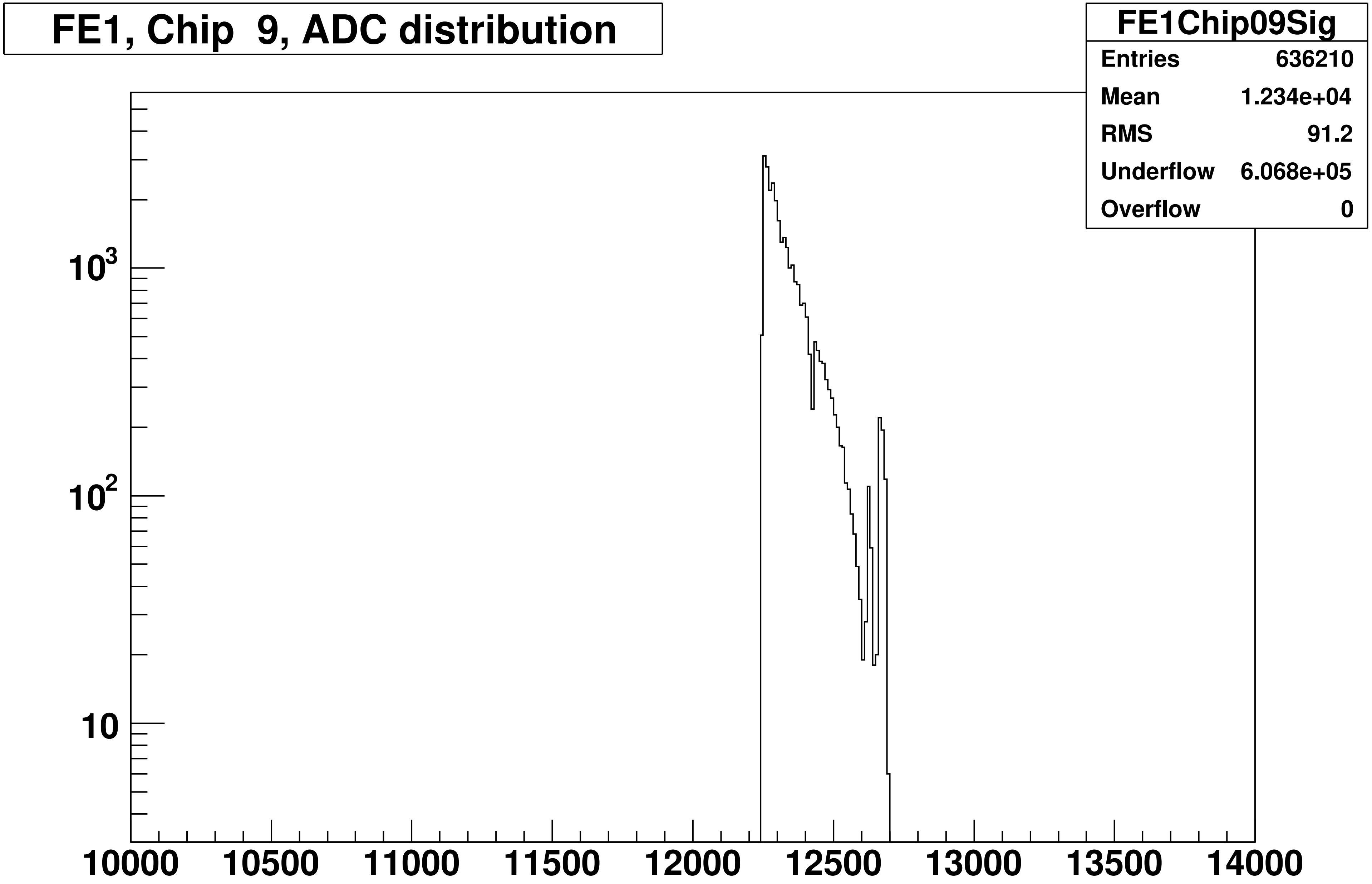
FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 636210
Mean 14
RMS 1.369
Underflow 0
Overflow 2.957e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

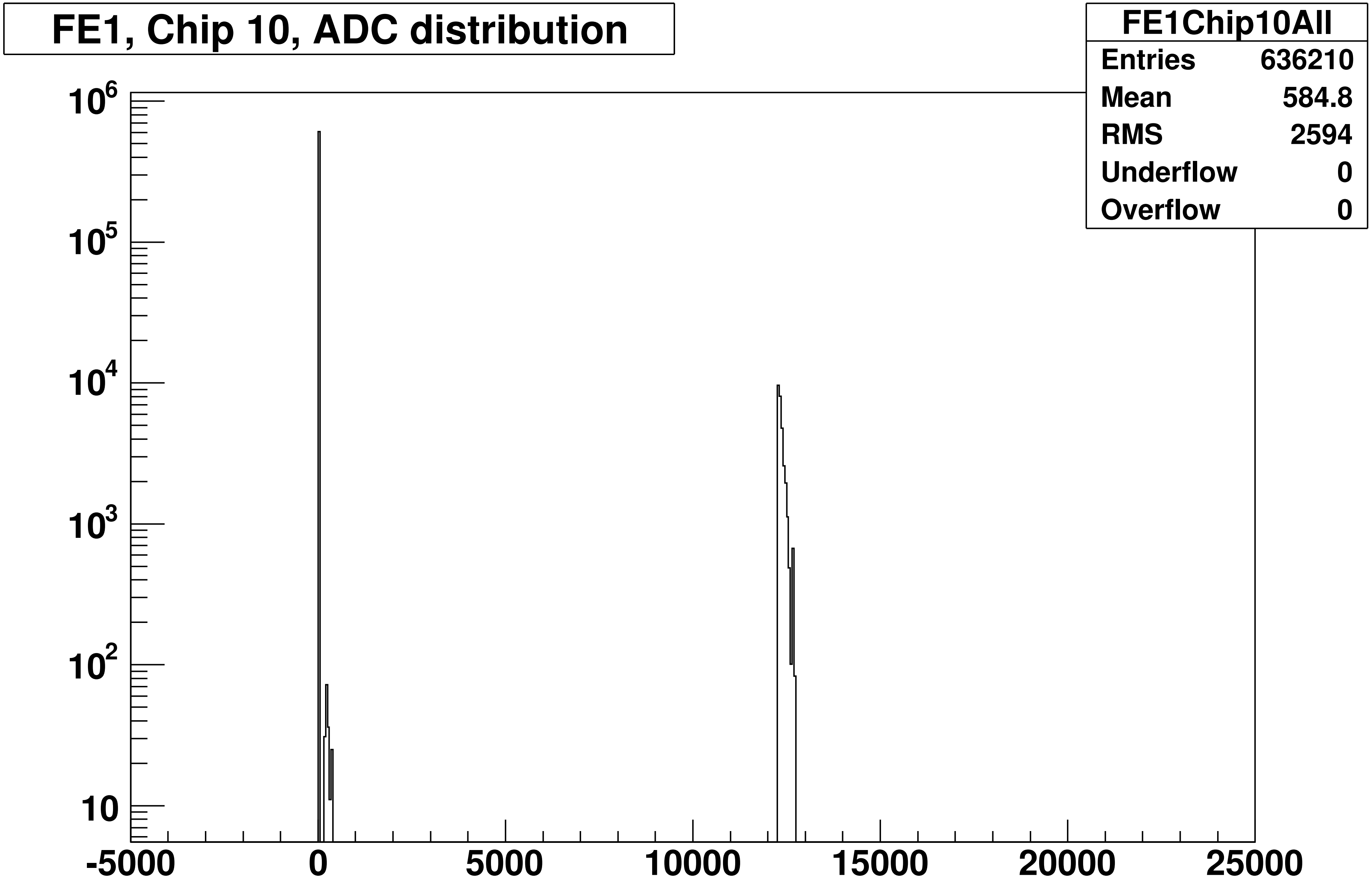

FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 636210
Mean 1.232e+04
RMS 79.26
Underflow 6.073e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000



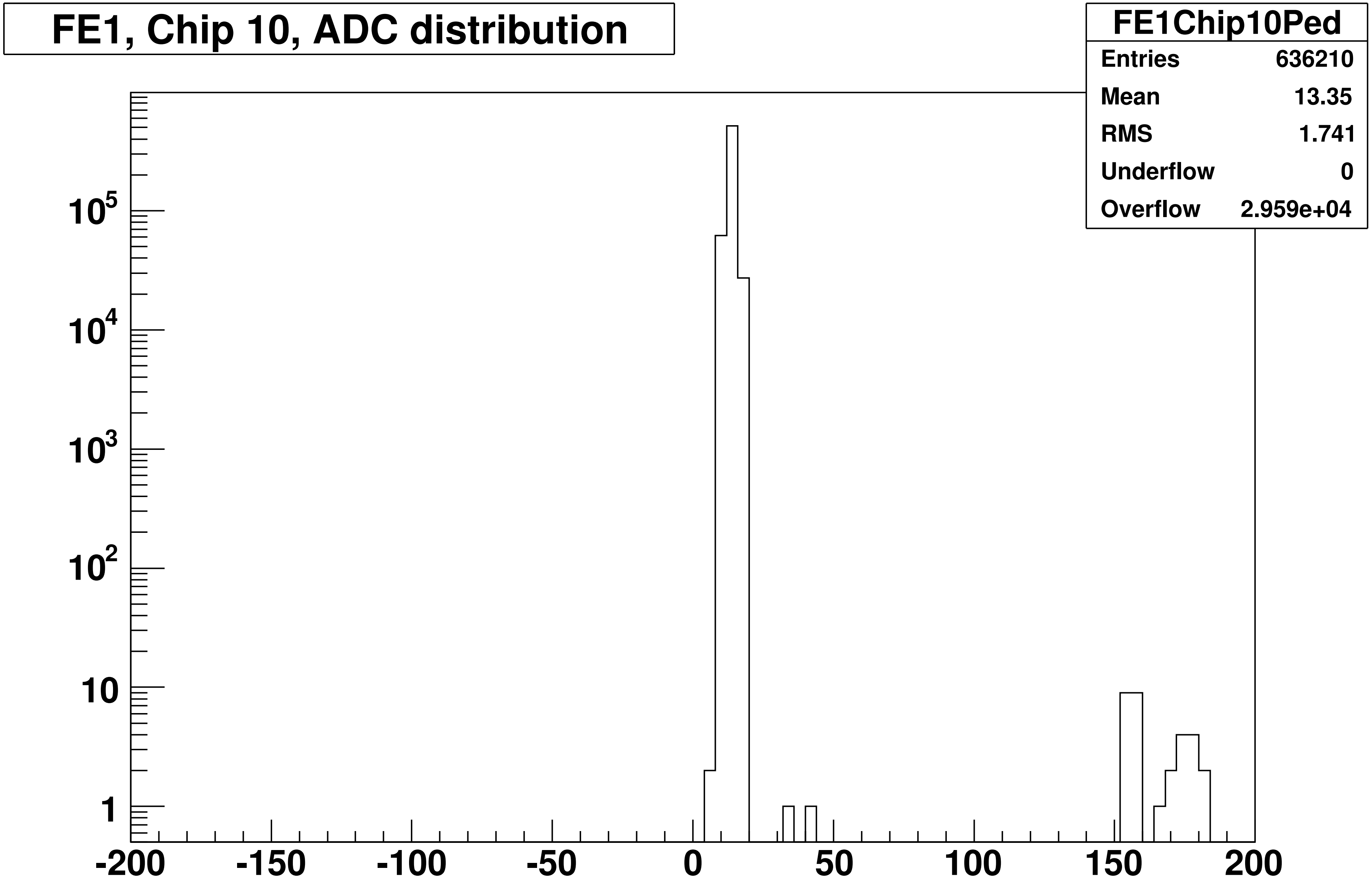
FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 636210
Mean 570.8
RMS 2593
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

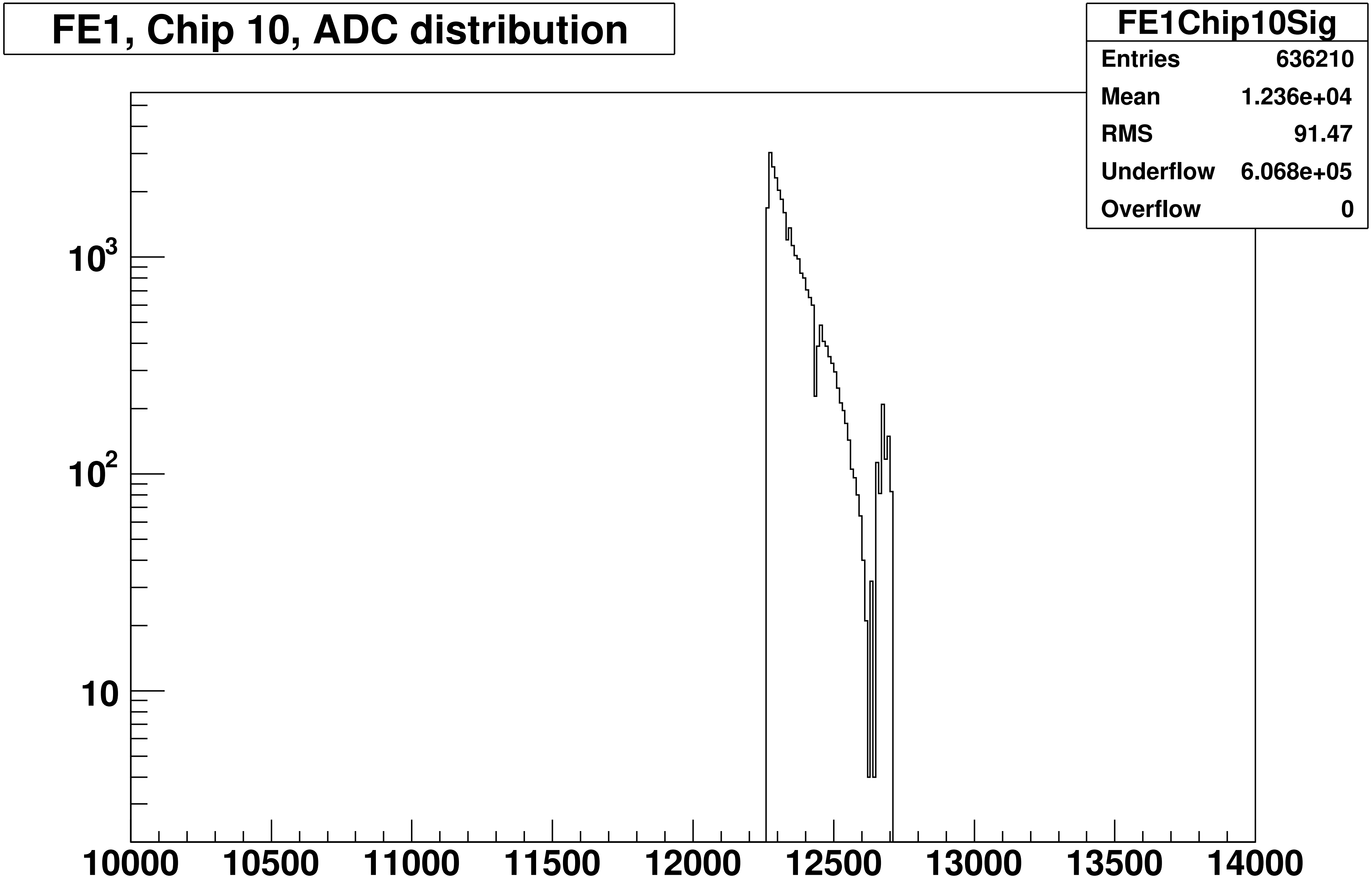

FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 636210
Mean -0.5114
RMS 1.458
Underflow 0
Overflow 2.972e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 636210
Mean 1.234e+04
RMS 91.2
Underflow 6.068e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000

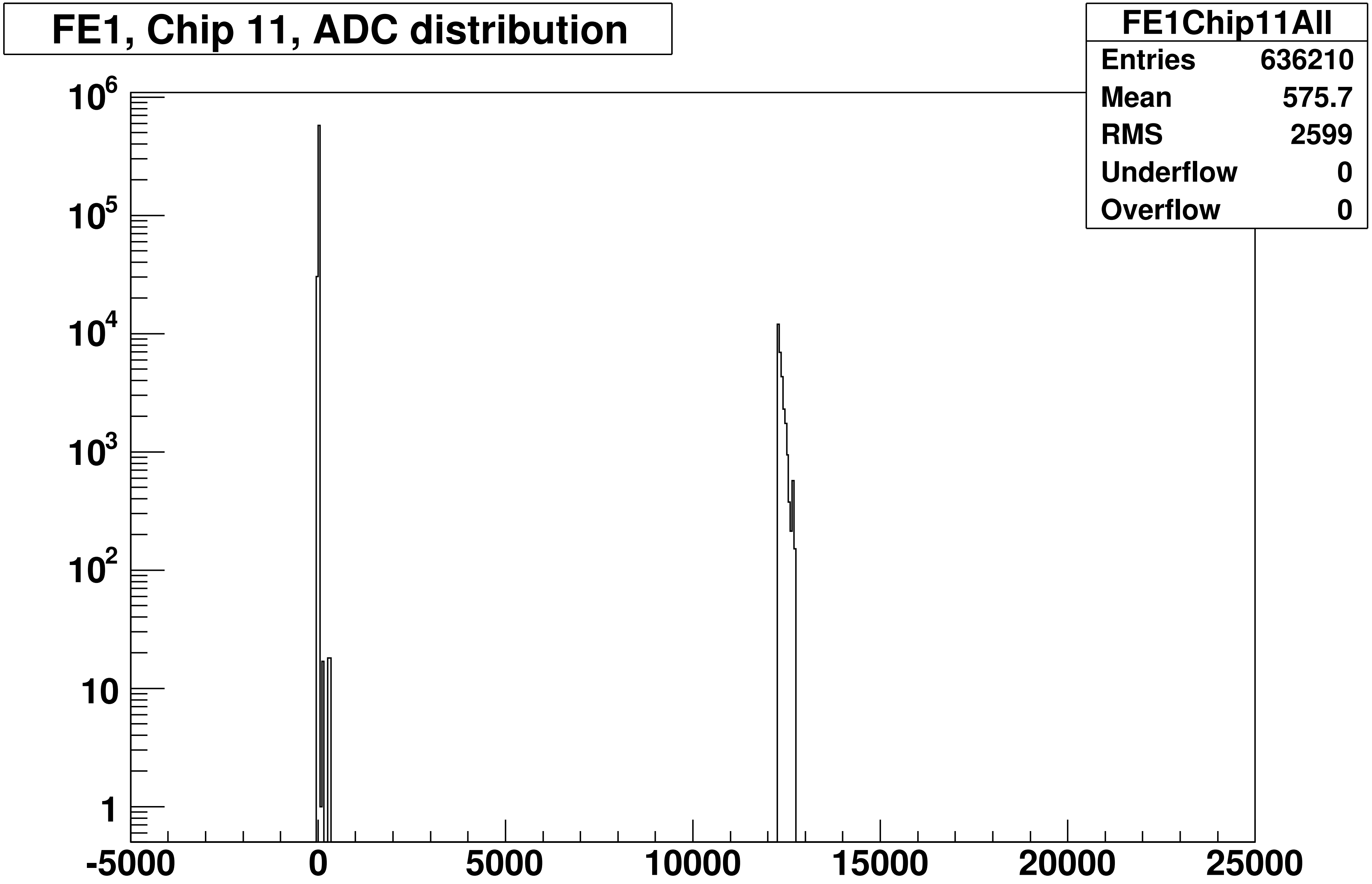


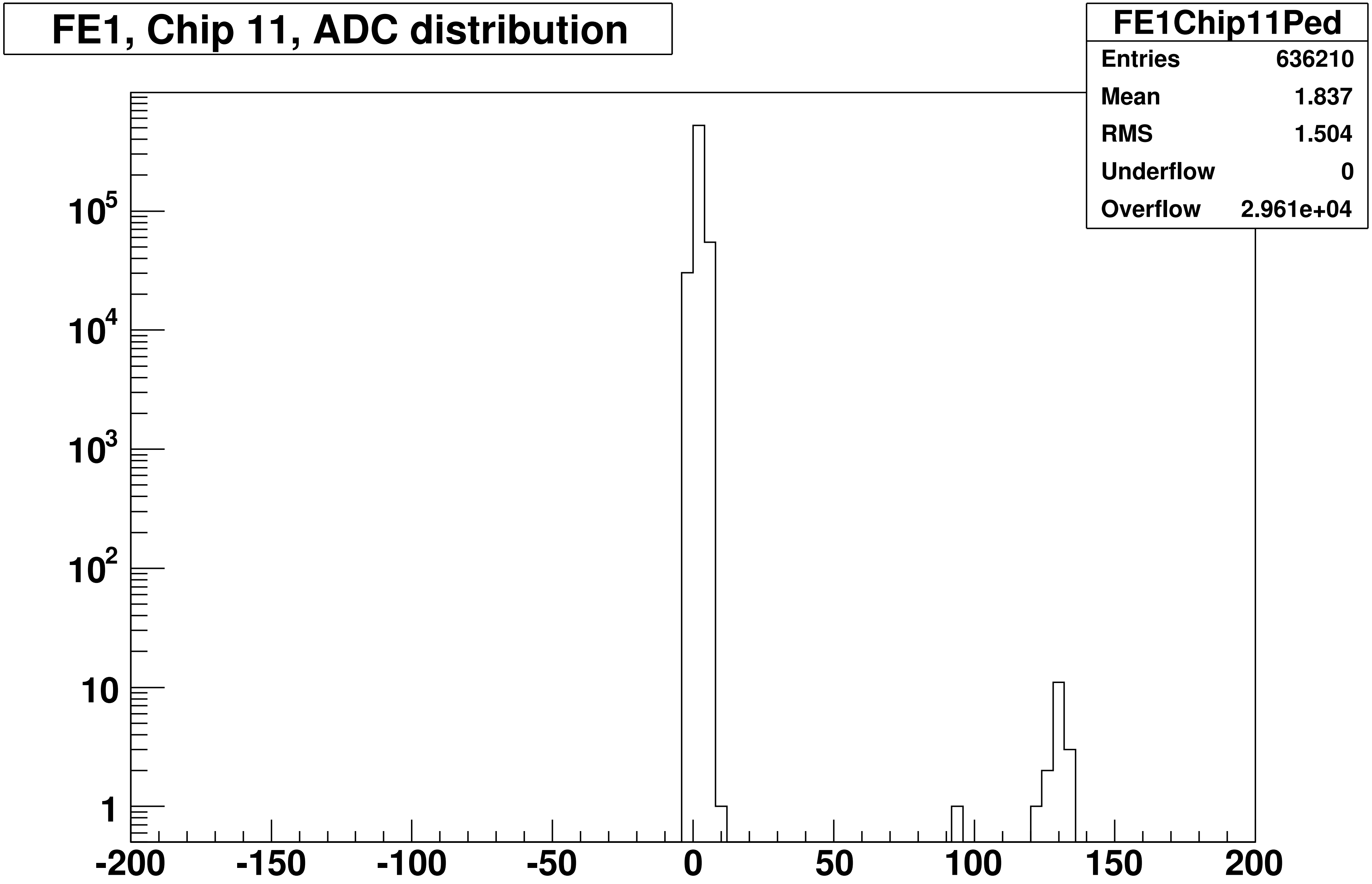
FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 636210
Mean 584.8
RMS 2594
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

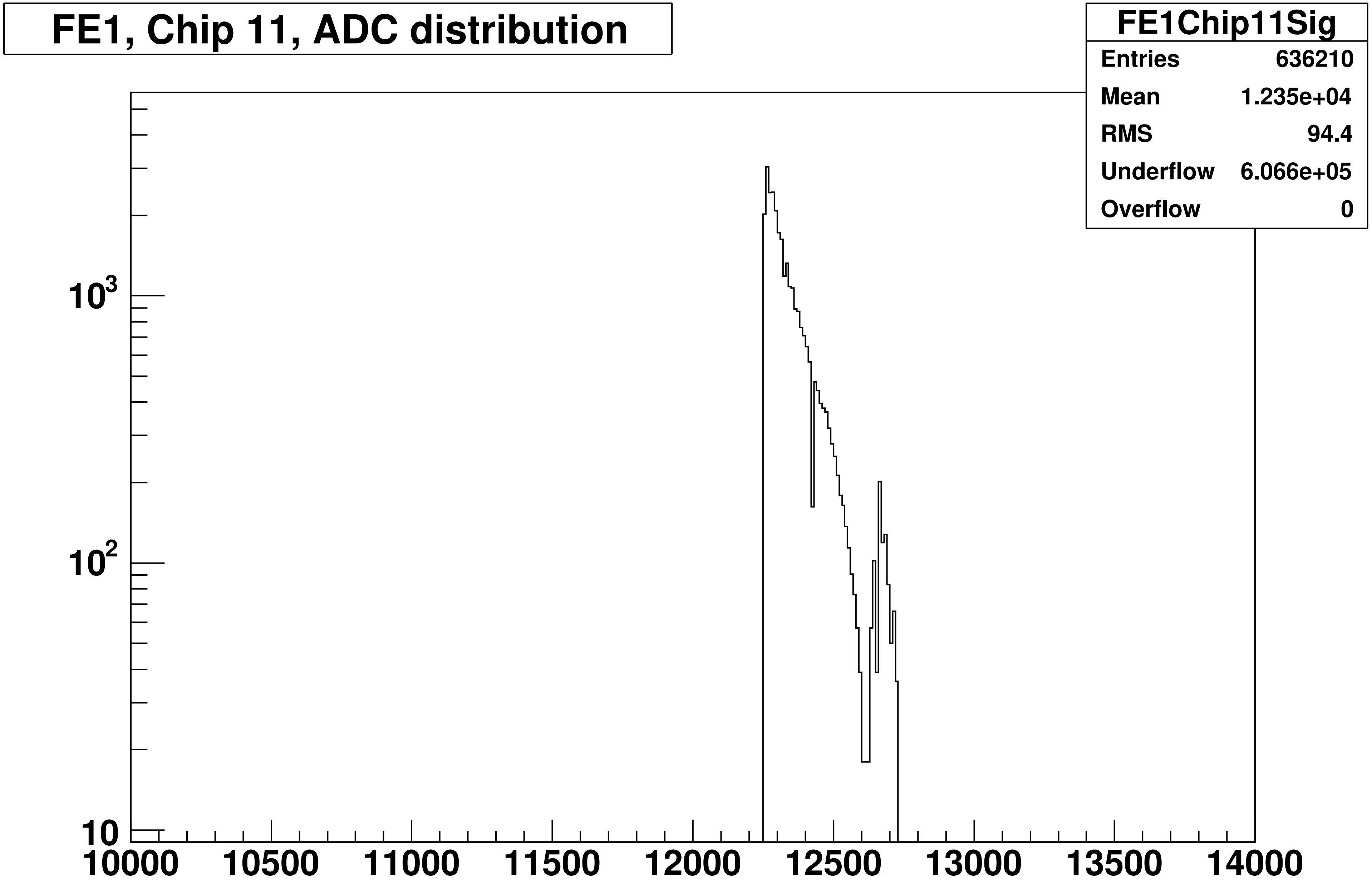

FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 636210
Mean 13.35
RMS 1.741
Underflow 0
Overflow 2.959e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



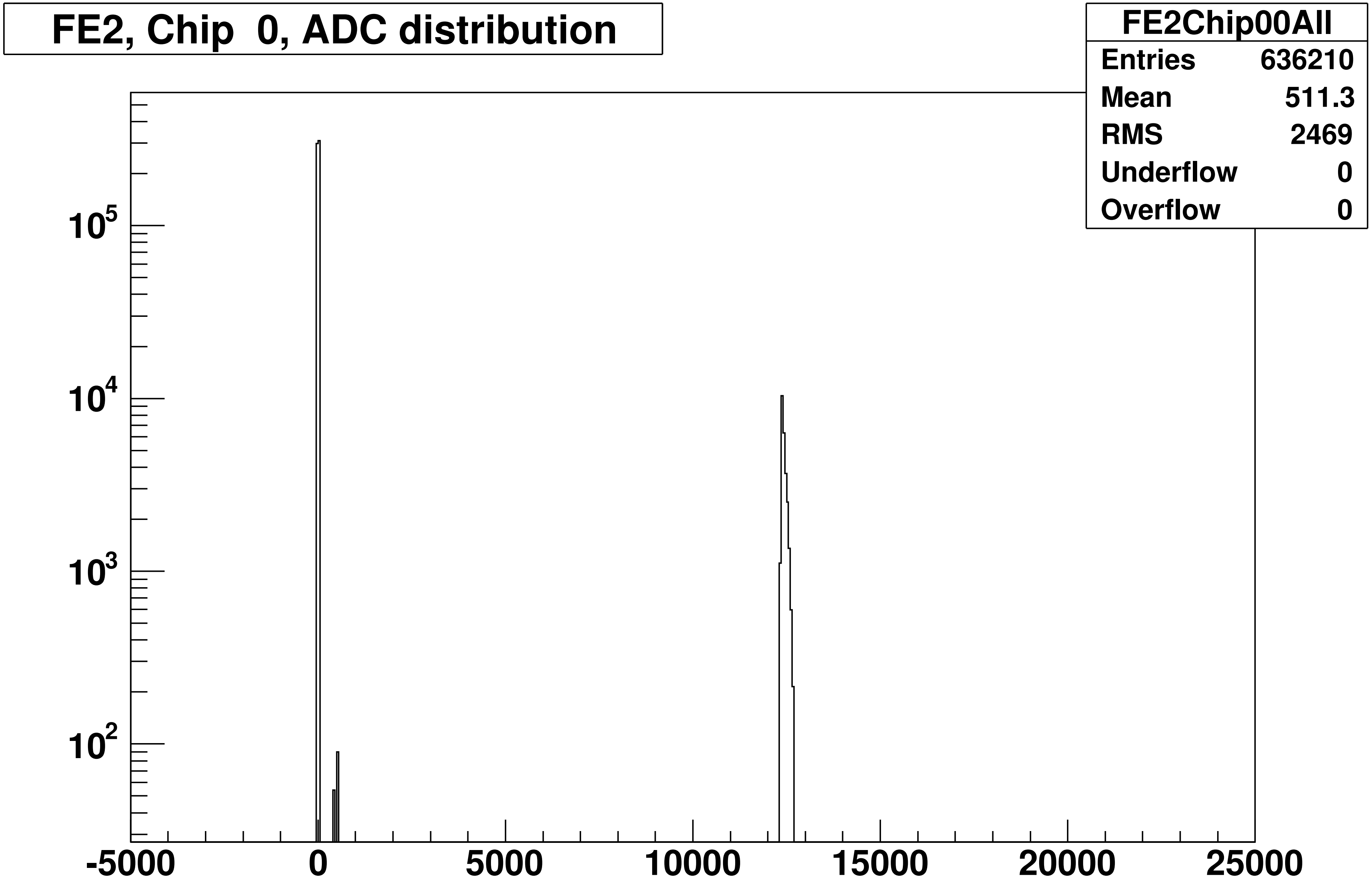
FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 636210
Mean 1.236e+04
RMS 91.47
Underflow 6.068e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

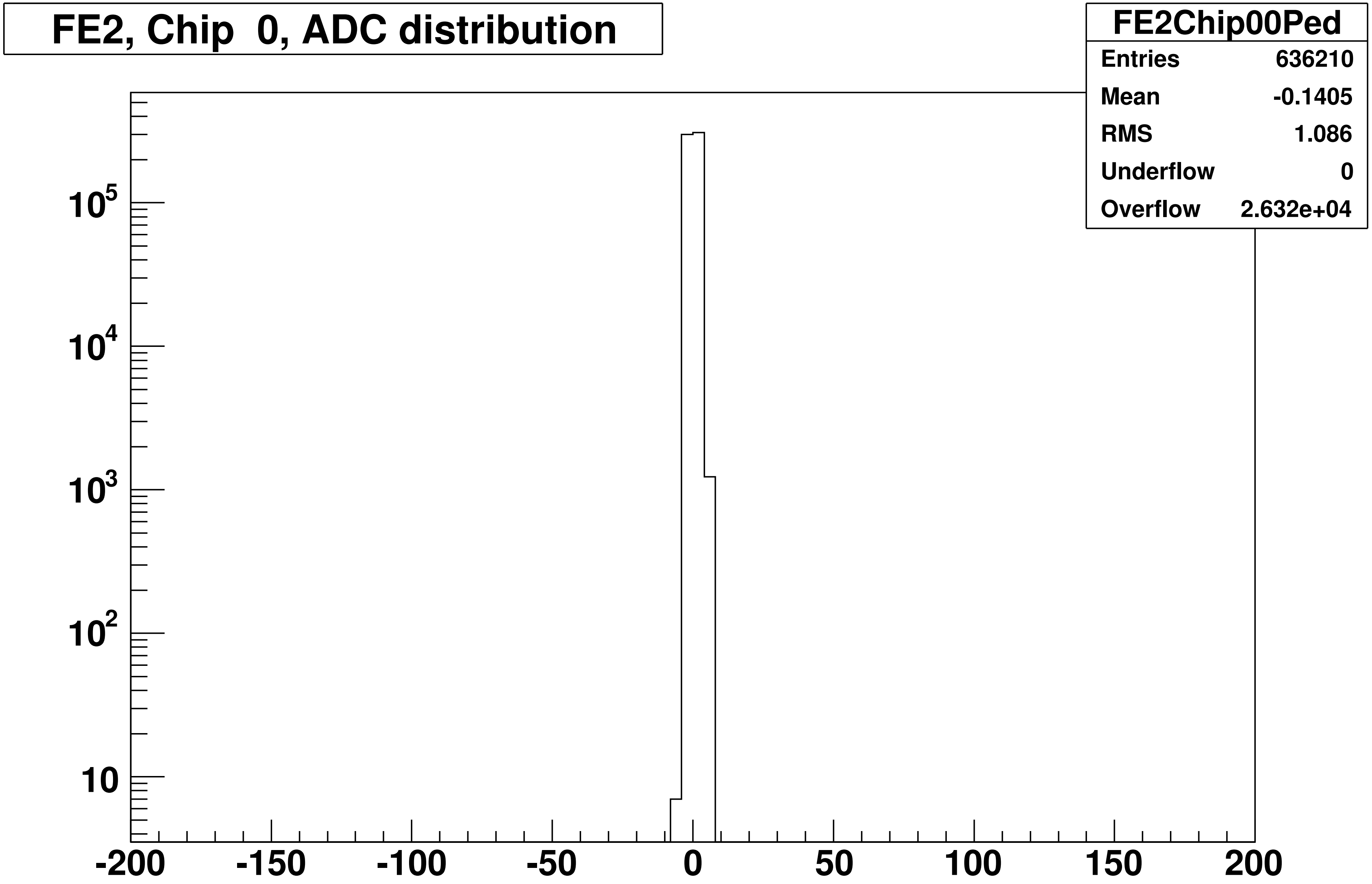

FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 636210
Mean 575.7
RMS 2599
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

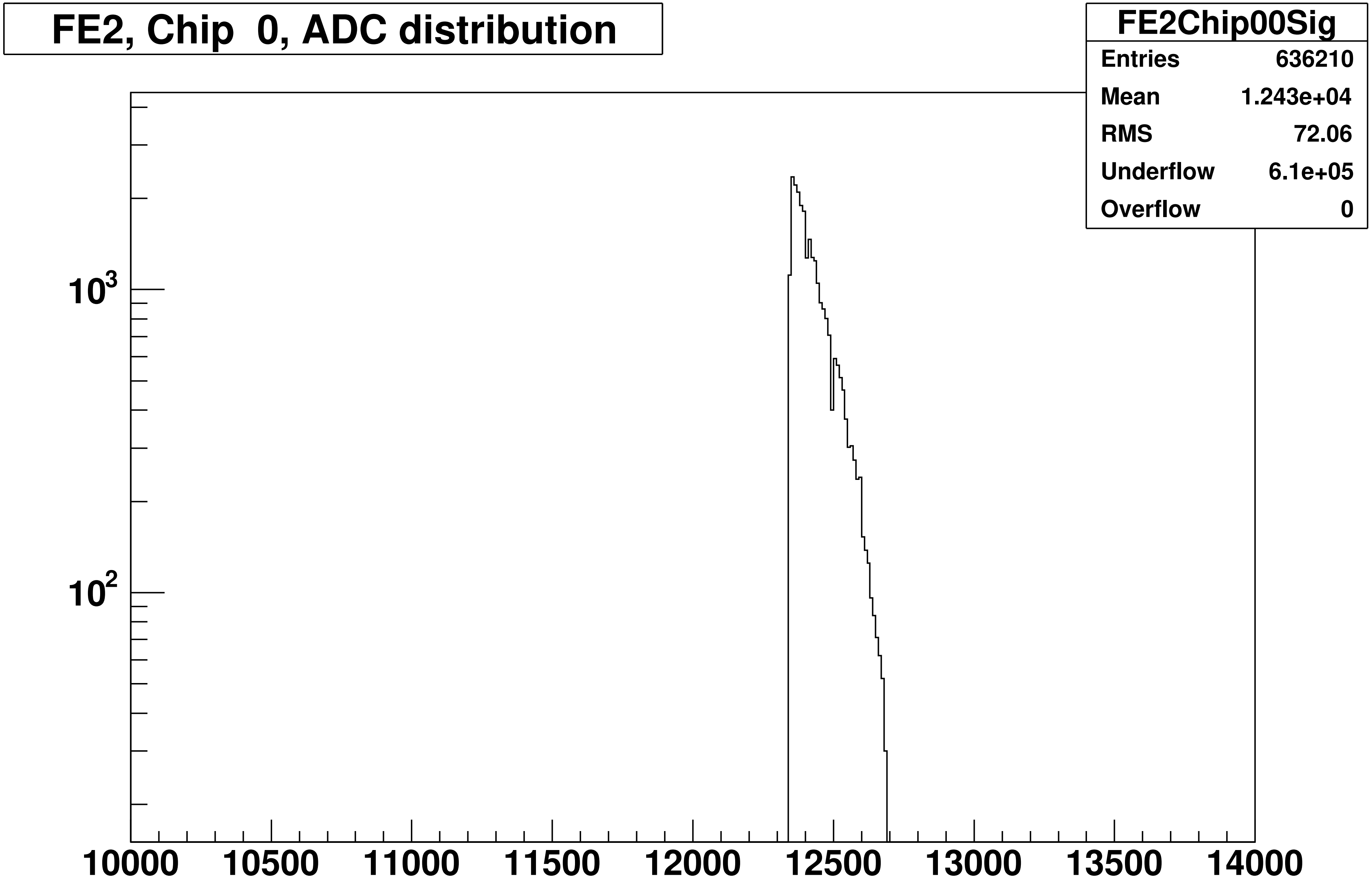

FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 636210
Mean 1.837
RMS 1.504
Underflow 0
Overflow 2.961e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



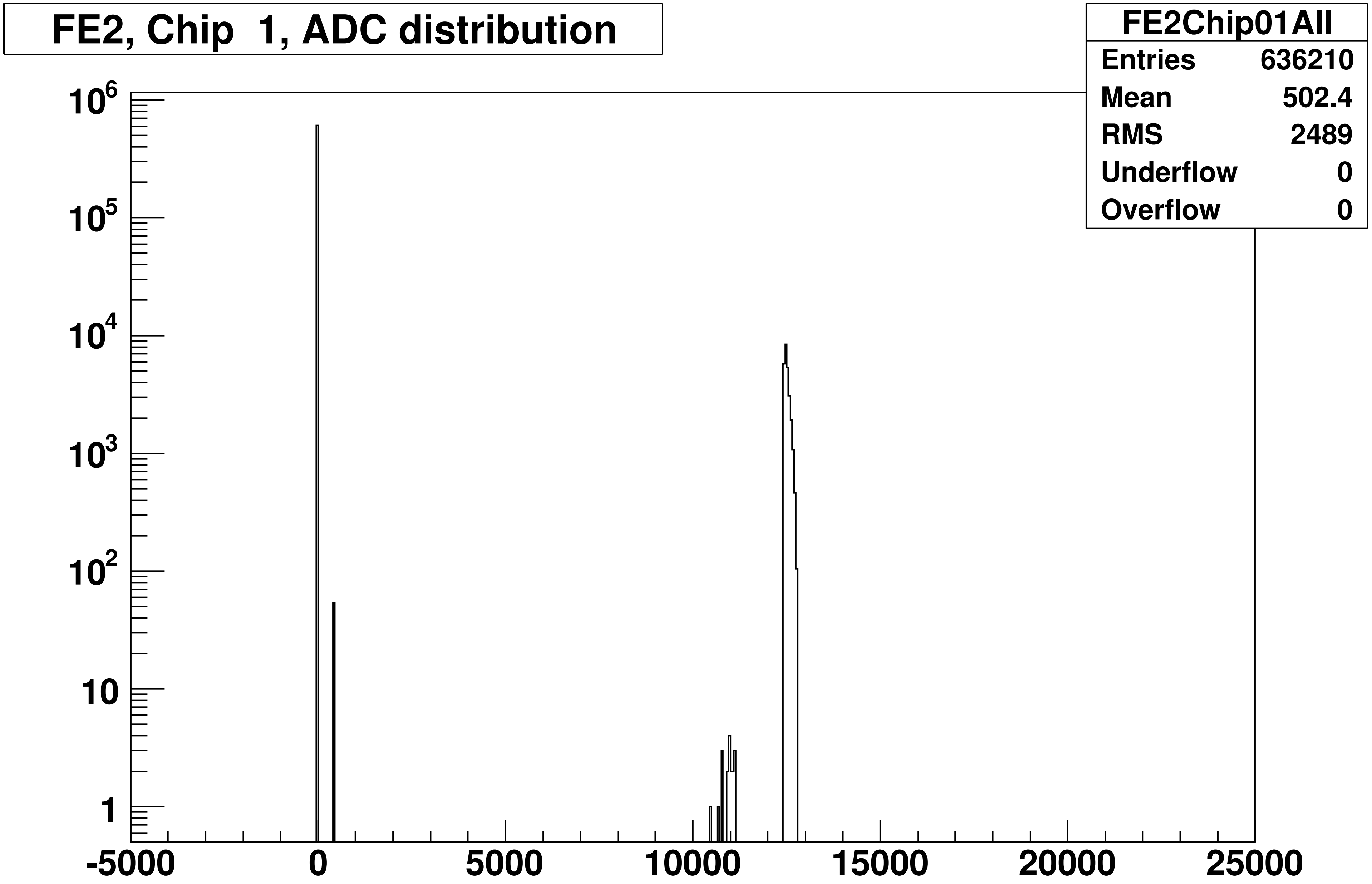
FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 636210
Mean 1.235e+04
RMS 94.4
Underflow 6.066e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000

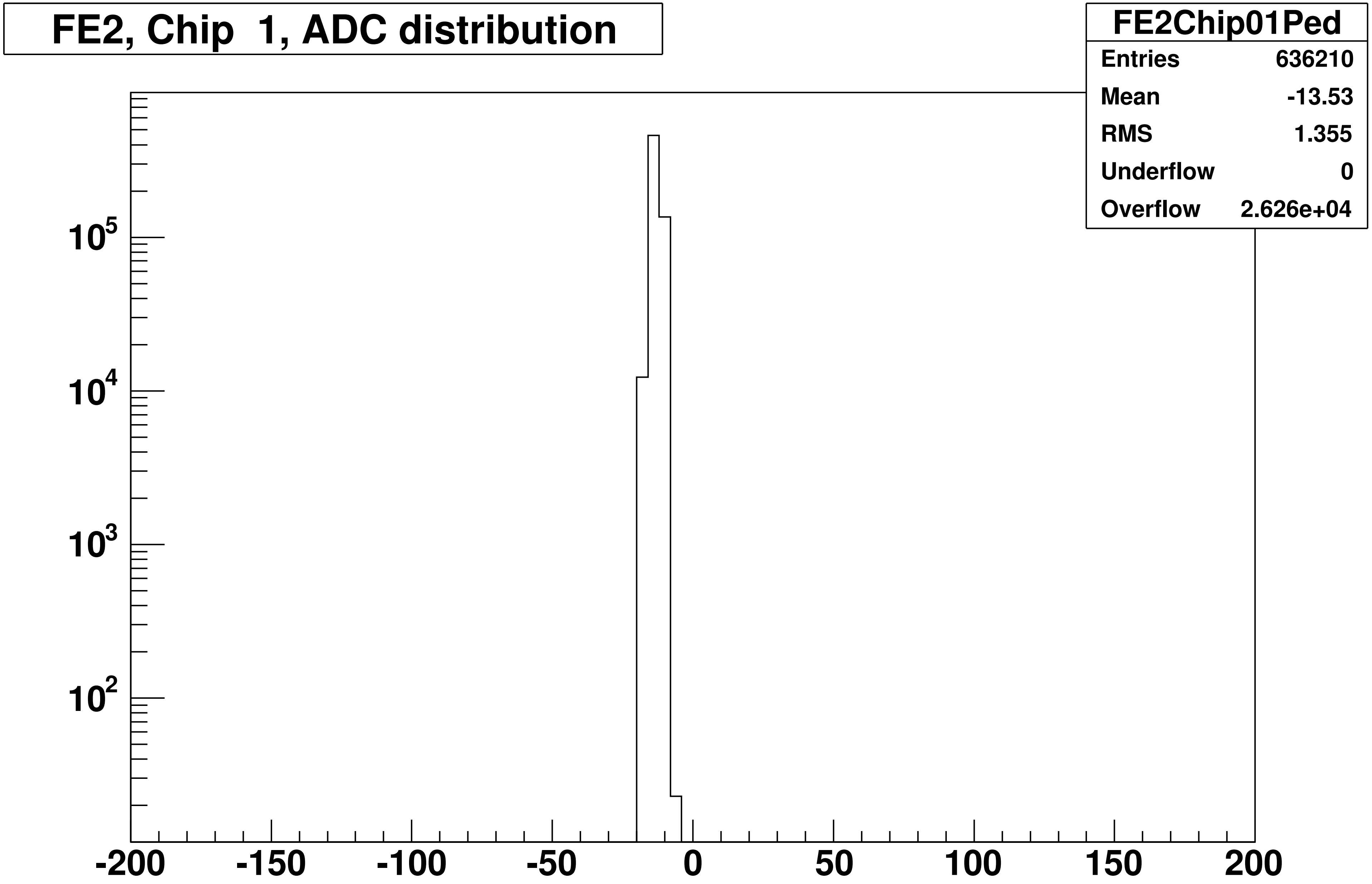

FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 636210
Mean 511.3
RMS 2469
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

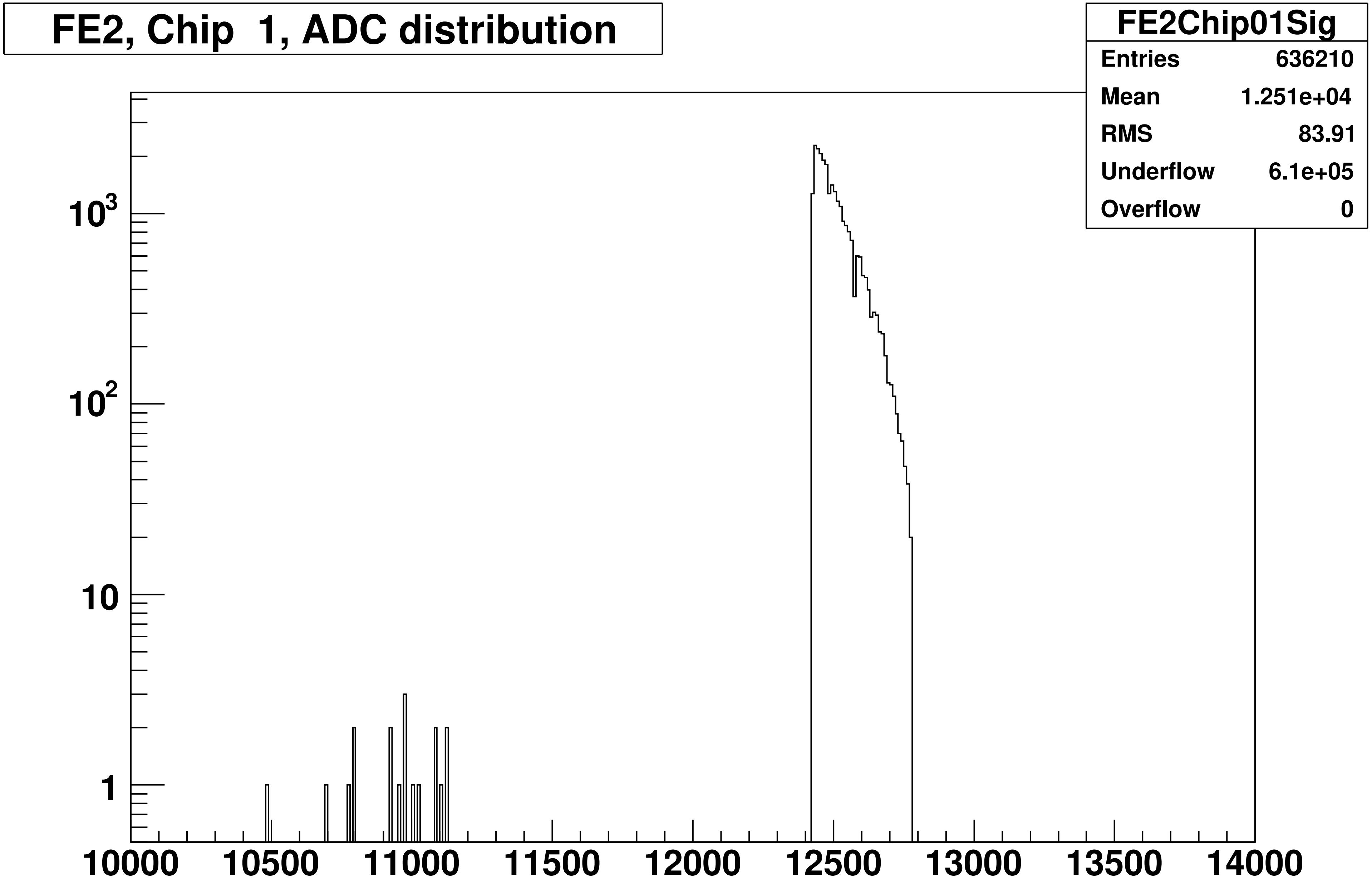

FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 636210
Mean -0.1405
RMS 1.086
Underflow 0
Overflow 2.632e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

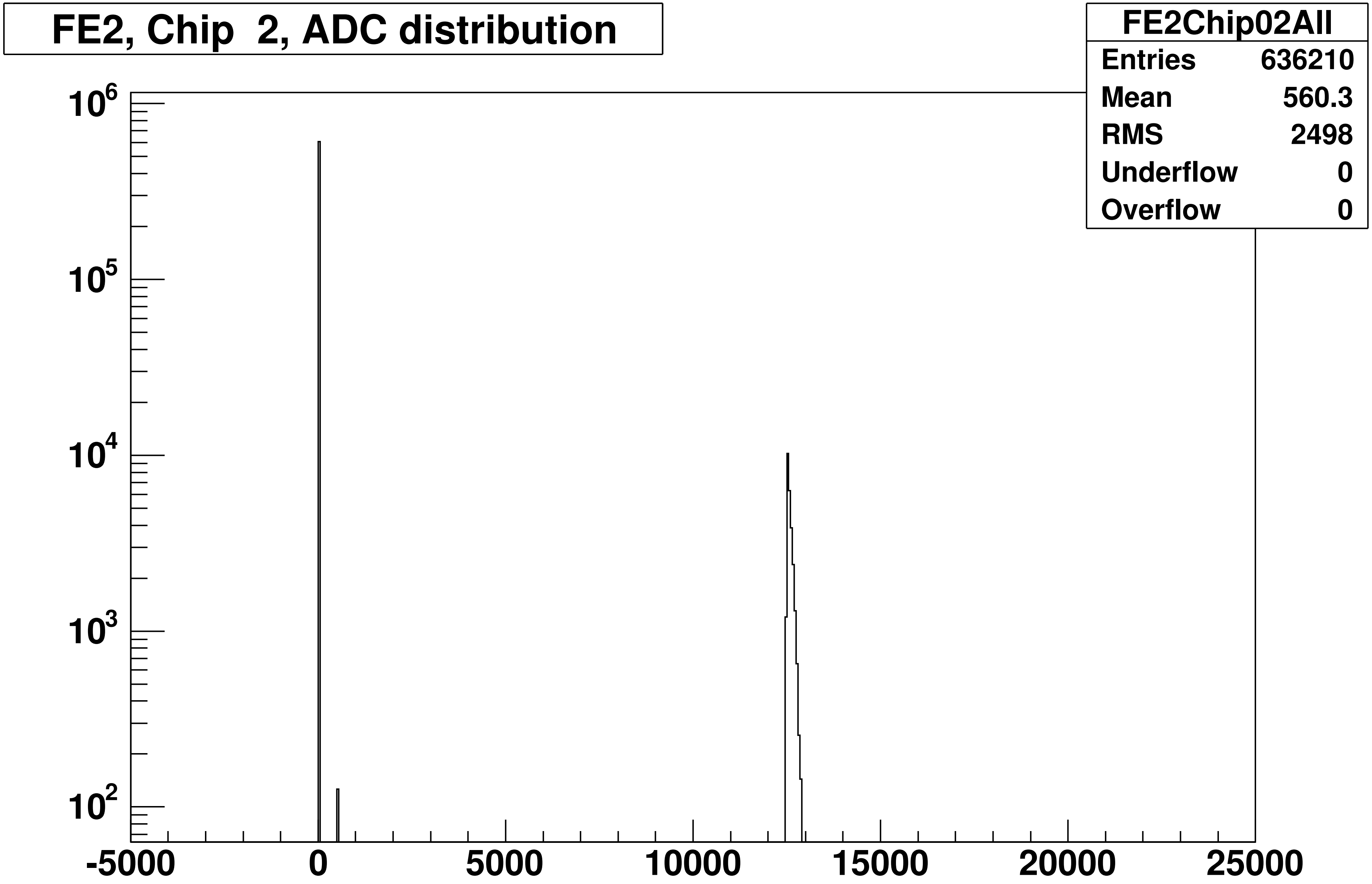


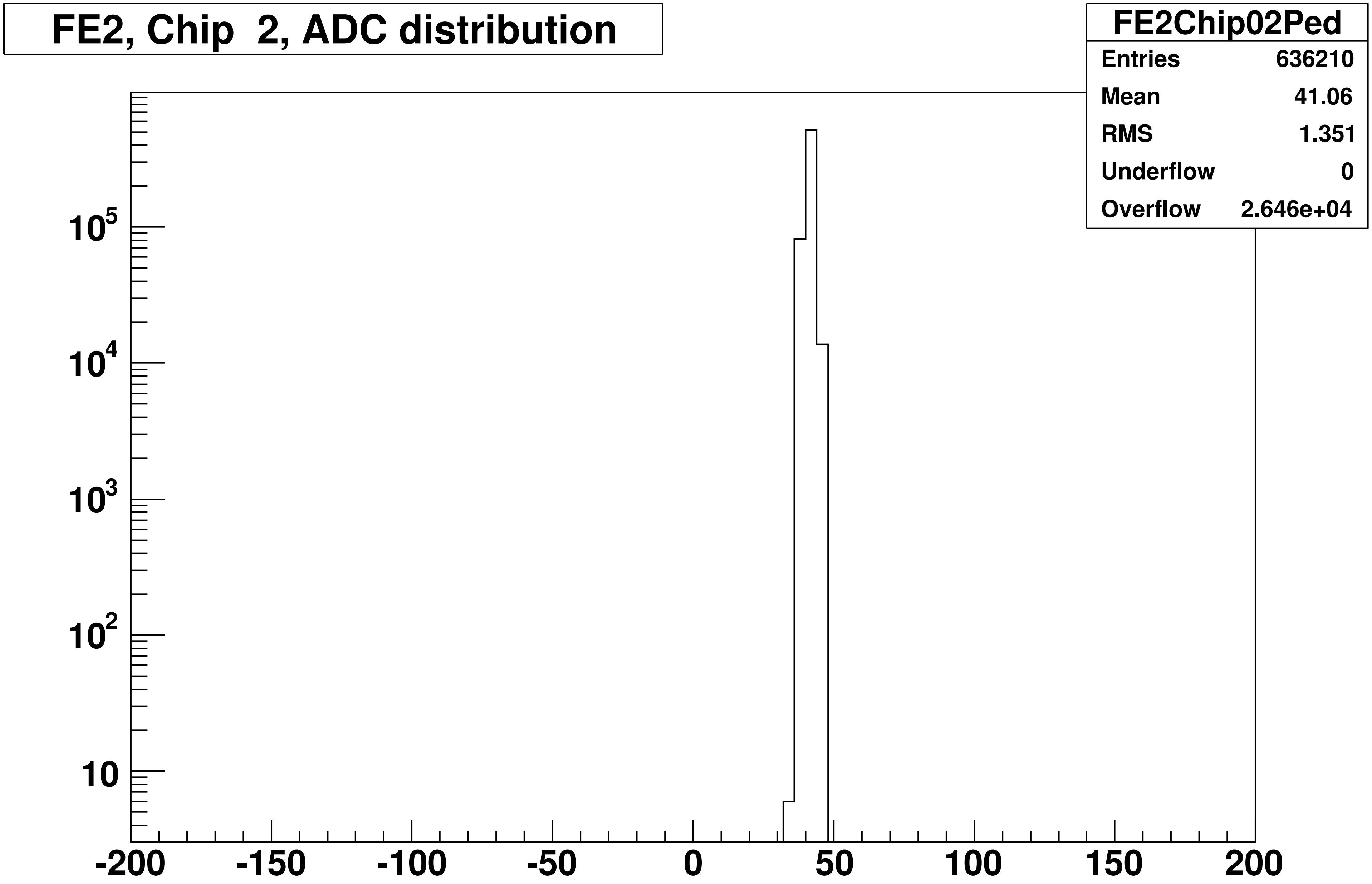
FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 636210
Mean 1.243e+04
RMS 72.06
Underflow 6.1e+05
Overflow 0
10^3
10^2
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 636210
Mean 502.4
RMS 2489
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

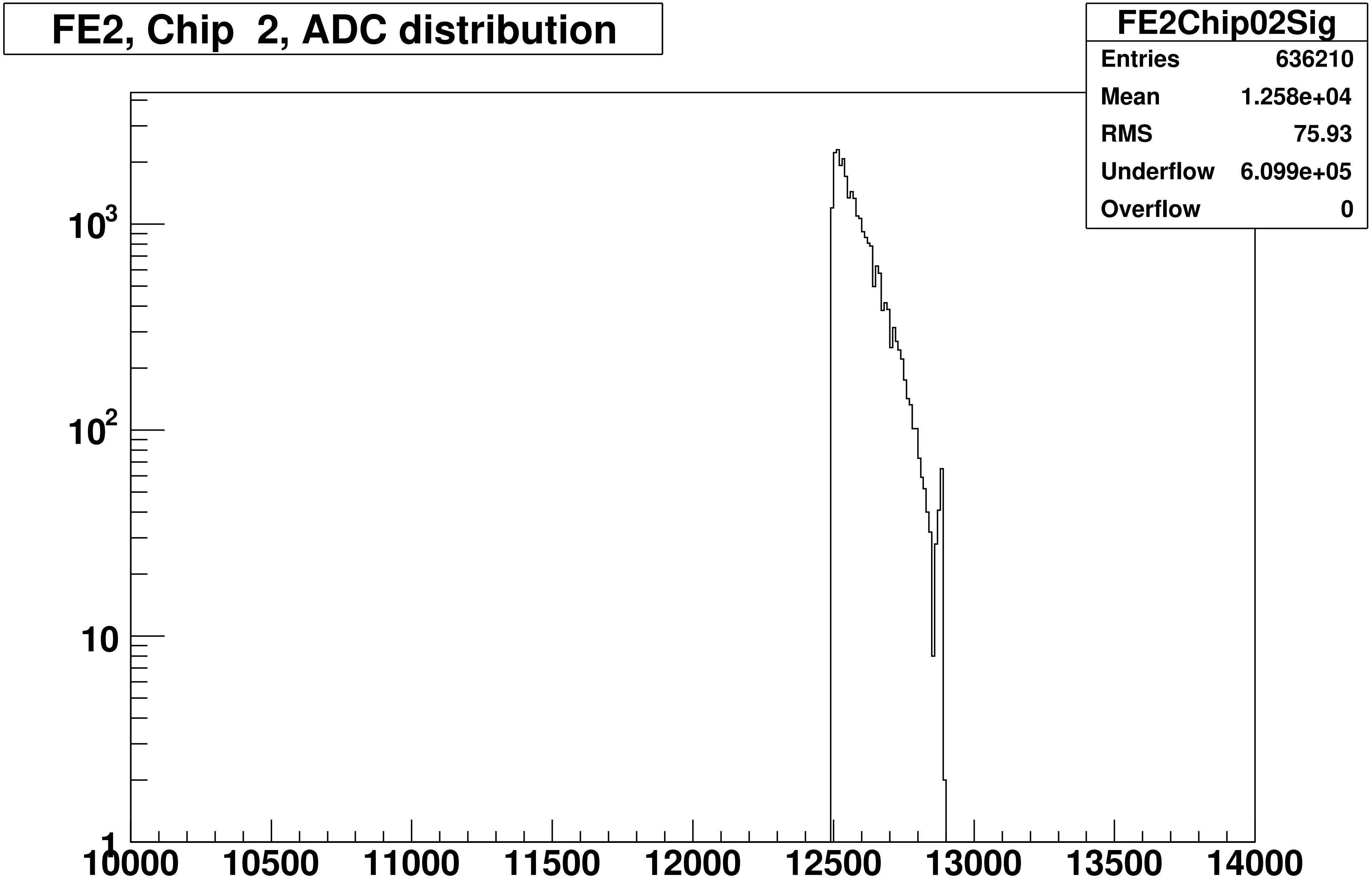

FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 636210
Mean -13.53
RMS 1.355
Underflow 0
Overflow 2.626e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 636210
Mean 1.251e+04
RMS 83.91
Underflow 6.1e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

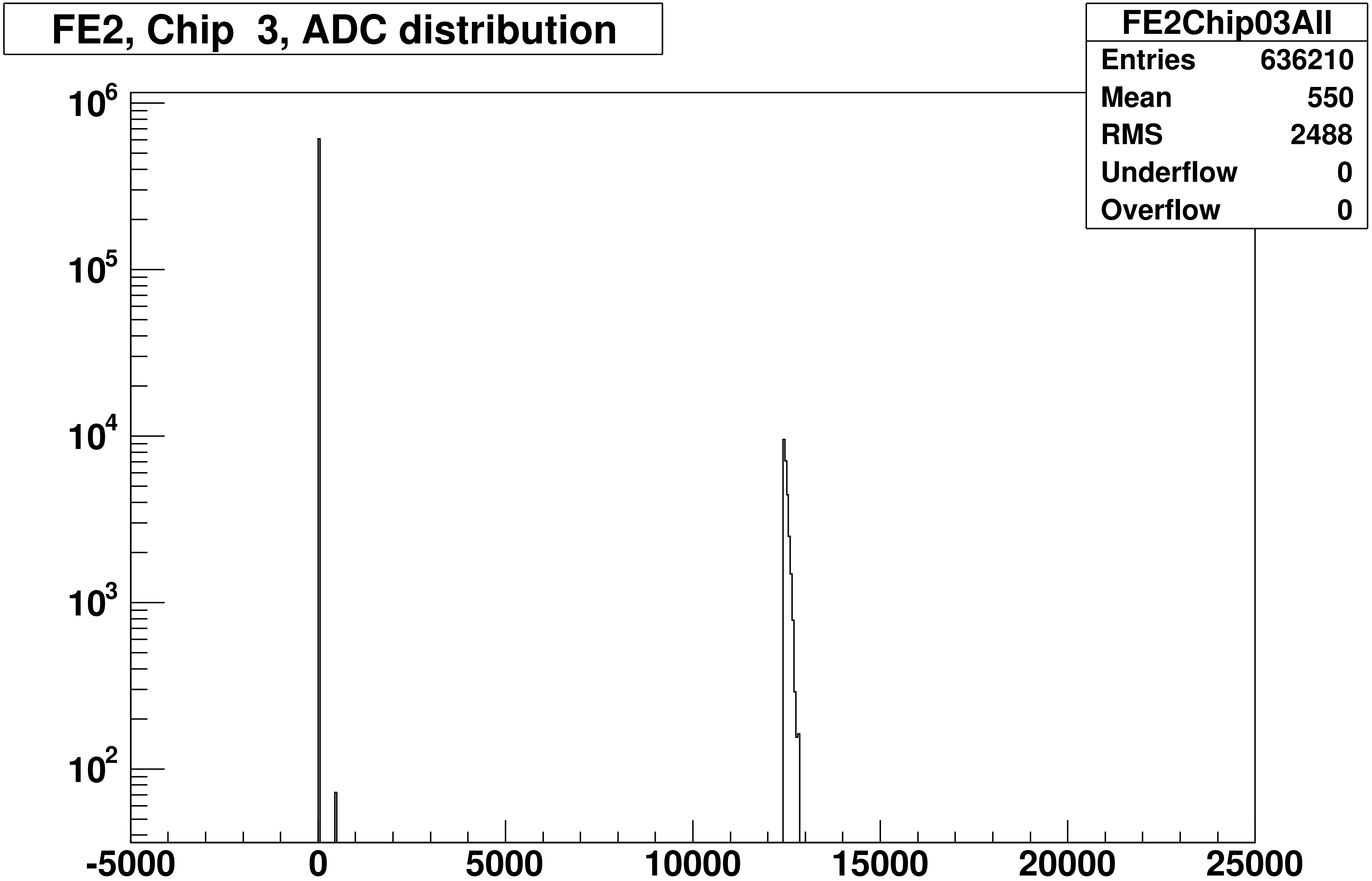

FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 636210
Mean 560.3
RMS 2498
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

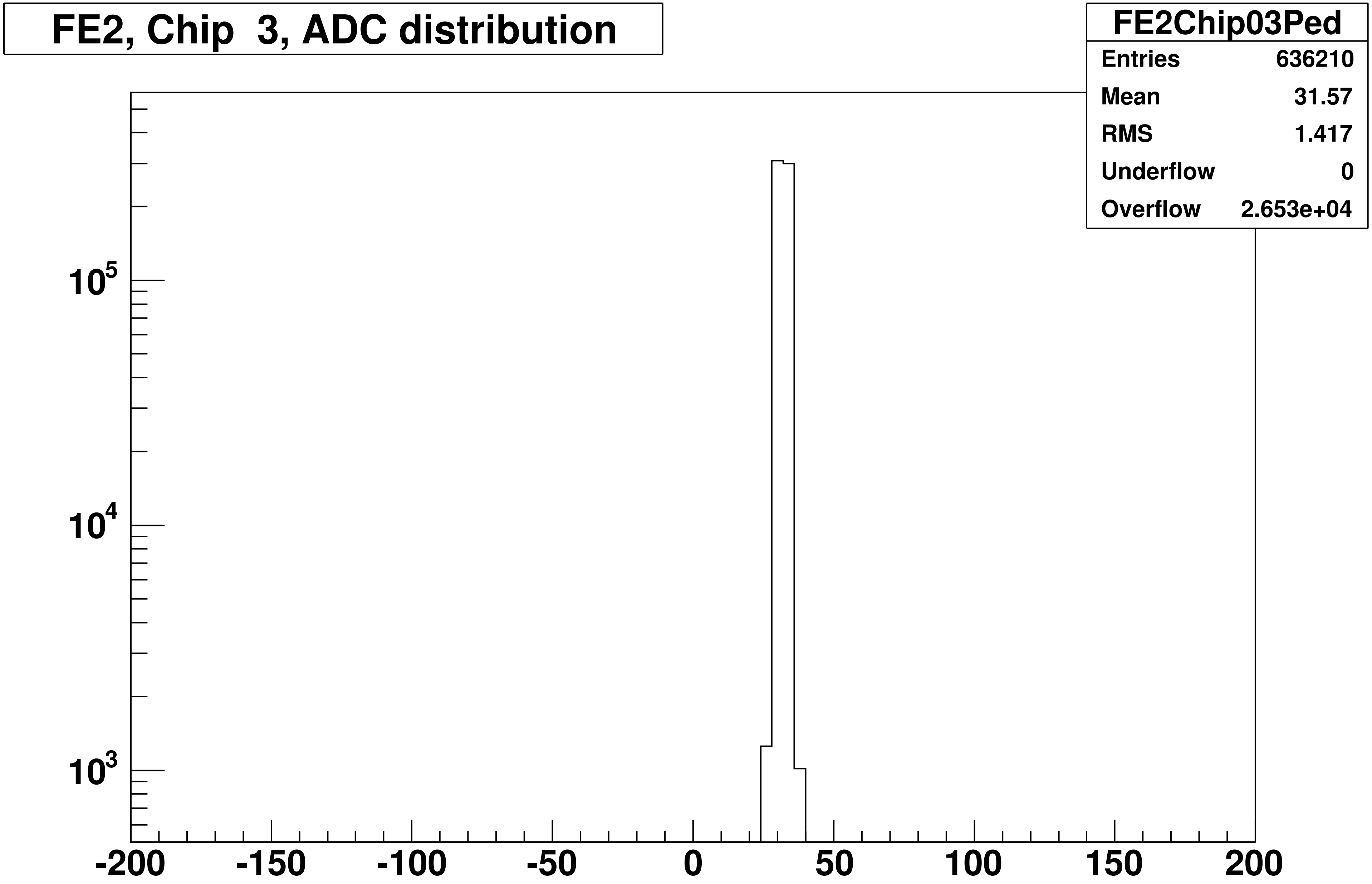


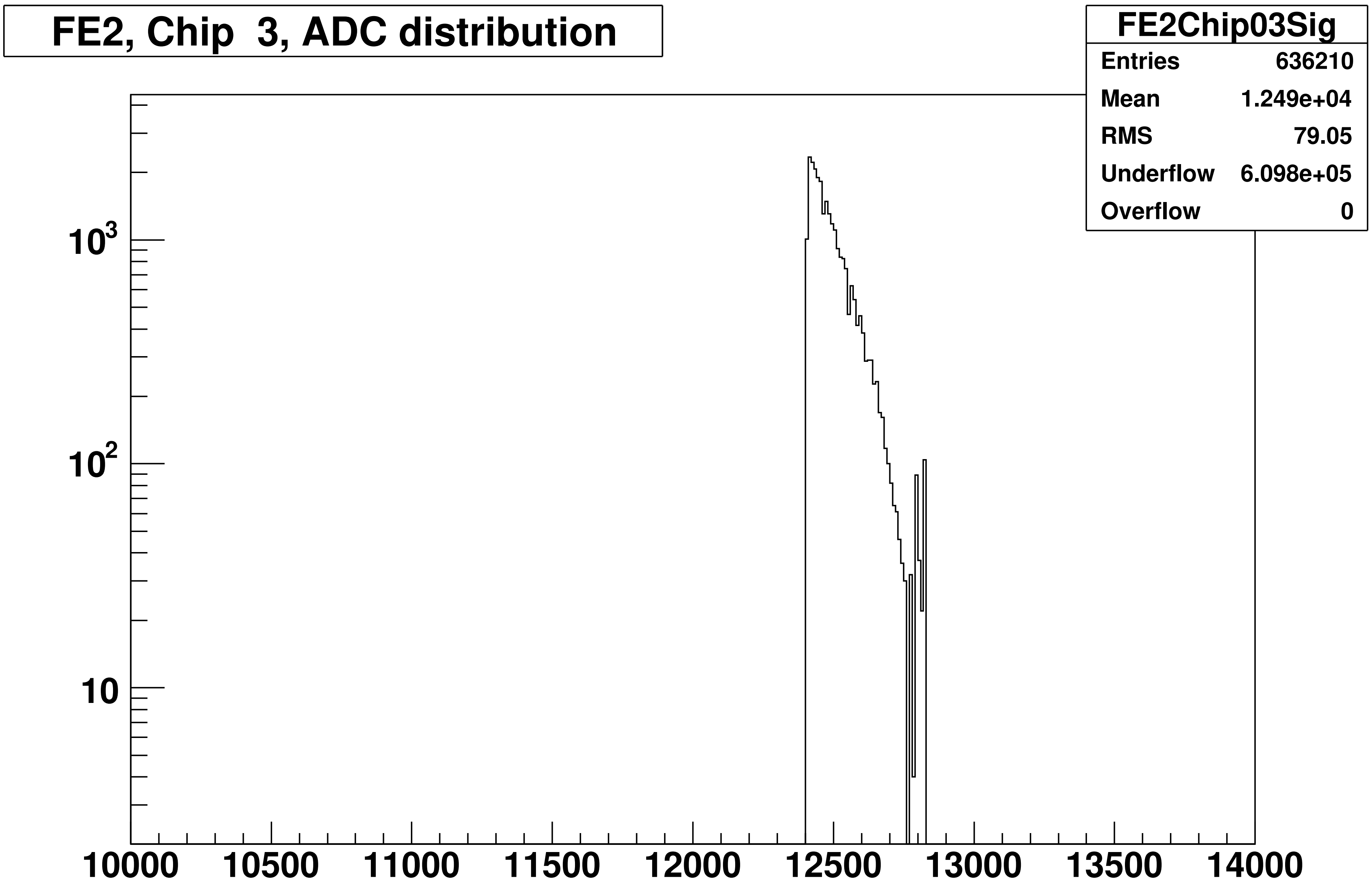
FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 636210
Mean 41.06
RMS 1.351
Underflow 0
Overflow 2.646e+04
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200



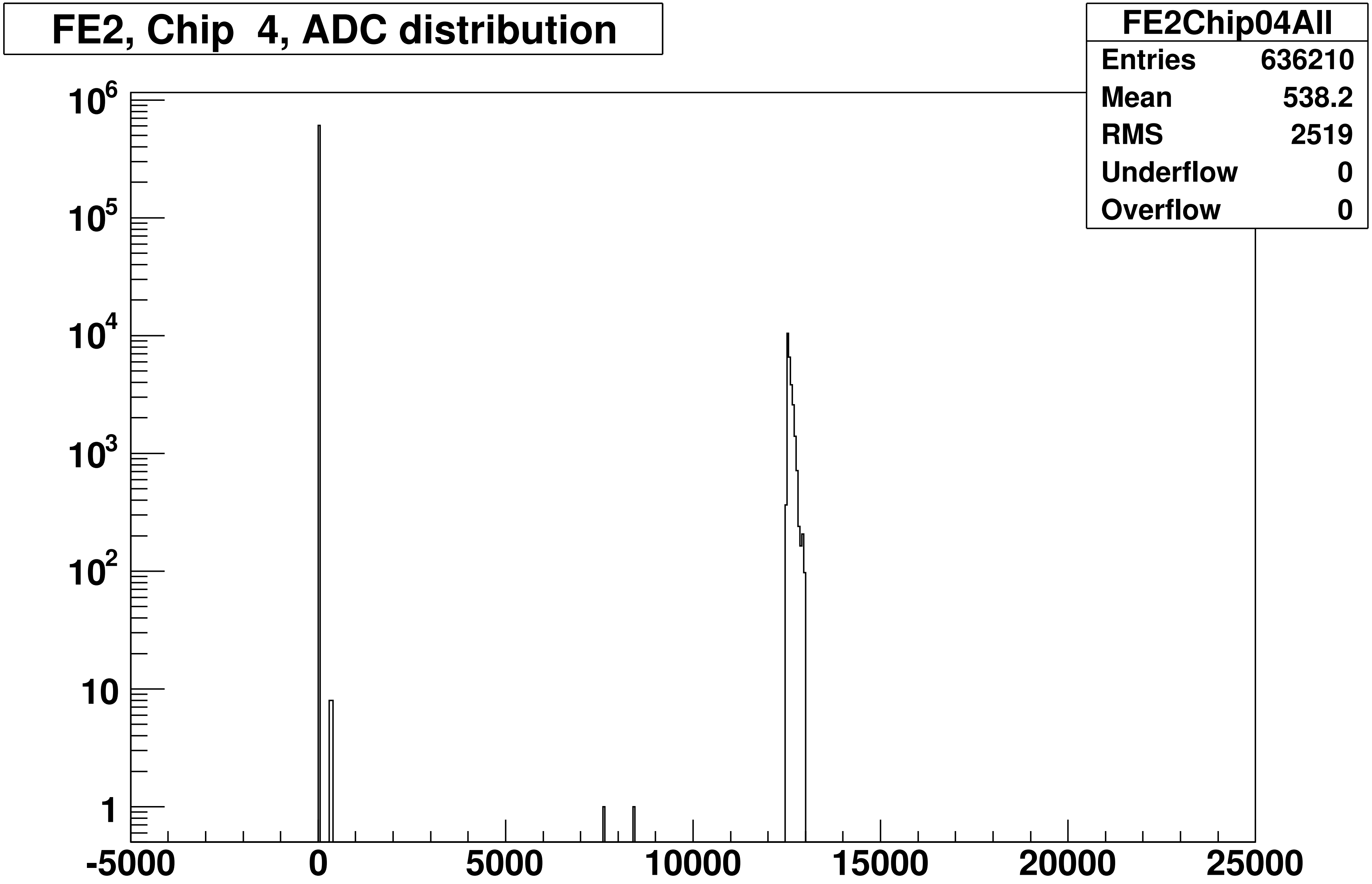
FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 636210
Mean 1.258e+04
RMS 75.93
Underflow 6.099e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

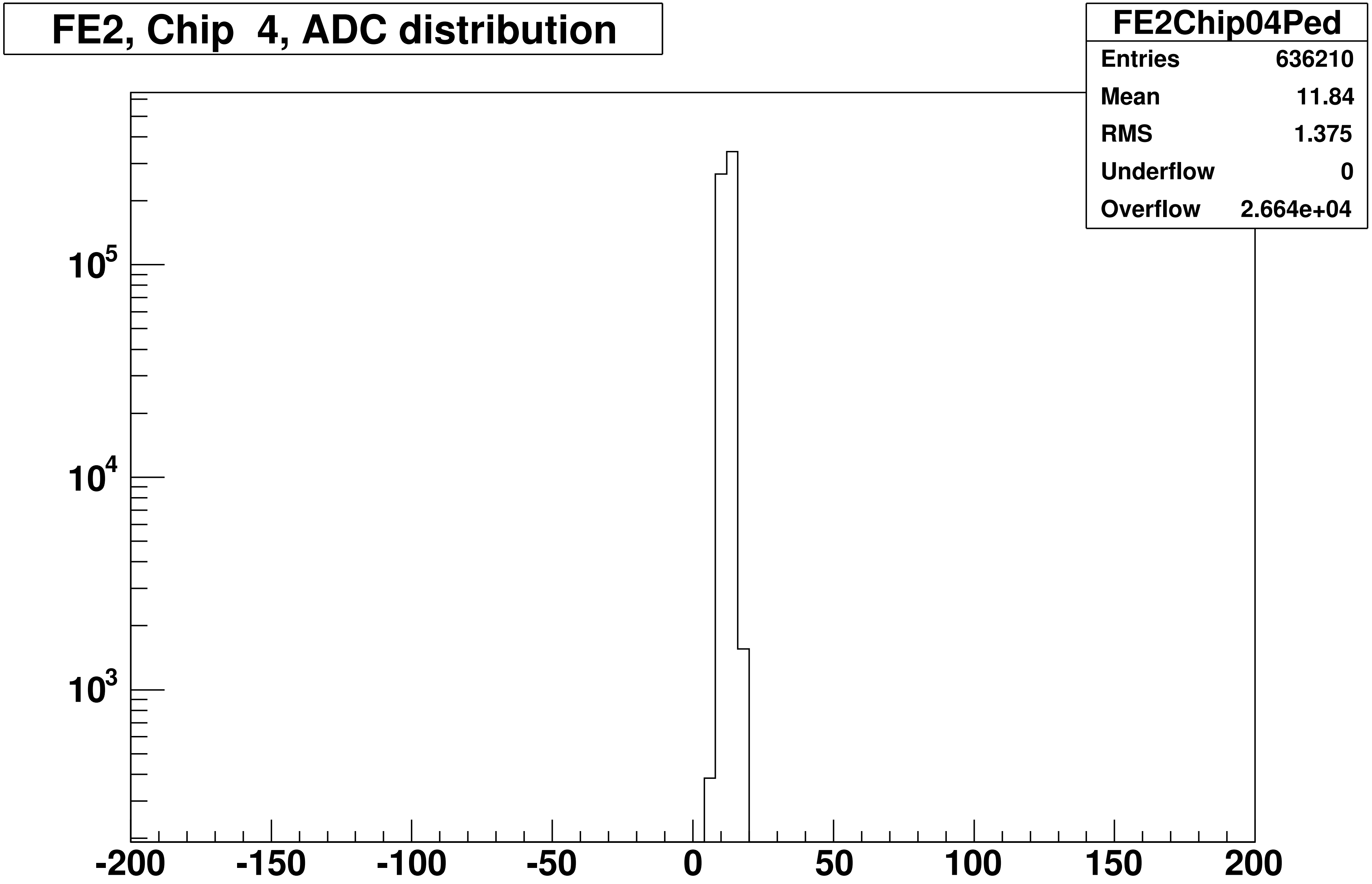

FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 636210
Mean 550
RMS 2488
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

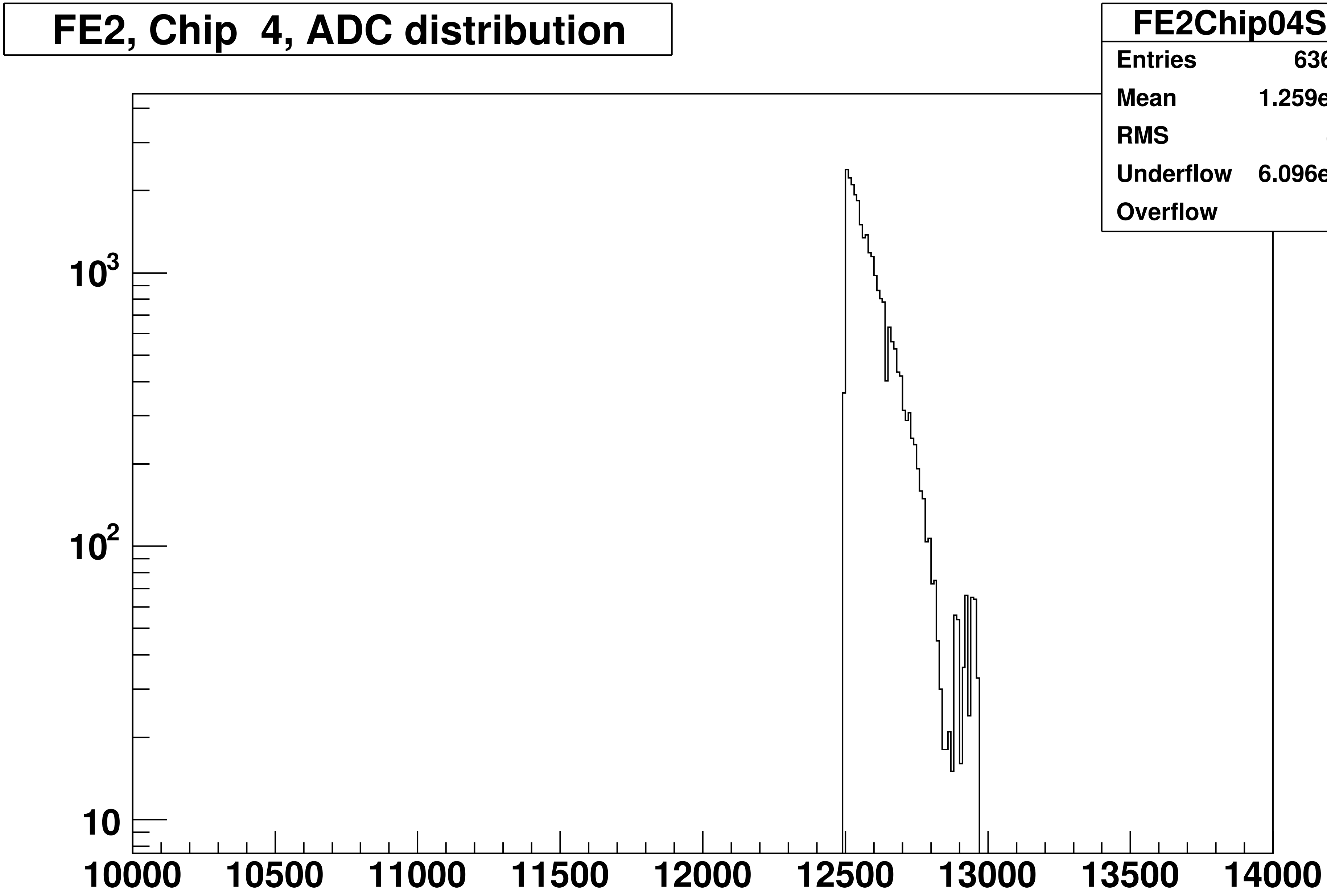

FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 636210
Mean 31.57
RMS 1.417
Underflow 0
Overflow 2.653e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

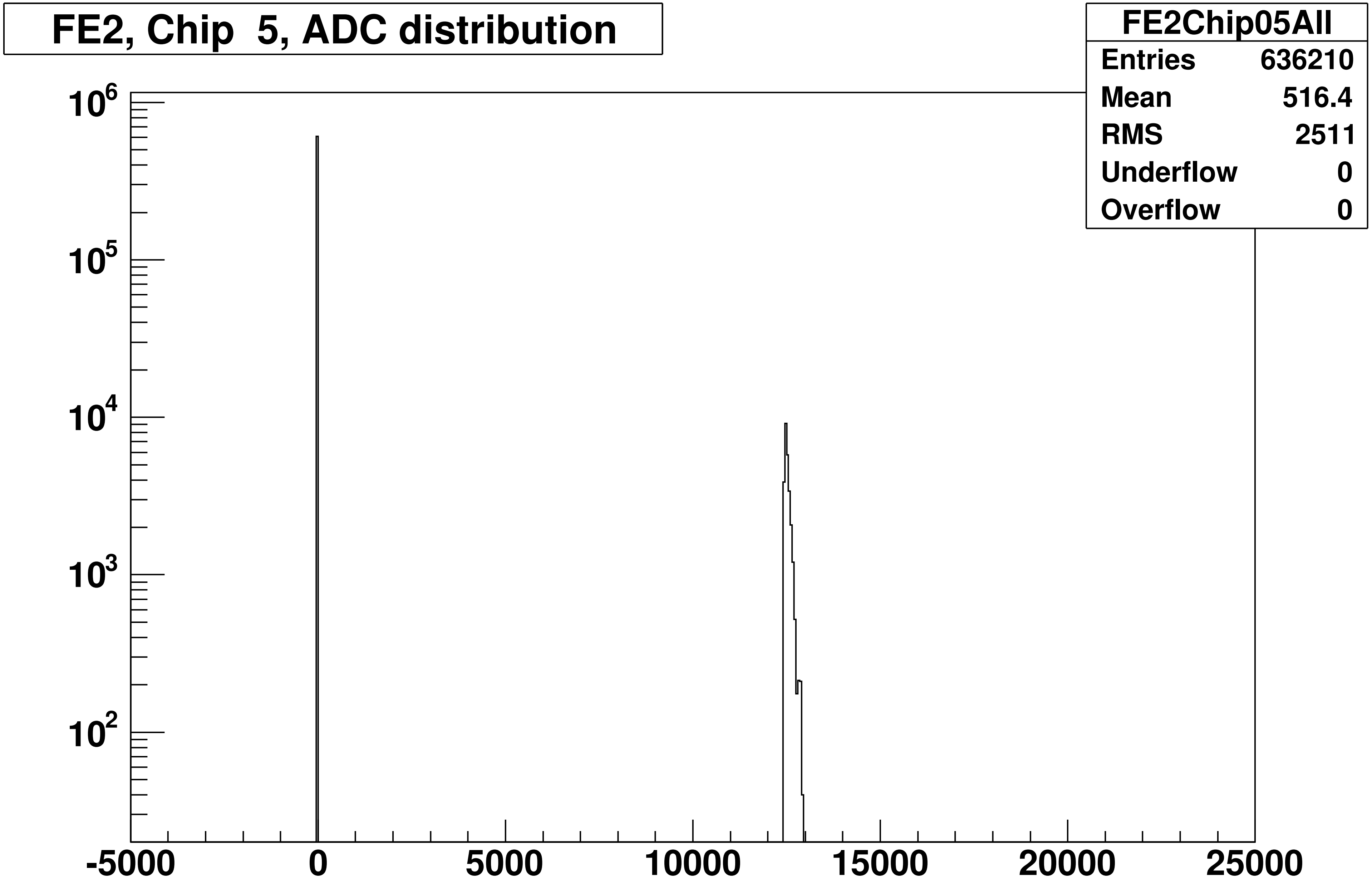


FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 636210
Mean 1.249e+04
RMS 79.05
Underflow 6.098e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

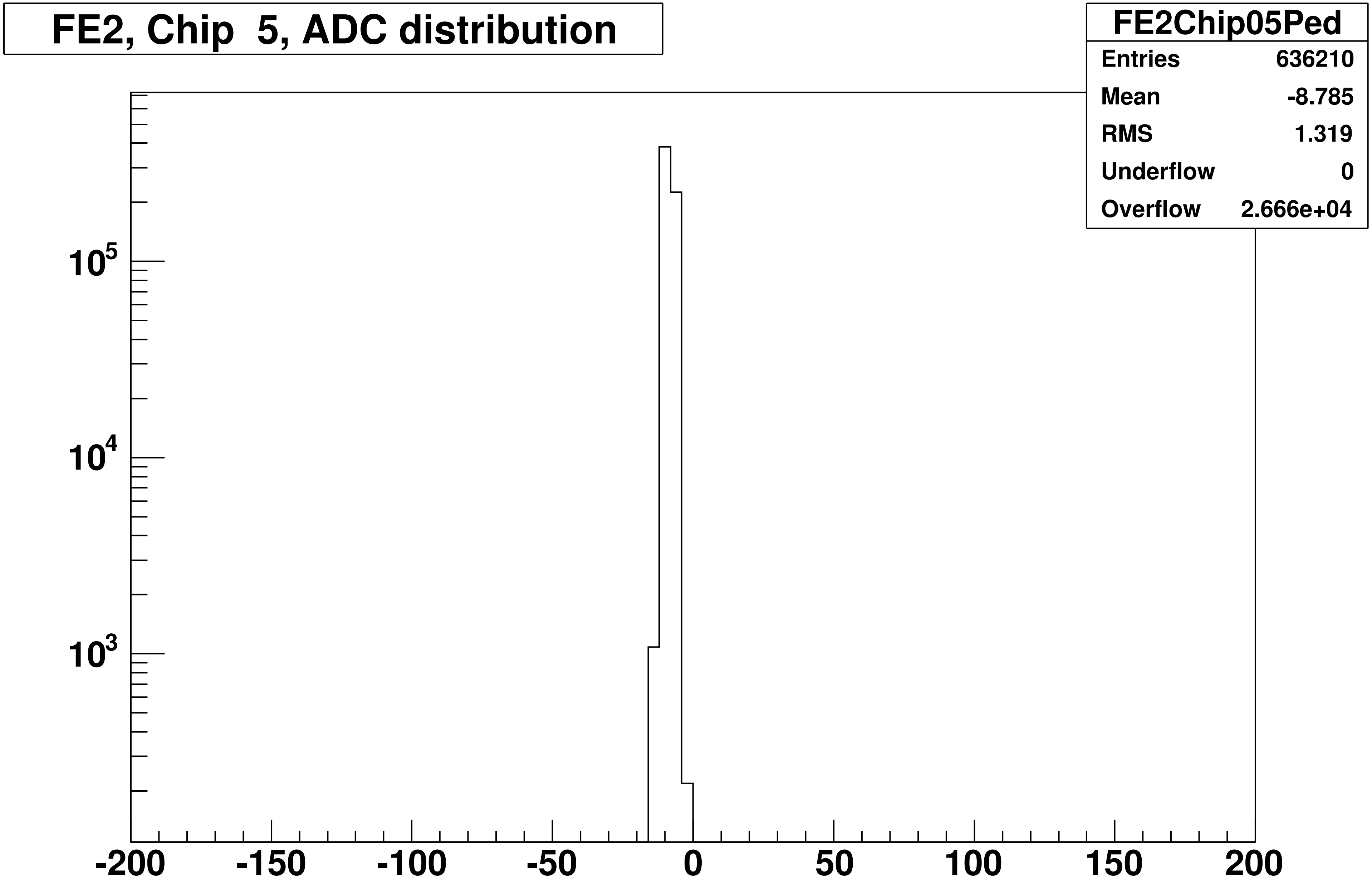

FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 636210
Mean 538.2
RMS 2519
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 636210
Mean 11.84
RMS 1.375
Underflow 0
Overflow 2.664e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

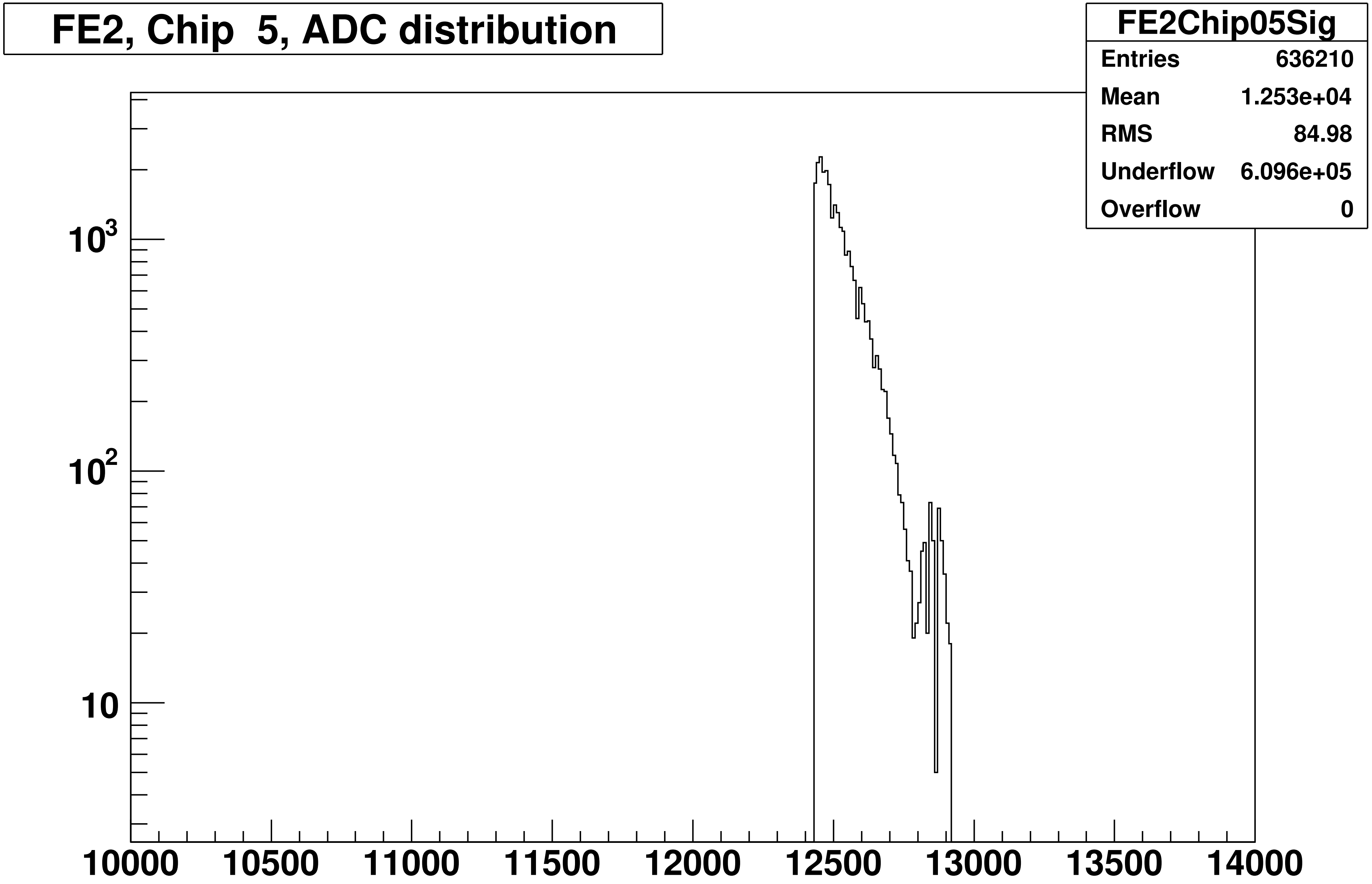

FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 636210
Mean 1.259e+04
RMS 84.4
Underflow 6.096e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

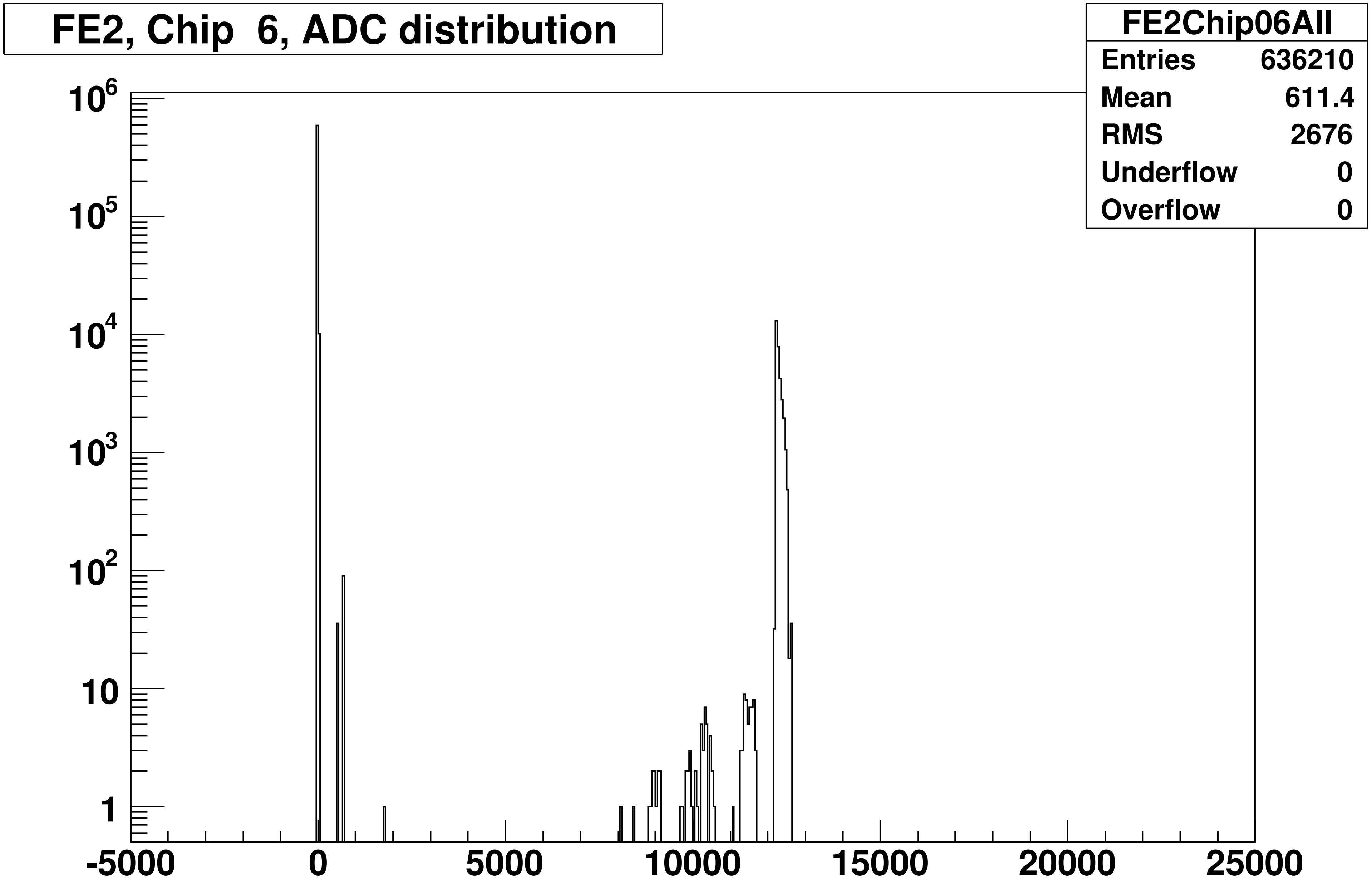


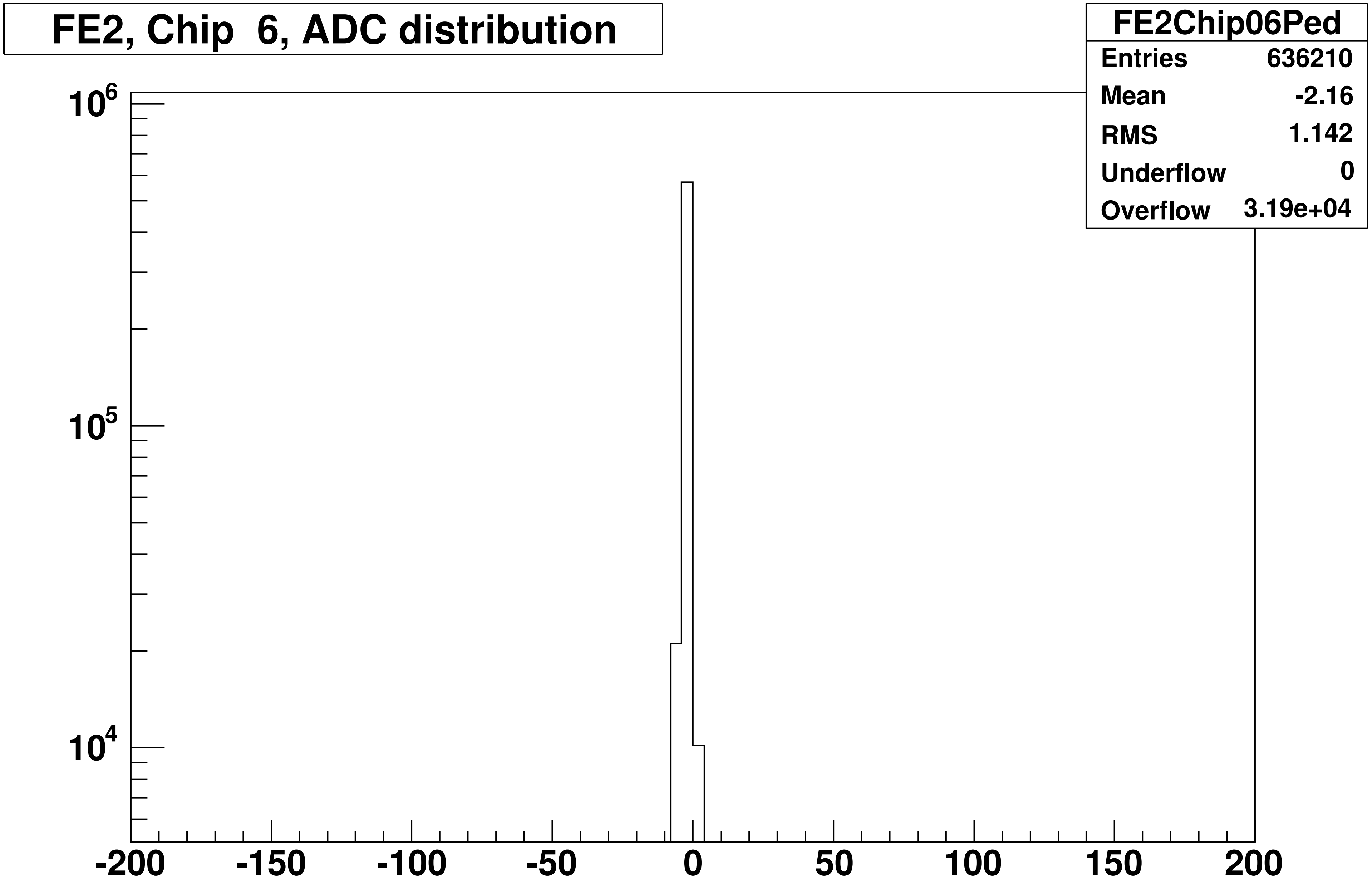
FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 636210
Mean 516.4
RMS 2511
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



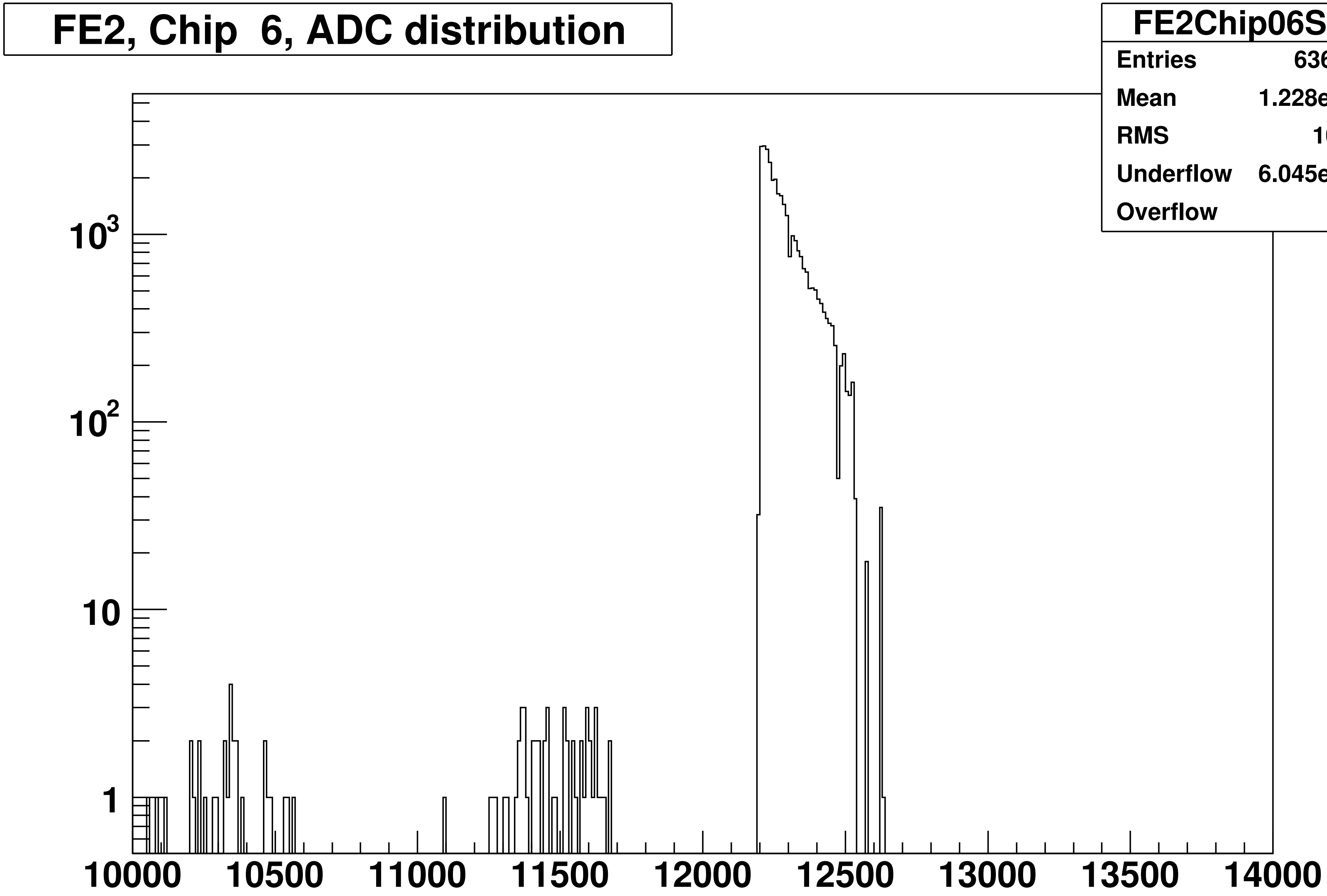
FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 636210
Mean -8.785
RMS 1.319
Underflow 0
Overflow 2.666e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

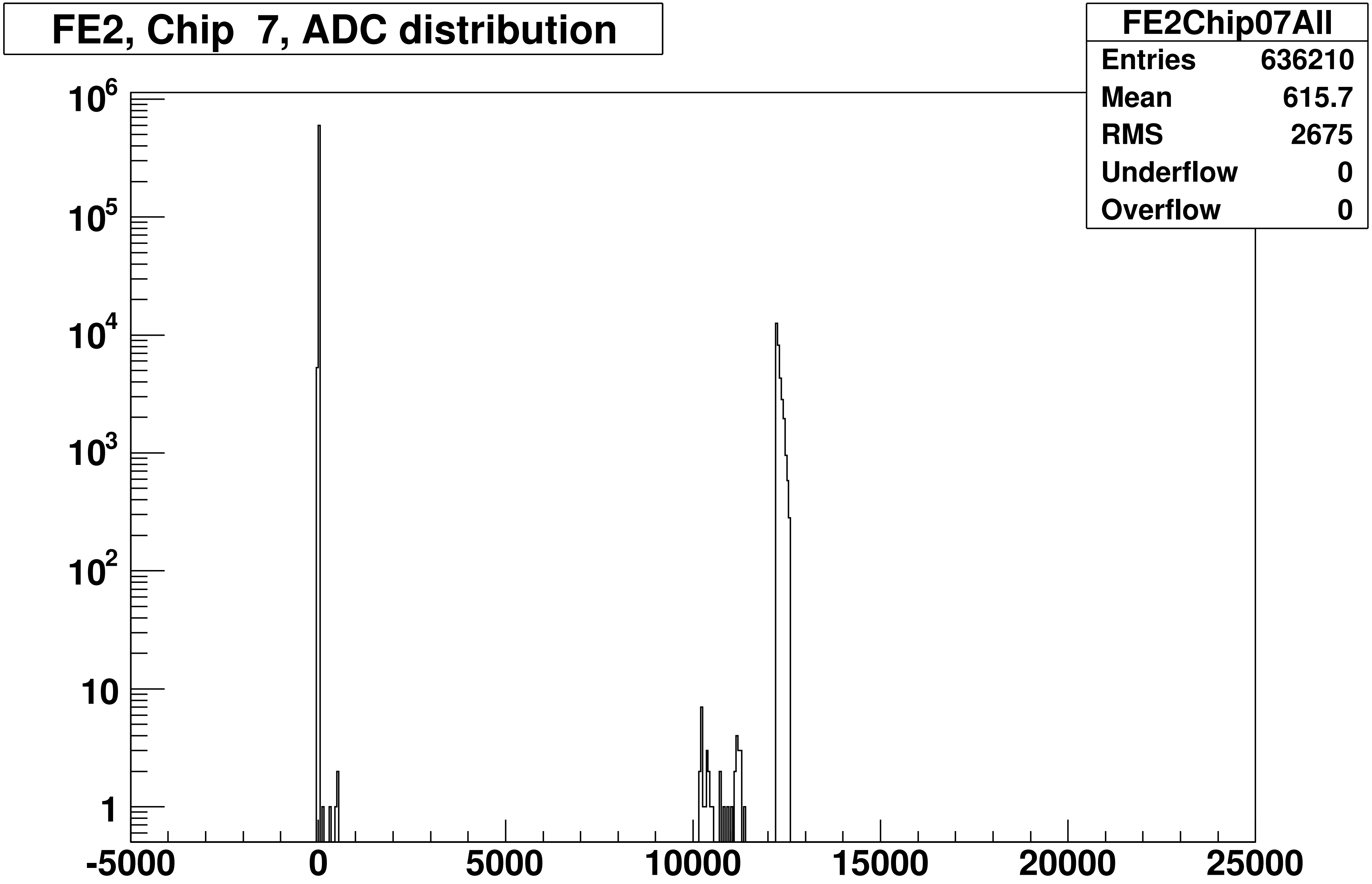

FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 636210
Mean 1.253e+04
RMS 84.98
Underflow 6.096e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

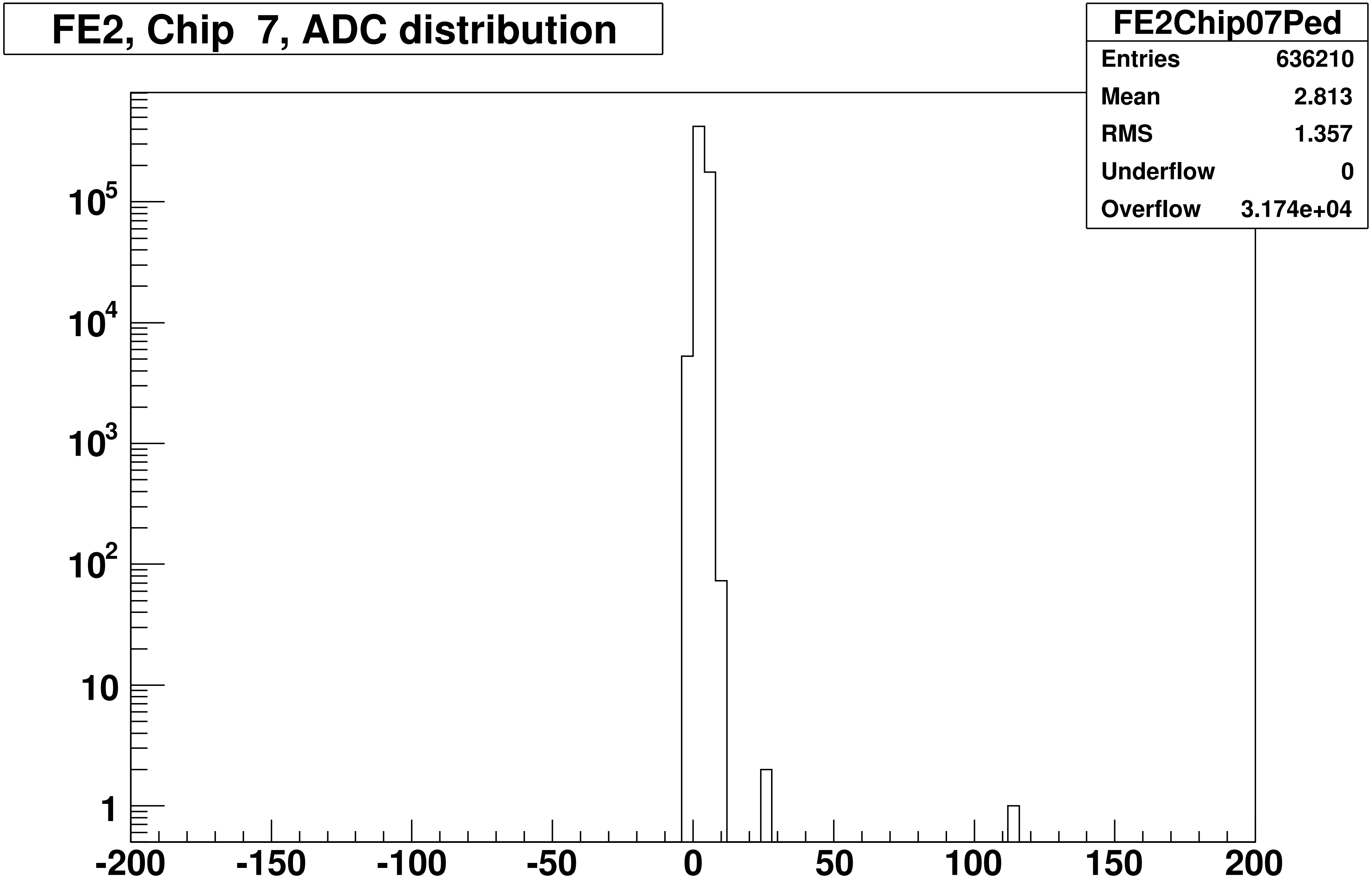

FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 636210
Mean 611.4
RMS 2676
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



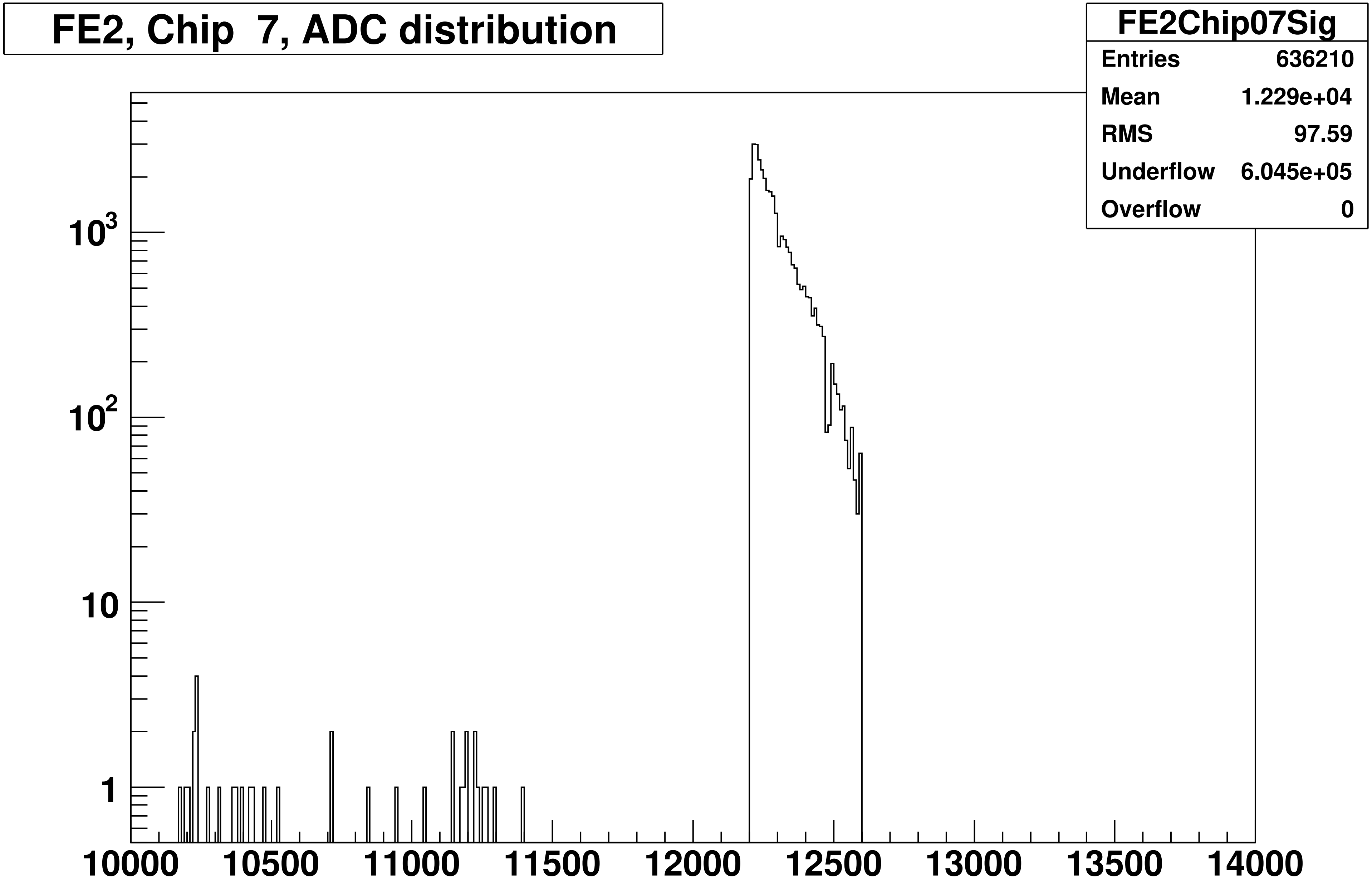
FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 636210
Mean -2.16
RMS 1.142
Underflow 0
Overflow 3.19e+04
10^6
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 636210
Mean 1.228e+04
RMS 104.2
Underflow 6.045e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

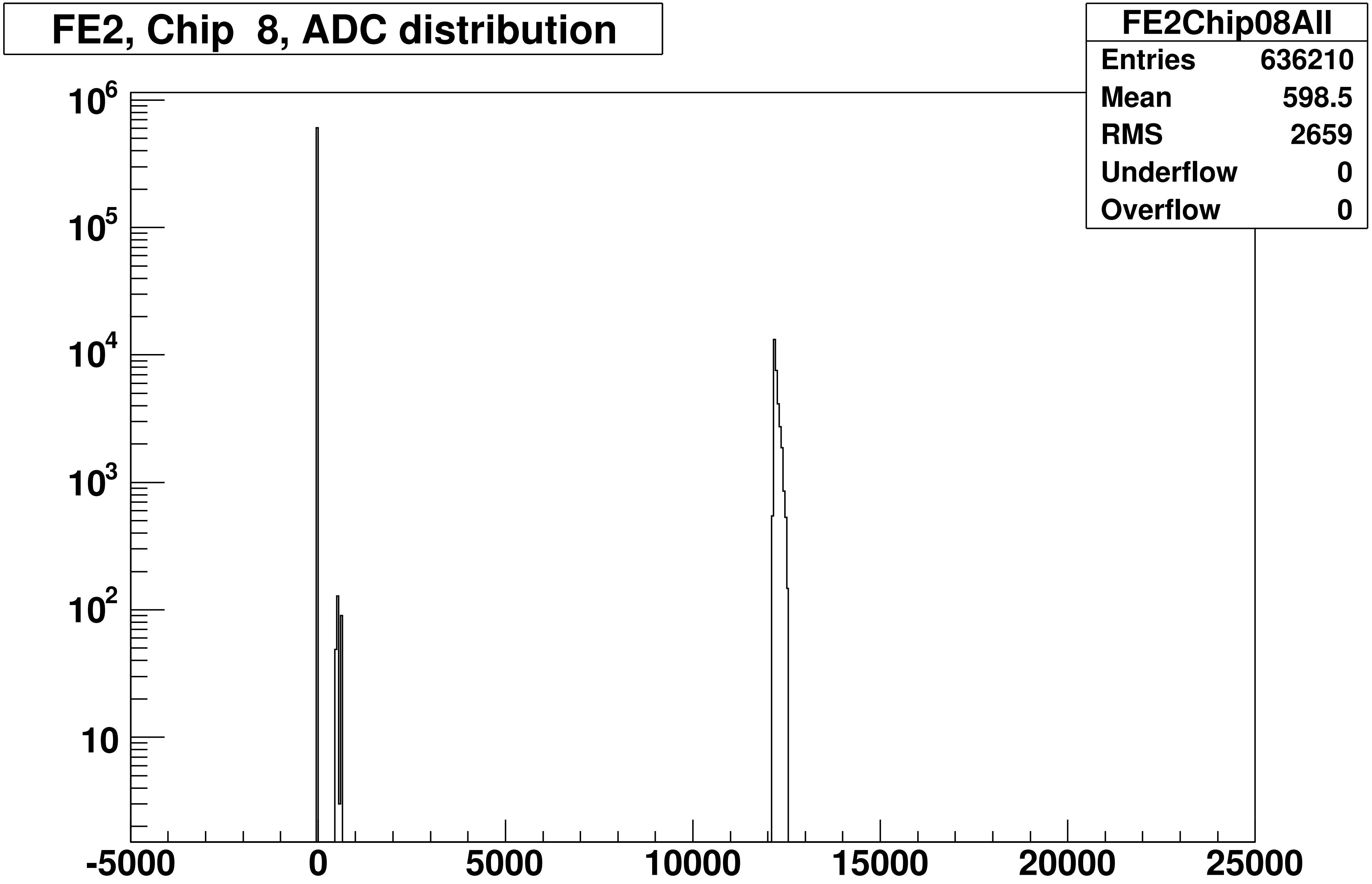

FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 636210
Mean 615.7
RMS 2675
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



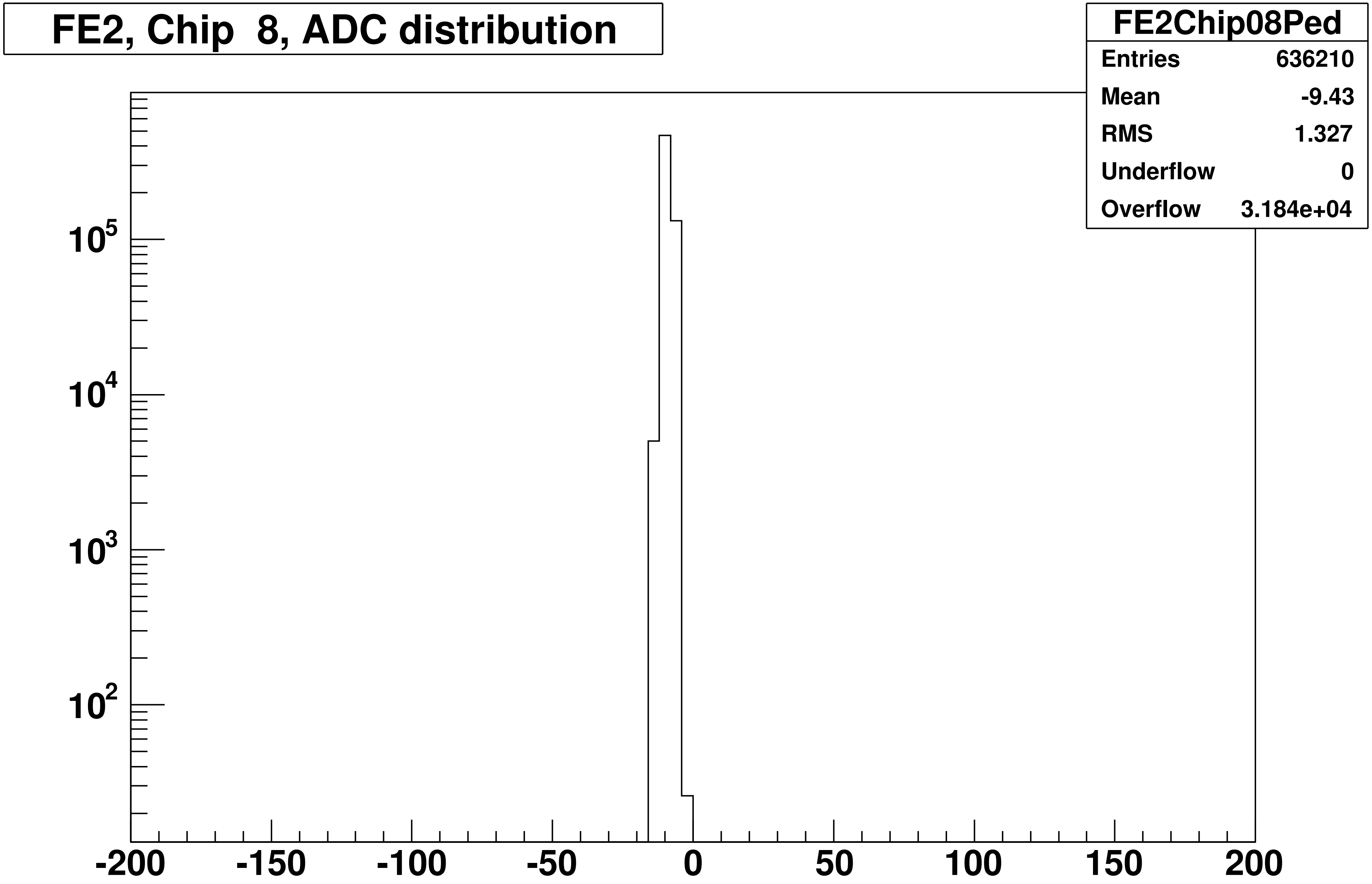
FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 636210
Mean 2.813
RMS 1.357
Underflow 0
Overflow 3.174e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

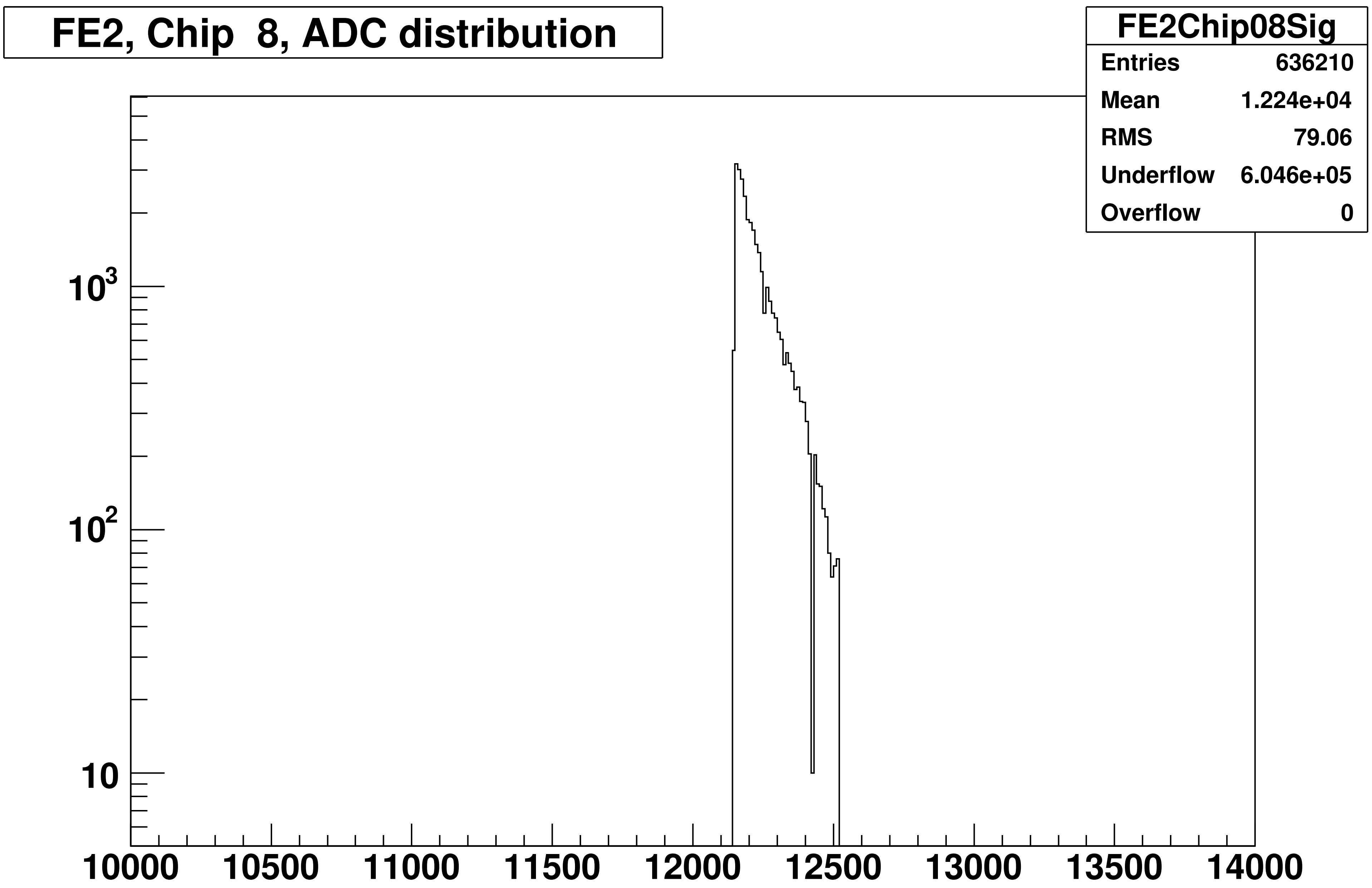


FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 636210
Mean 1.229e+04
RMS 97.59
Underflow 6.045e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

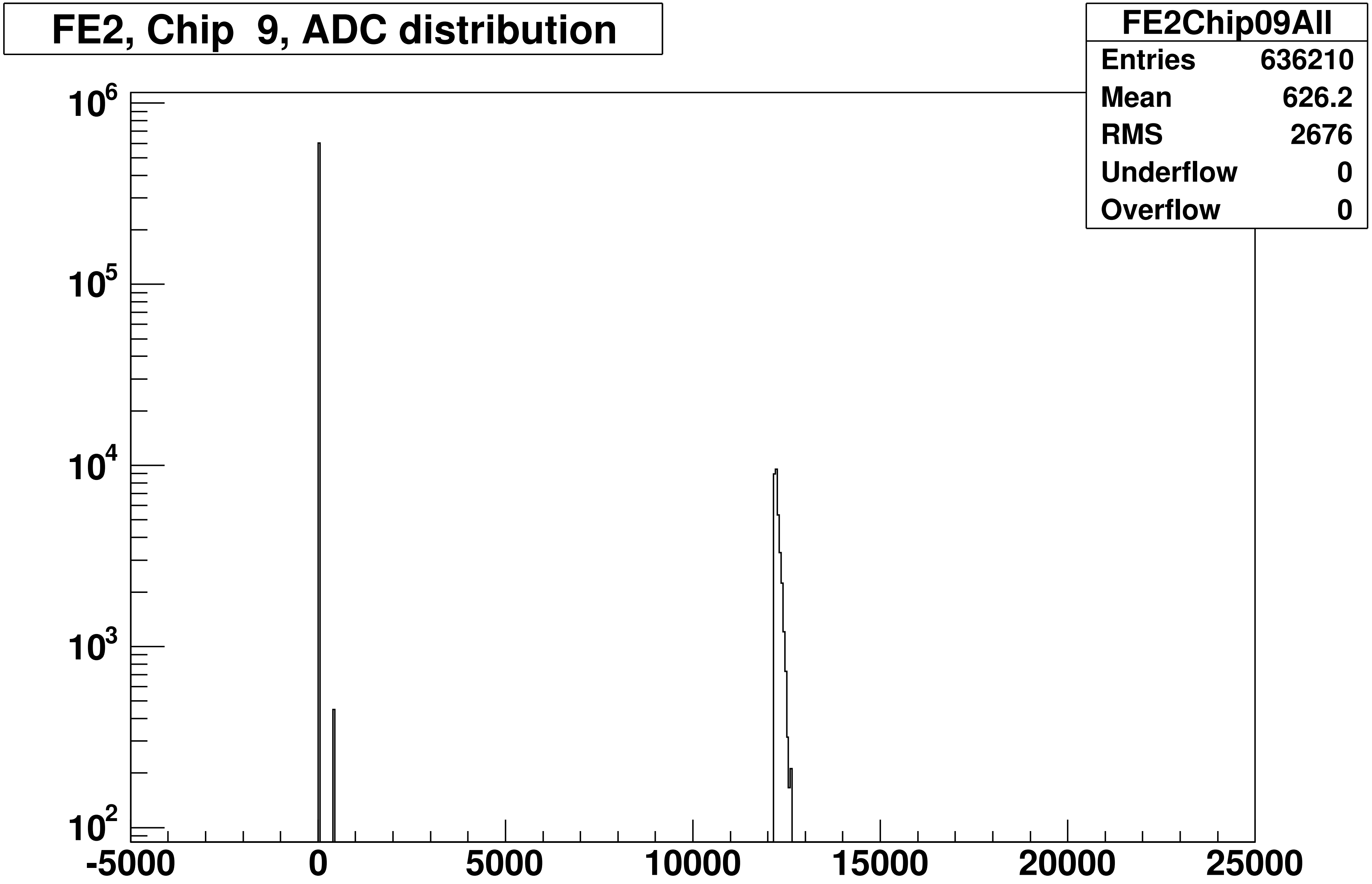


FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 636210
Mean 598.5
RMS 2659
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

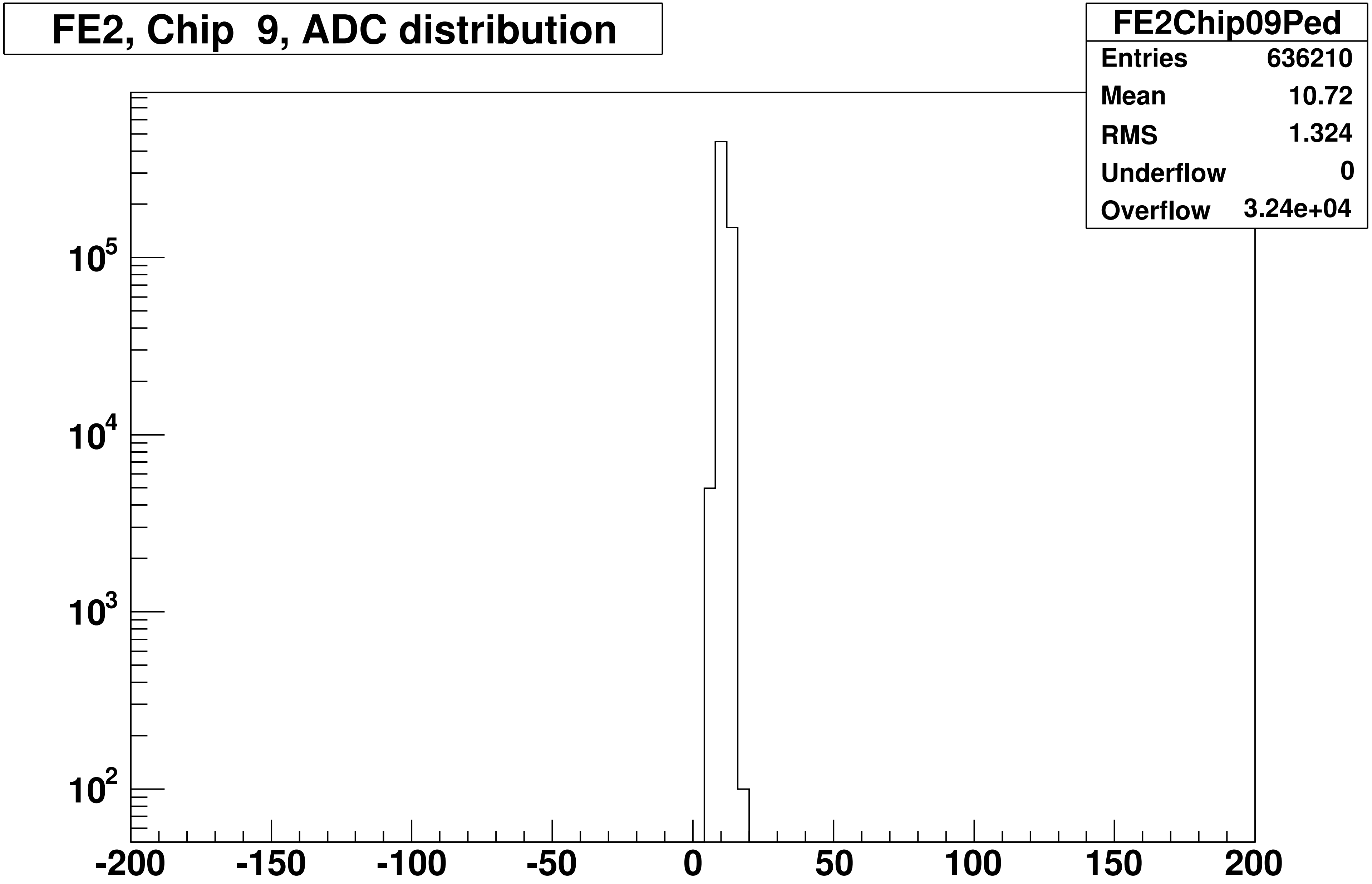

FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 636210
Mean -9.43
RMS 1.327
Underflow 0
Overflow 3.184e+04
10^5
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200

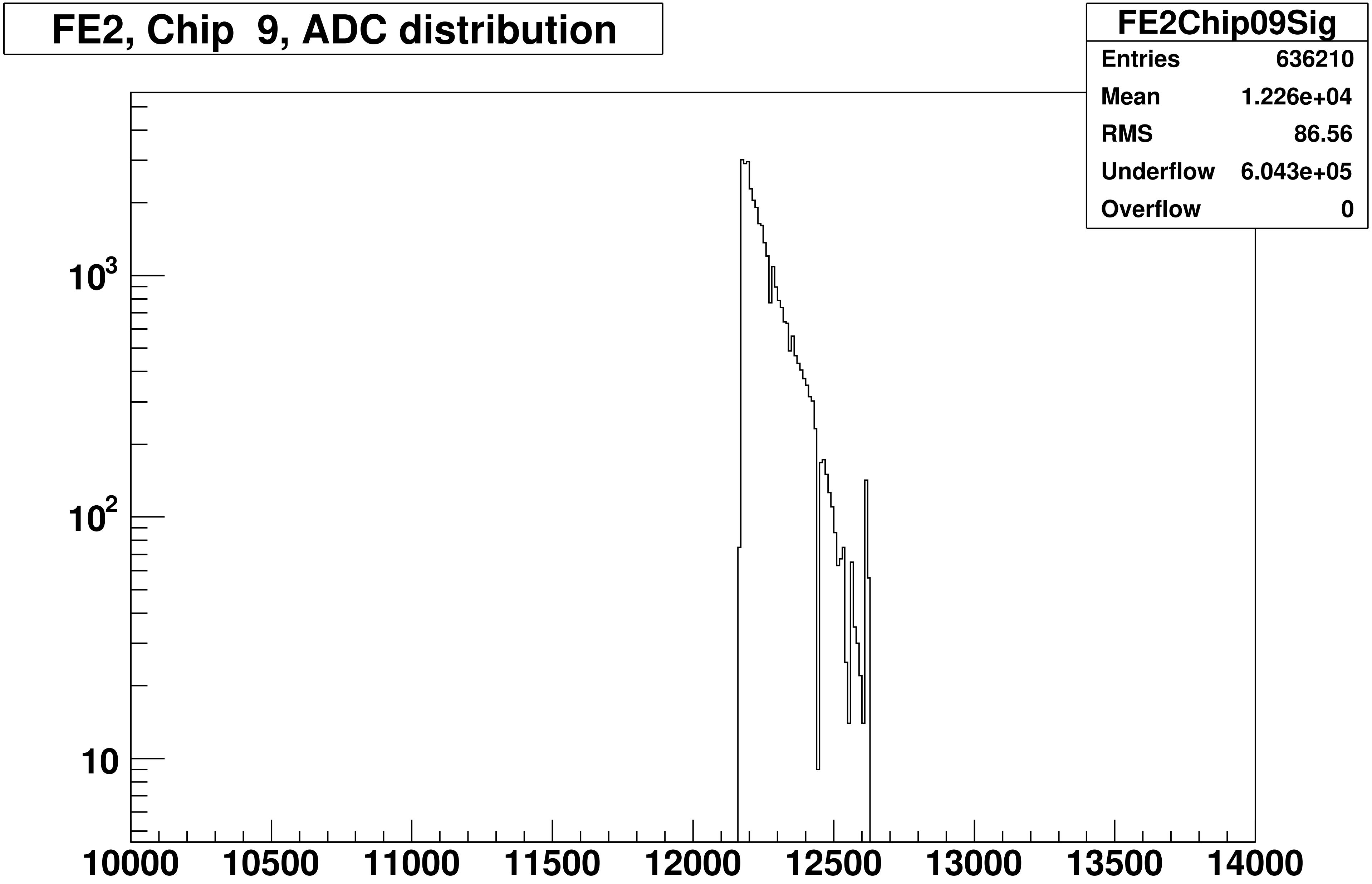


FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 636210
Mean 1.224e+04
RMS 79.06
Underflow 6.046e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

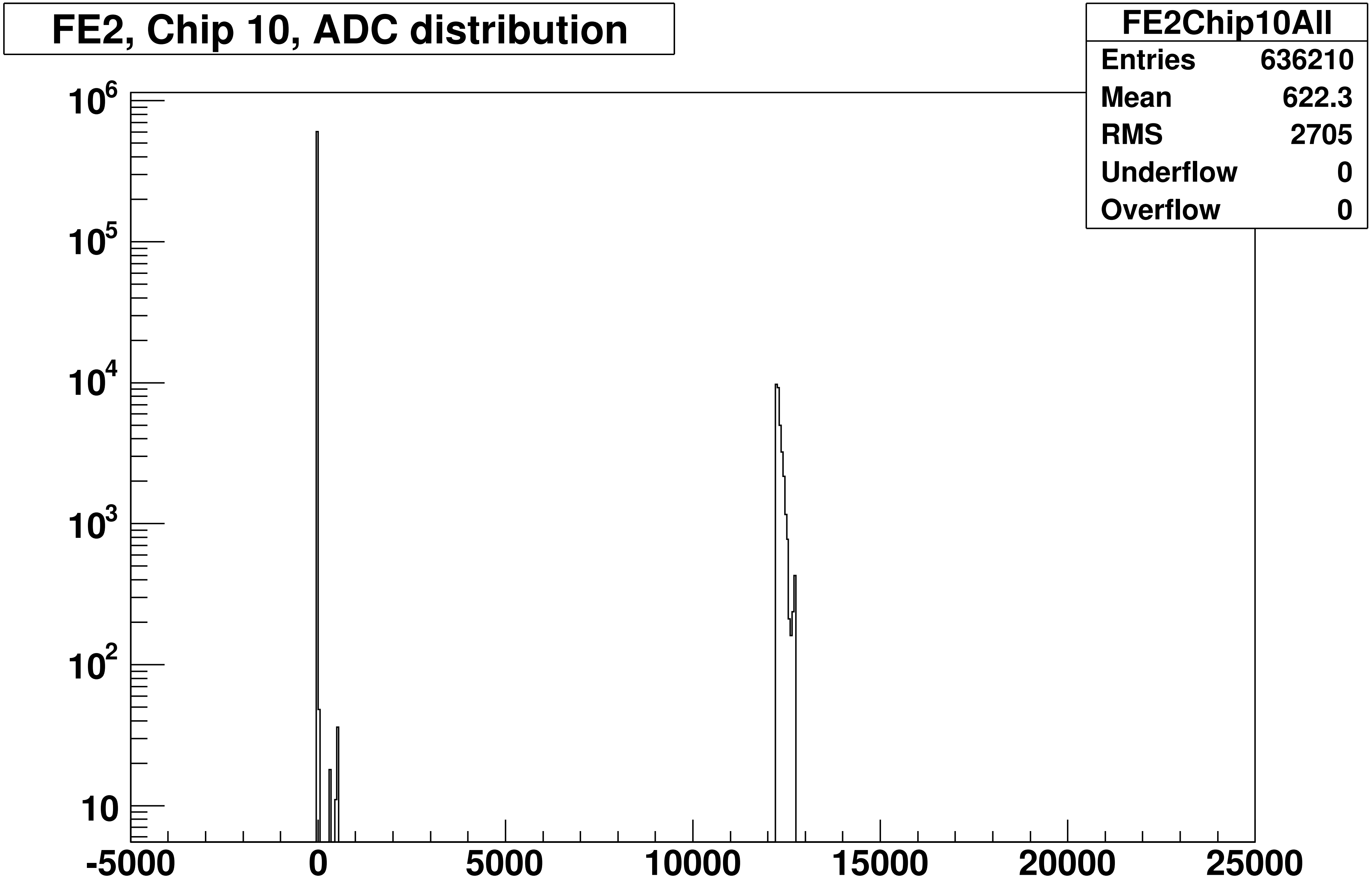


FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 636210
Mean 626.2
RMS 2676
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

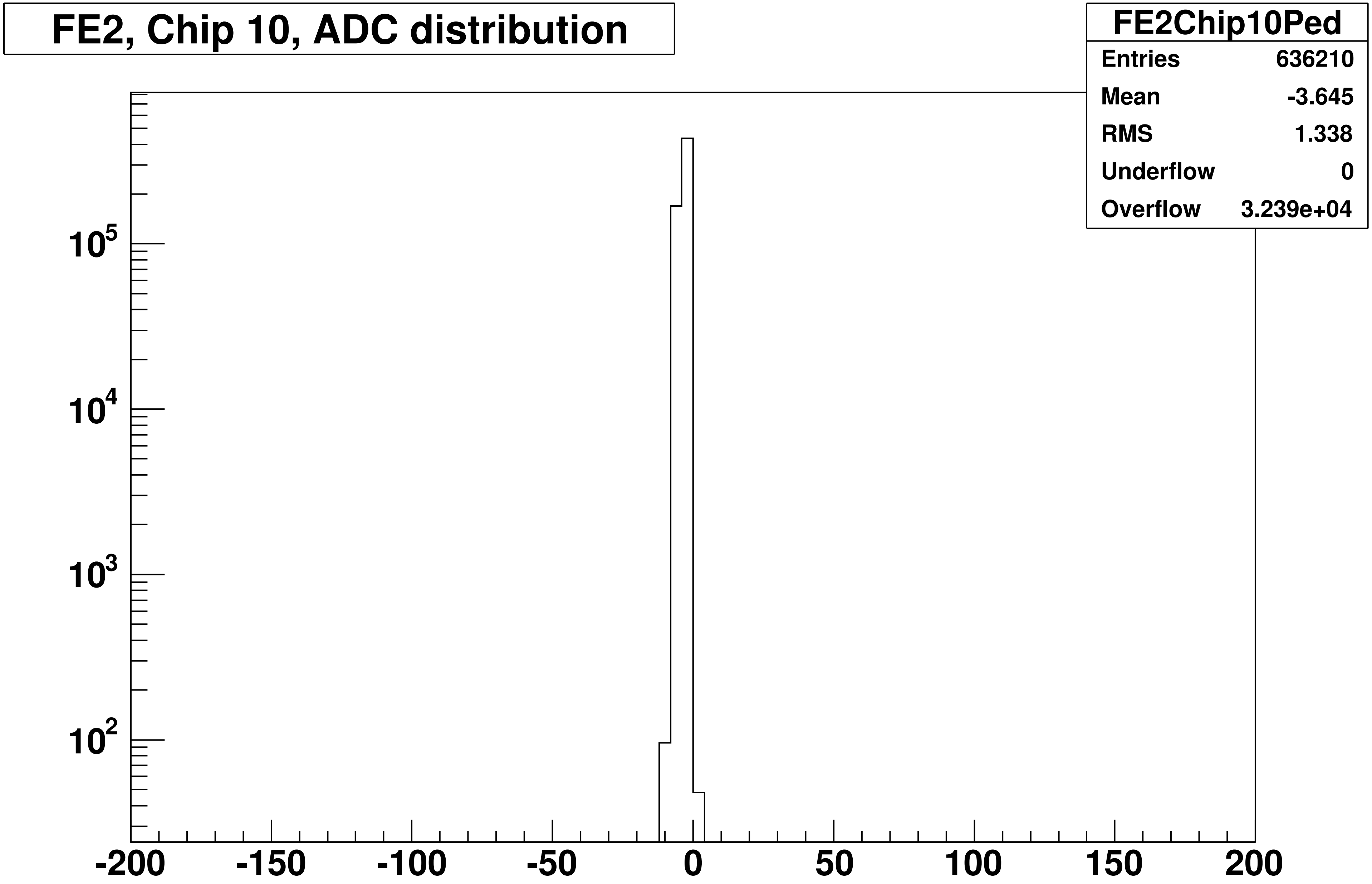

FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 636210
Mean 10.72
RMS 1.324
Underflow 0
Overflow 3.24e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

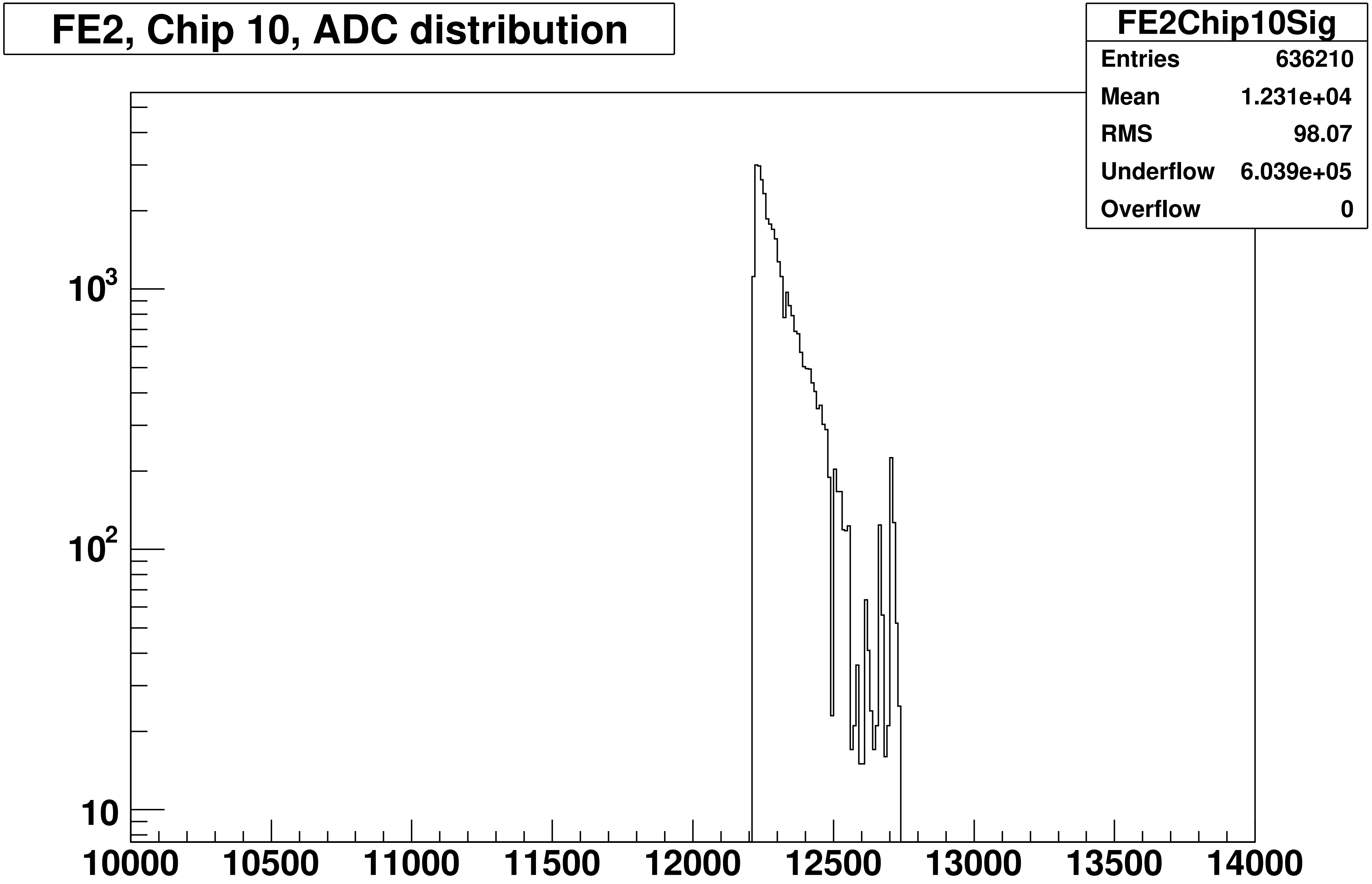


FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 636210
Mean 1.226e+04
RMS 86.56
Underflow 6.043e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

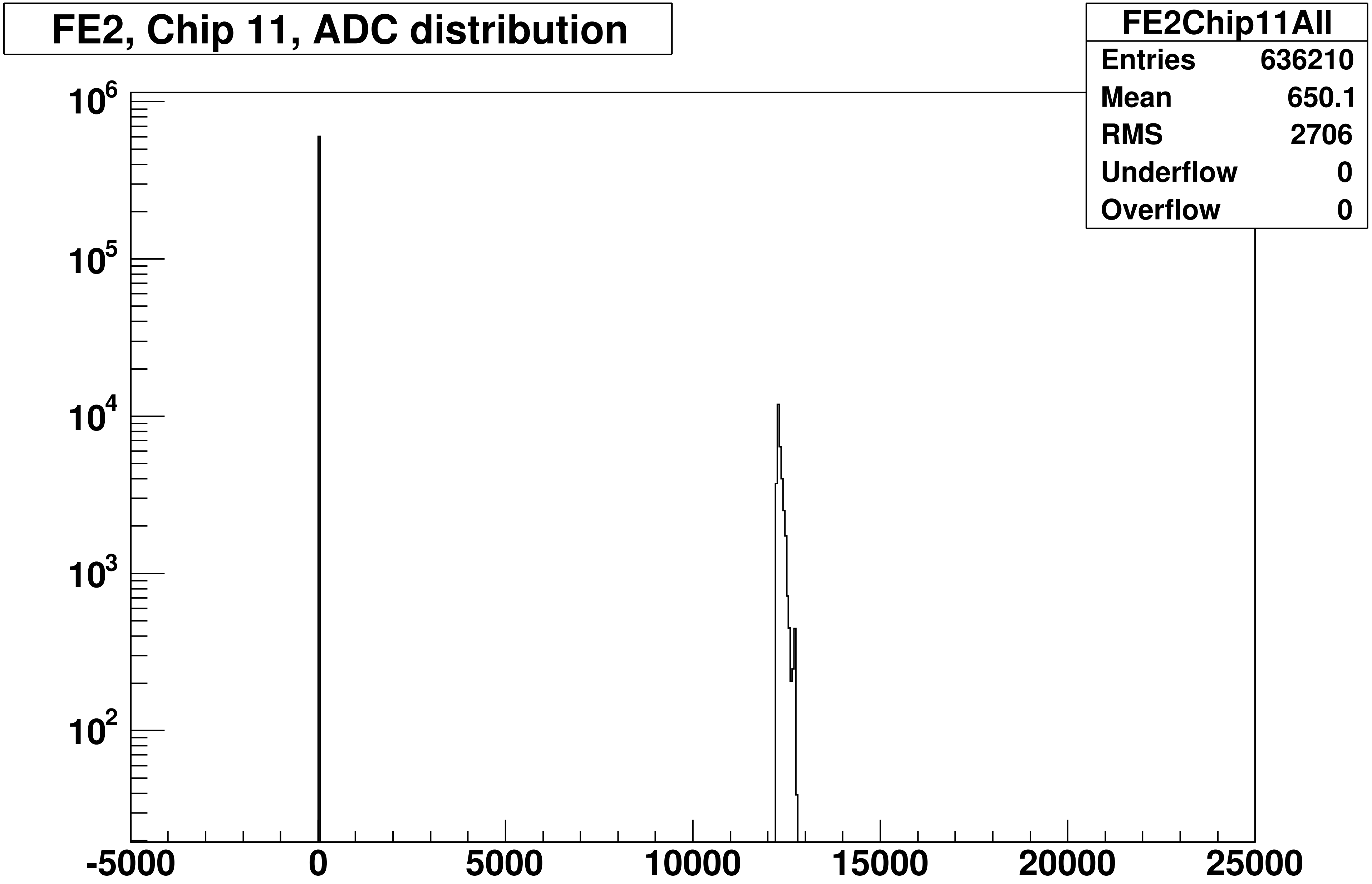


FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 636210
Mean 622.3
RMS 2705
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

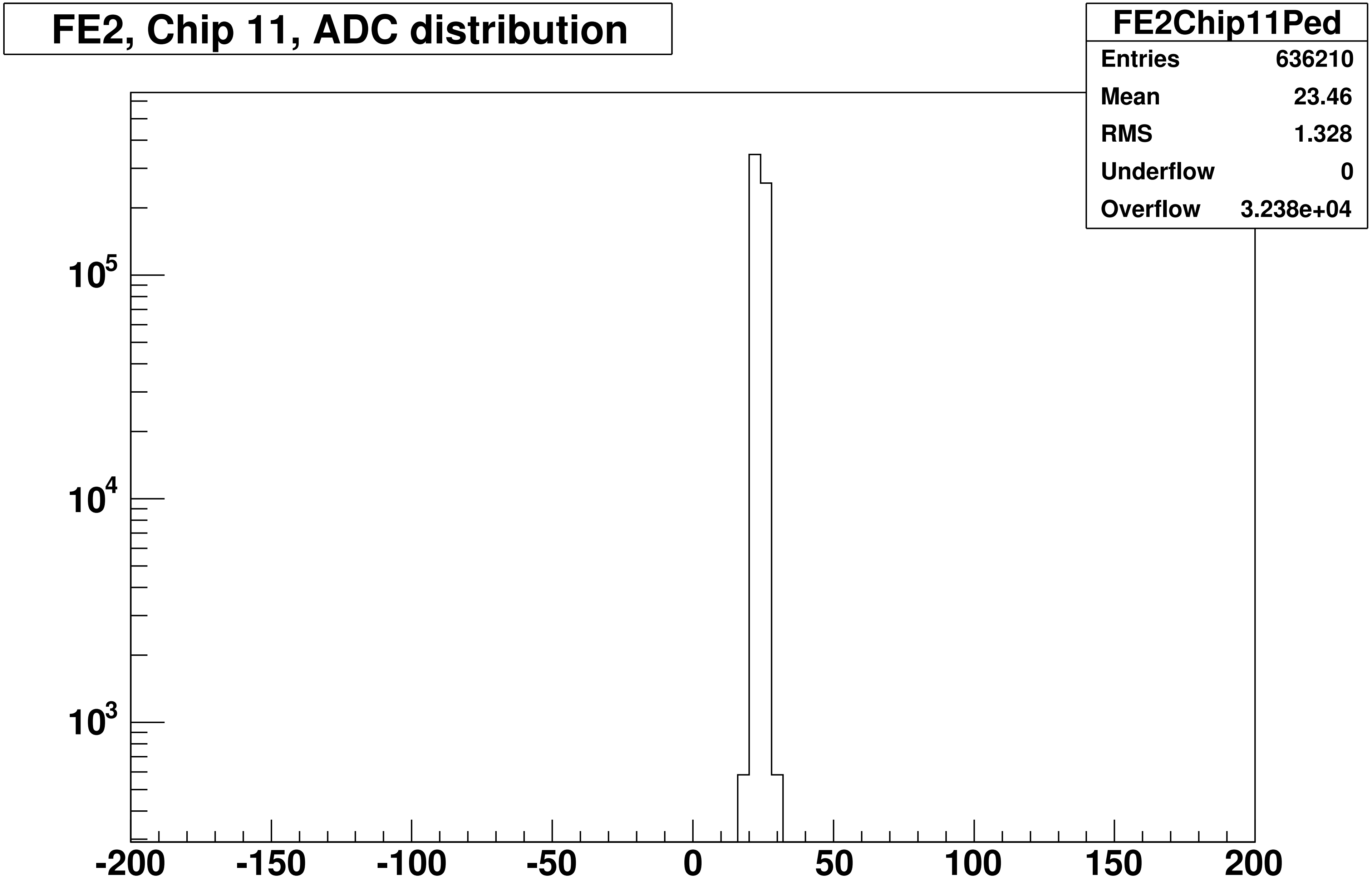

FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 636210
Mean -3.645
RMS 1.338
Underflow 0
Overflow 3.239e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

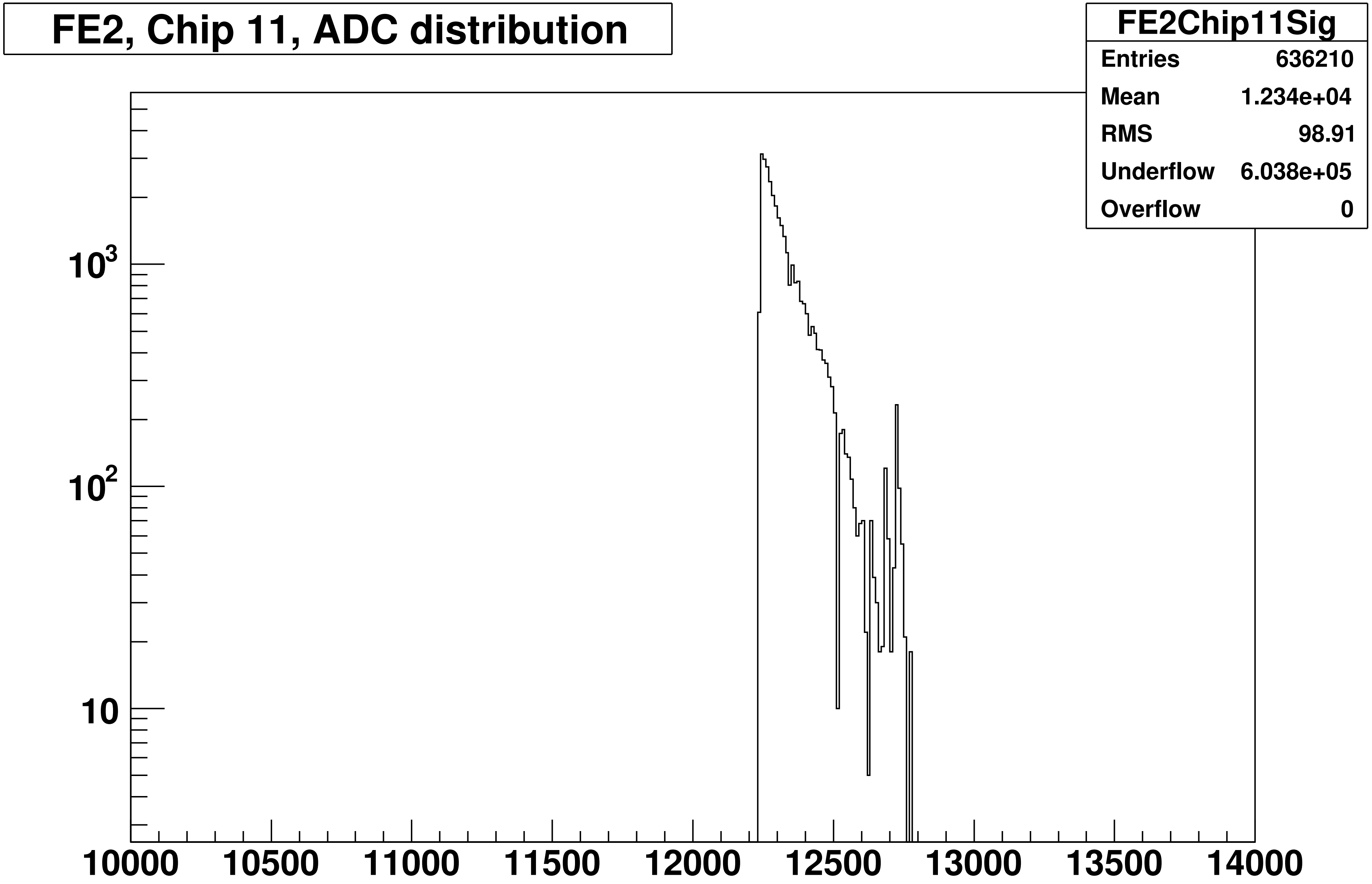


FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 636210
Mean 1.231e+04
RMS 98.07
Underflow 6.039e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

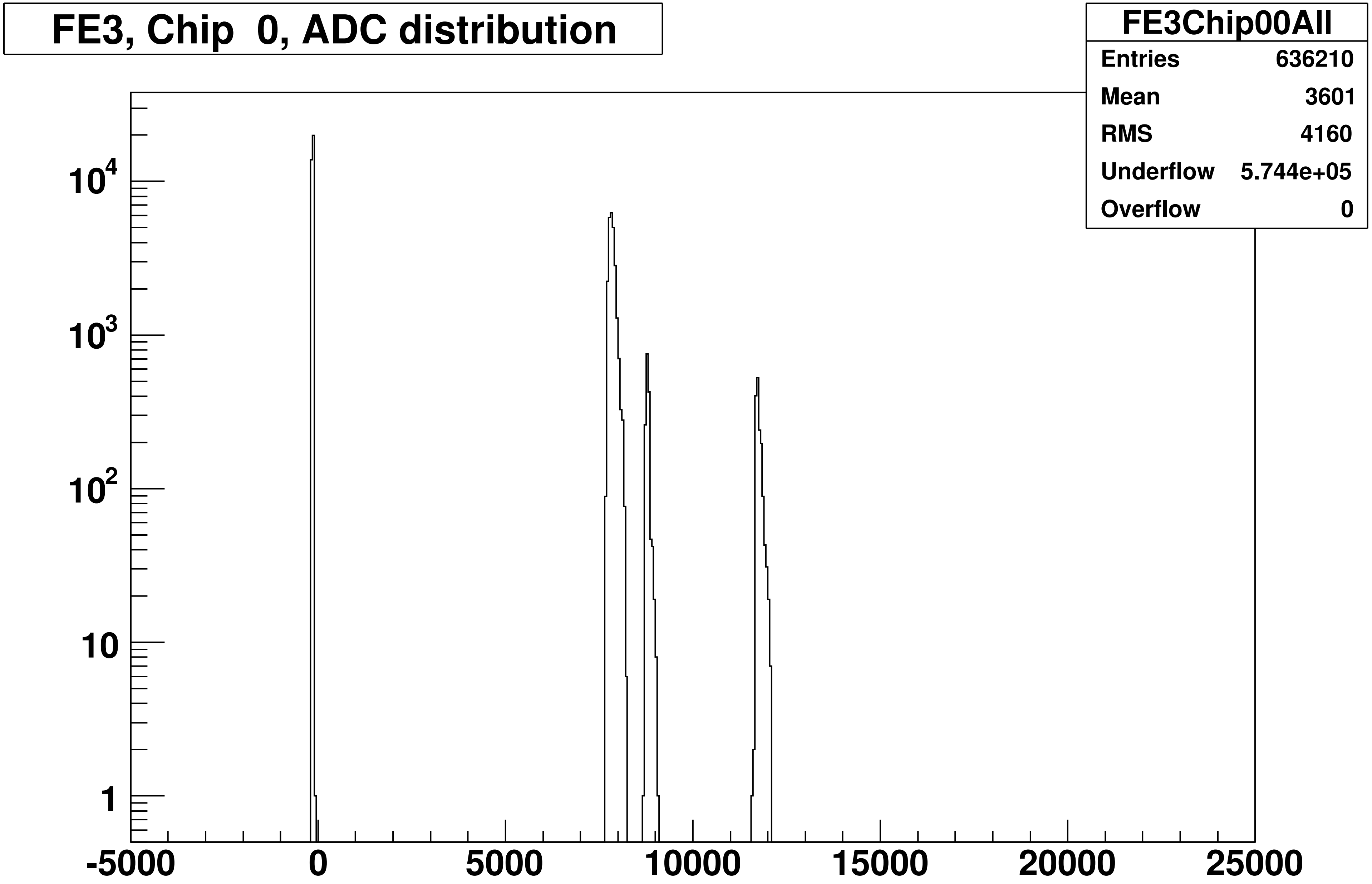


FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 636210
Mean 650.1
RMS 2706
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

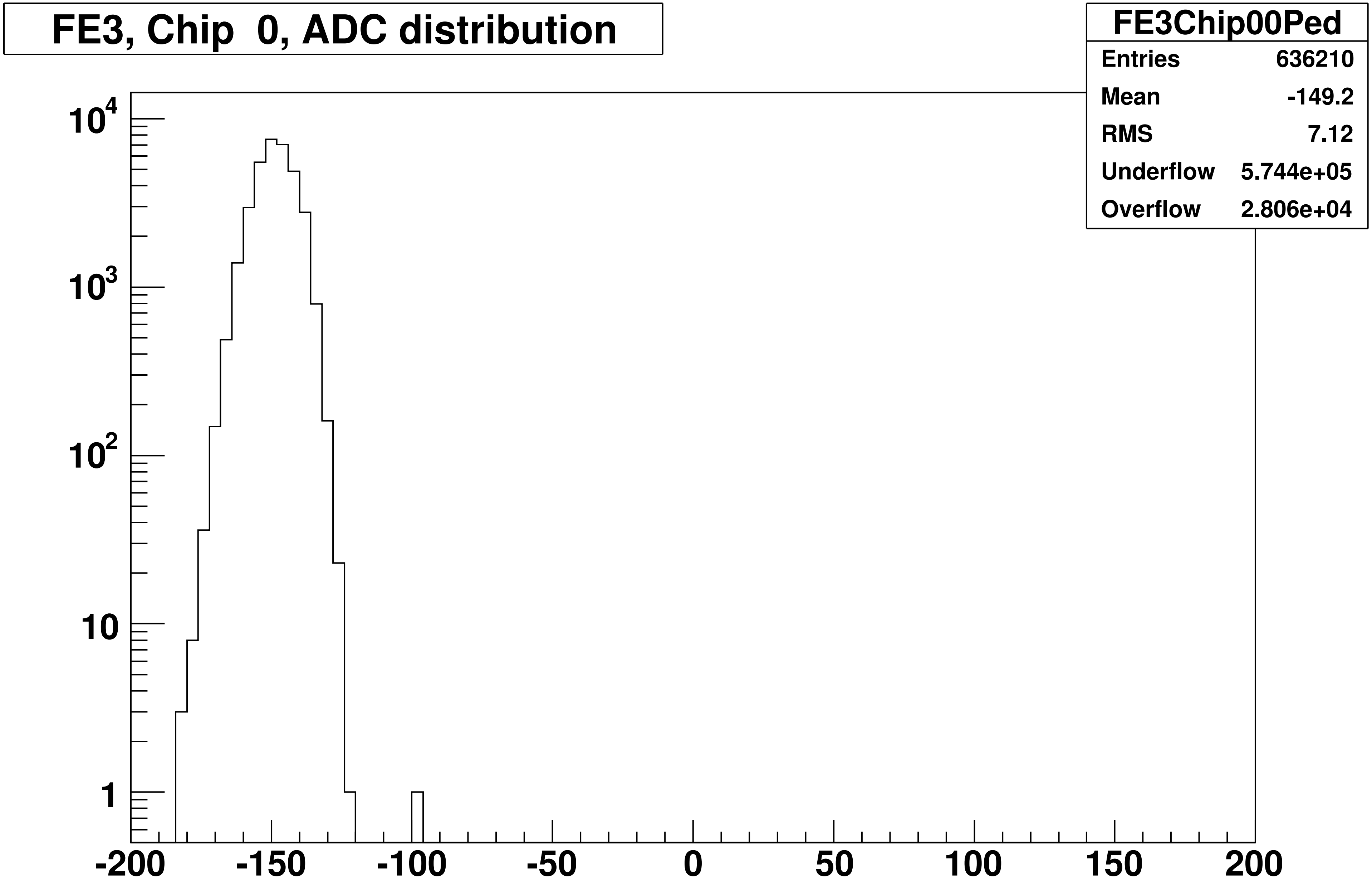

FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 636210
Mean 23.46
RMS 1.328
Underflow 0
Overflow 3.238e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

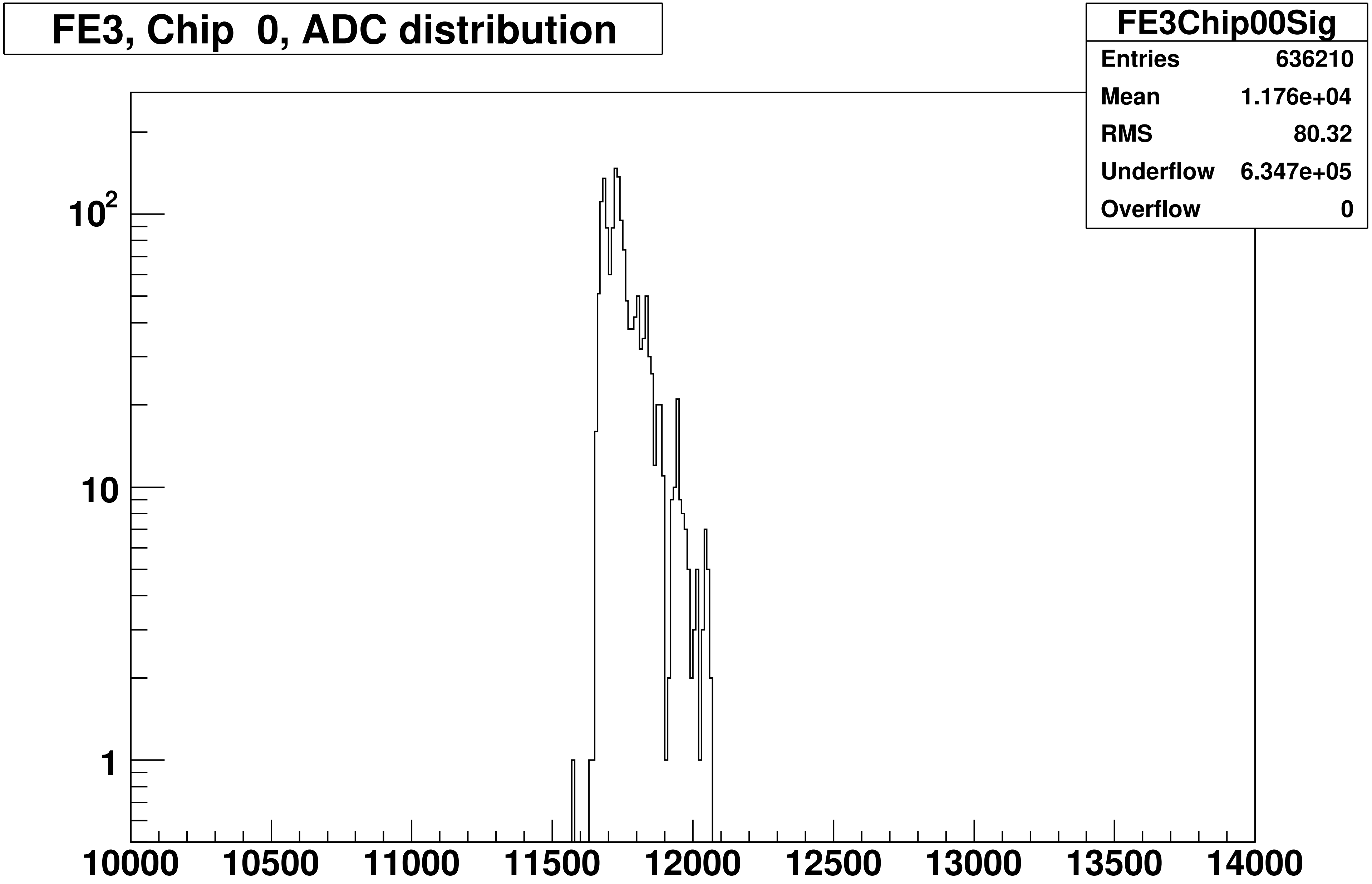


FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 636210
Mean 1.234e+04
RMS 98.91
Underflow 6.038e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000

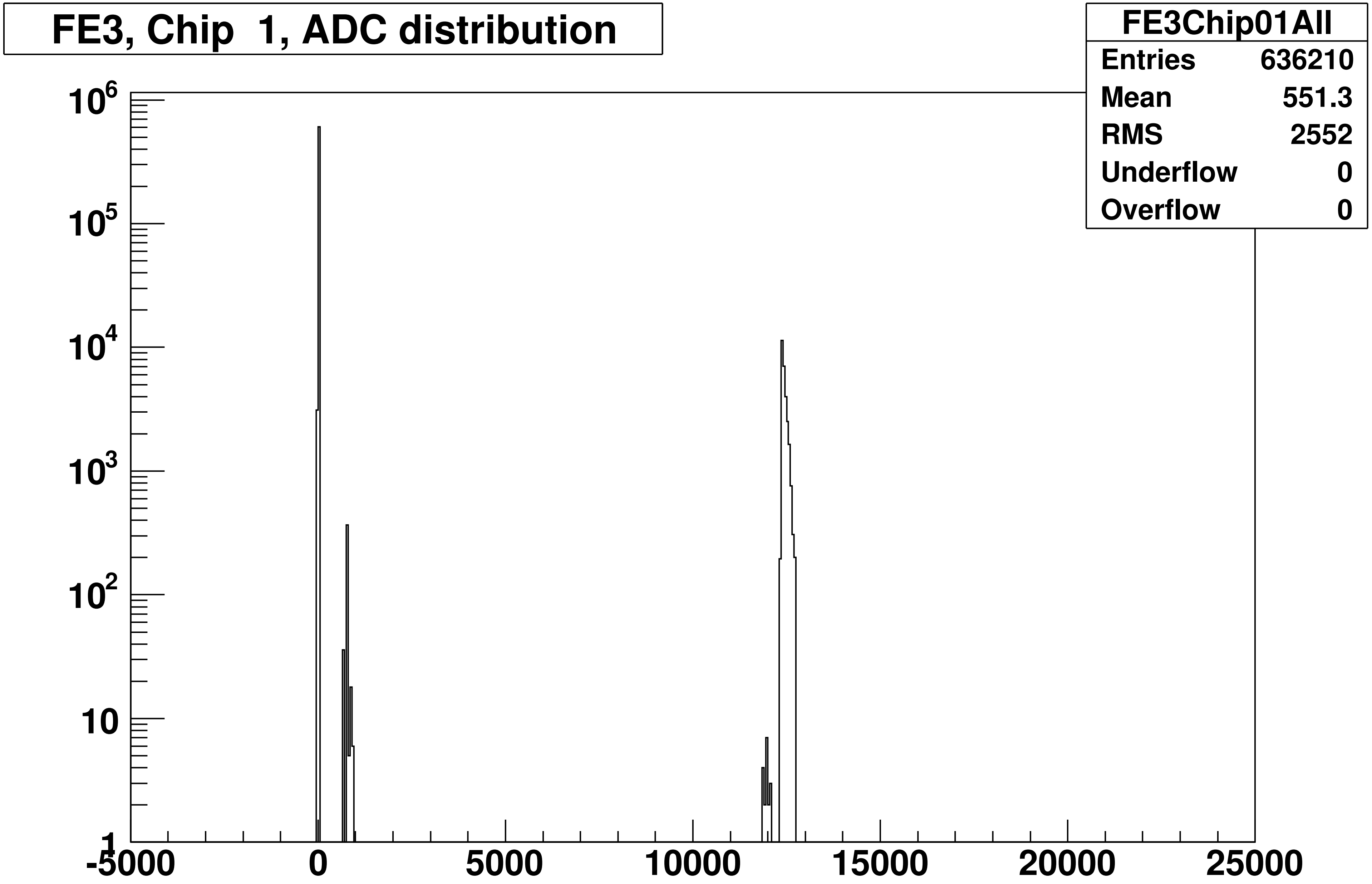


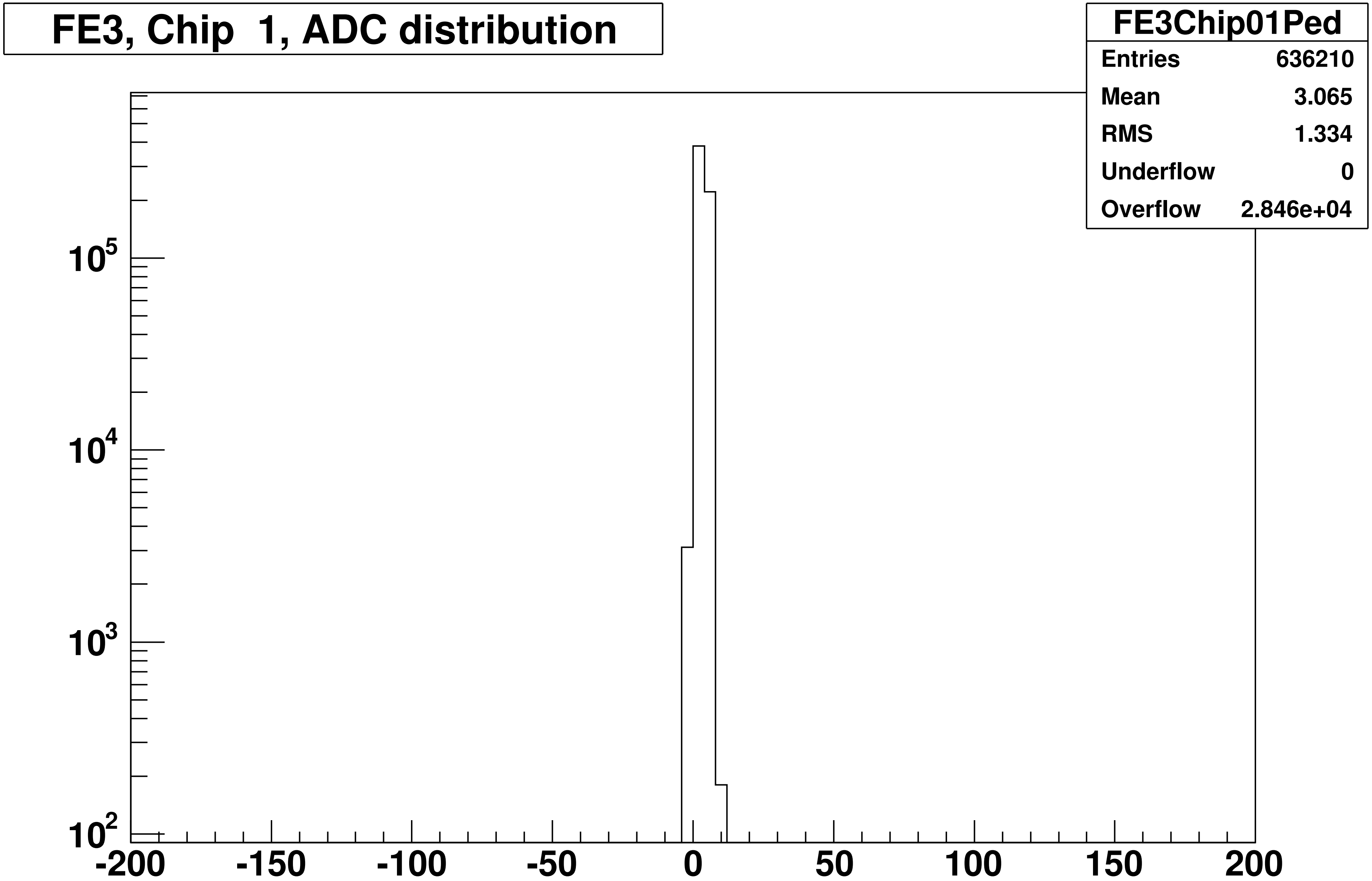
FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 636210
Mean 3601
RMS 4160
Underflow 5.744e+05
Overflow 0
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 636210
Mean -149.2
RMS 7.12
Underflow 5.744e+05
Overflow 2.806e+04
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

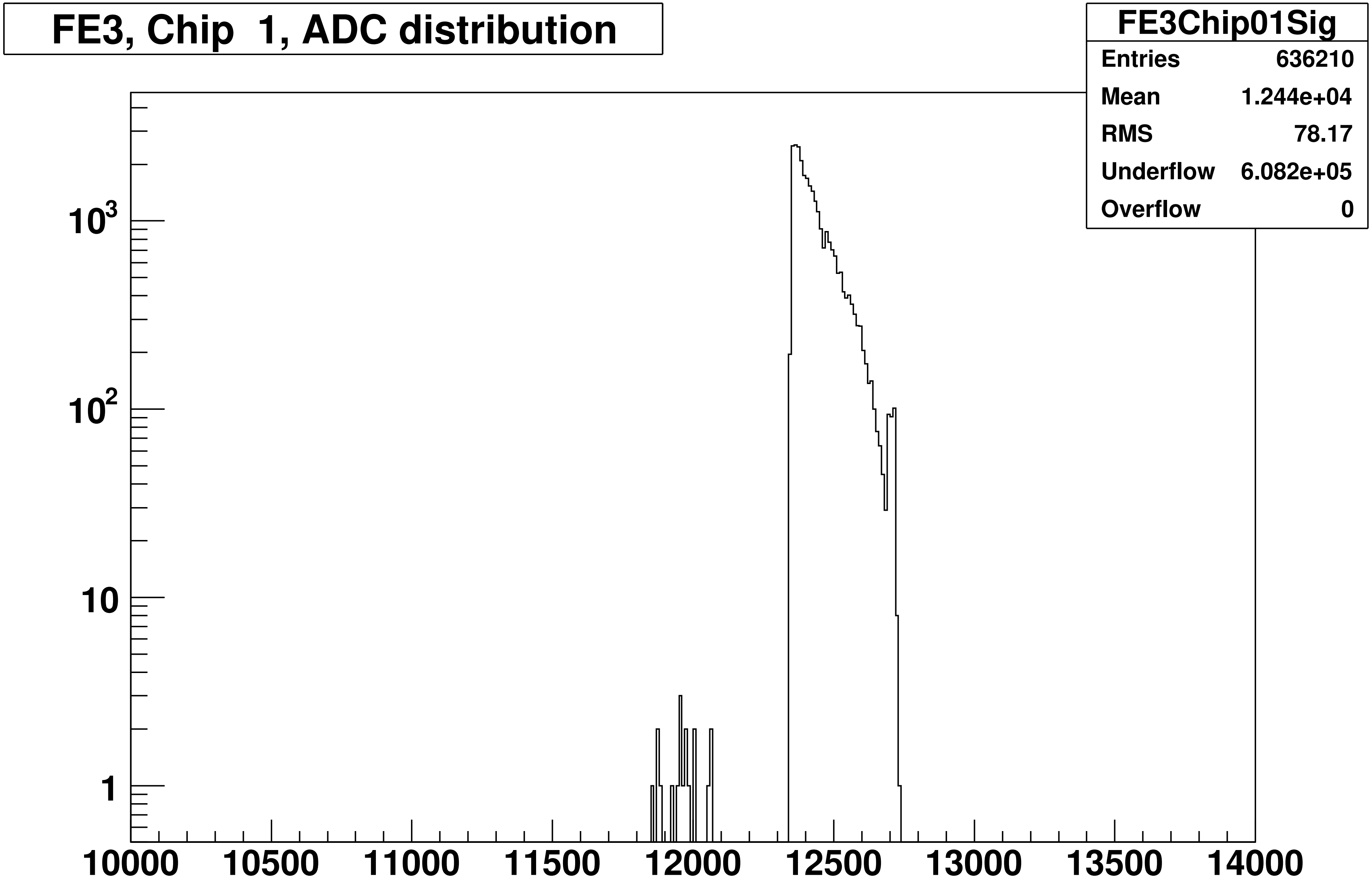


FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 636210
Mean 1.176e+04
RMS 80.32
Underflow 6.347e+05
Overflow 0
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

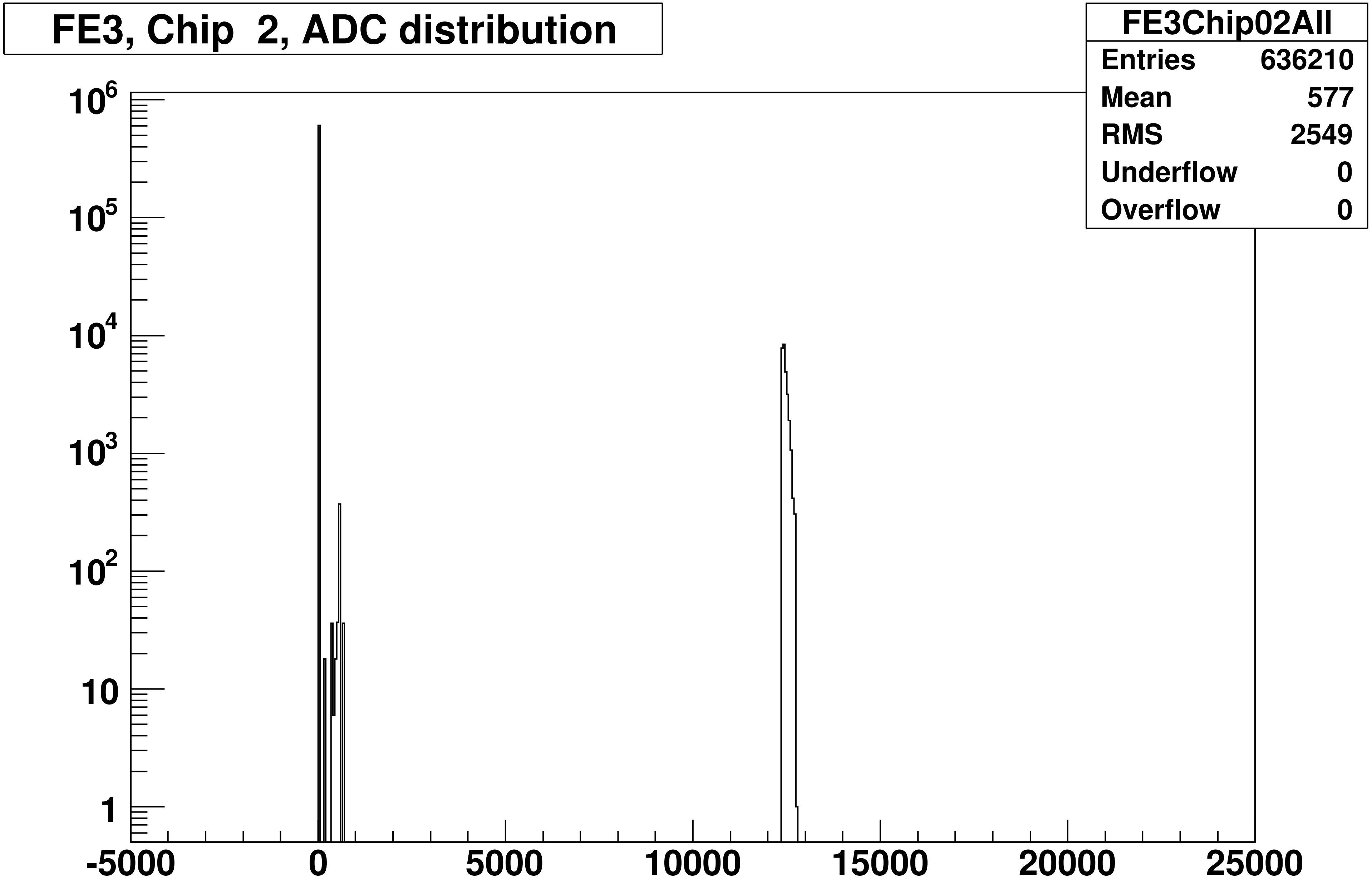

FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 636210
Mean 551.3
RMS 2552
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



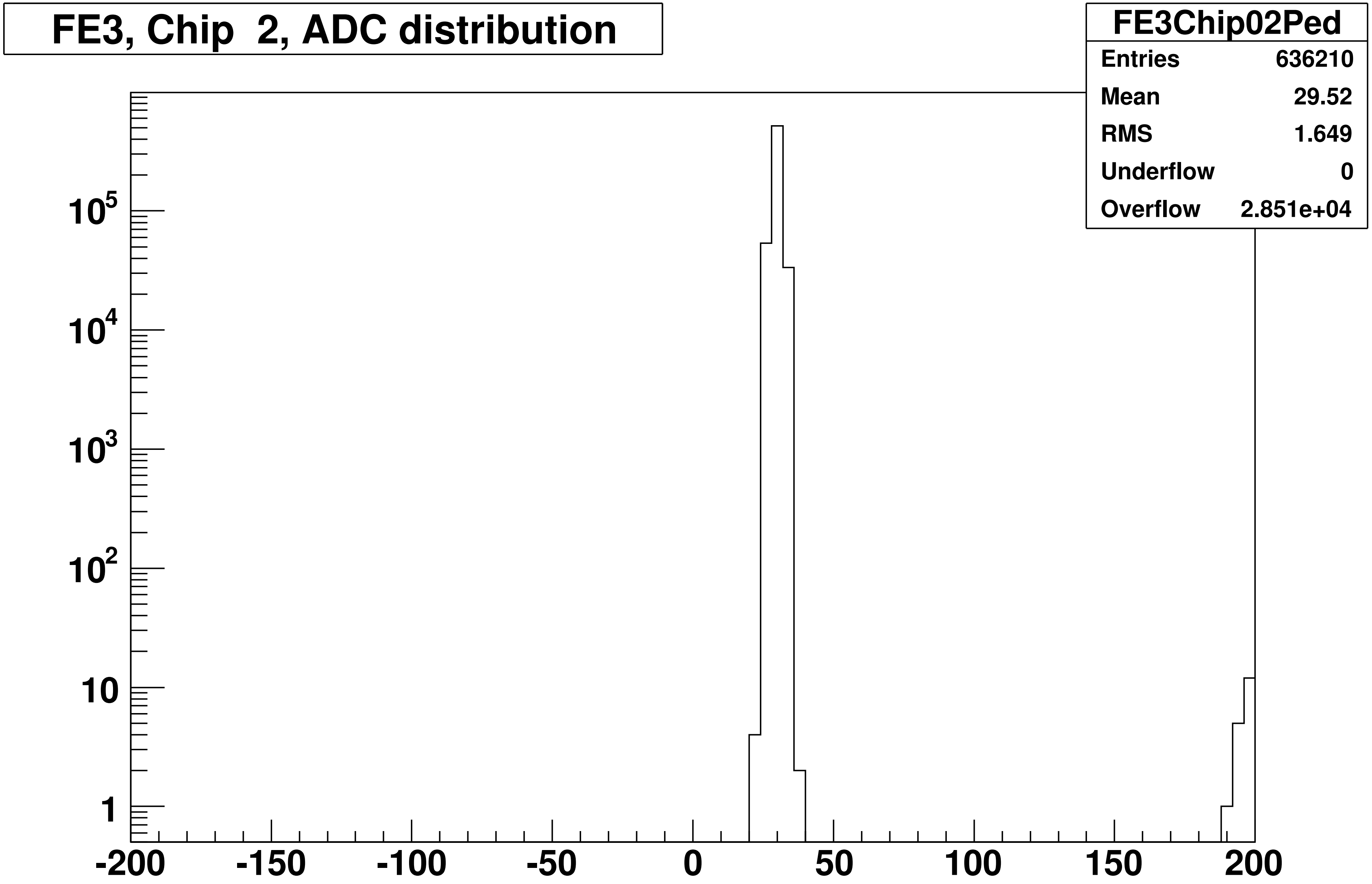
FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 636210
Mean 3.065
RMS 1.334
Underflow 0
Overflow 2.846e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

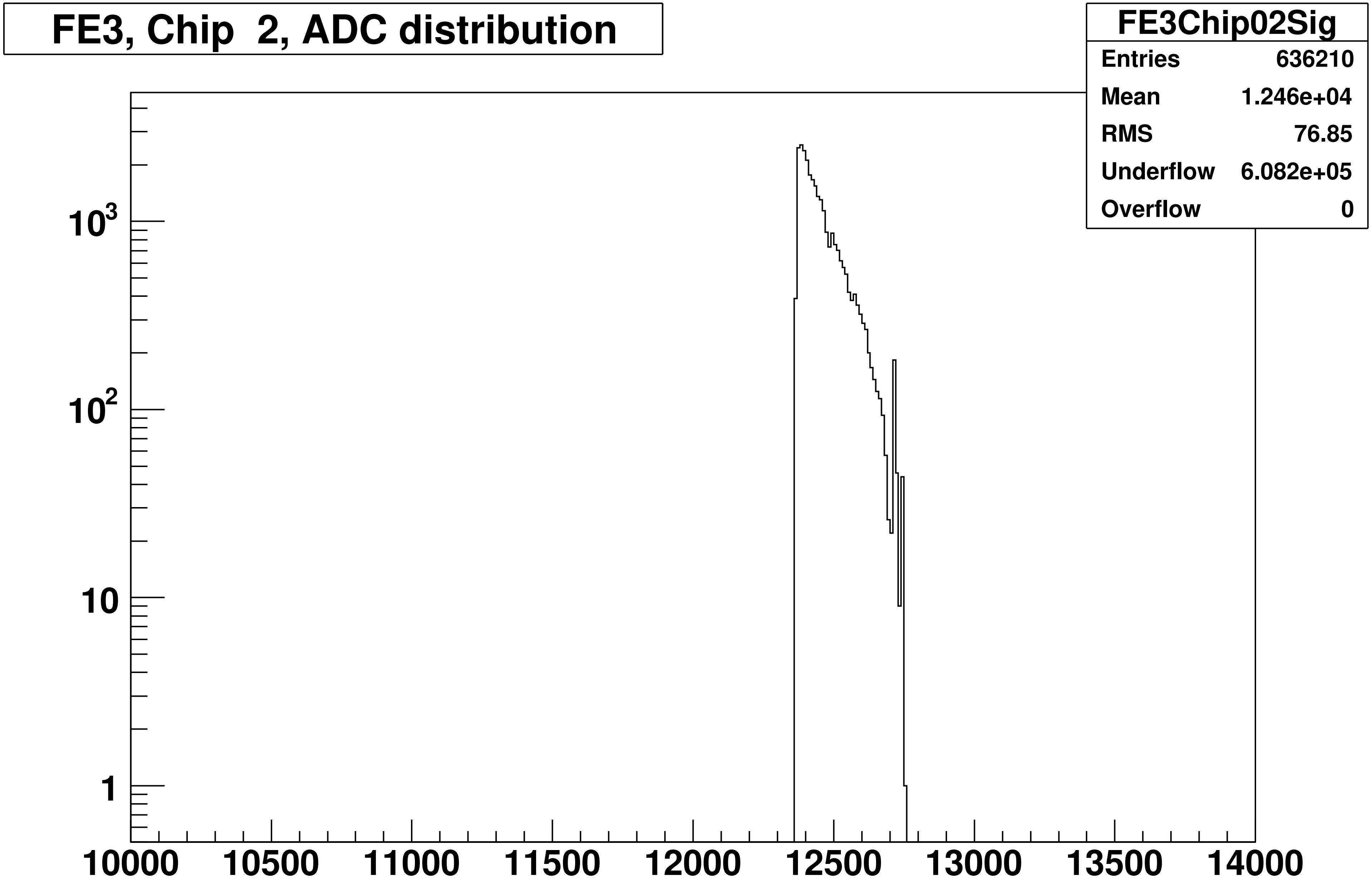


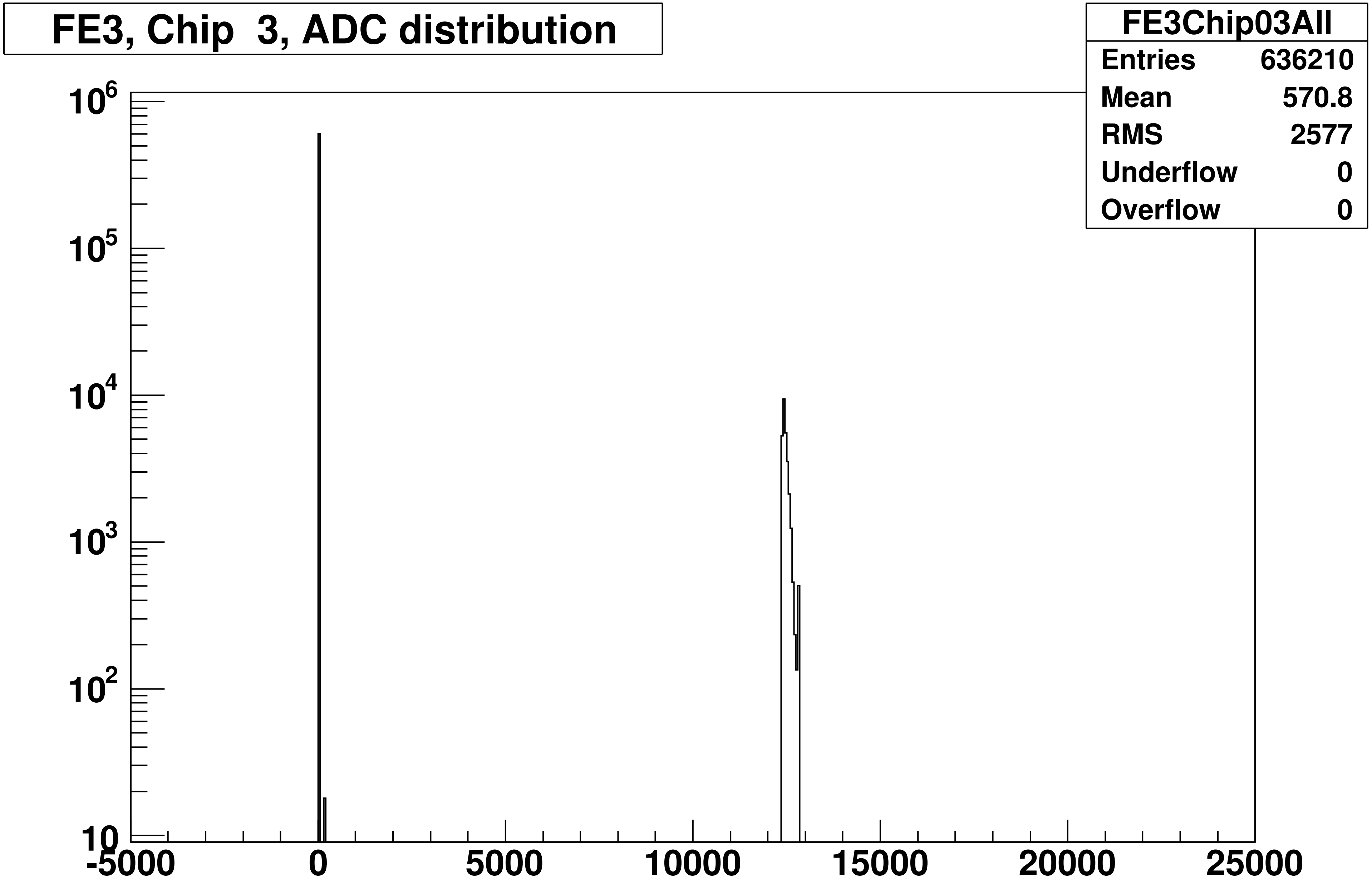
FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 636210
Mean 1.244e+04
RMS 78.17
Underflow 6.082e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000



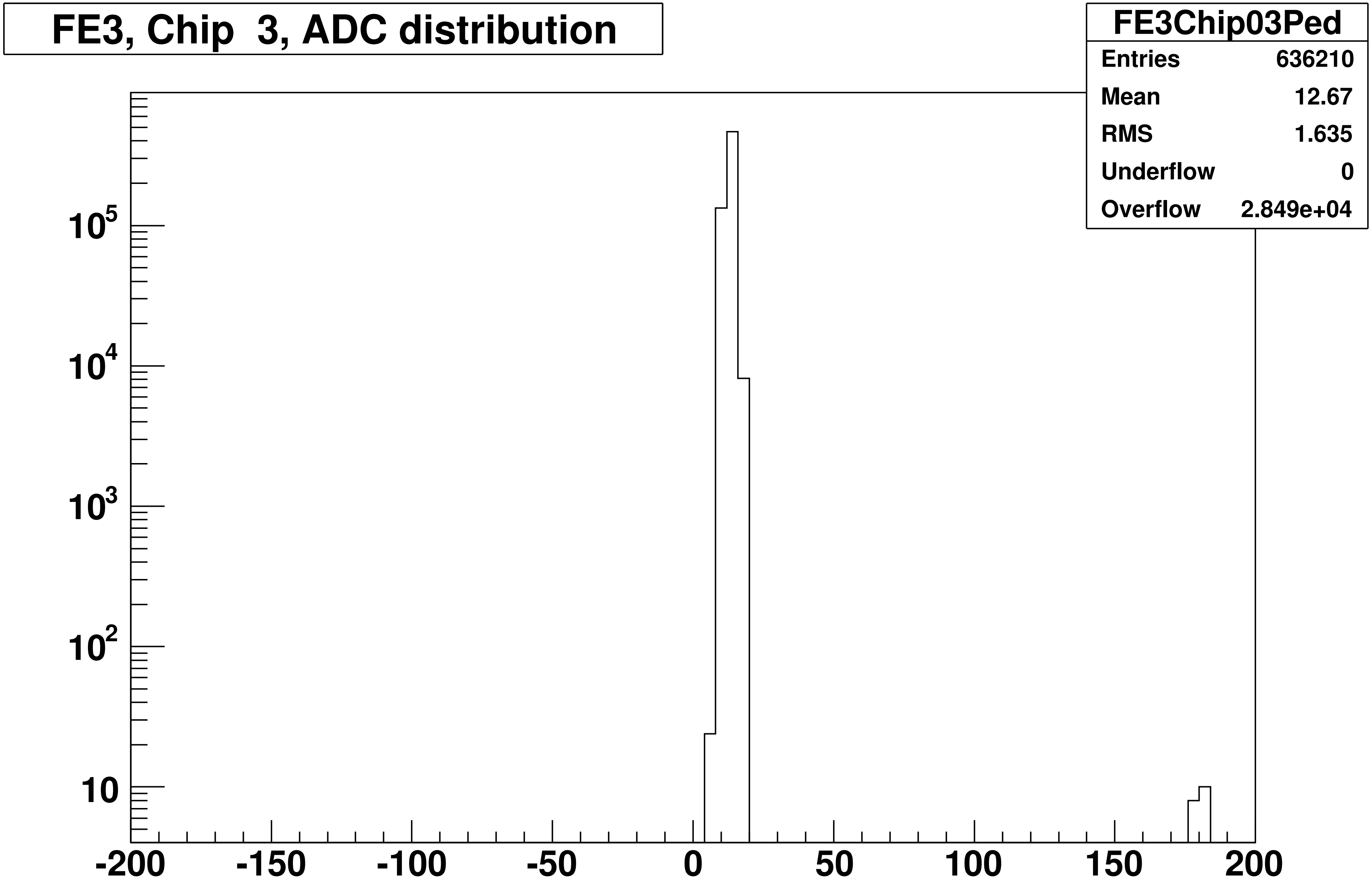
FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 636210
Mean 577
RMS 2549
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

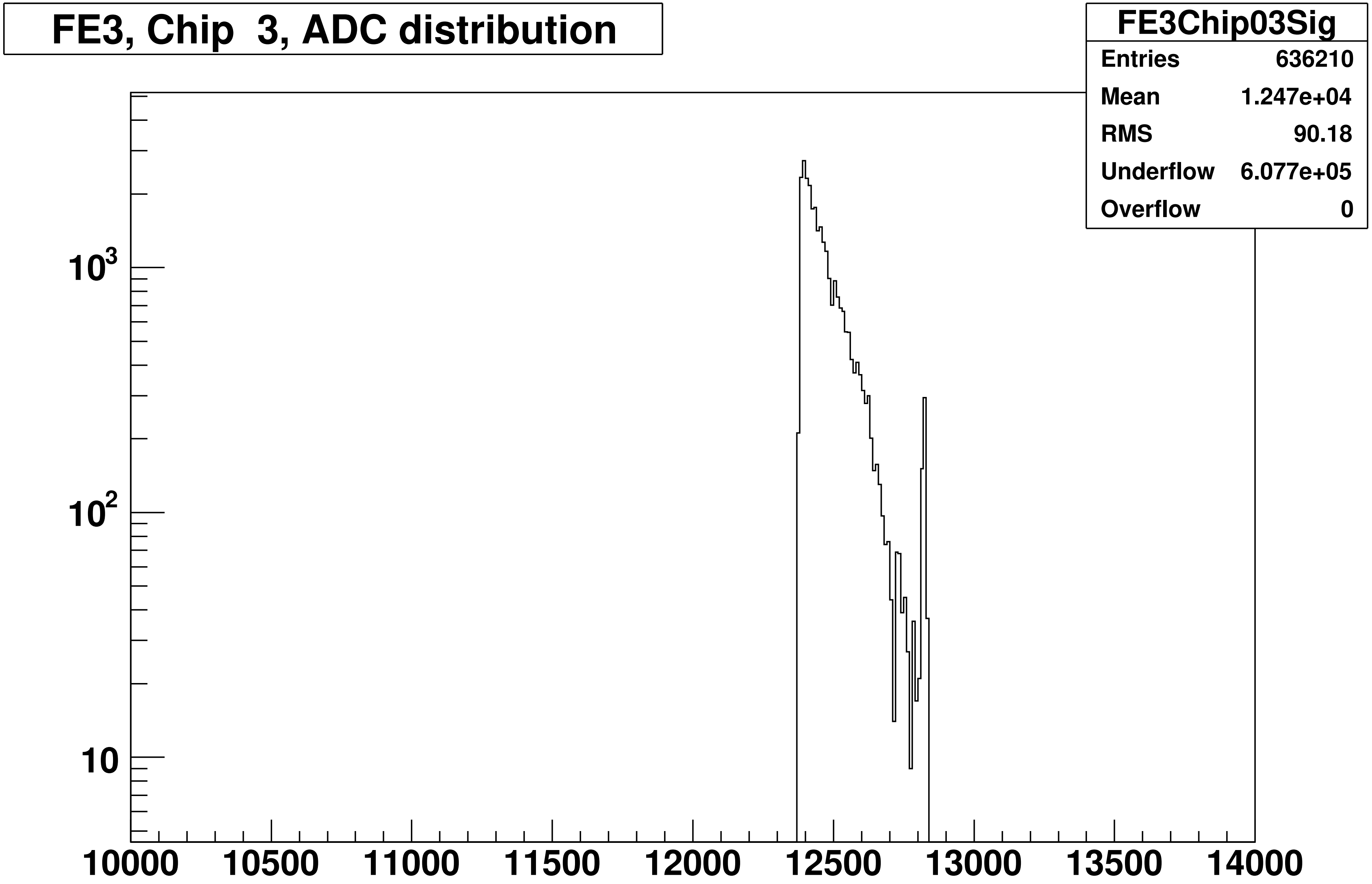

FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 636210
Mean 29.52
RMS 1.649
Underflow 0
Overflow 2.851e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 636210
Mean 1.246e+04
RMS 76.85
Underflow 6.082e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

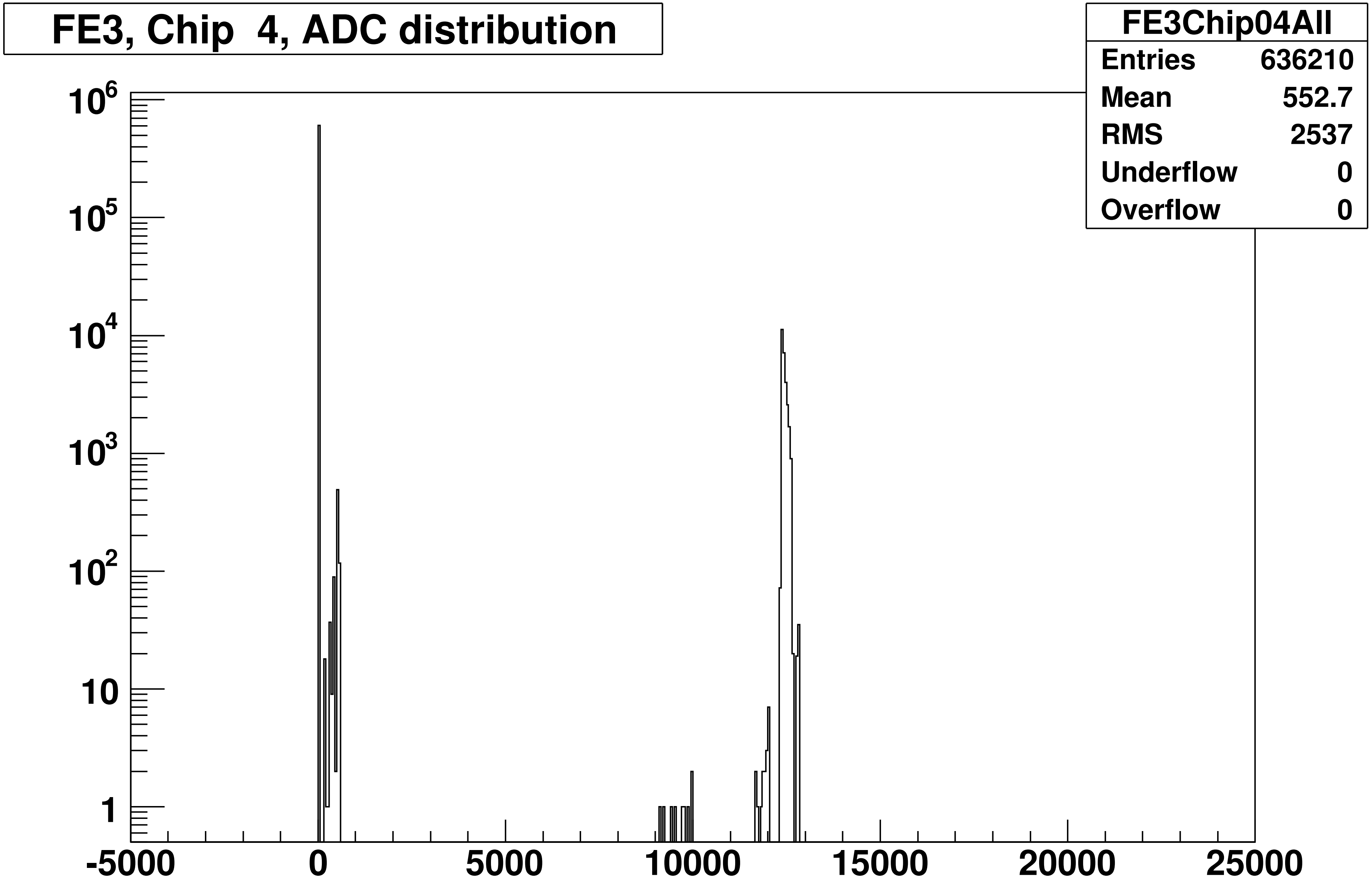


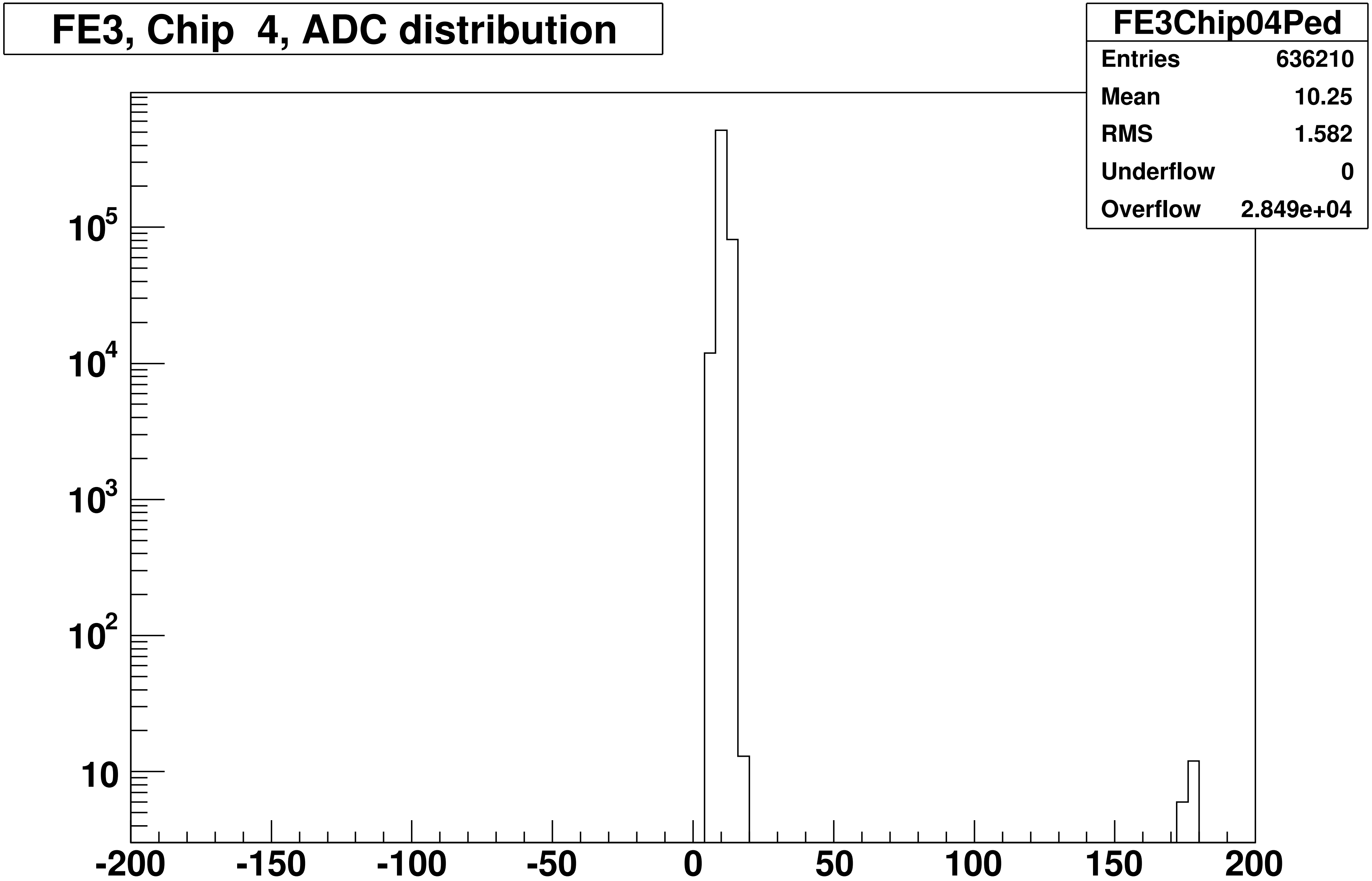
FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 636210
Mean 570.8
RMS 2577
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

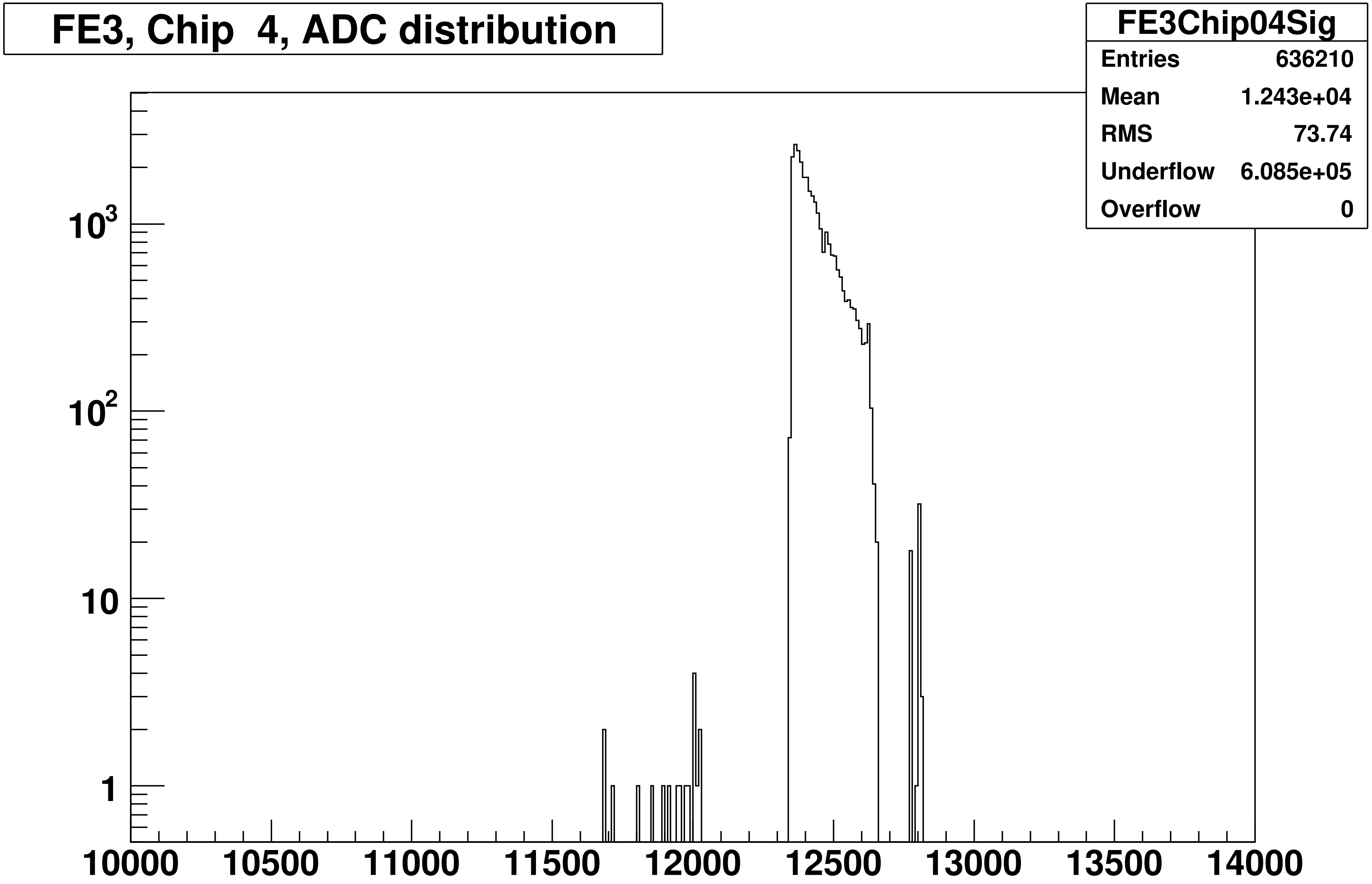

FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 636210
Mean 12.67
RMS 1.635
Underflow 0
Overflow 2.849e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



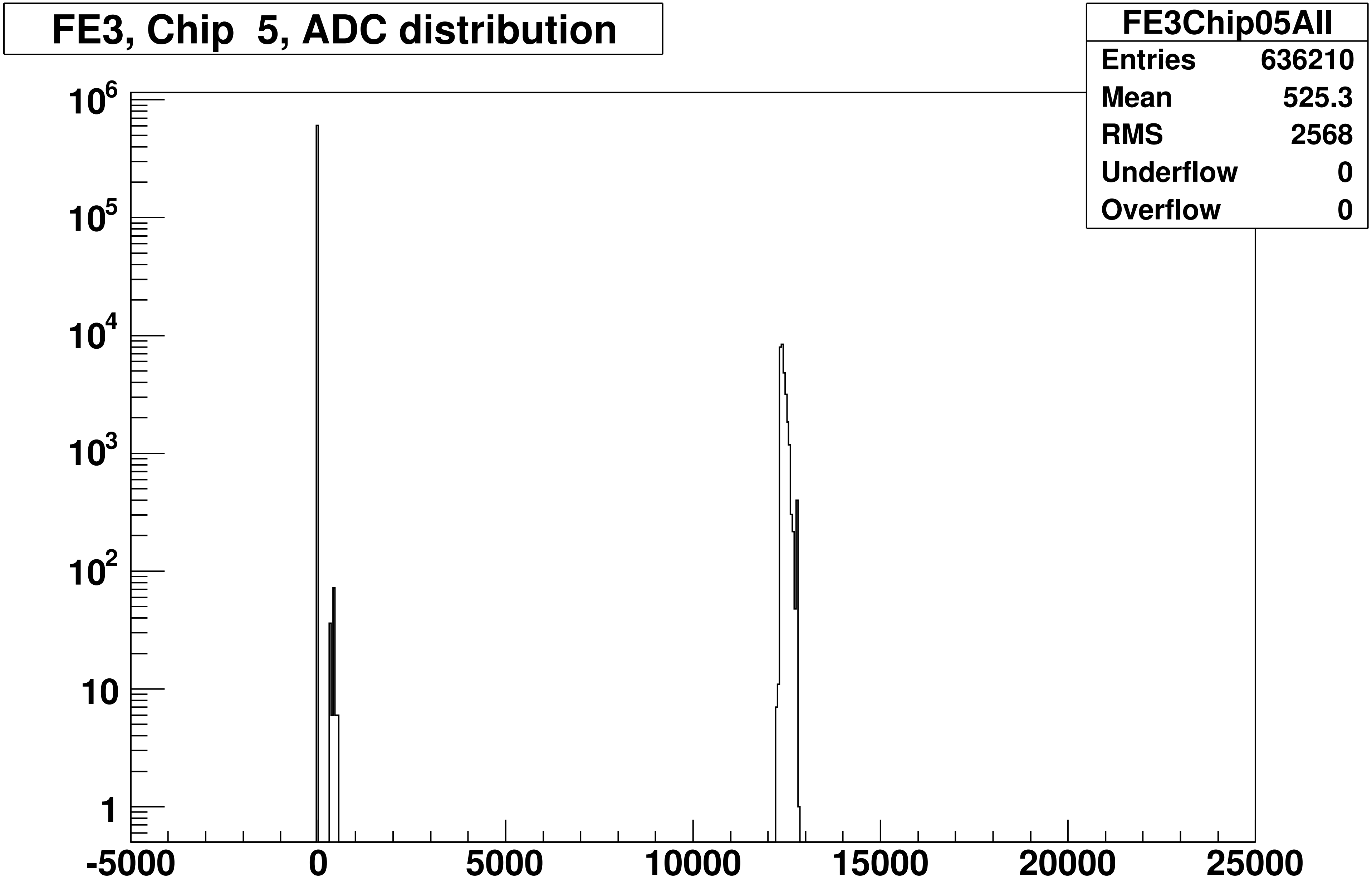
FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 636210
Mean 1.247e+04
RMS 90.18
Underflow 6.077e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

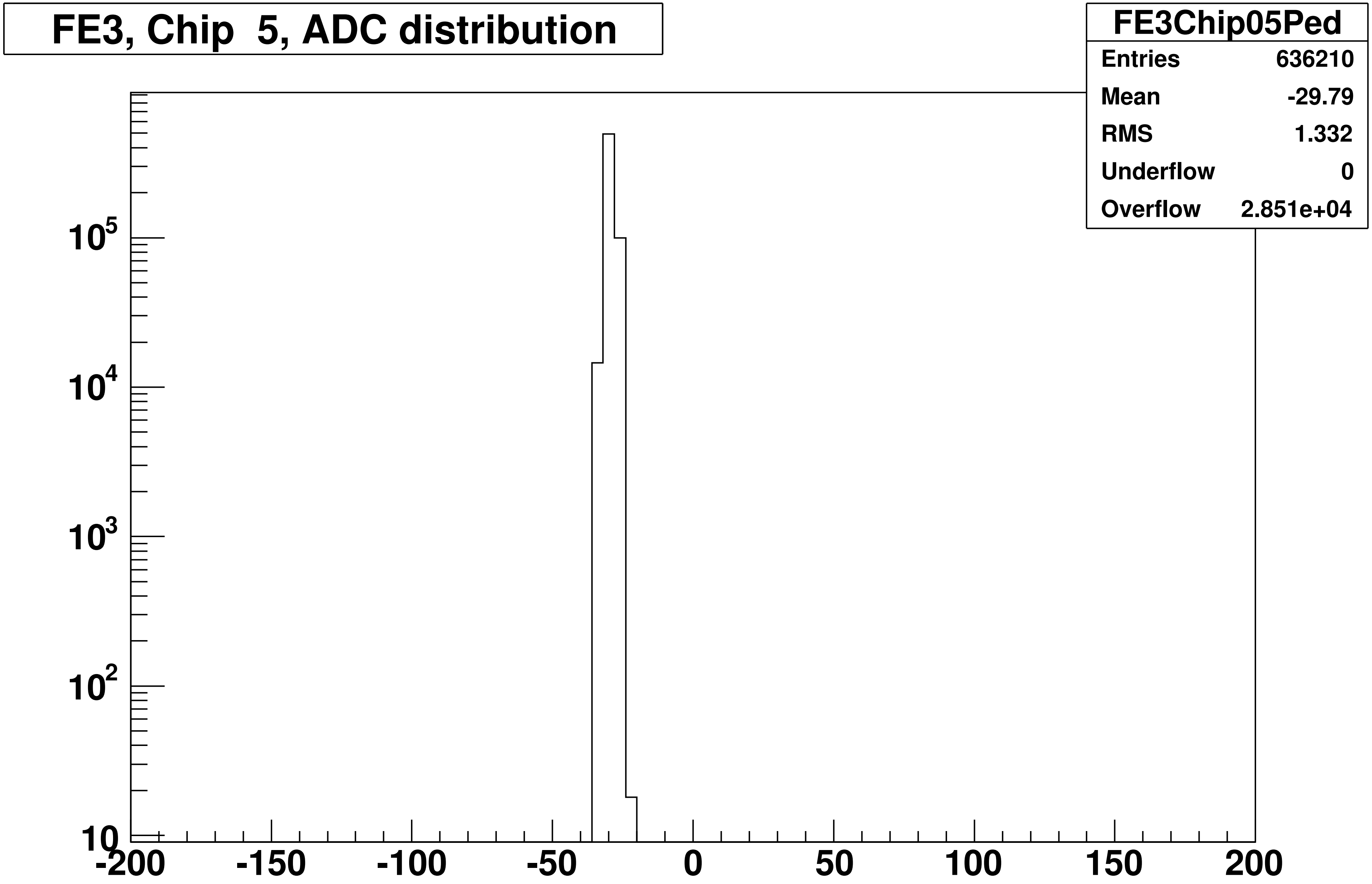

FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 636210
Mean 552.7
RMS 2537
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

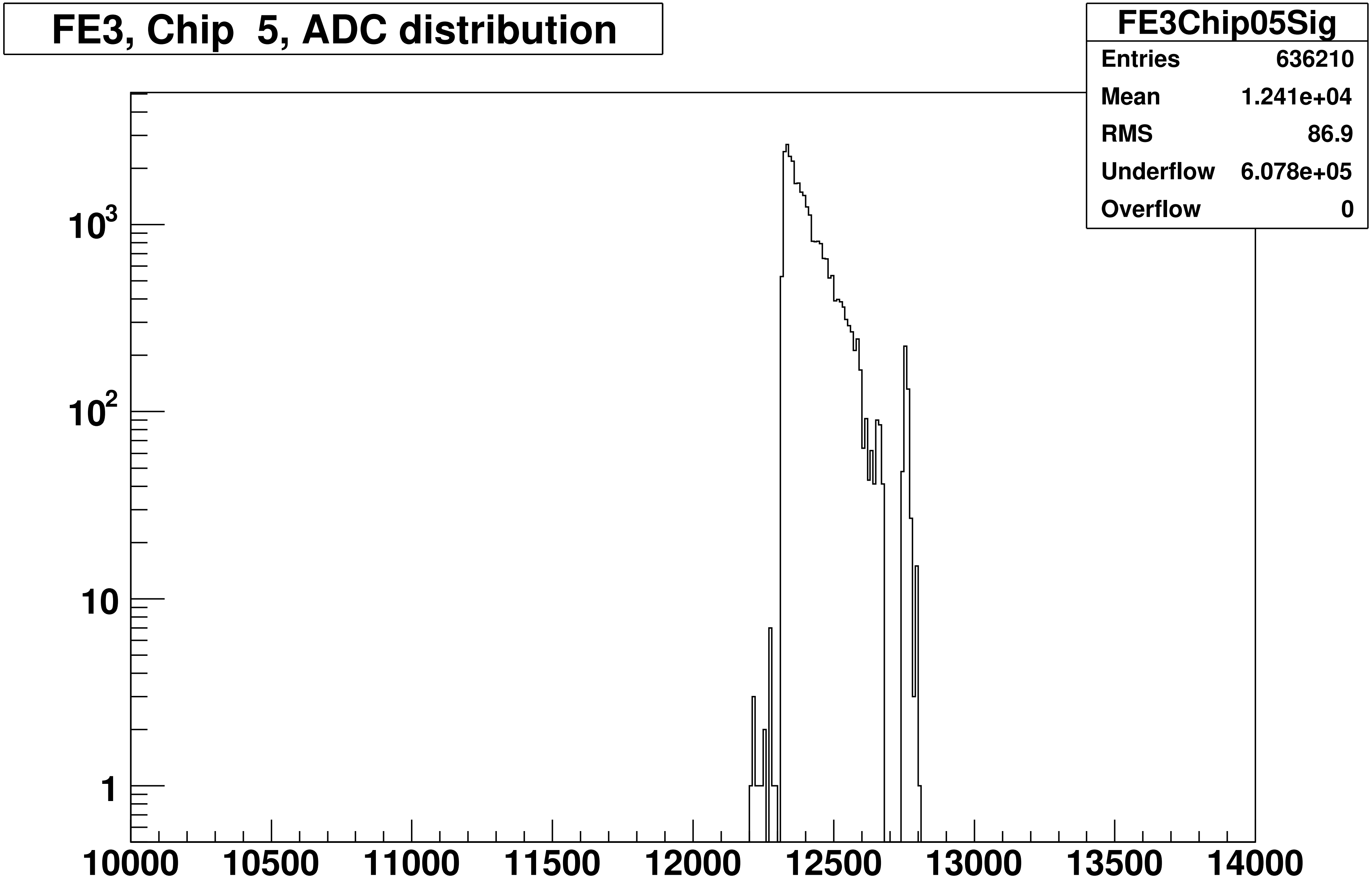

FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 636210
Mean 10.25
RMS 1.582
Underflow 0
Overflow 2.849e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

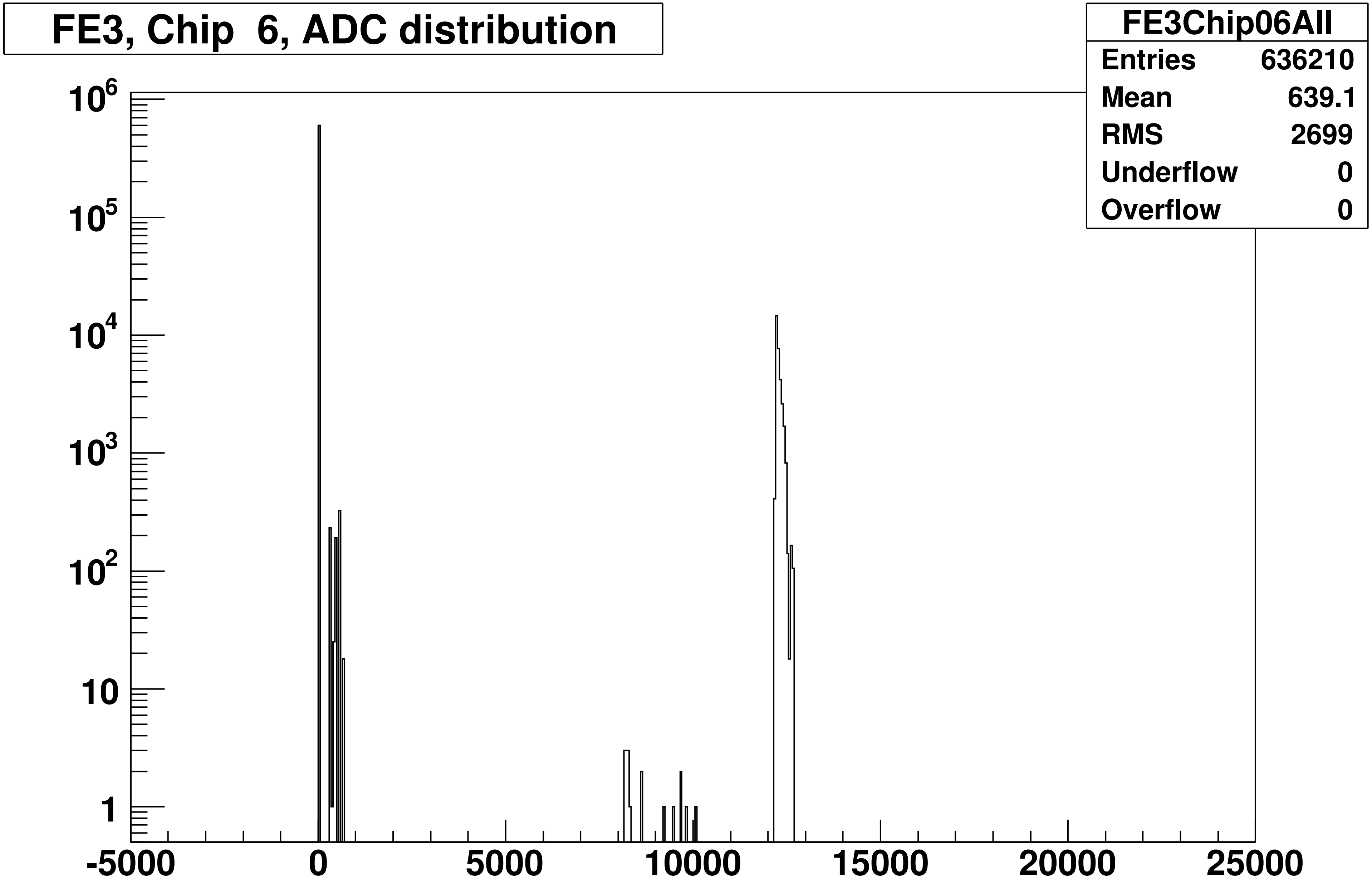


FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 636210
Mean 1.243e+04
RMS 73.74
Underflow 6.085e+05
Overflow 0
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

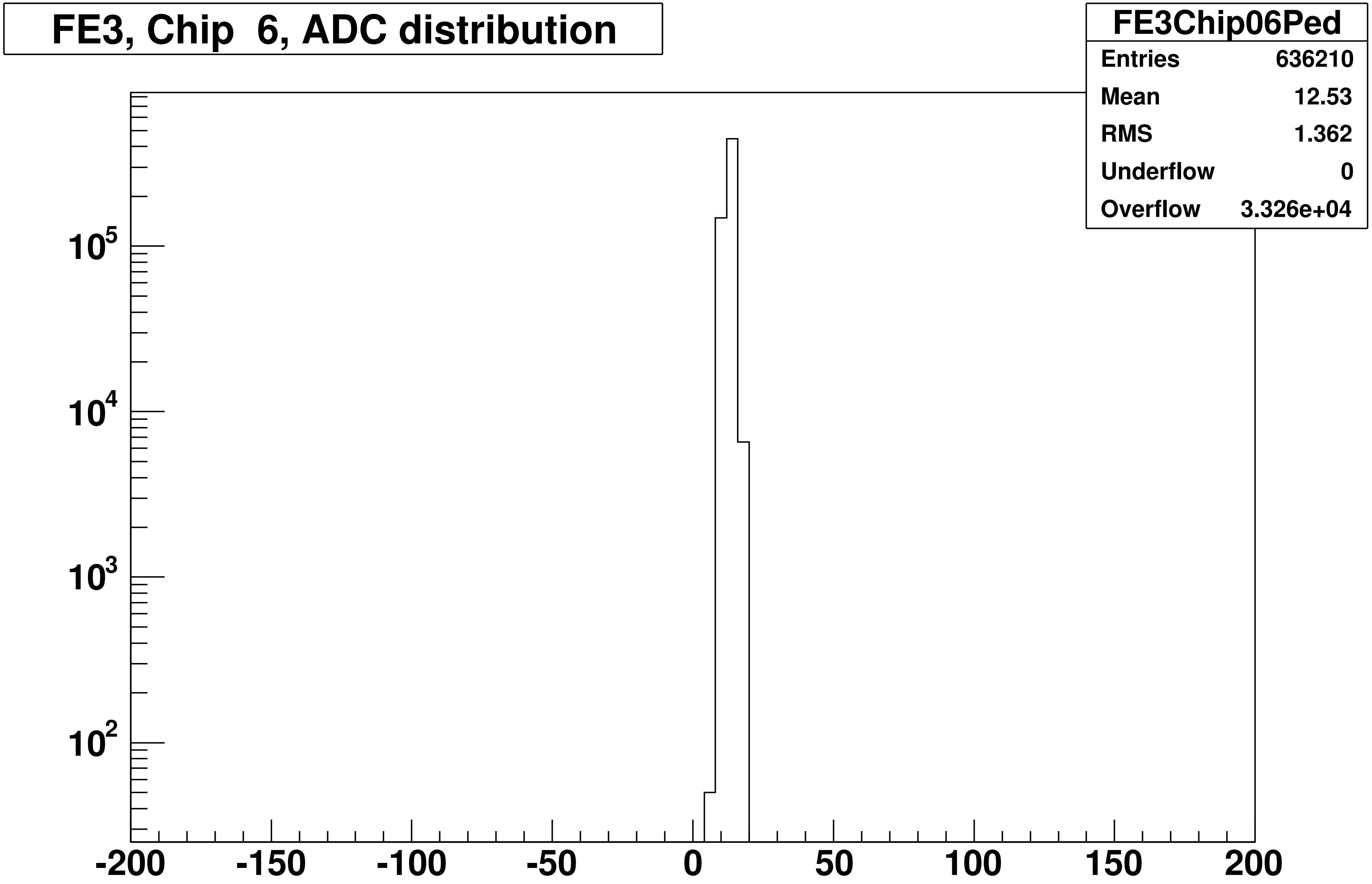

FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 636210
Mean 525.3
RMS 2568
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

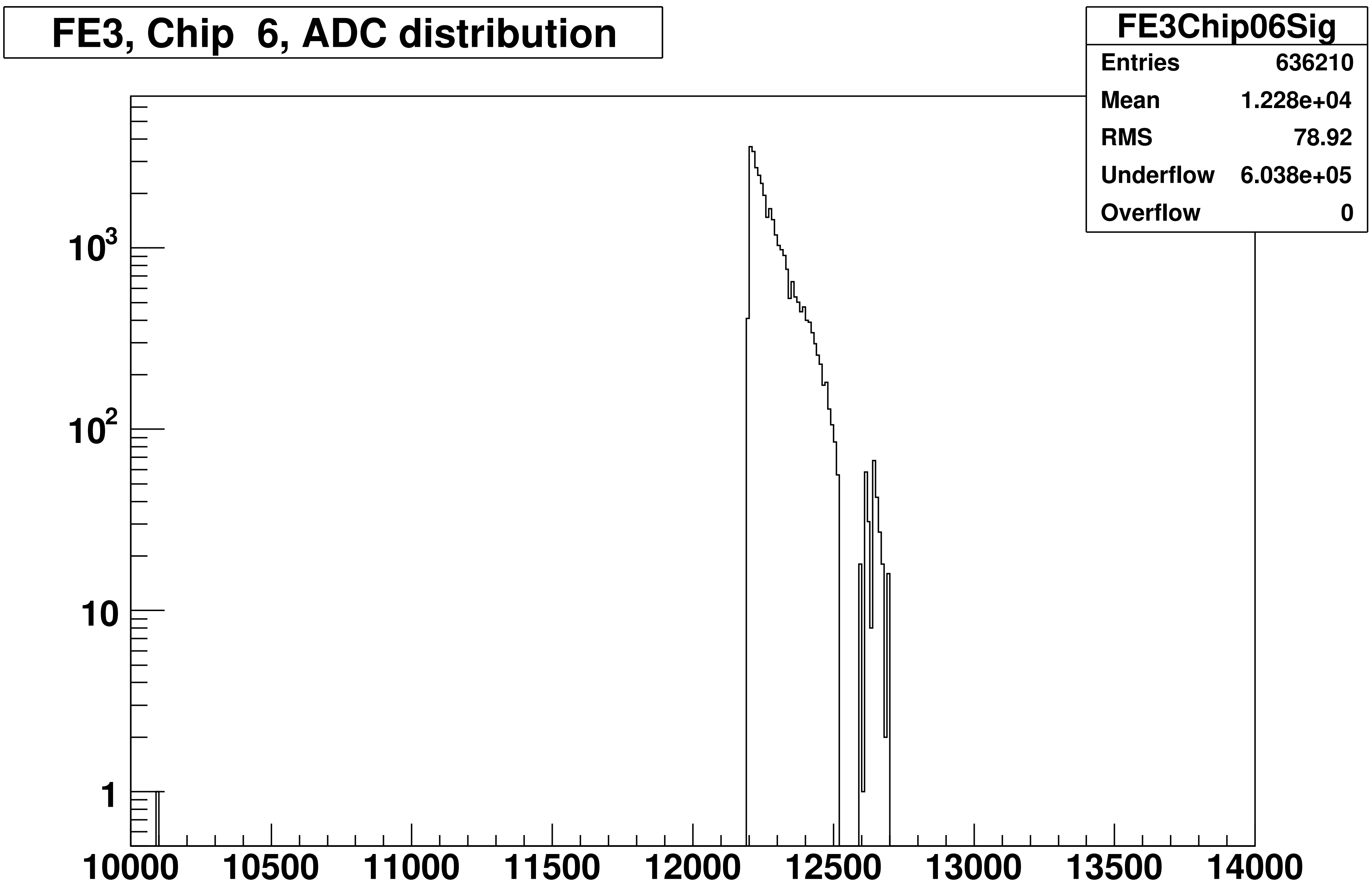

FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 636210
Mean -29.79
RMS 1.332
Underflow 0
Overflow 2.851e+04
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200

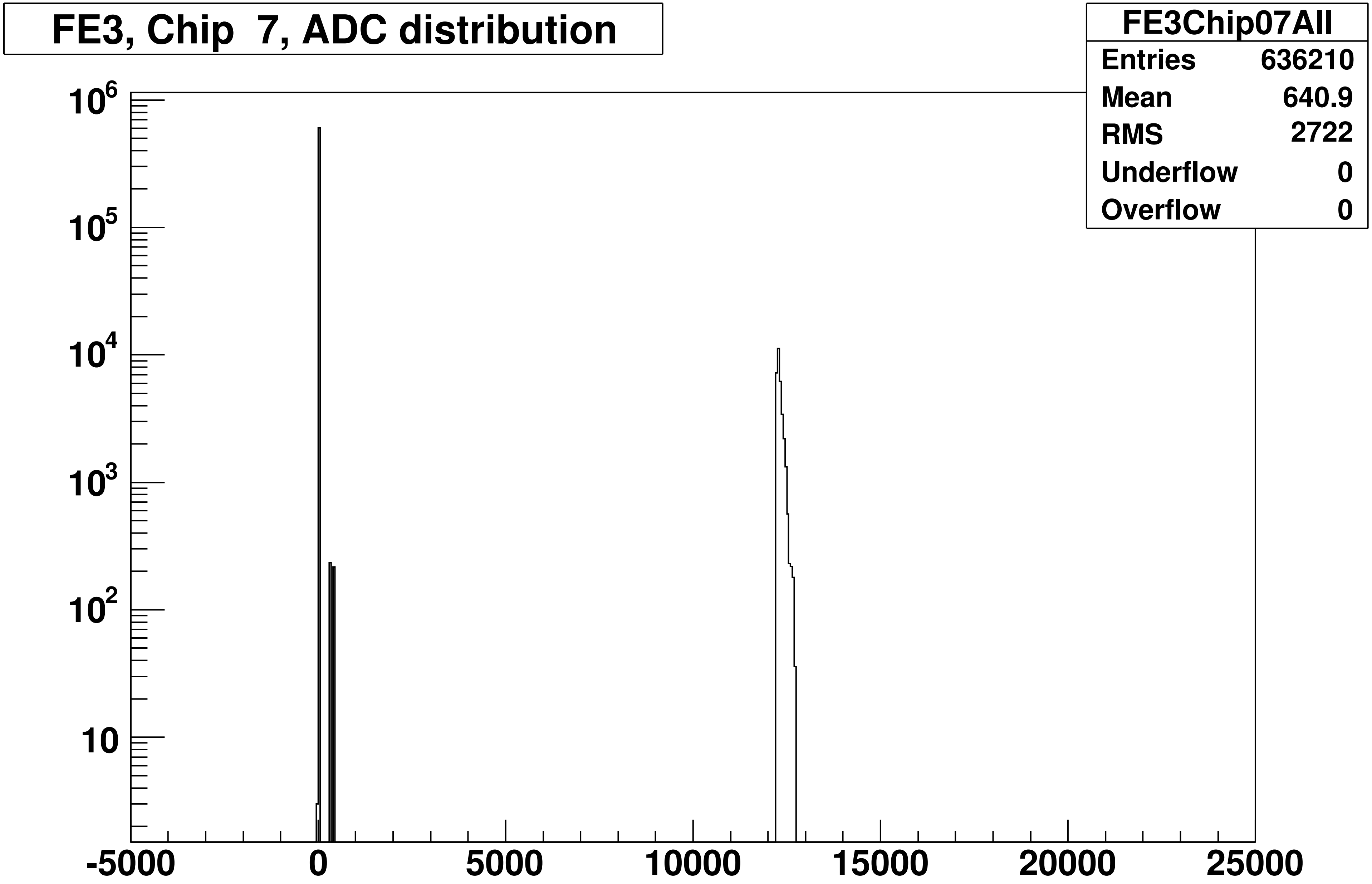

FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 636210
Mean 1.241e+04
RMS 86.9
Underflow 6.078e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

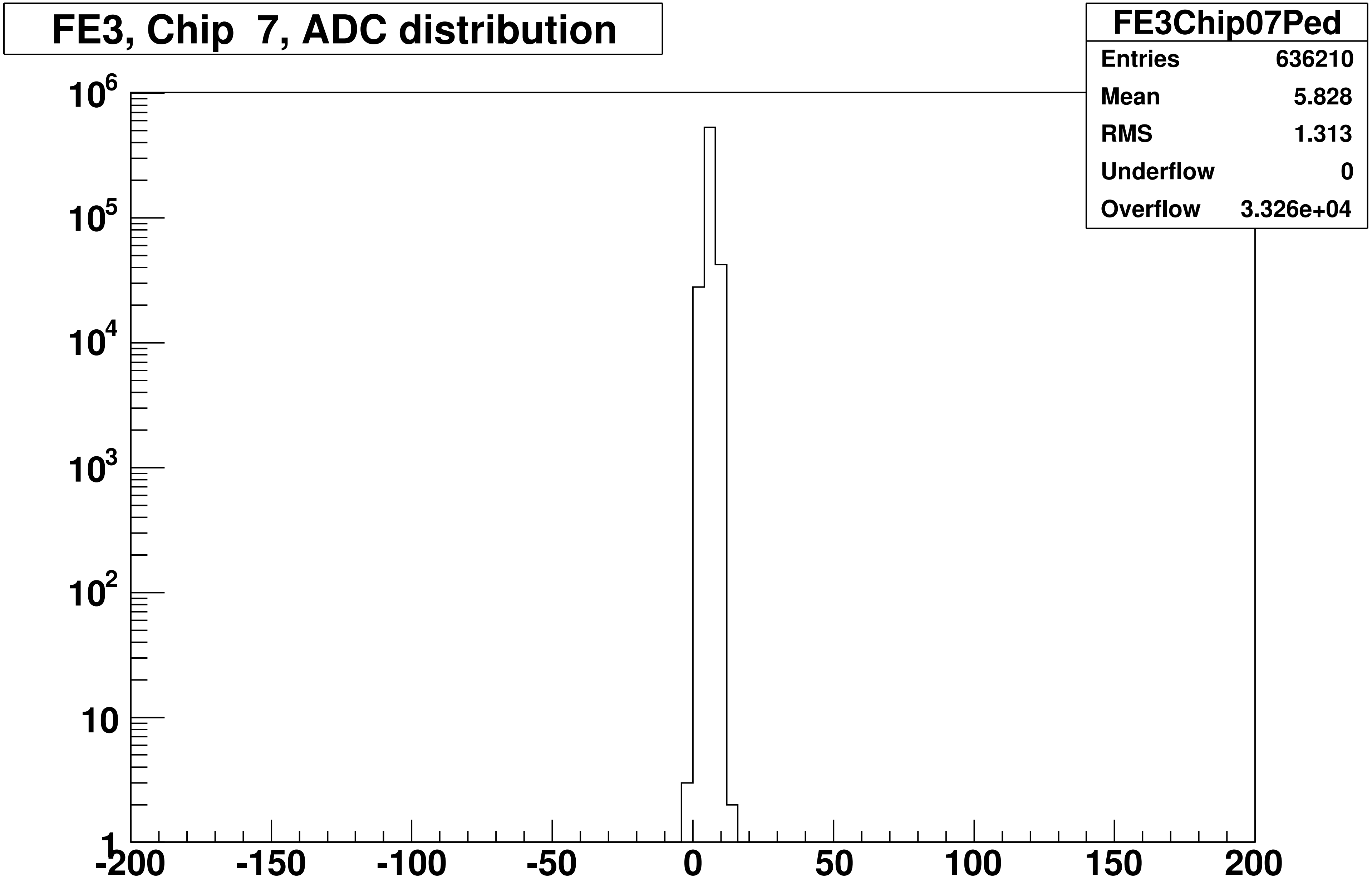


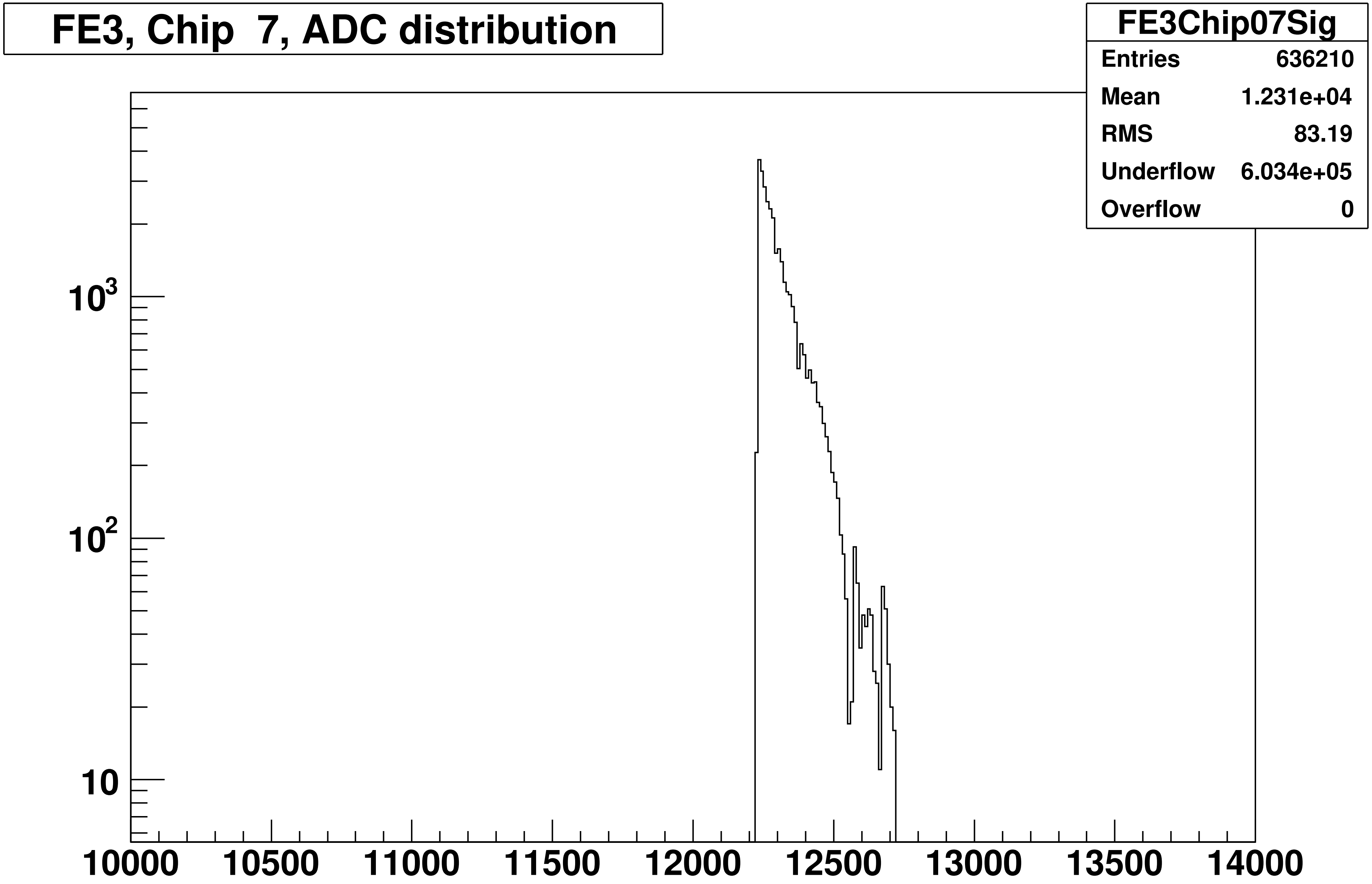
FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 636210
Mean 639.1
RMS 2699
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

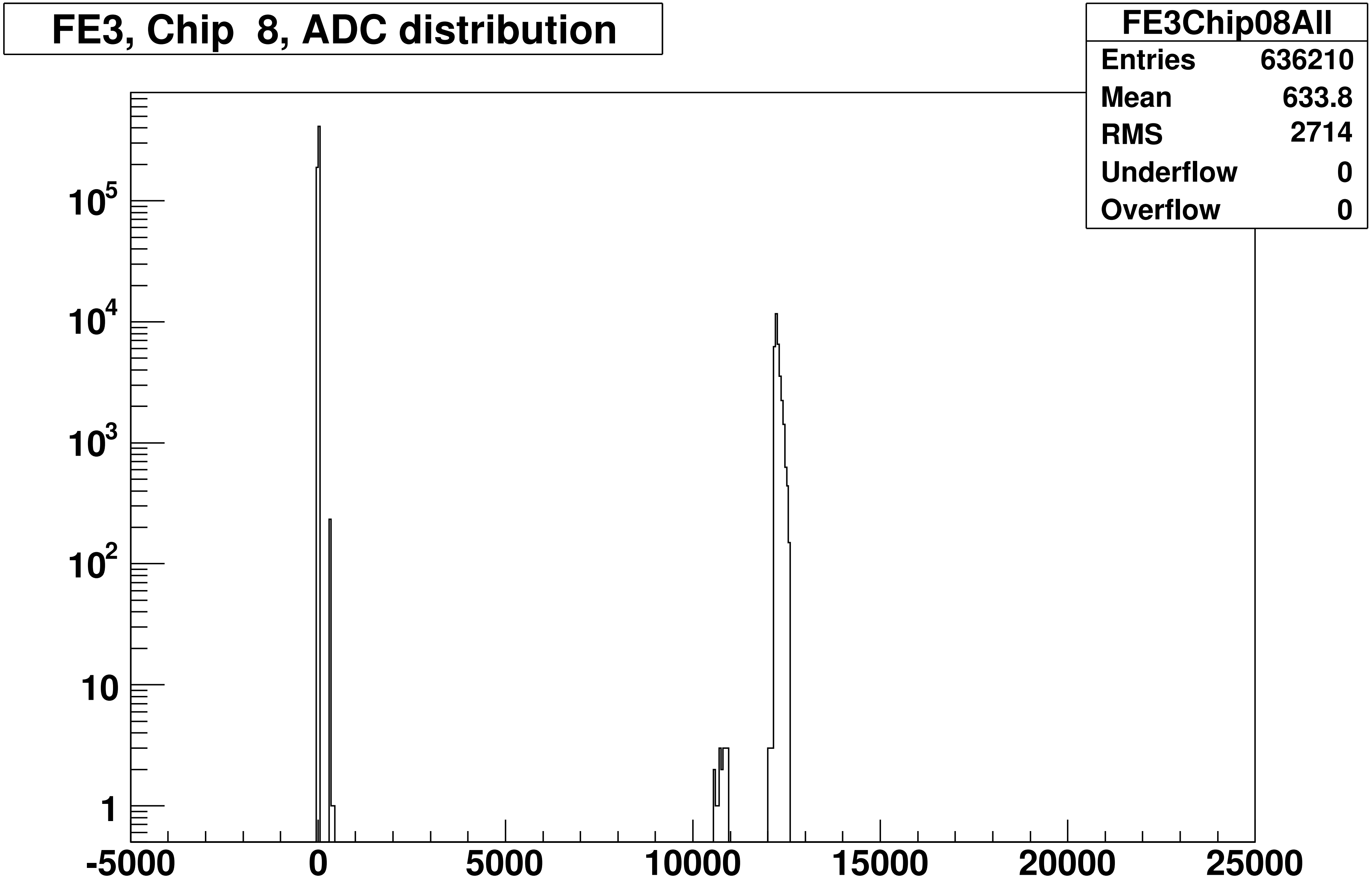

FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 636210
Mean 12.53
RMS 1.362
Underflow 0
Overflow 3.326e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

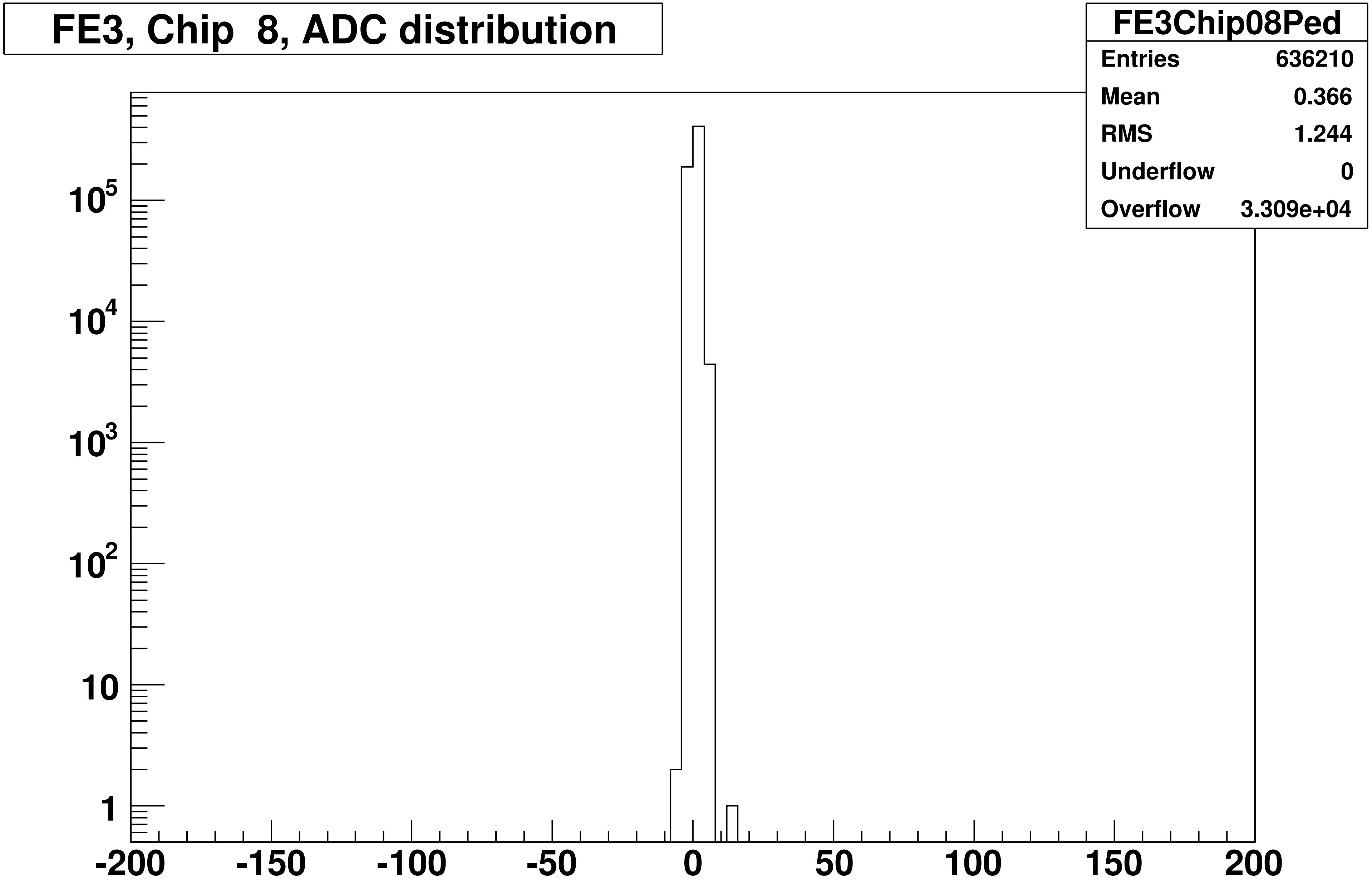

FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 636210
Mean 1.228e+04
RMS 78.92
Underflow 6.038e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

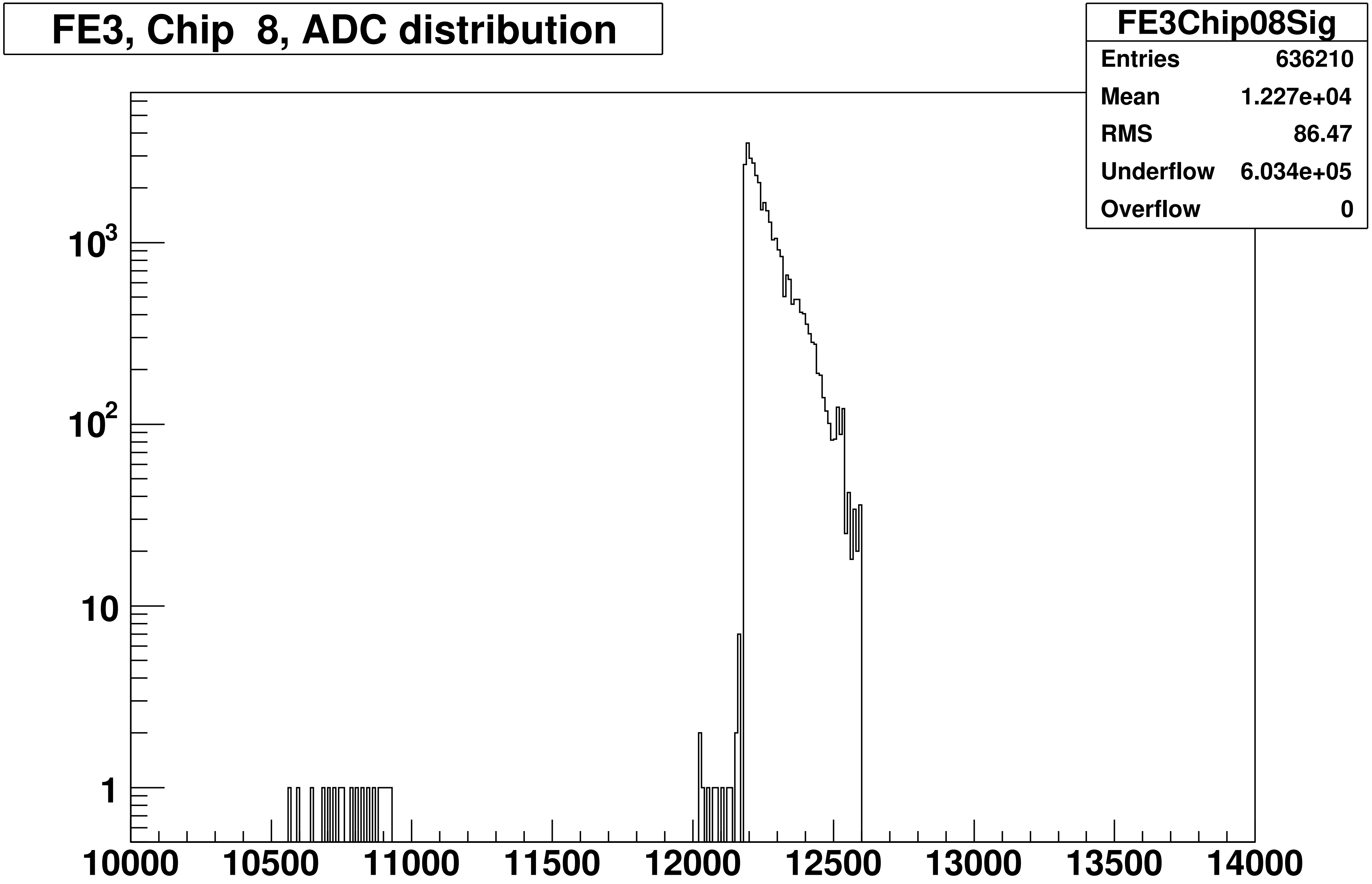

FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 636210
Mean 640.9
RMS 2722
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 636210
Mean 5.828
RMS 1.313
Underflow 0
Overflow 3.326e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

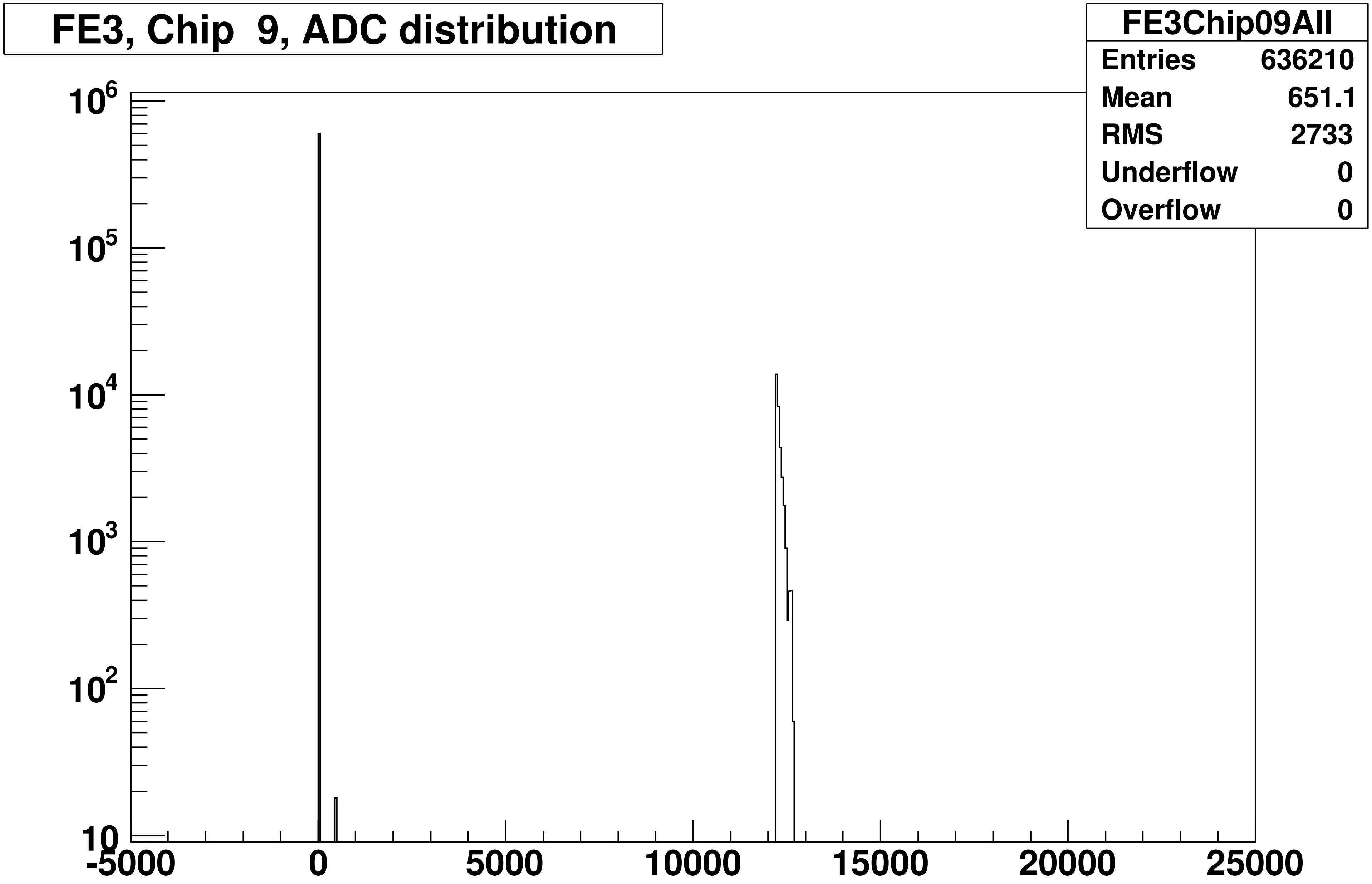

FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 636210
Mean 1.231e+04
RMS 83.19
Underflow 6.034e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

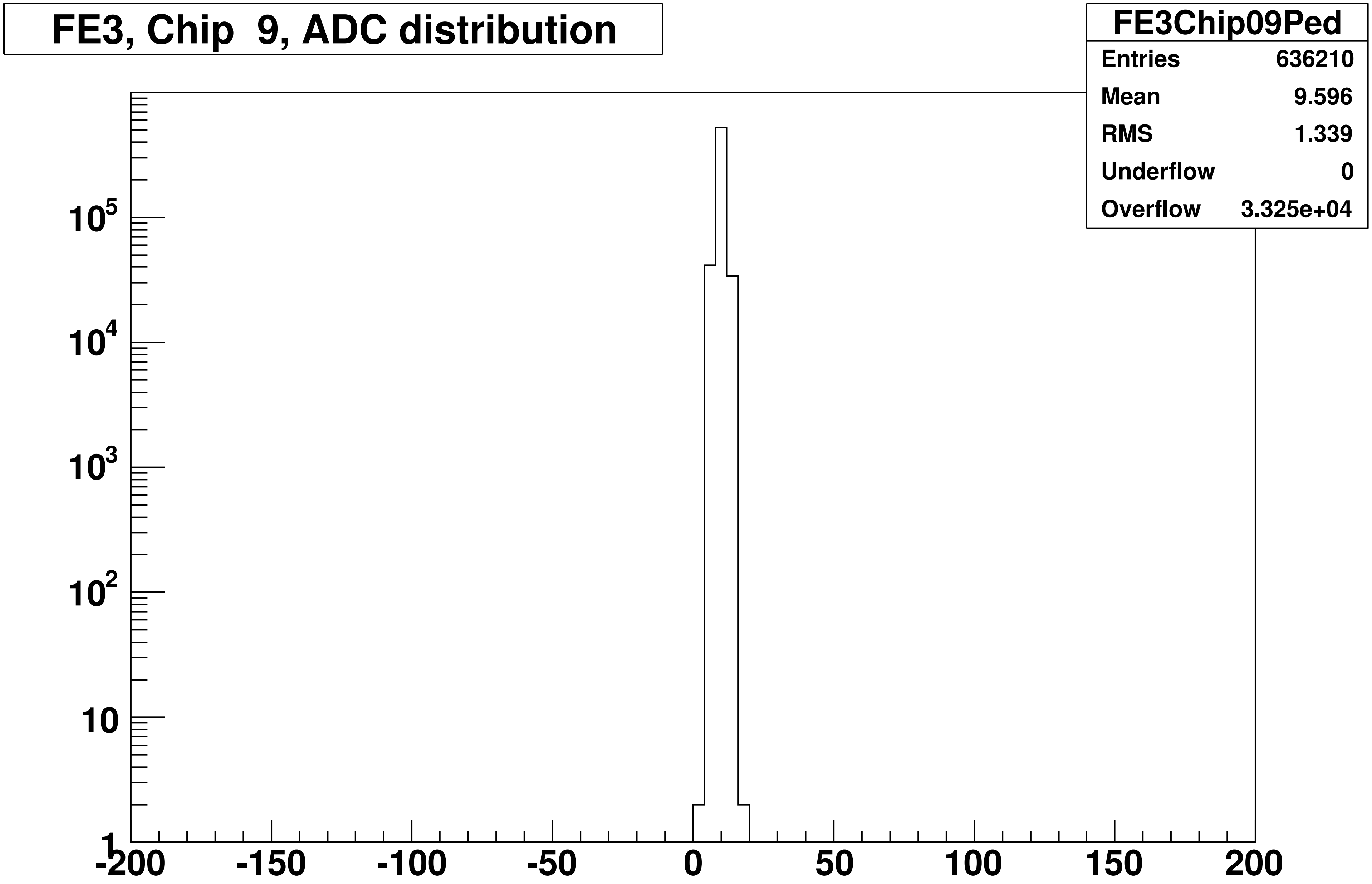

FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 636210
Mean 633.8
RMS 2714
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

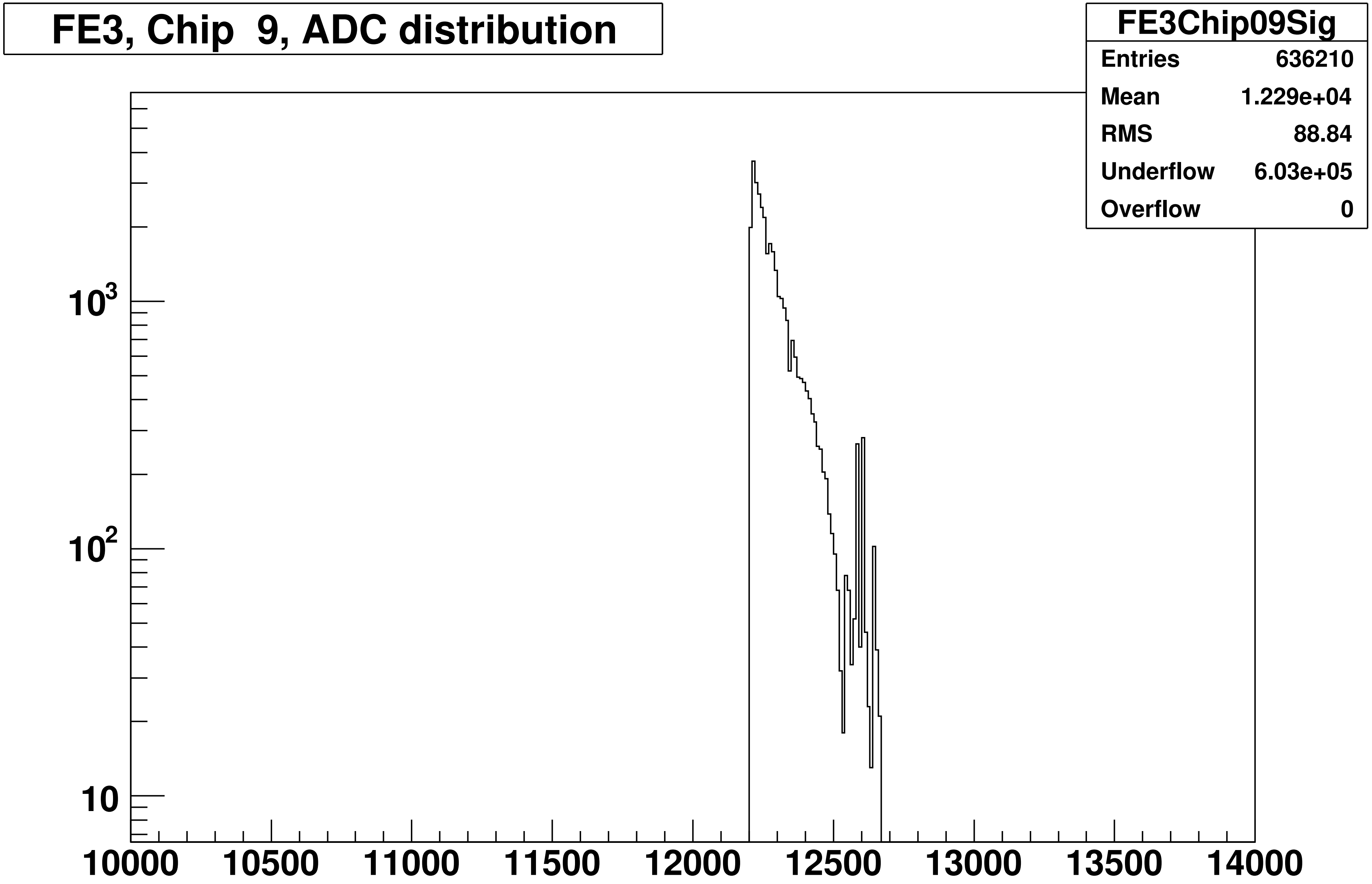

FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 636210
Mean 0.366
RMS 1.244
Underflow 0
Overflow 3.309e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

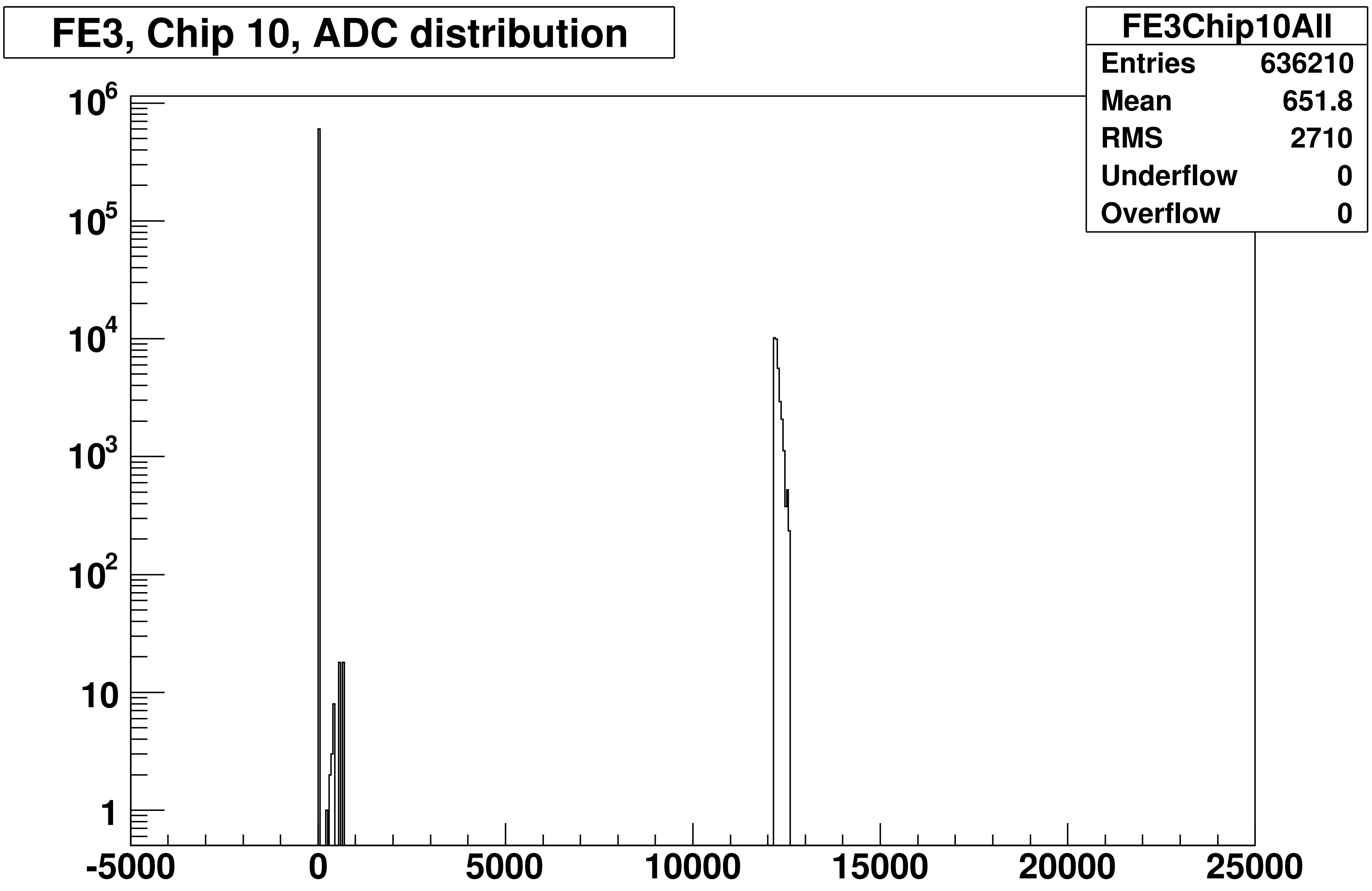


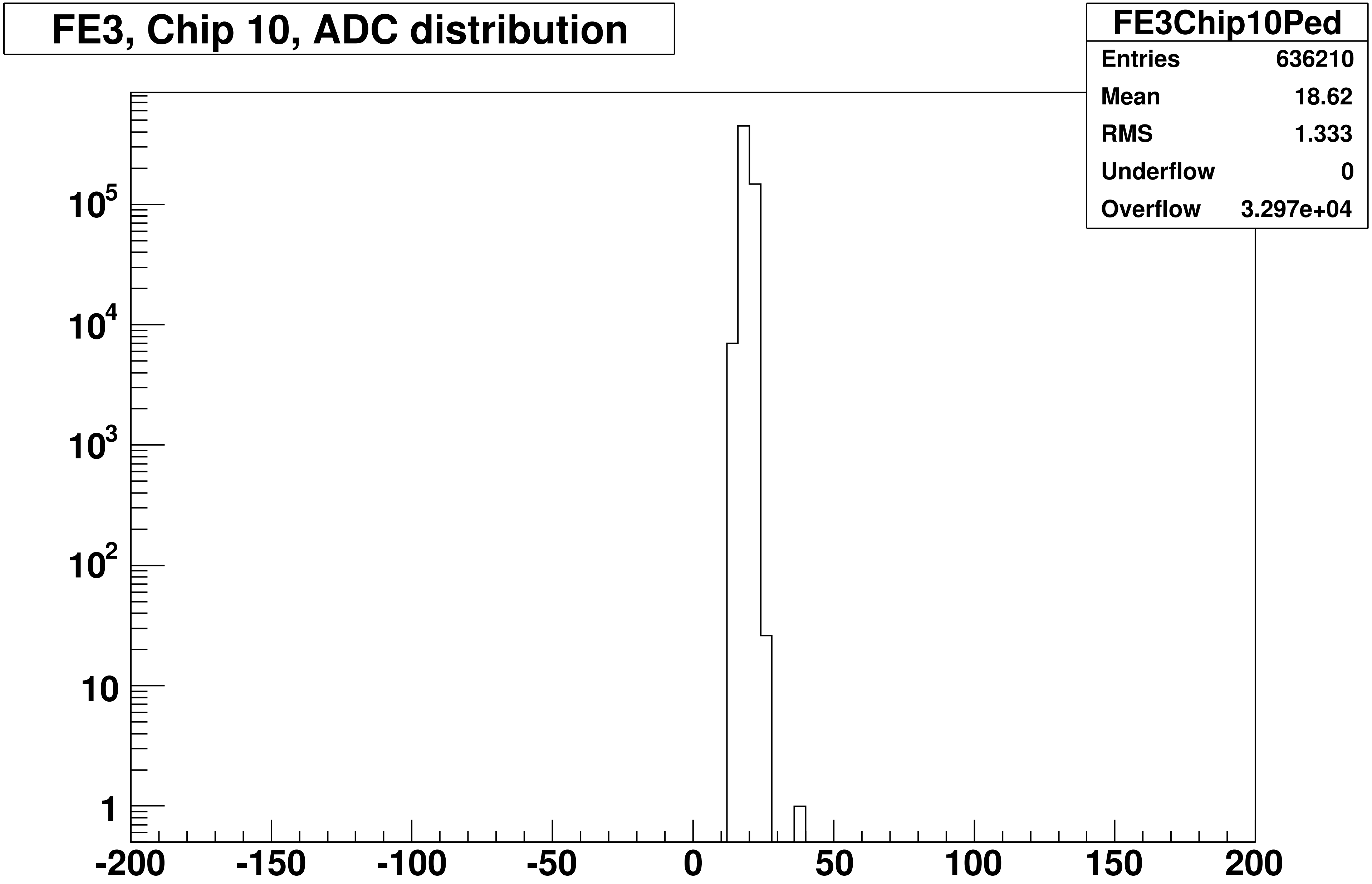
FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 636210
Mean 1.227e+04
RMS 86.47
Underflow 6.034e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

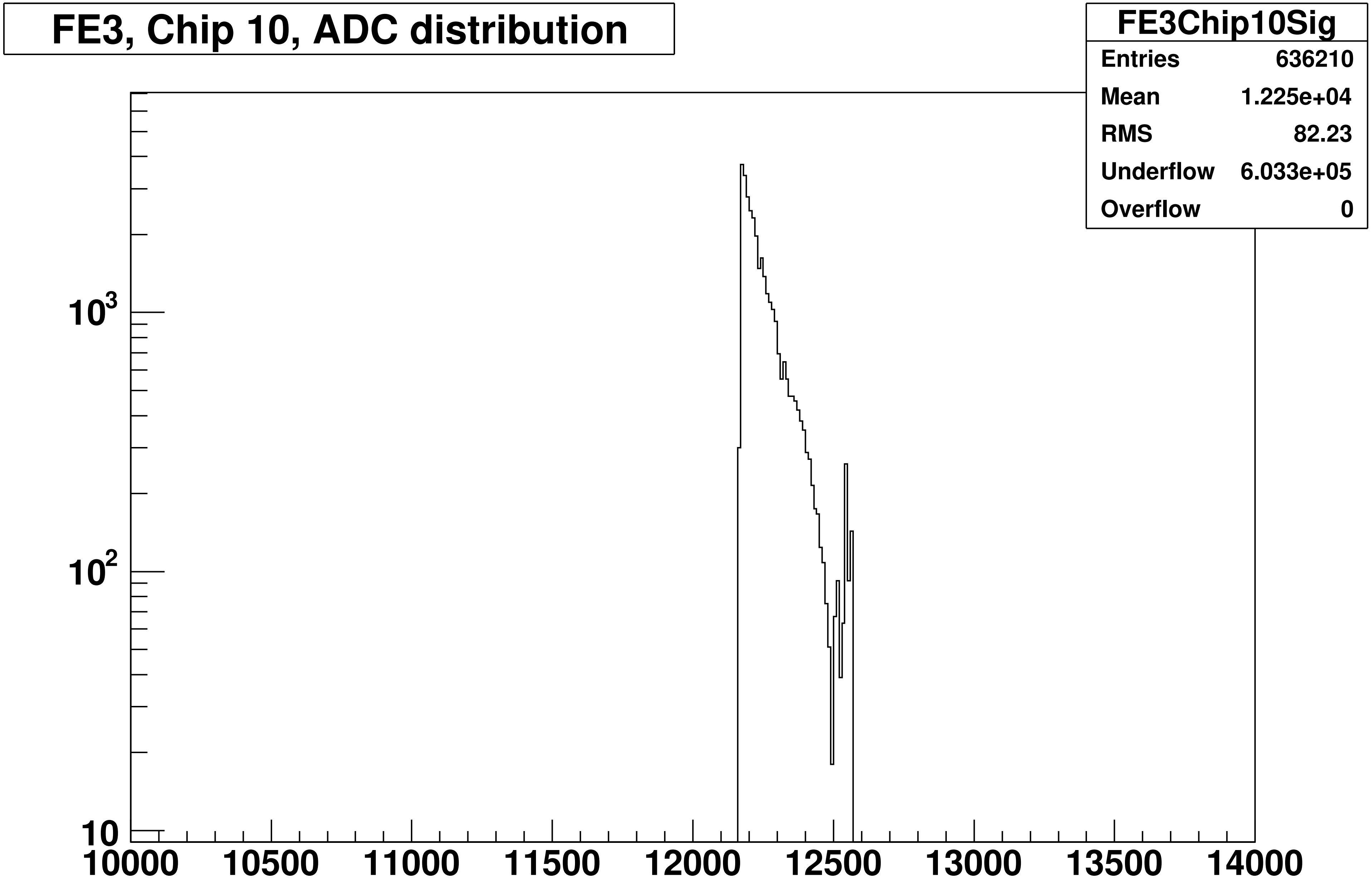

FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 636210
Mean 651.1
RMS 2733
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

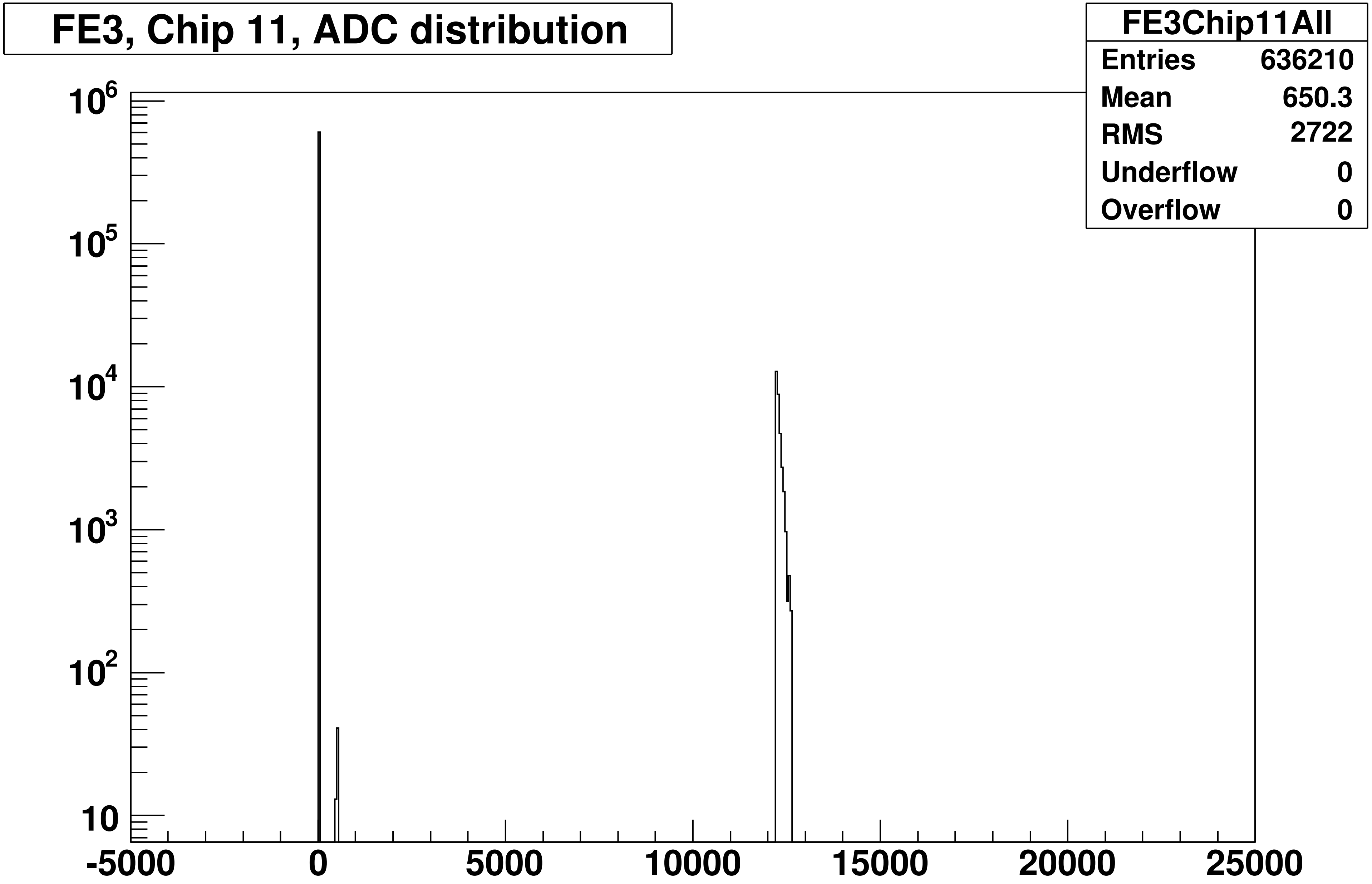

FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 636210
Mean 9.596
RMS 1.339
Underflow 0
Overflow 3.325e+04
1
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 636210
Mean 1.229e+04
RMS 88.84
Underflow 6.03e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

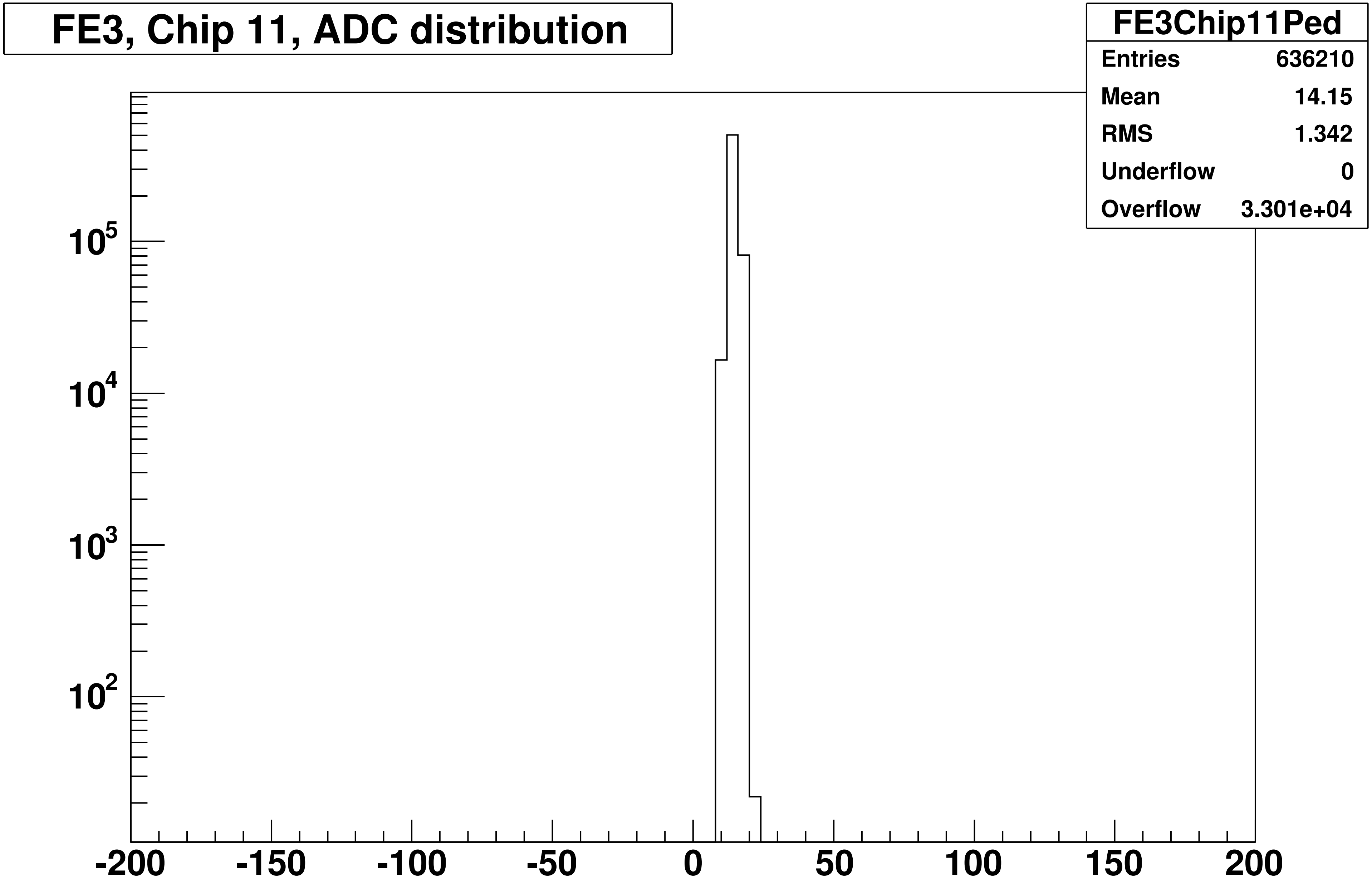

FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 636210
Mean 651.8
RMS 2710
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

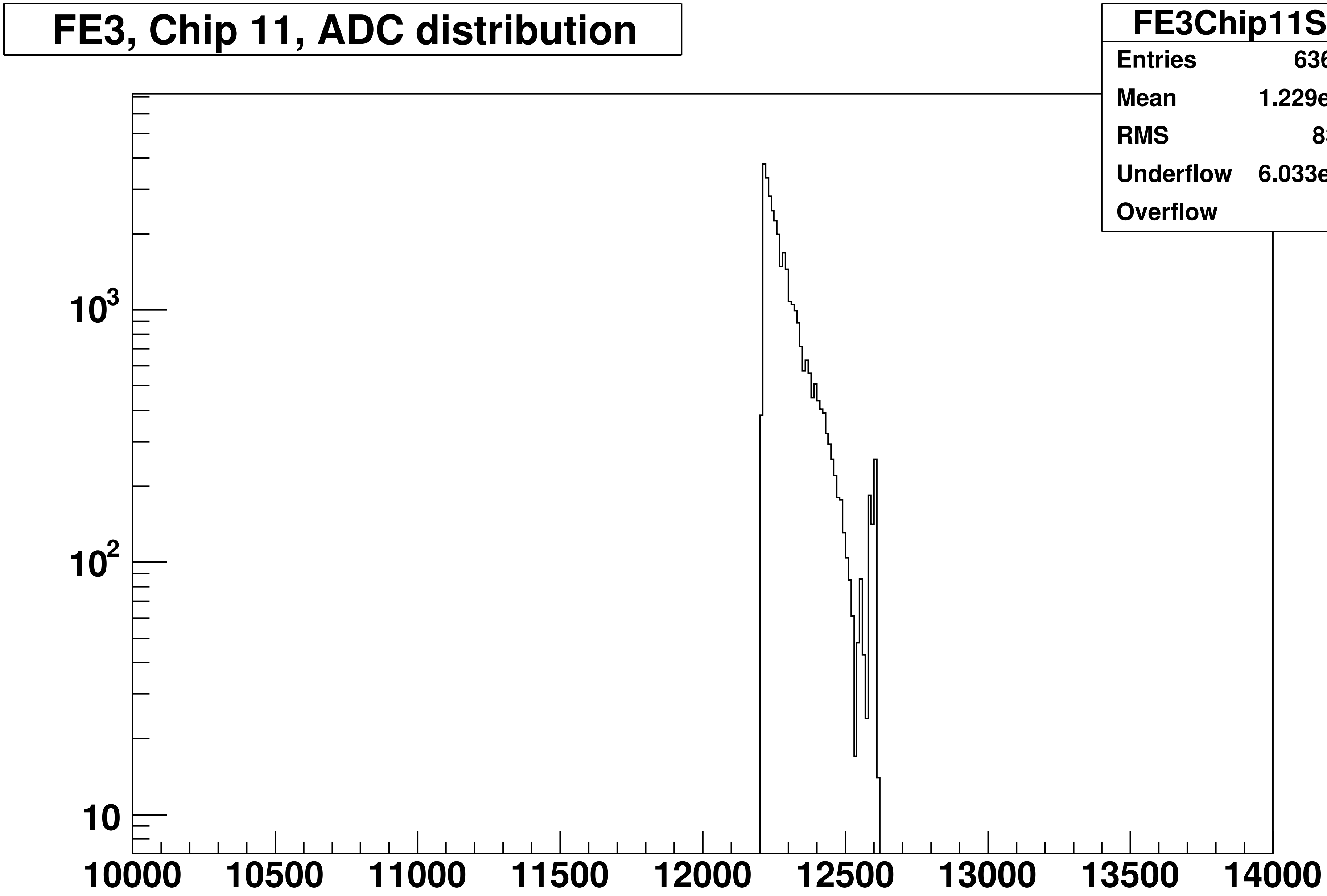

FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 636210
Mean 18.62
RMS 1.333
Underflow 0
Overflow 3.297e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 636210
Mean 1.225e+04
RMS 82.23
Underflow 6.033e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

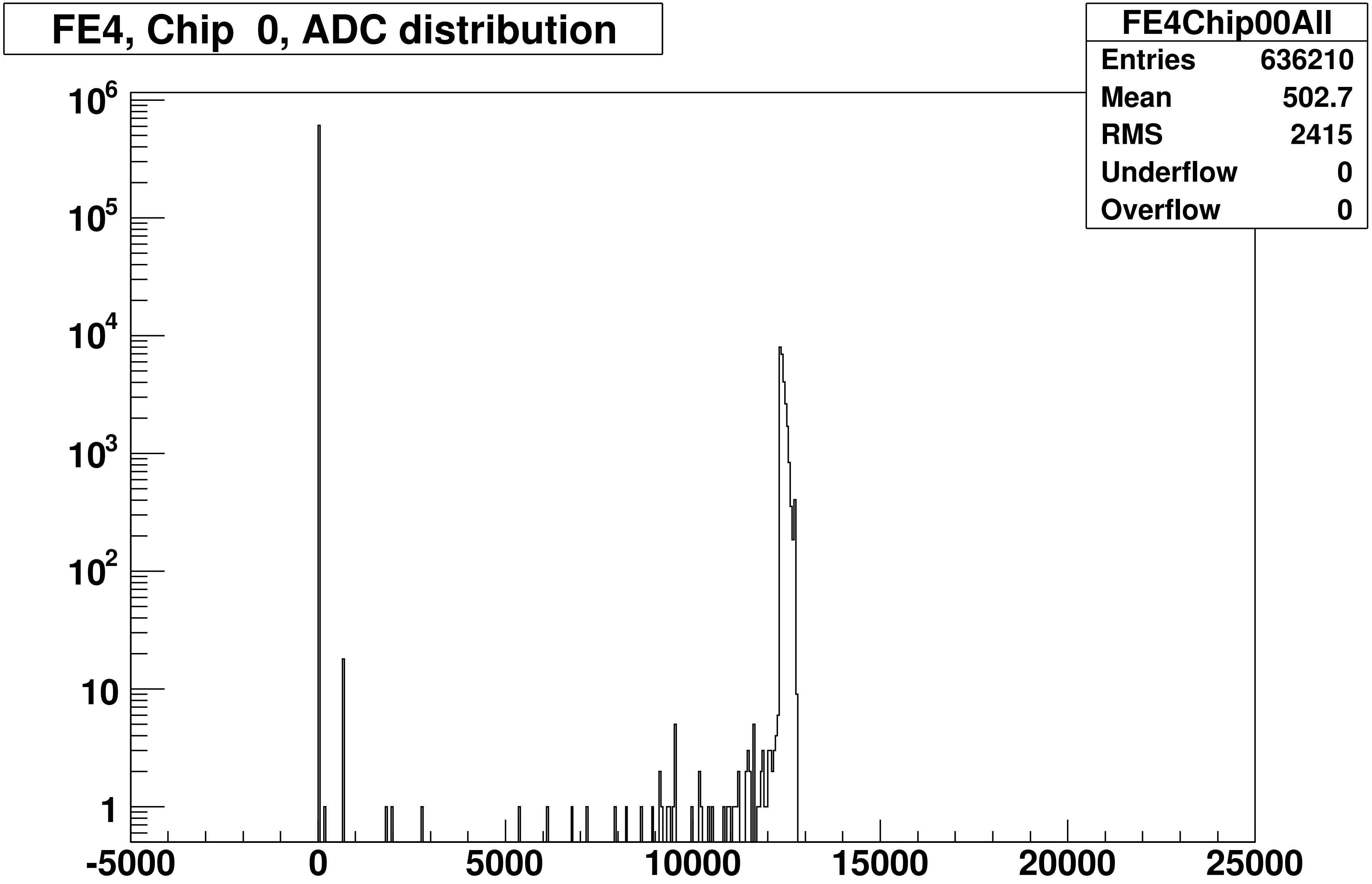


FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 636210
Mean 650.3
RMS 2722
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

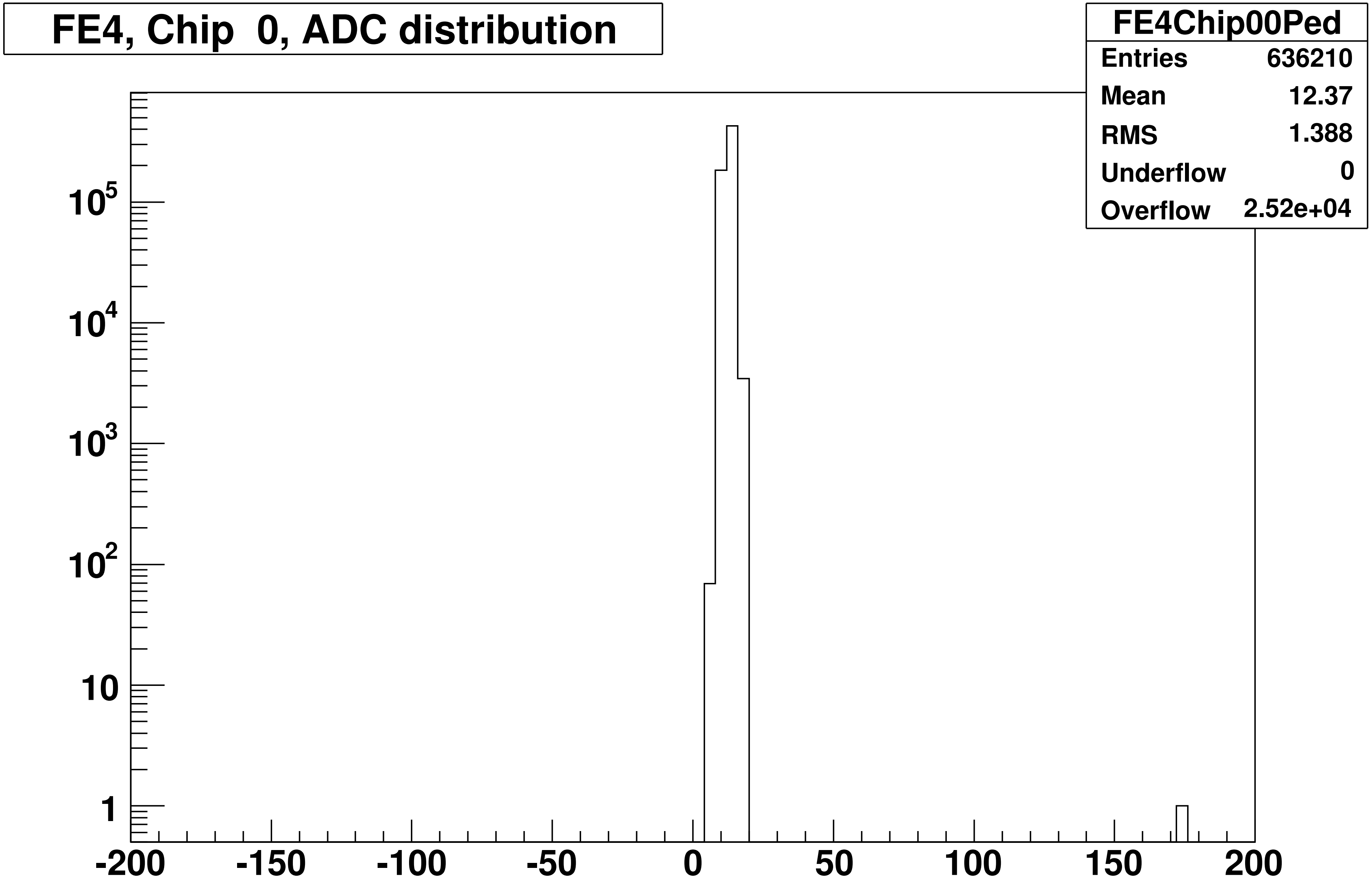

FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 636210
Mean 14.15
RMS 1.342
Underflow 0
Overflow 3.301e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

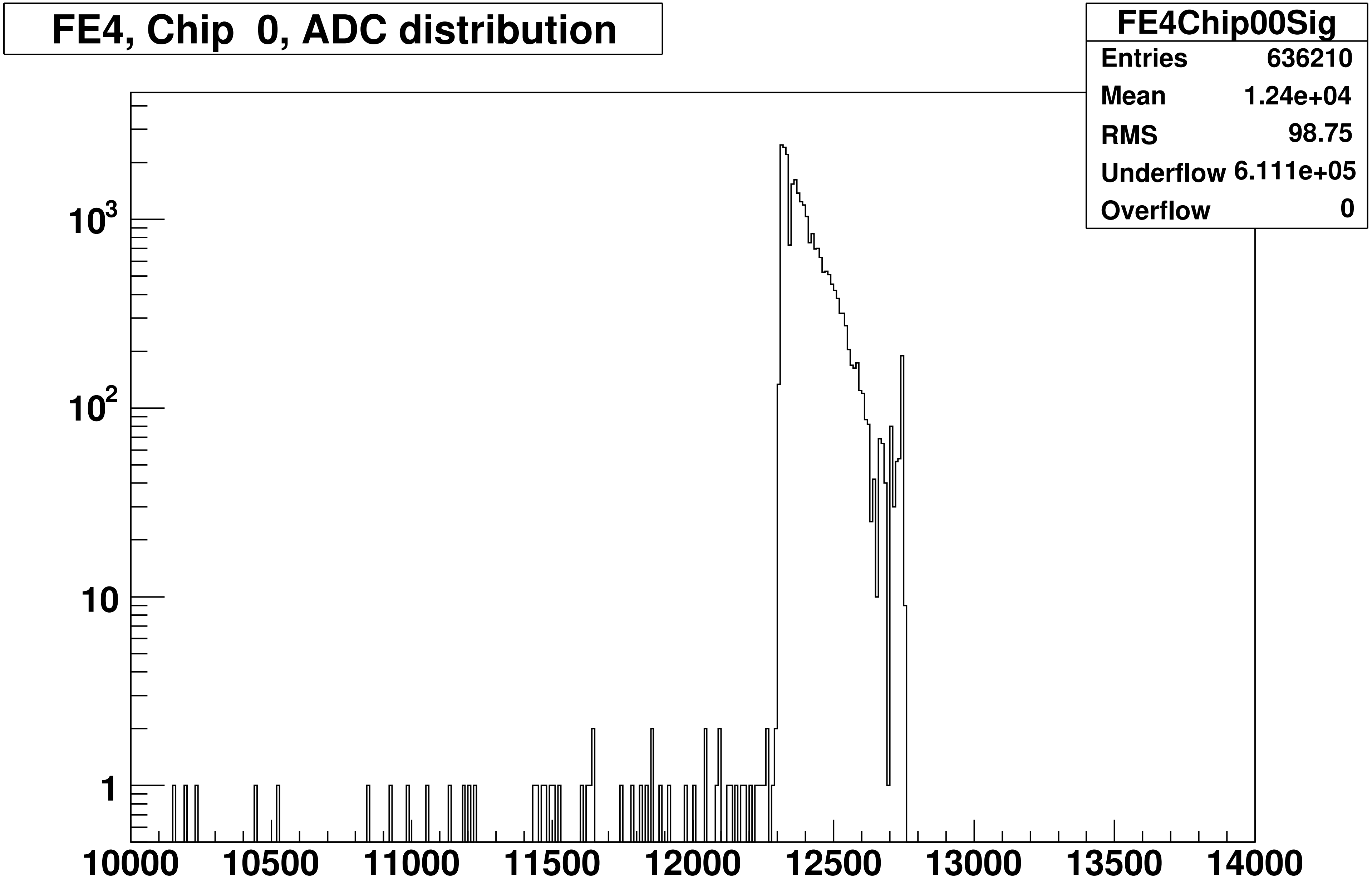


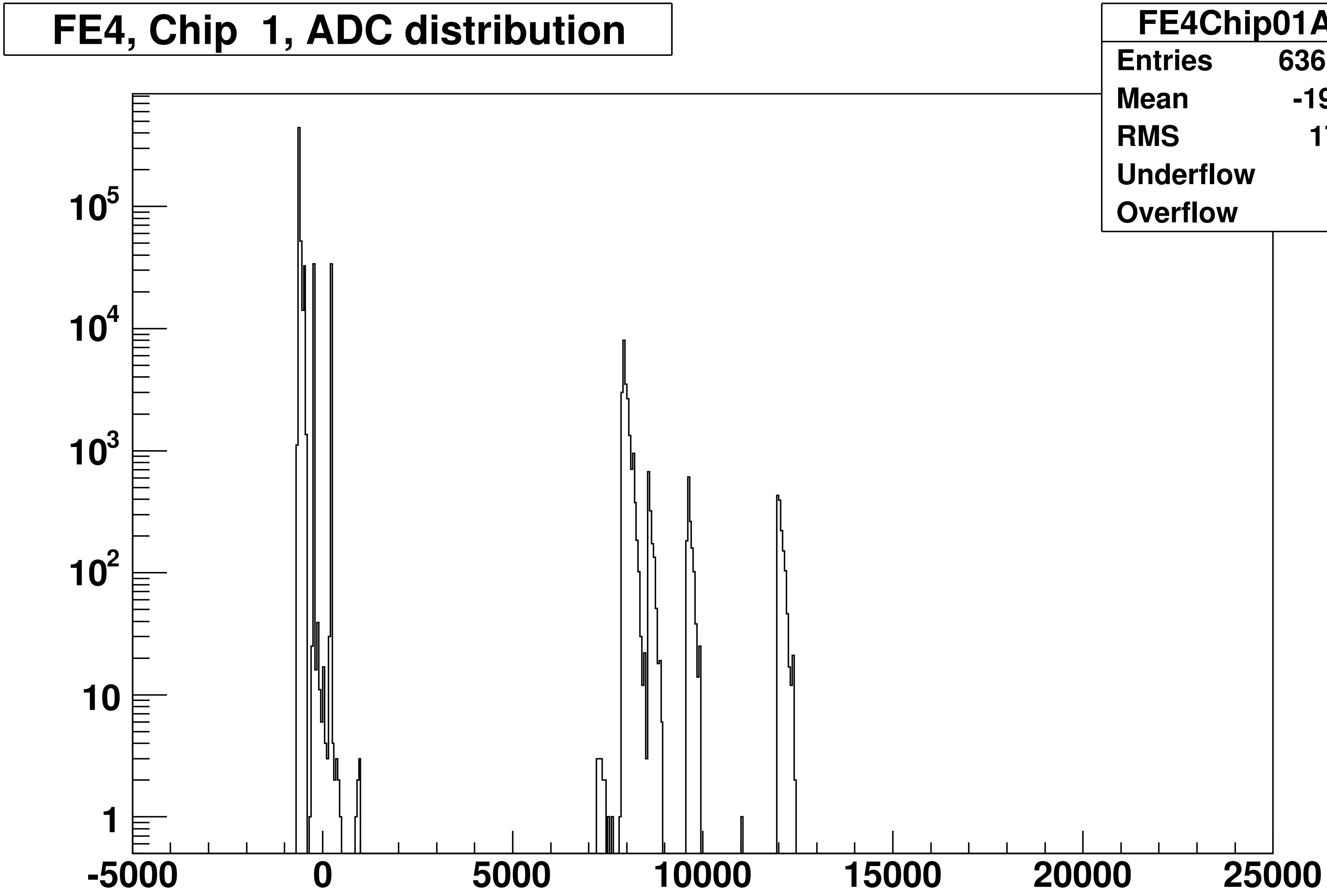
FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 636210
Mean 1.229e+04
RMS 83.58
Underflow 6.033e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000



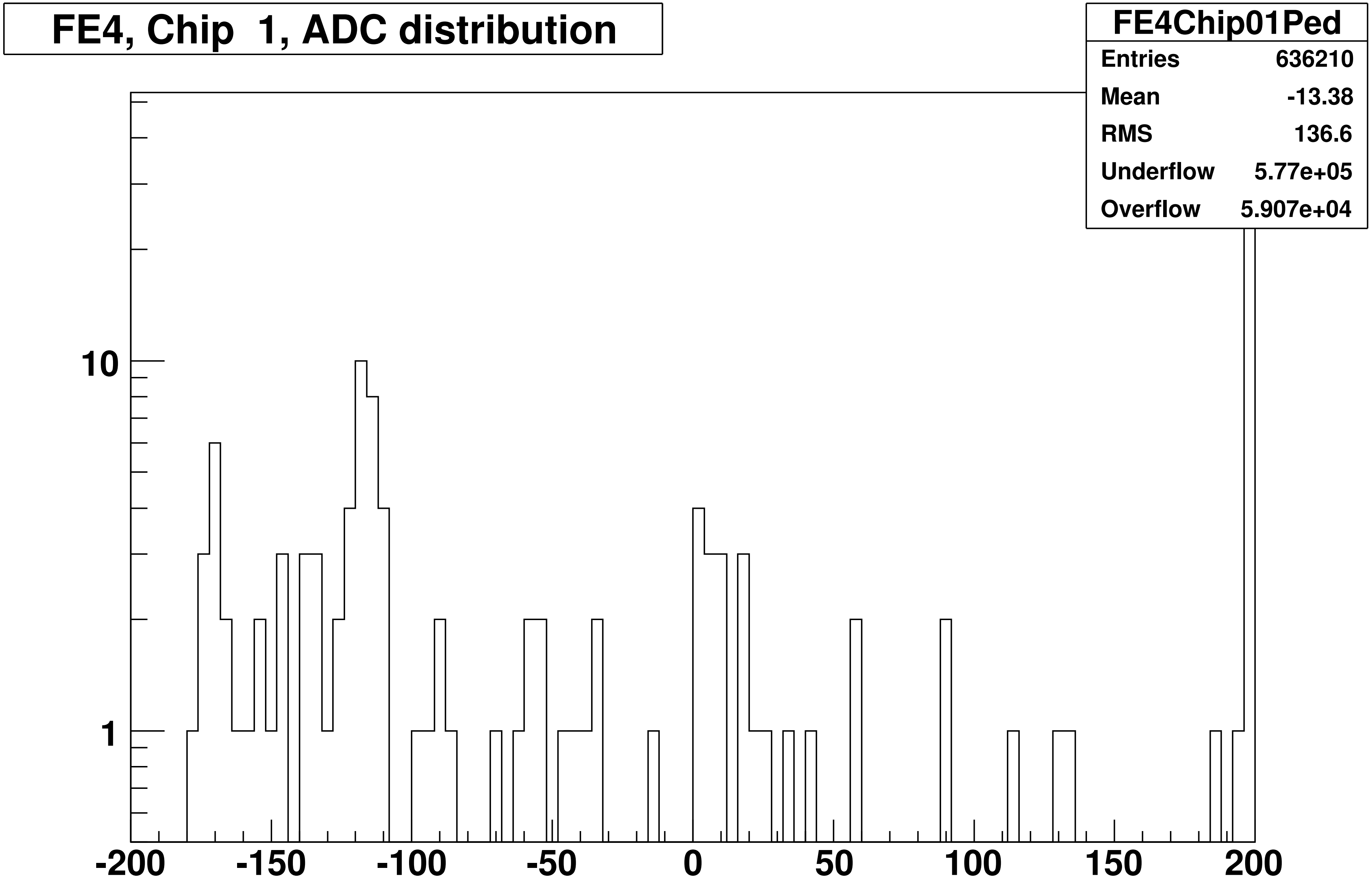
FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 636210
Mean 502.7
RMS 2415
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

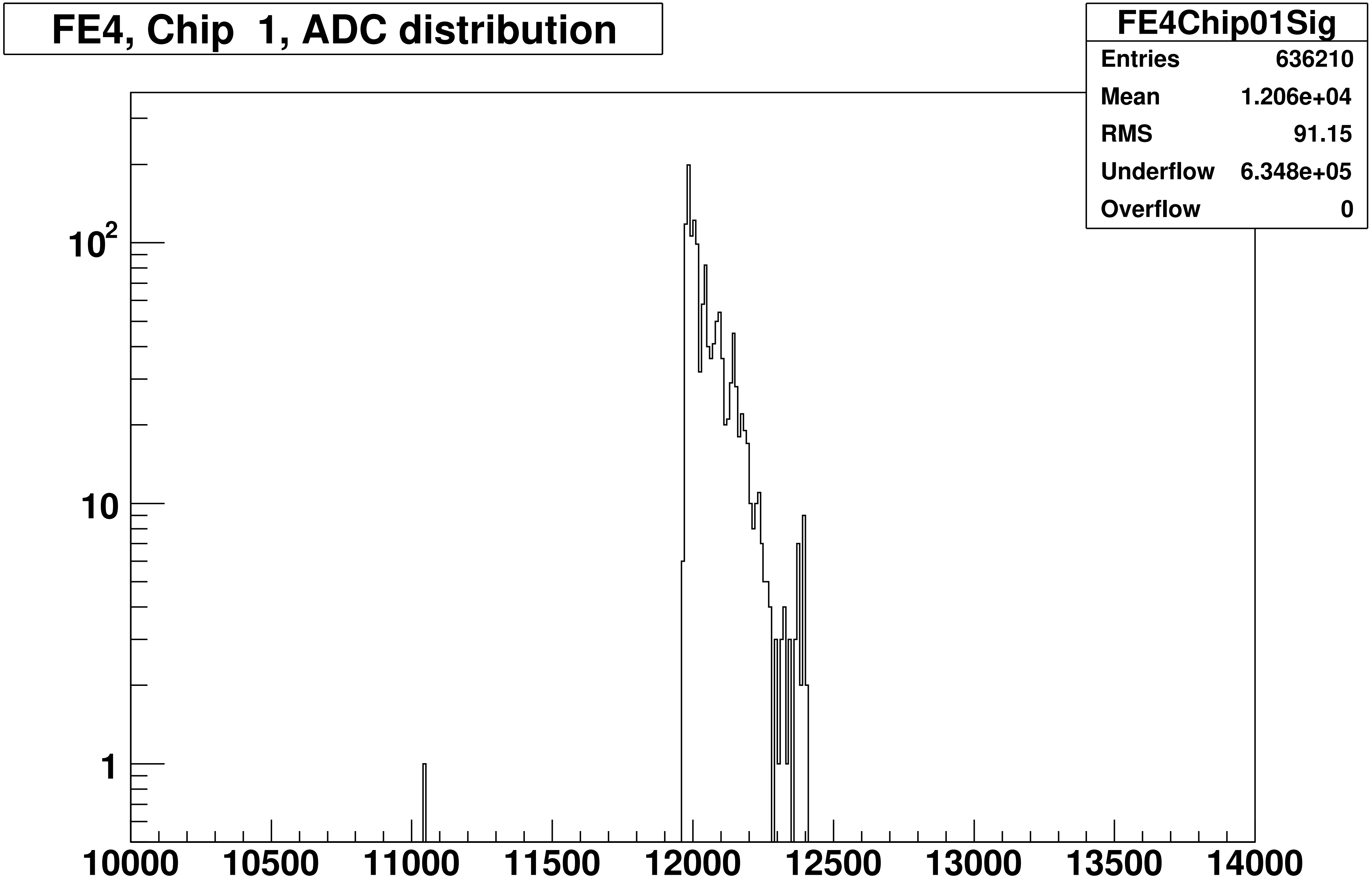

FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 636210
Mean 12.37
RMS 1.388
Underflow 0
Overflow 2.52e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

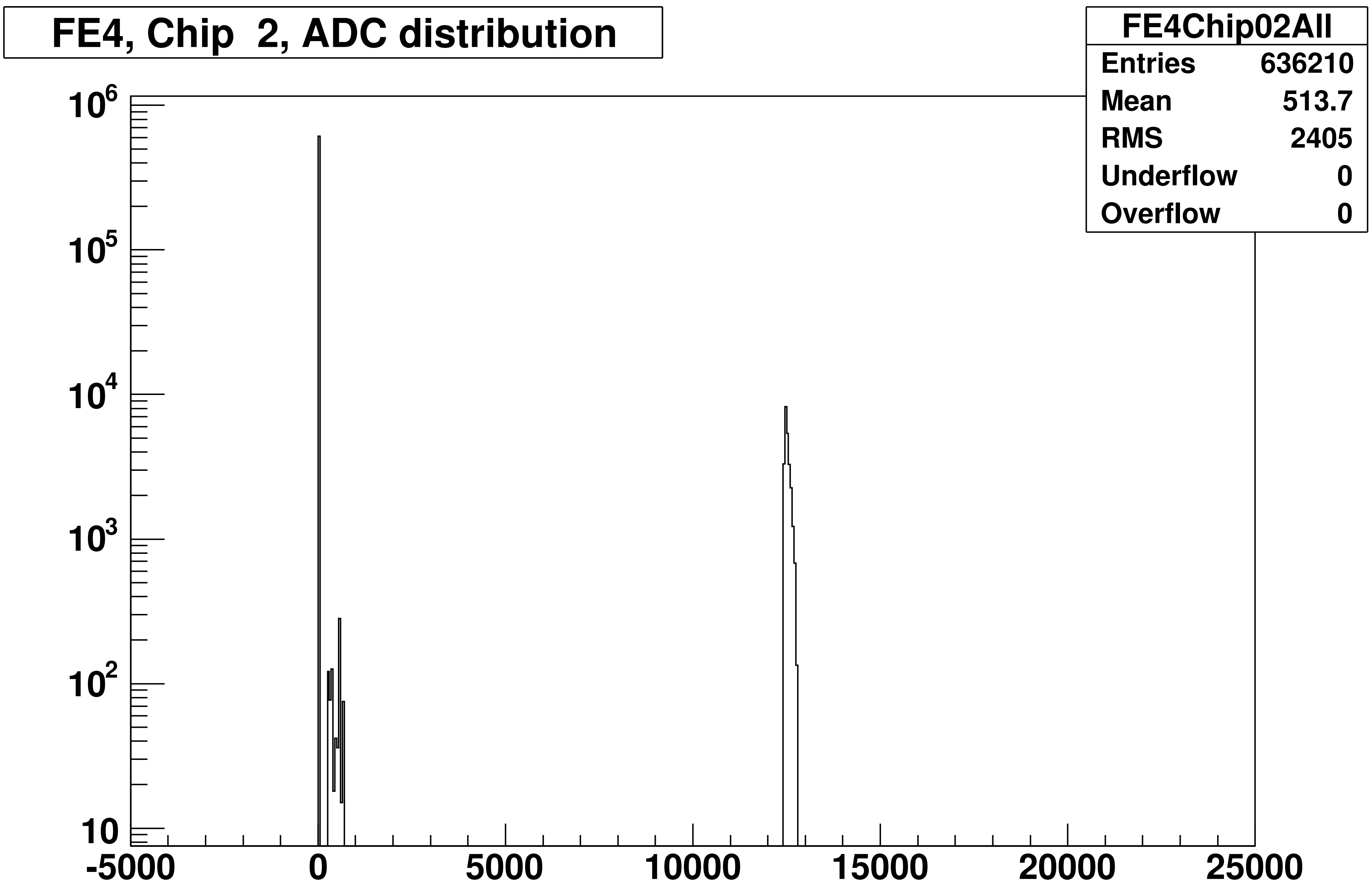

FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 636210
Mean 1.24e+04
RMS 98.75
Underflow 6.111e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000



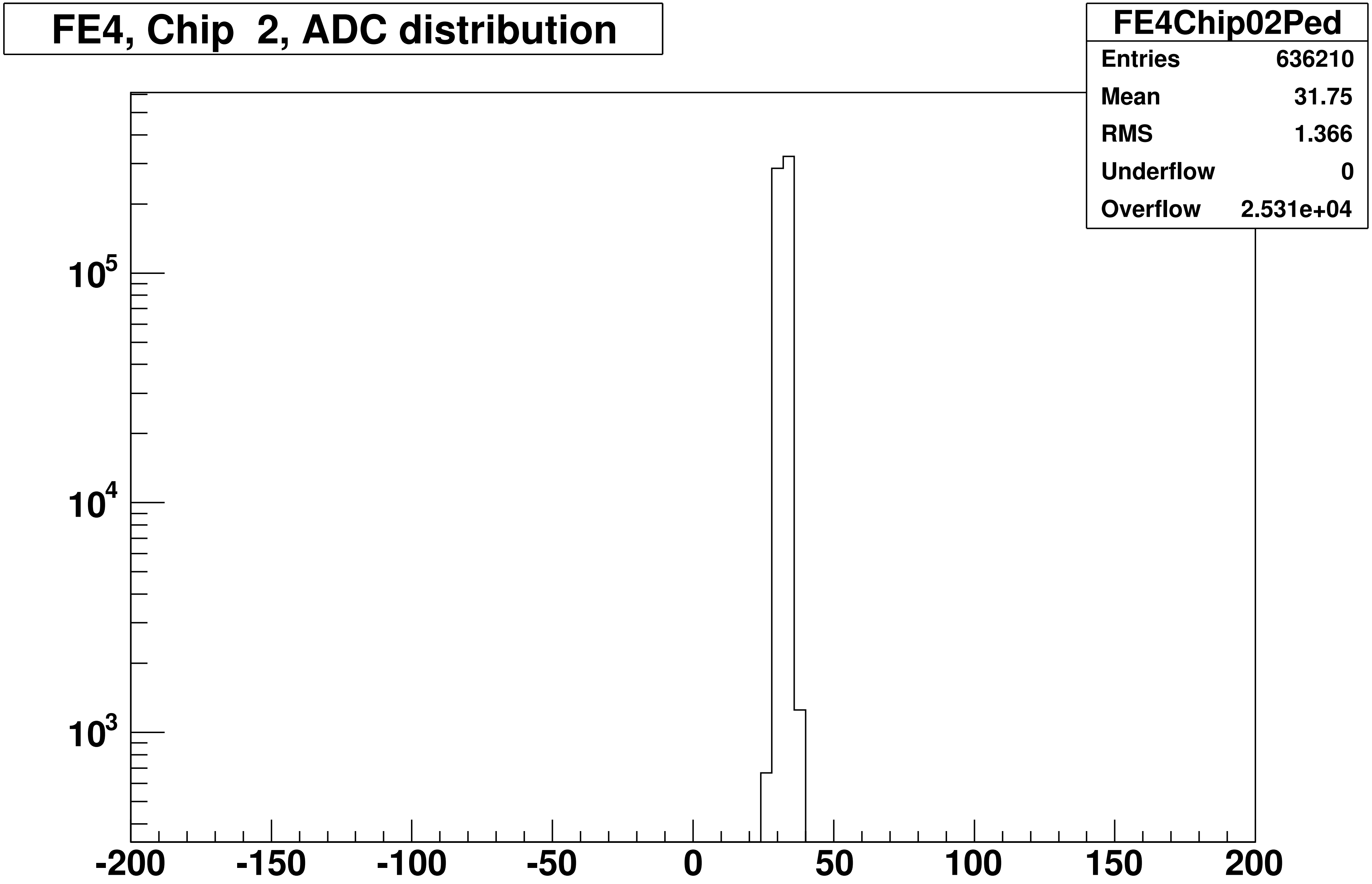
FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 636210
Mean -191.9
RMS 1753
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 636210
Mean -13.38
RMS 136.6
Underflow 5.77e+05
Overflow 5.907e+04
10
1
-200
-150
-100
-50
0
50
100
150
200

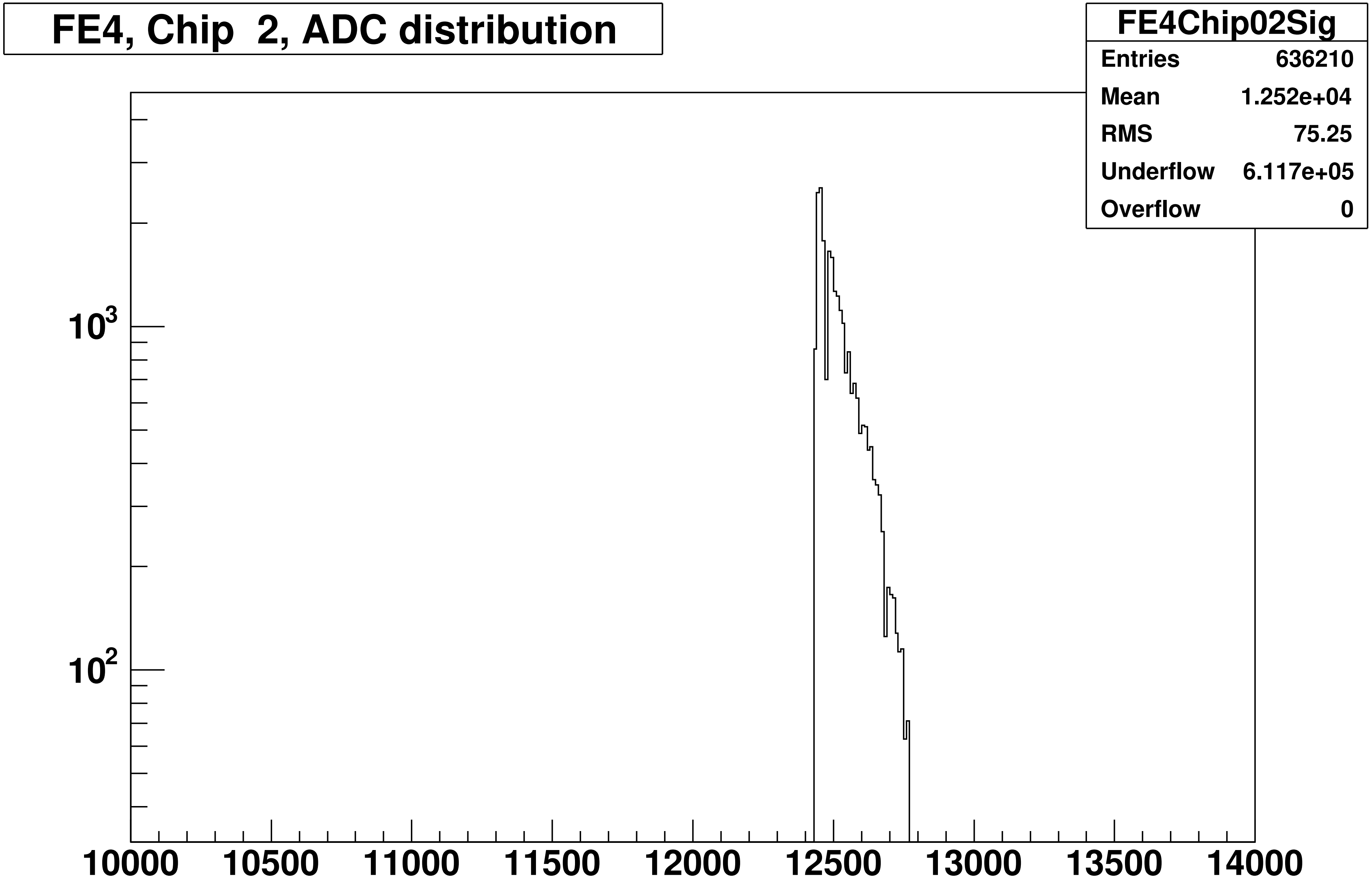

FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 636210
Mean 1.206e+04
RMS 91.15
Underflow 6.348e+05
Overflow 0
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000



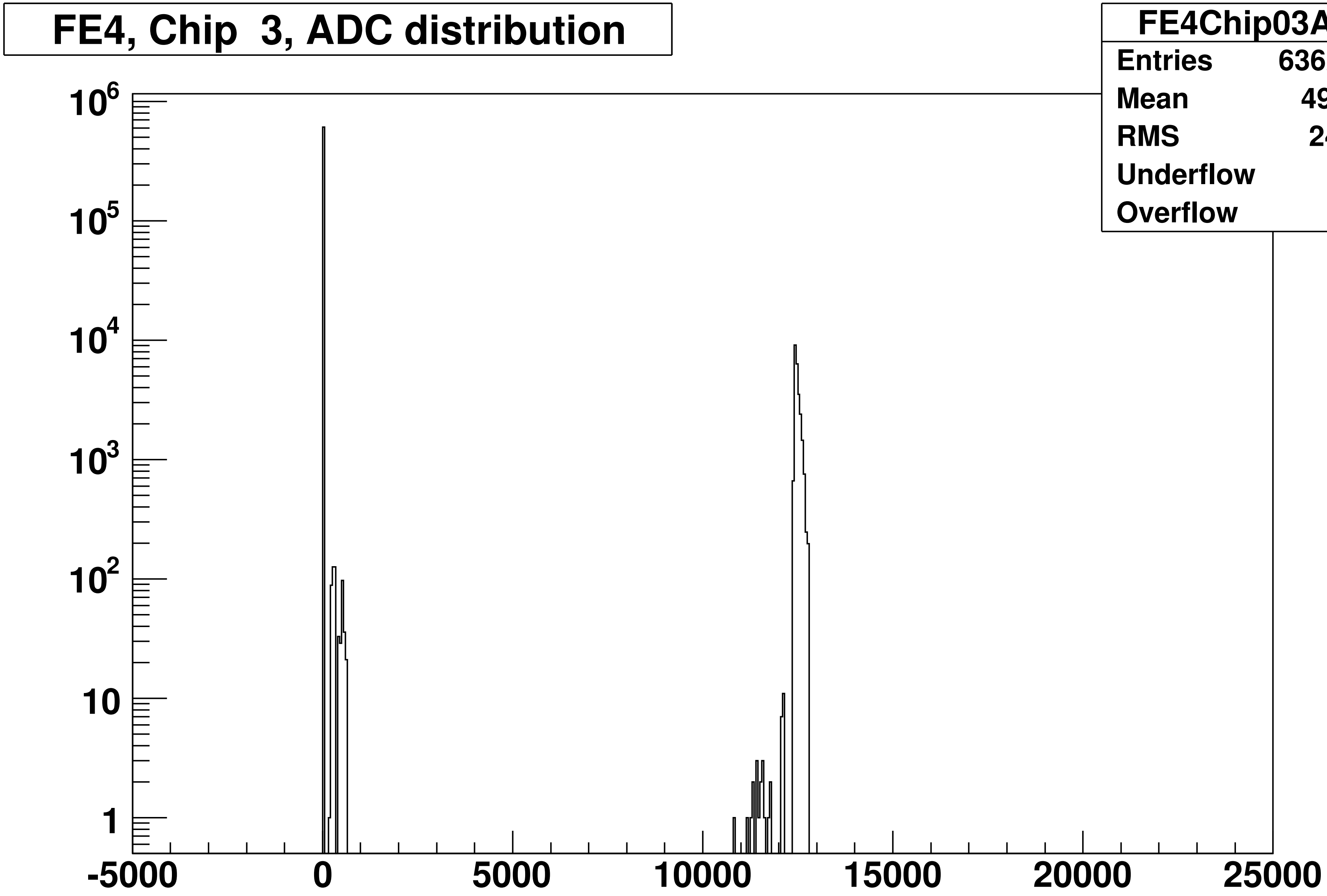
FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 636210
Mean 513.7
RMS 2405
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



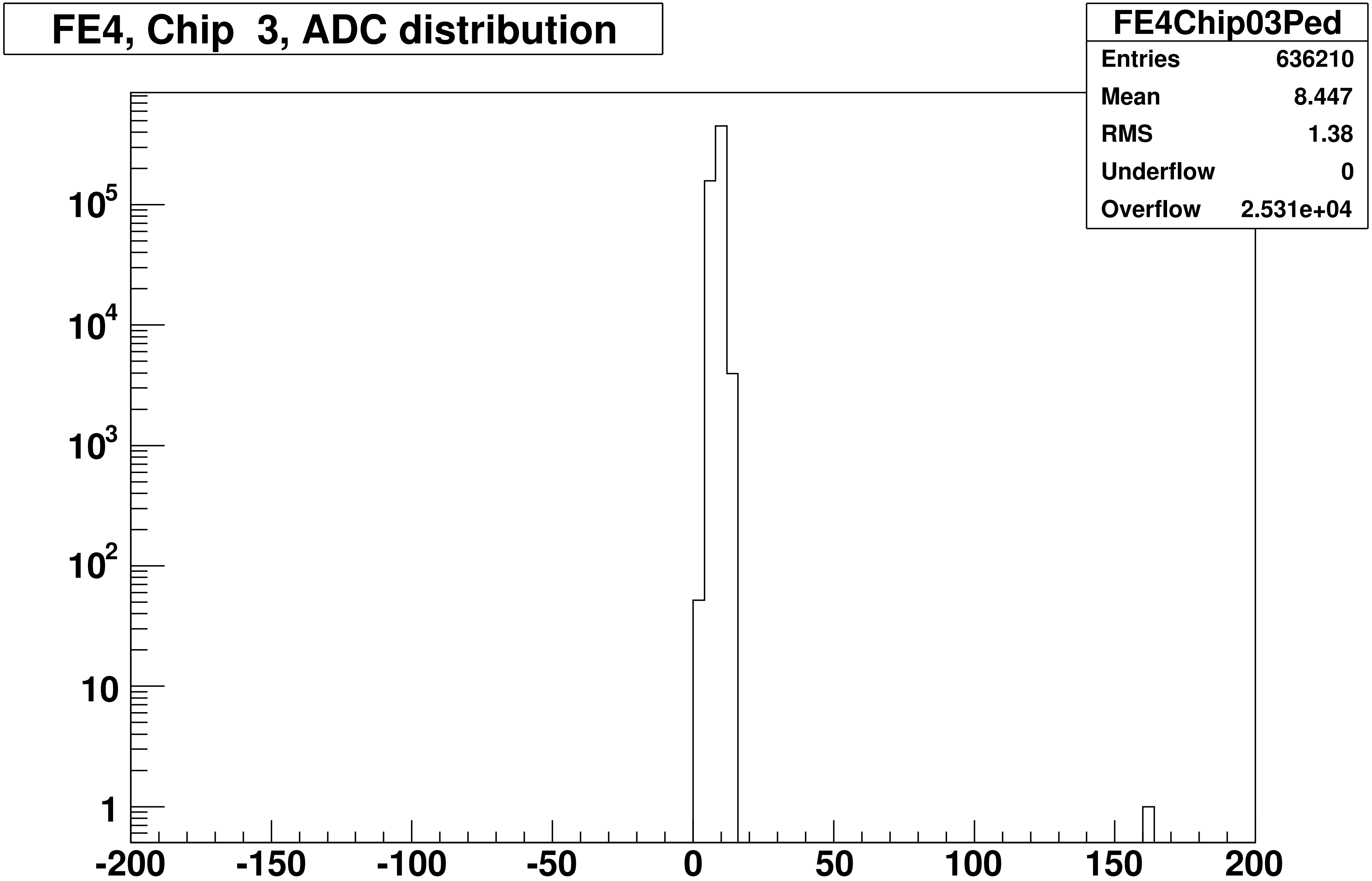
FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 636210
Mean 31.75
RMS 1.366
Underflow 0
Overflow 2.531e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

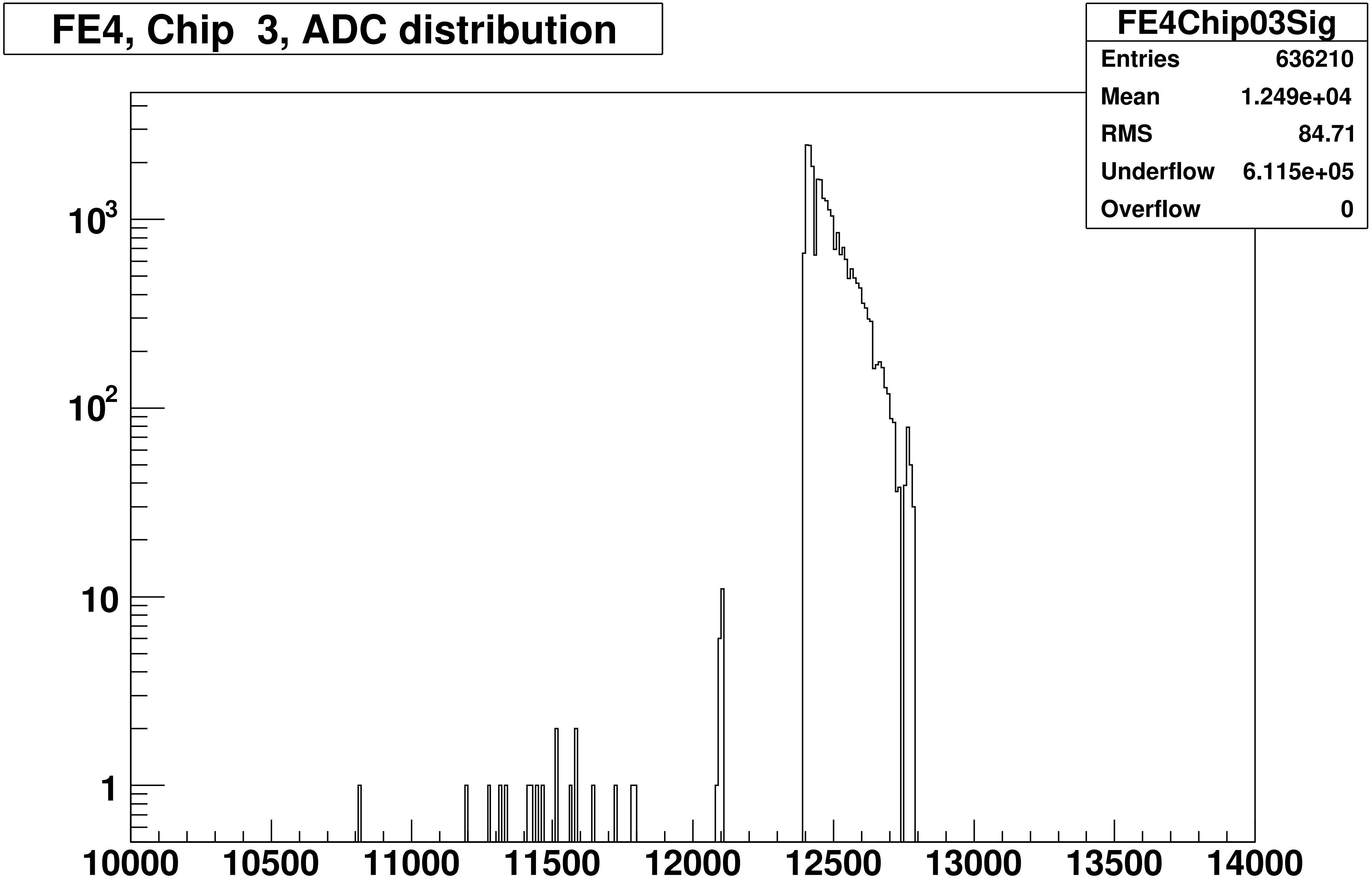


FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 636210
Mean 1.252e+04
RMS 75.25
Underflow 6.117e+05
Overflow 0
10^3
10^2
10000
10500
11000
11500
12000
12500
13000
13500
14000

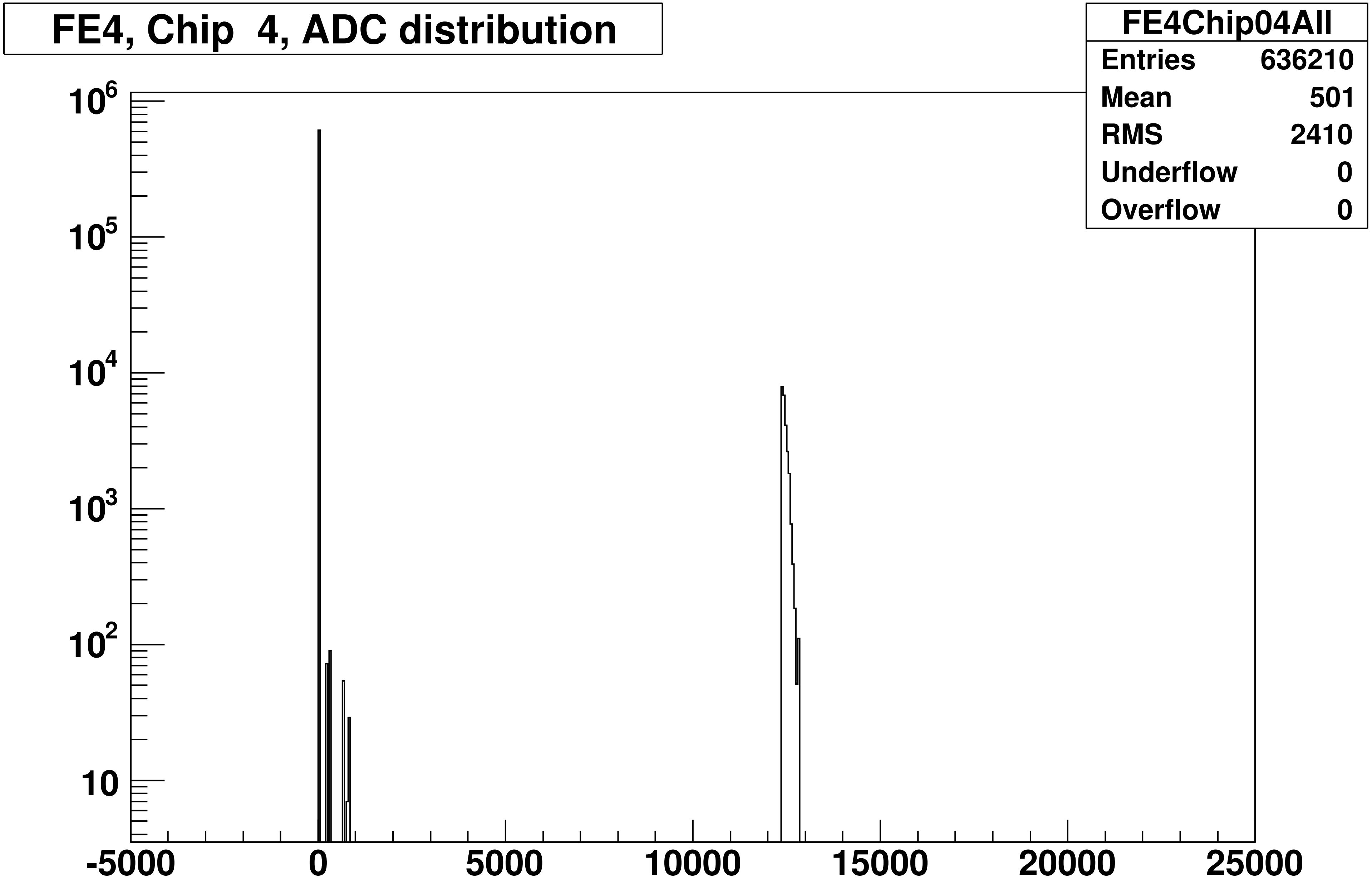


FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 636210
Mean 494.2
RMS 2413
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

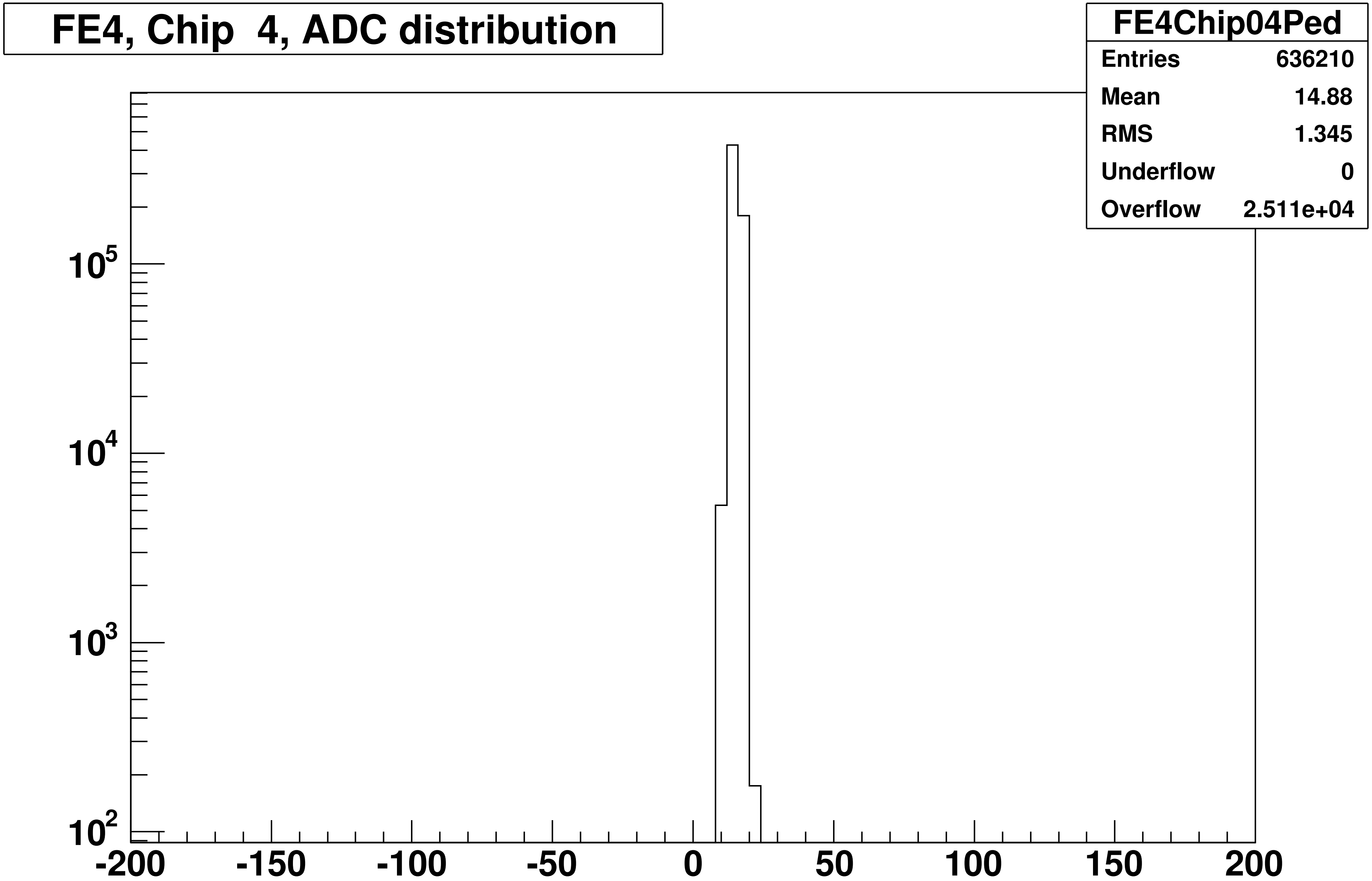

FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 636210
Mean 8.447
RMS 1.38
Underflow 0
Overflow 2.531e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

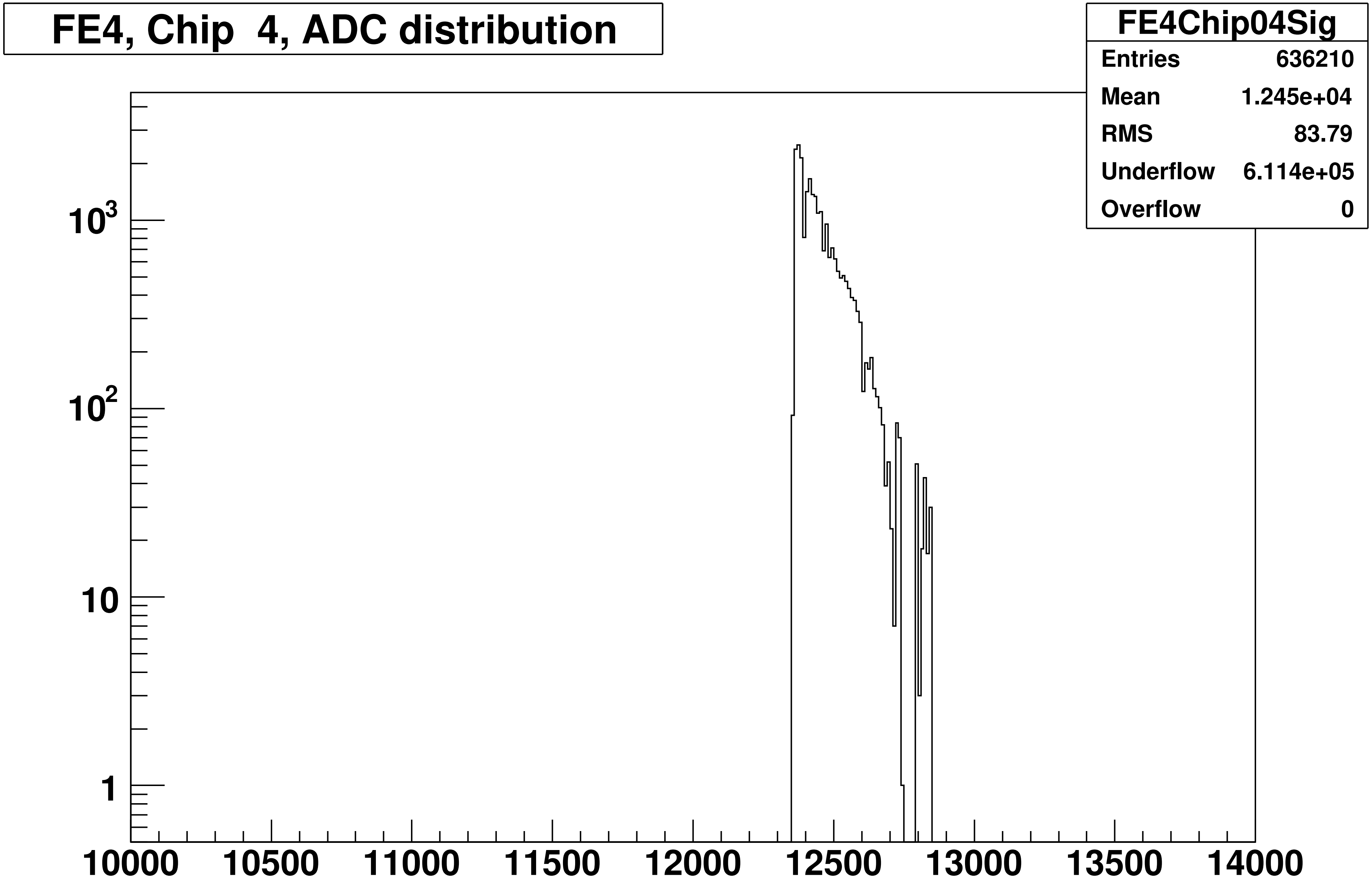


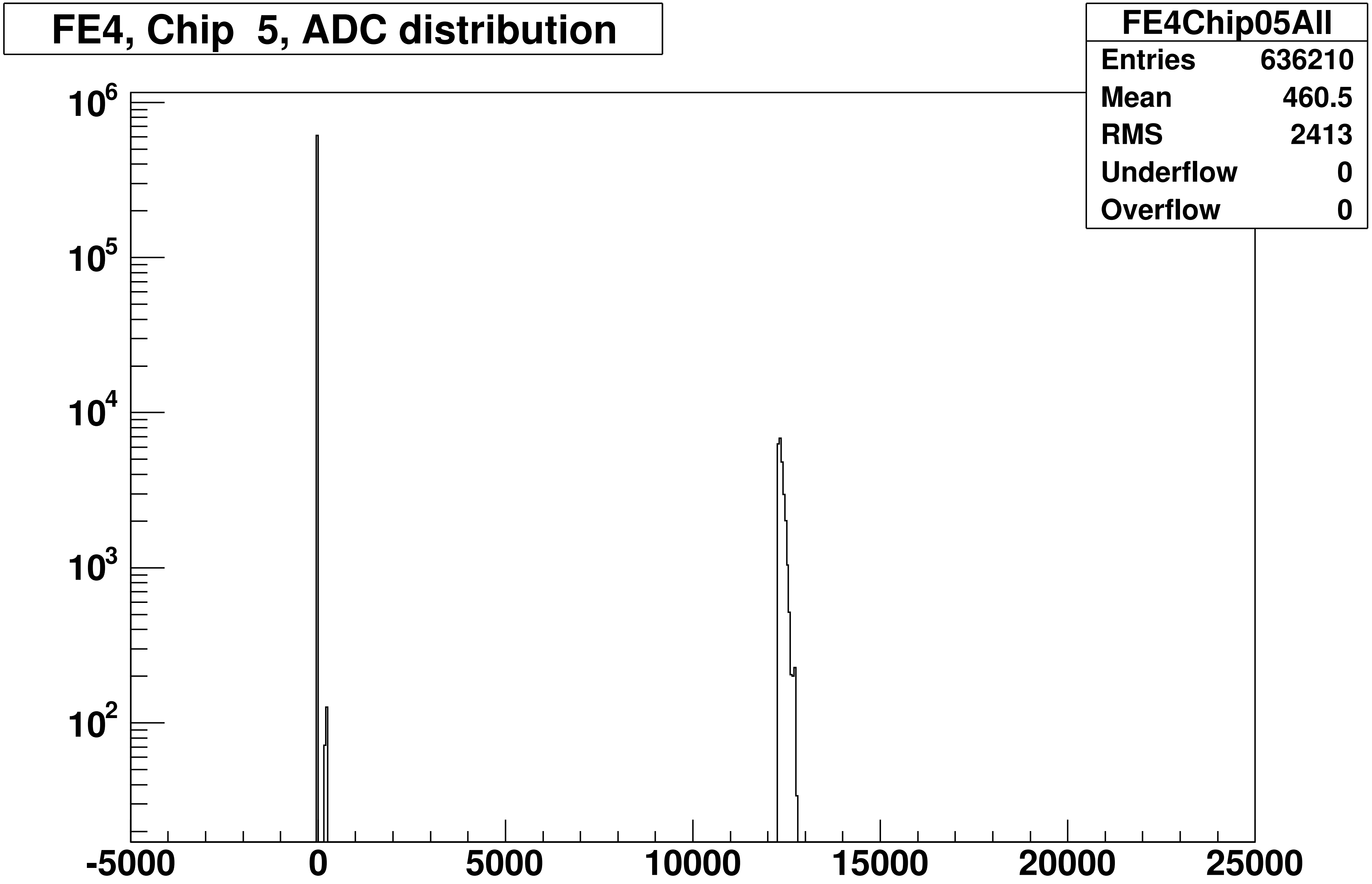
FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 636210
Mean 1.249e+04
RMS 84.71
Underflow 6.115e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

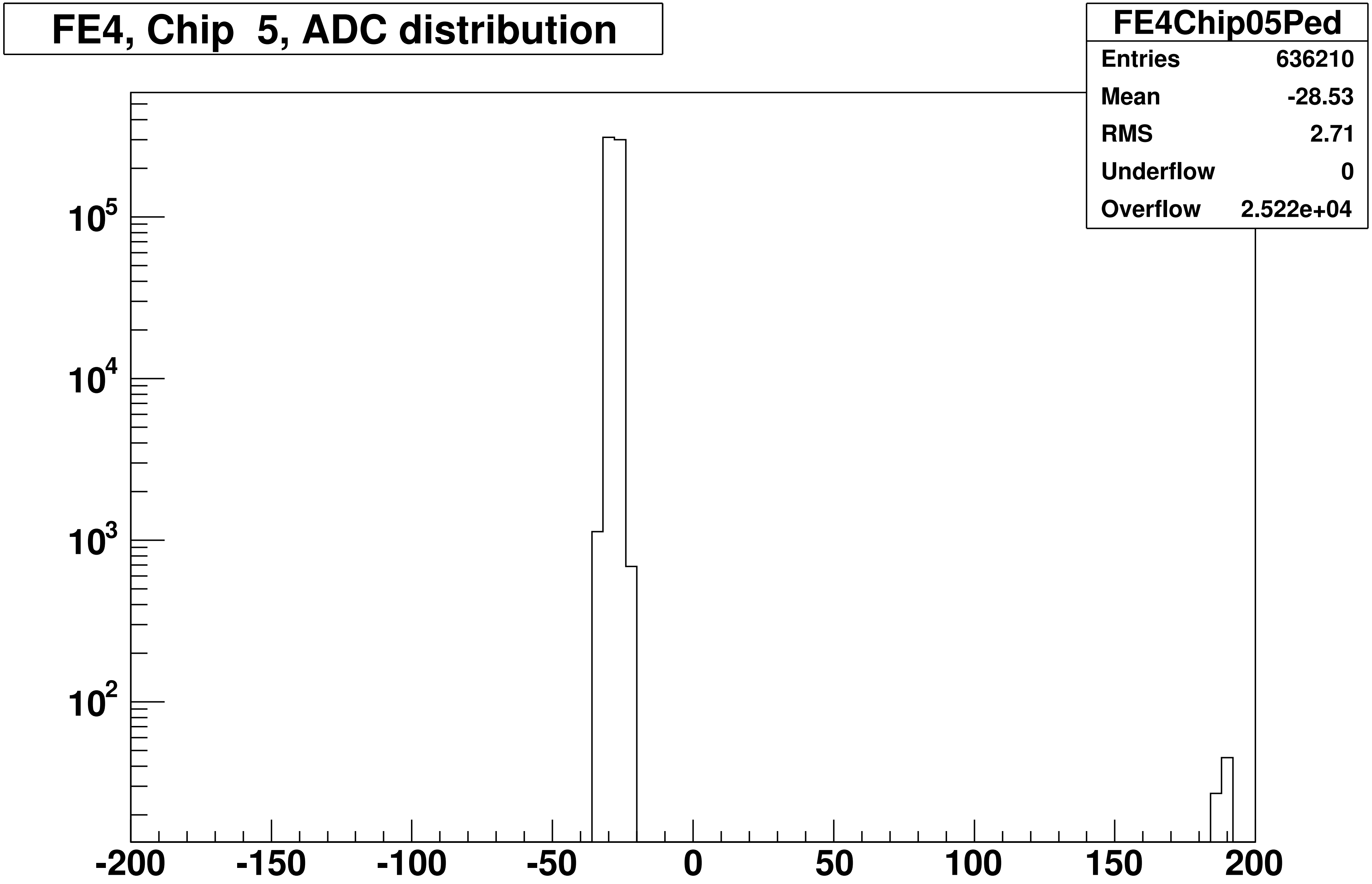


FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 636210
Mean 501
RMS 2410
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

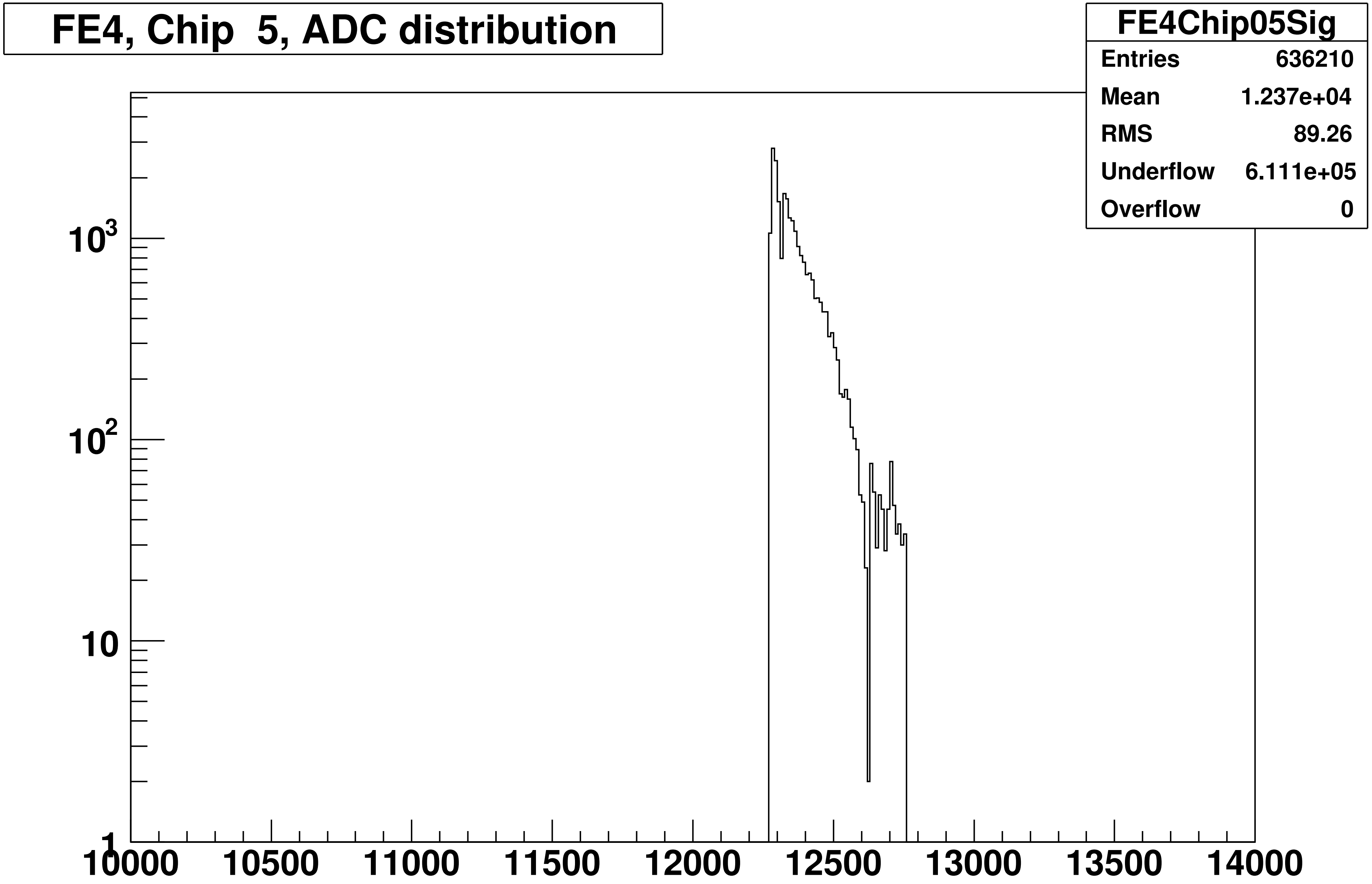

FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 636210
Mean 14.88
RMS 1.345
Underflow 0
Overflow 2.511e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 636210
Mean 1.245e+04
RMS 83.79
Underflow 6.114e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

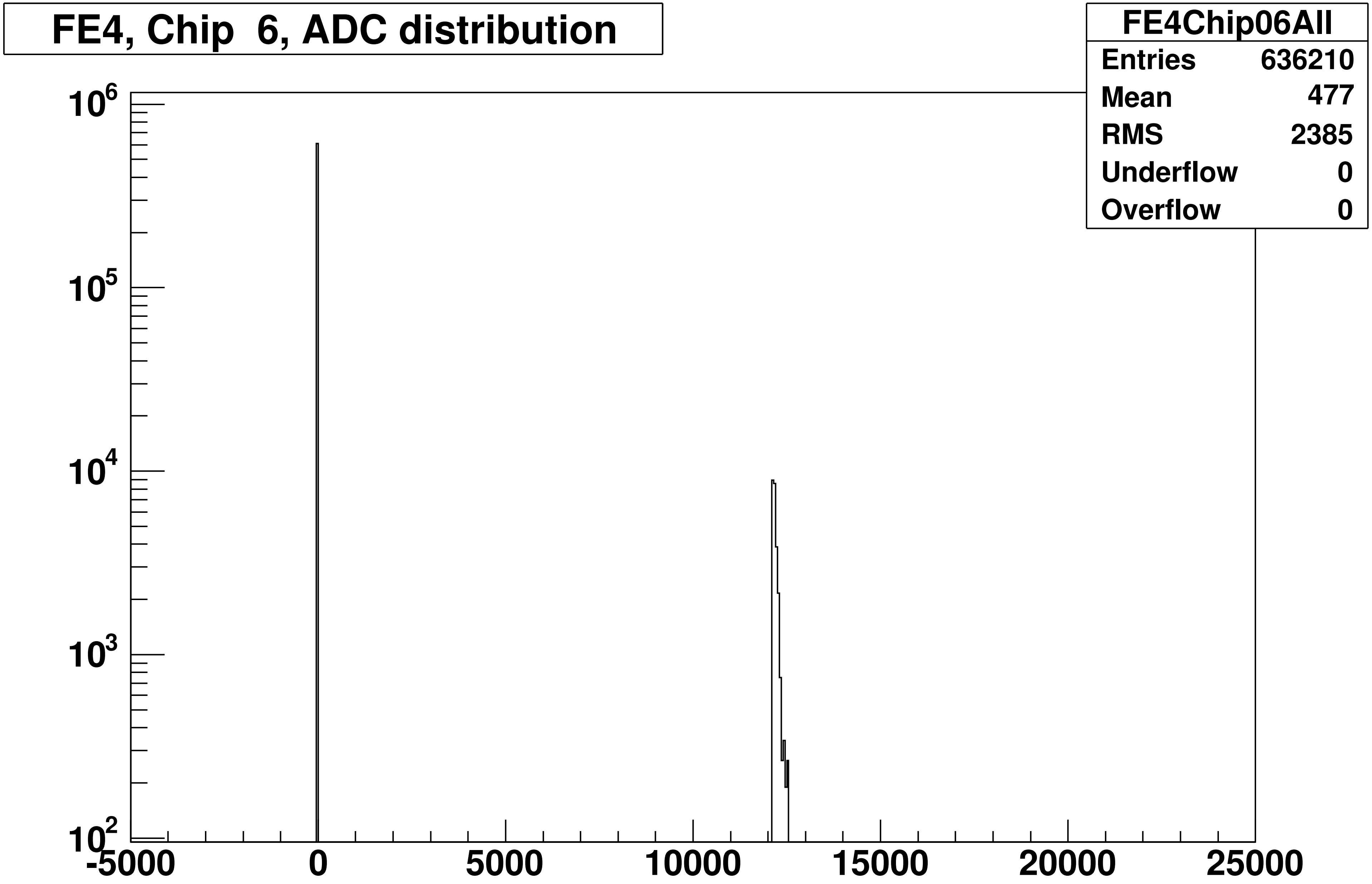

FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 636210
Mean 460.5
RMS 2413
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



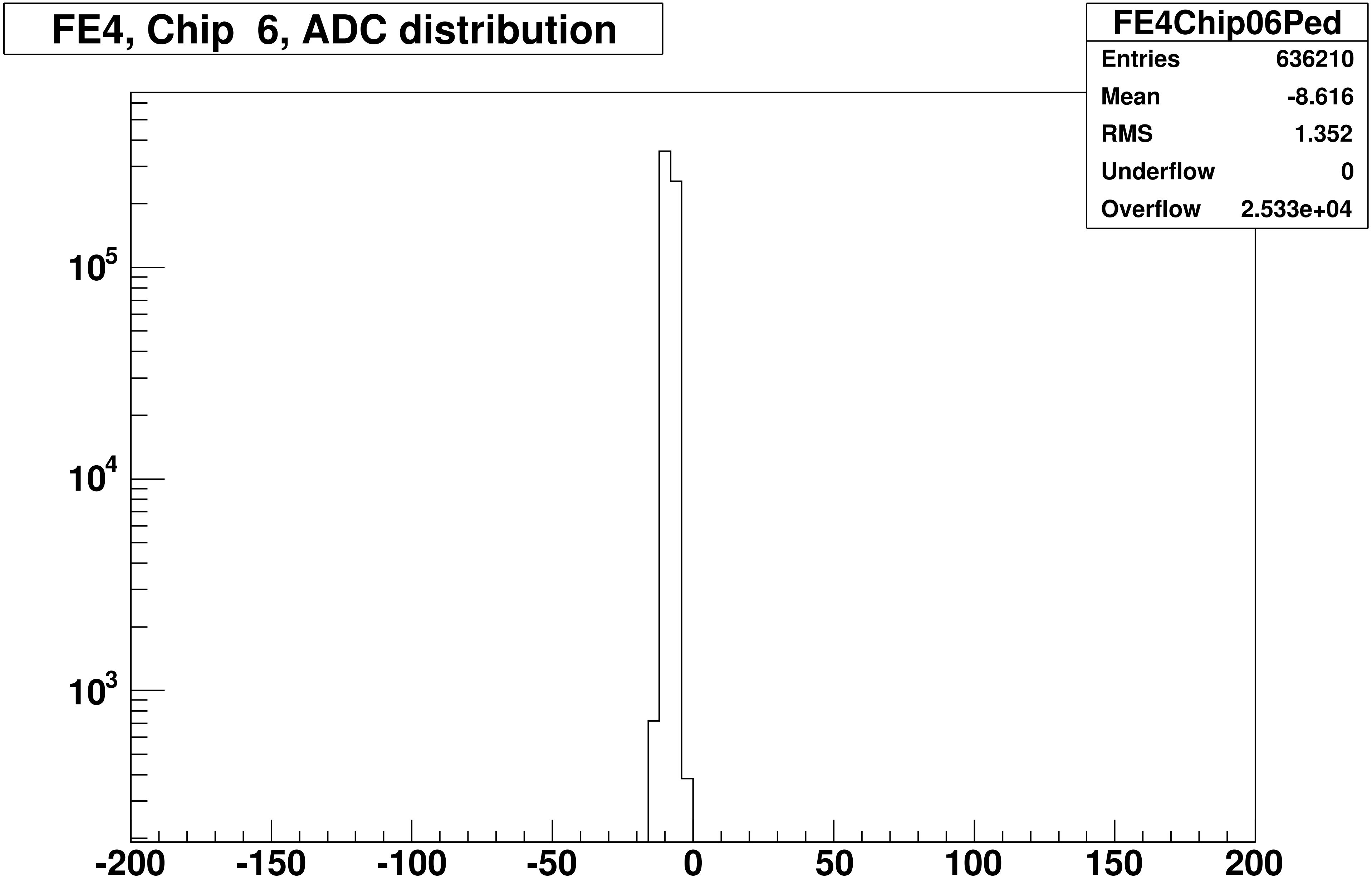
FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 636210
Mean -28.53
RMS 2.71
Underflow 0
Overflow 2.522e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



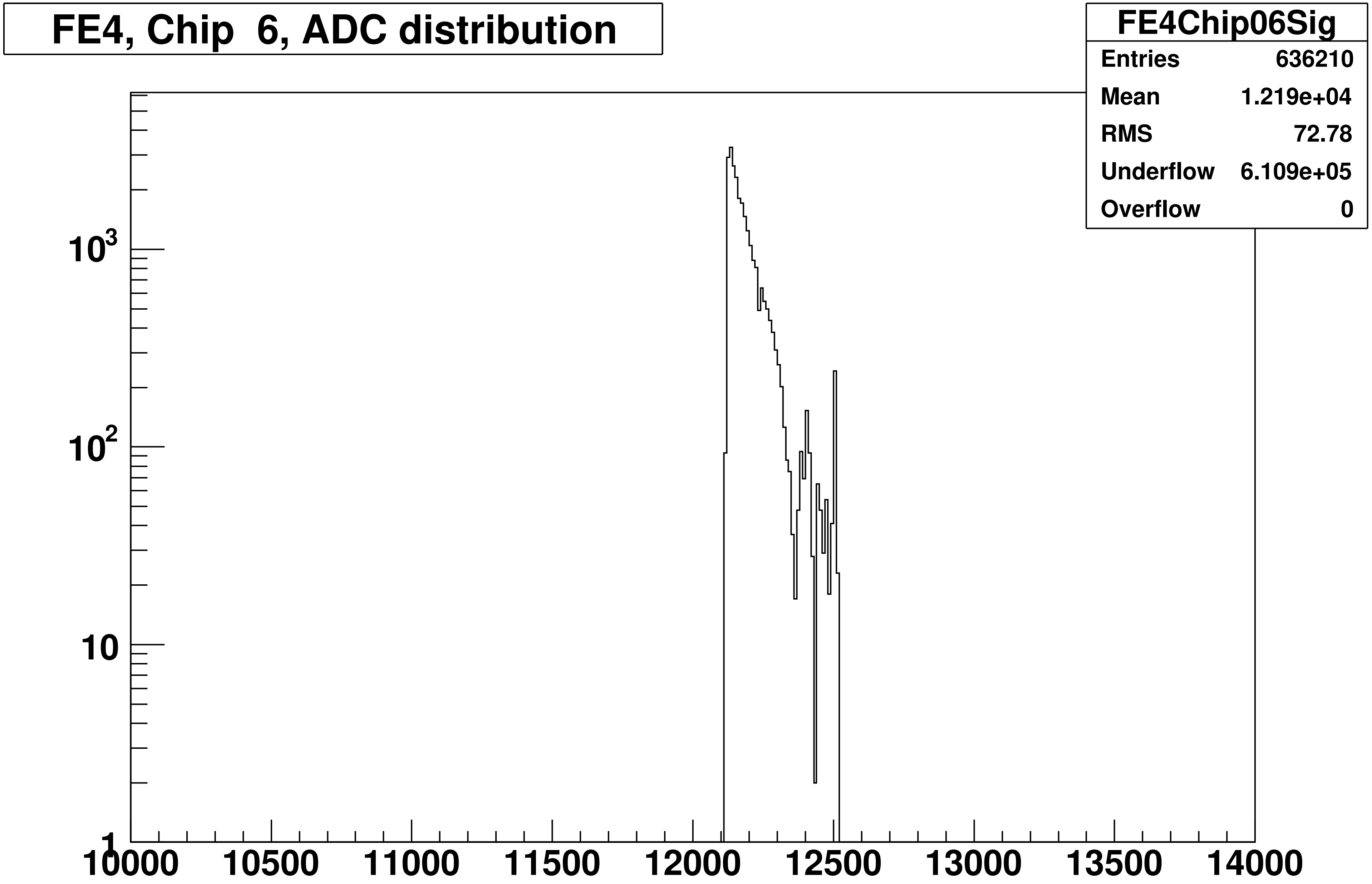
FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 636210
Mean 1.237e+04
RMS 89.26
Underflow 6.111e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

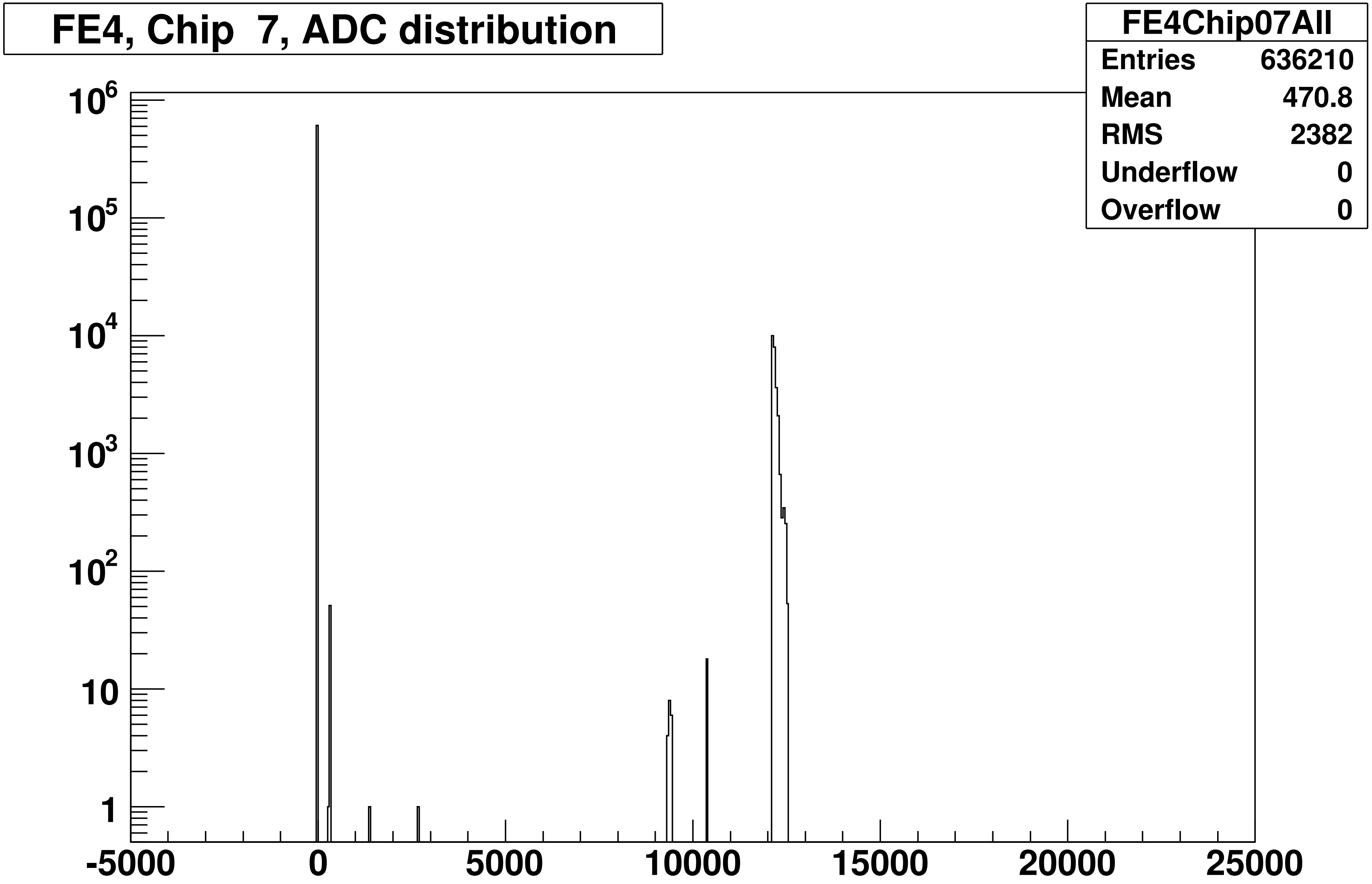


FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 636210
Mean 477
RMS 2385
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

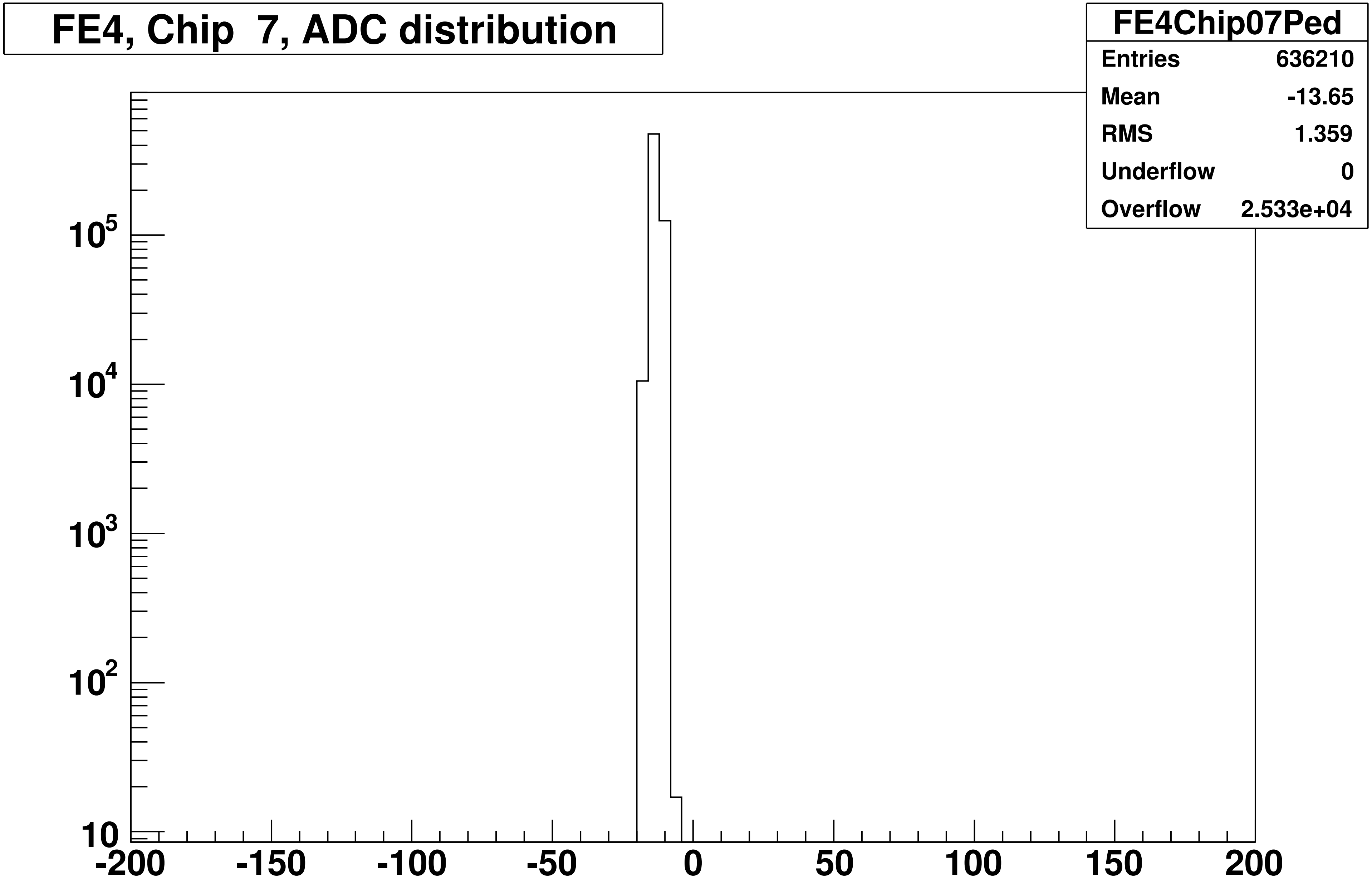


FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 636210
Mean -8.616
RMS 1.352
Underflow 0
Overflow 2.533e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

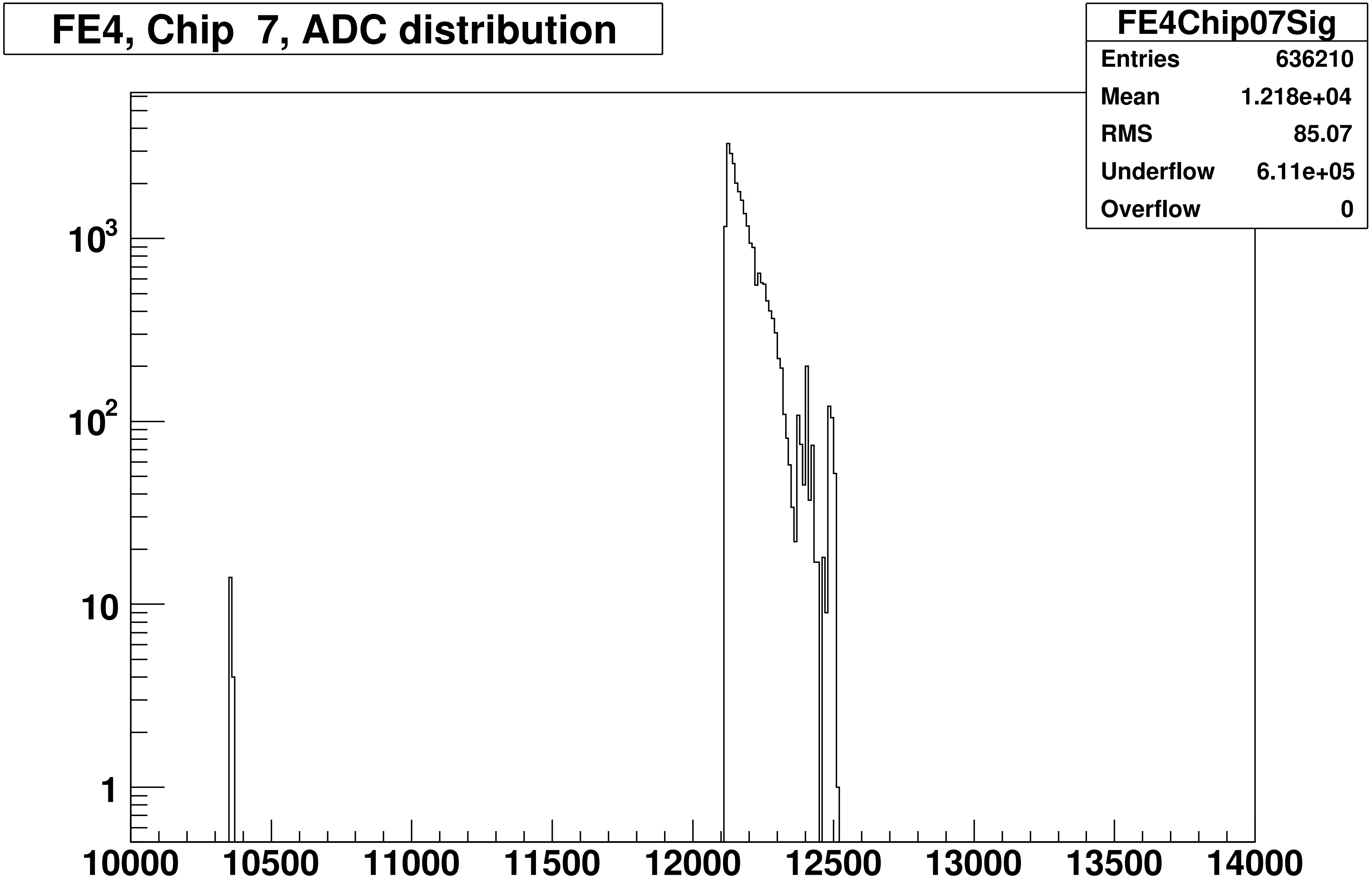

FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 636210
Mean 1.219e+04
RMS 72.78
Underflow 6.109e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

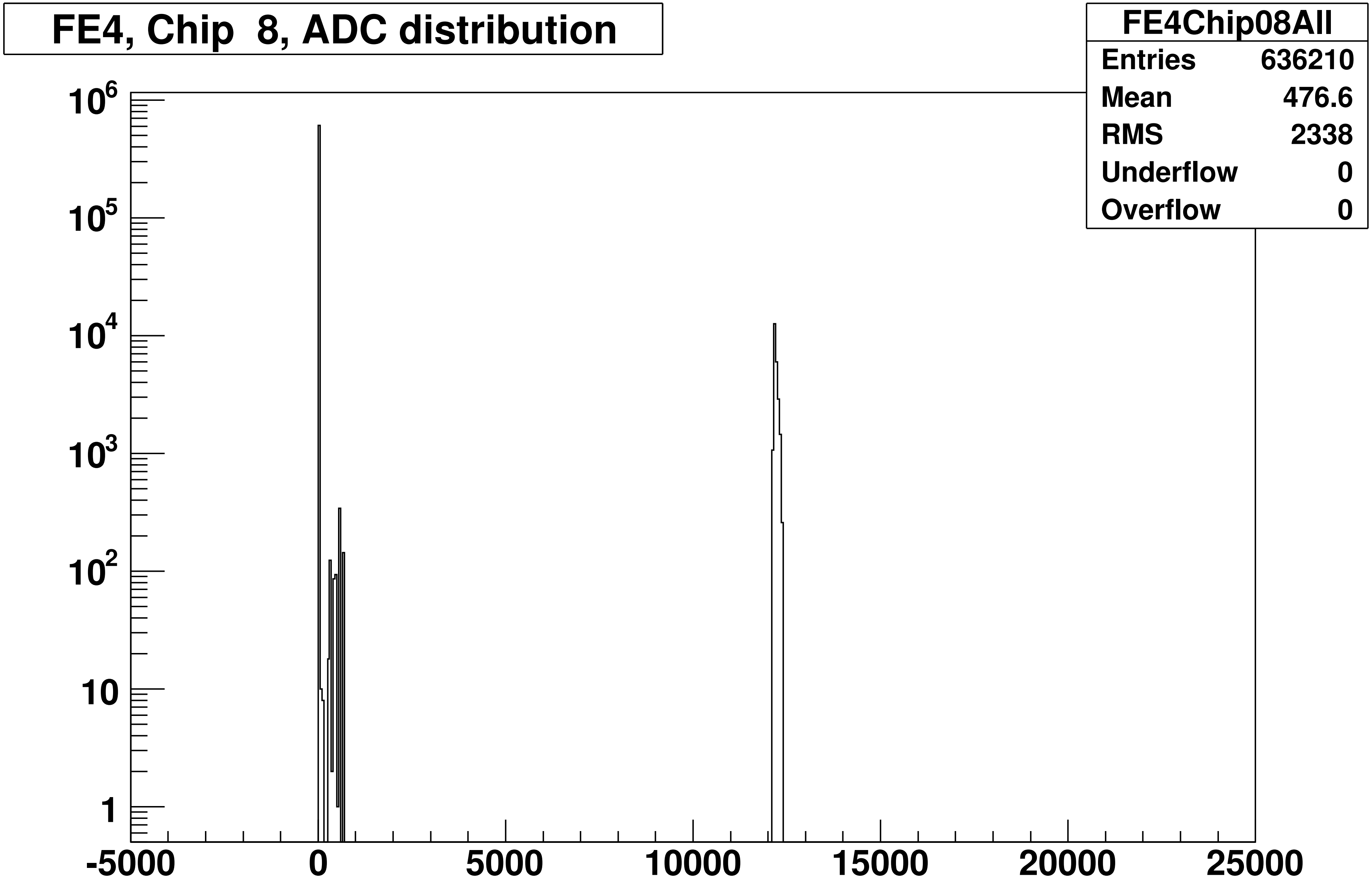


FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 636210
Mean 470.8
RMS 2382
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

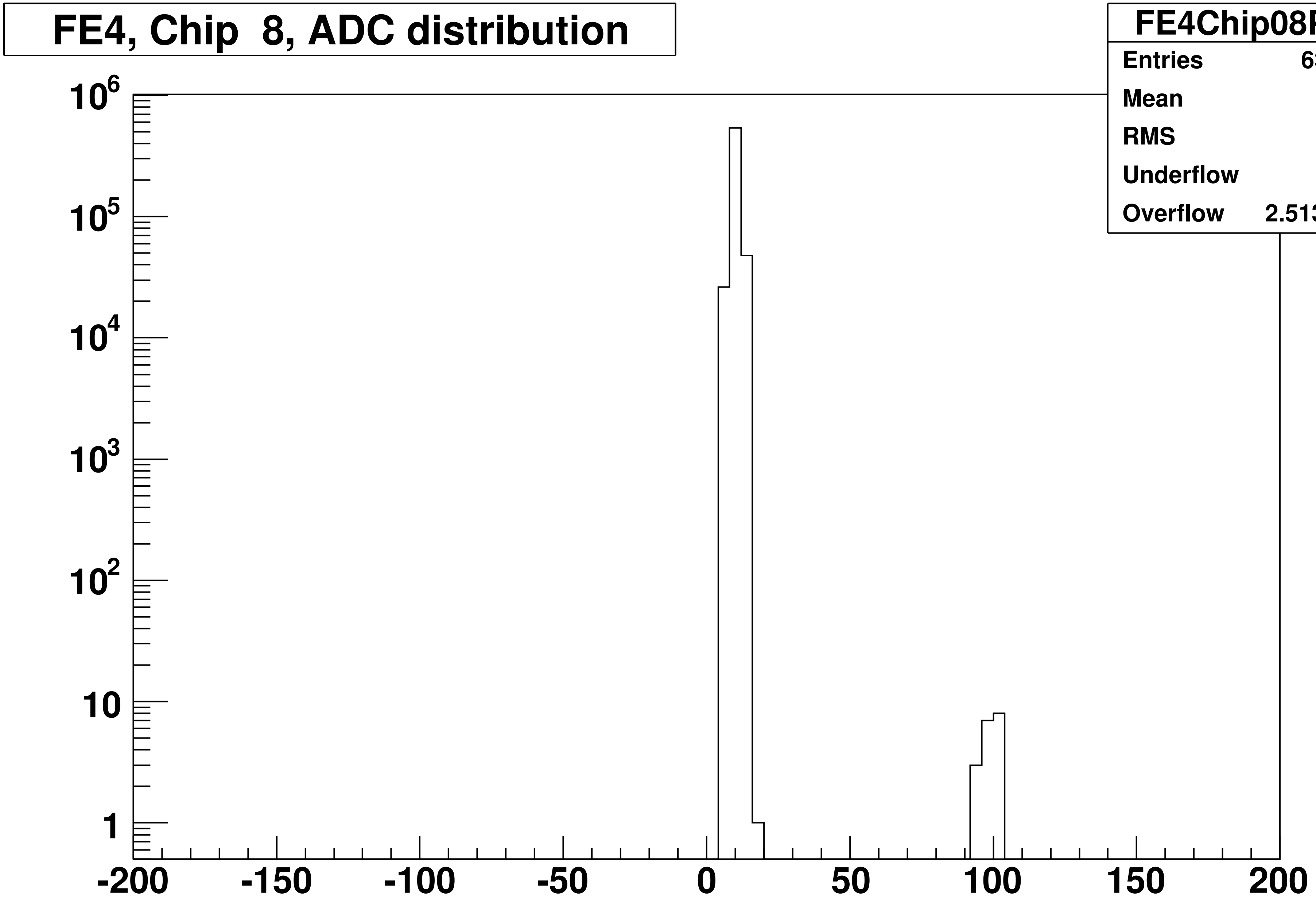


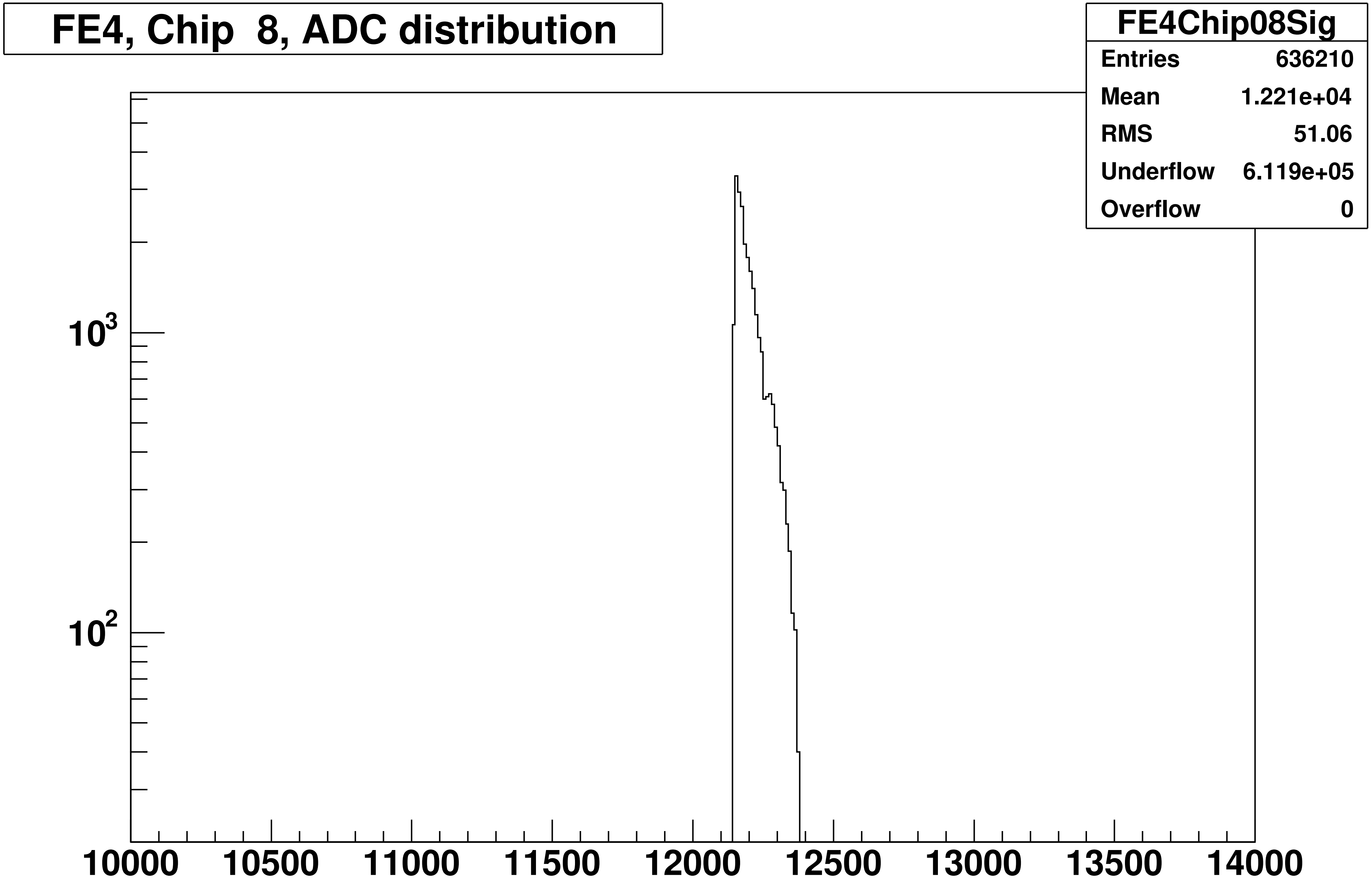
FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 636210
Mean -13.65
RMS 1.359
Underflow 0
Overflow 2.533e+04
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200


FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 636210
Mean 1.218e+04
RMS 85.07
Underflow 6.11e+05
Overflow 0
1
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000

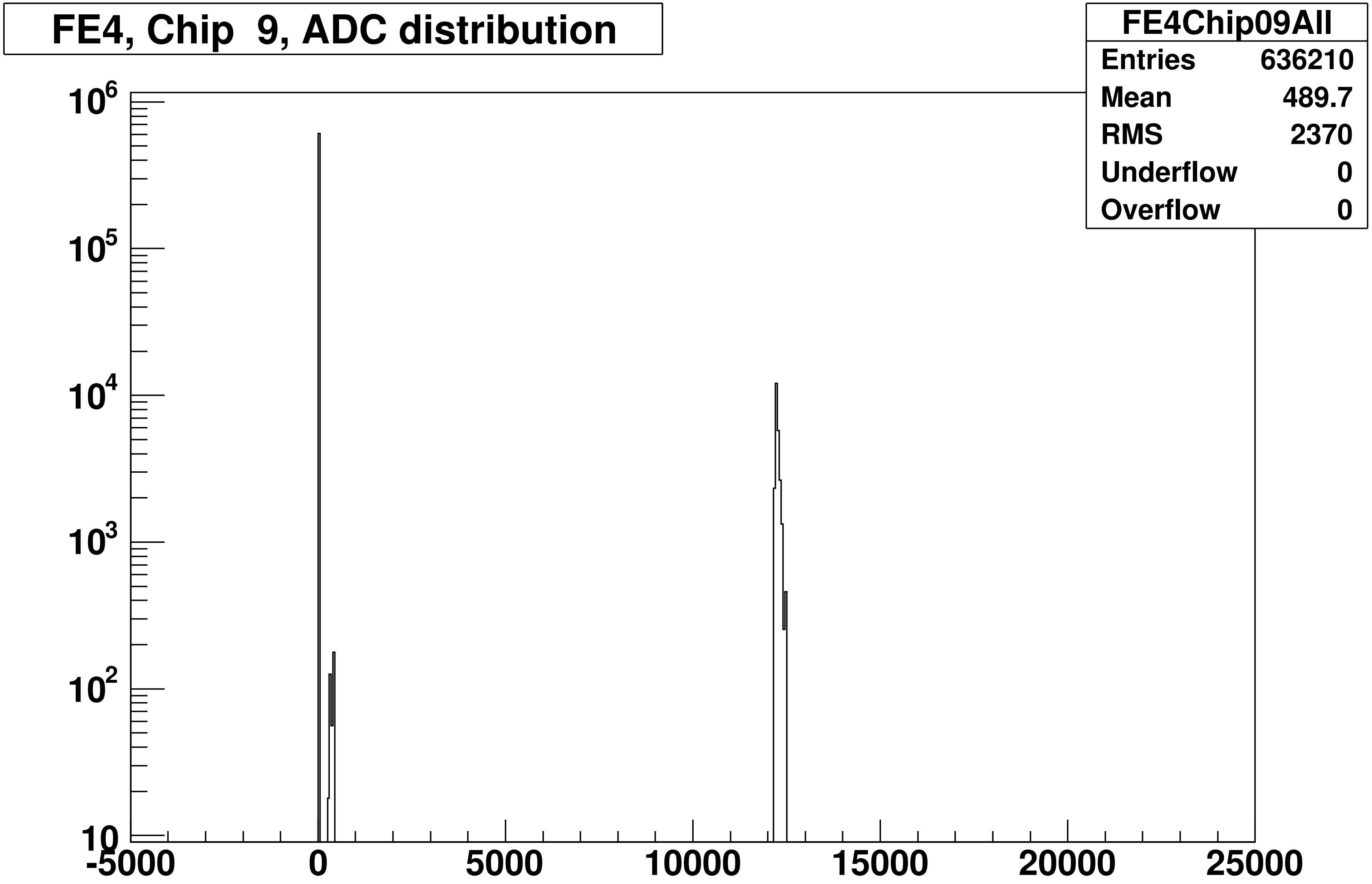


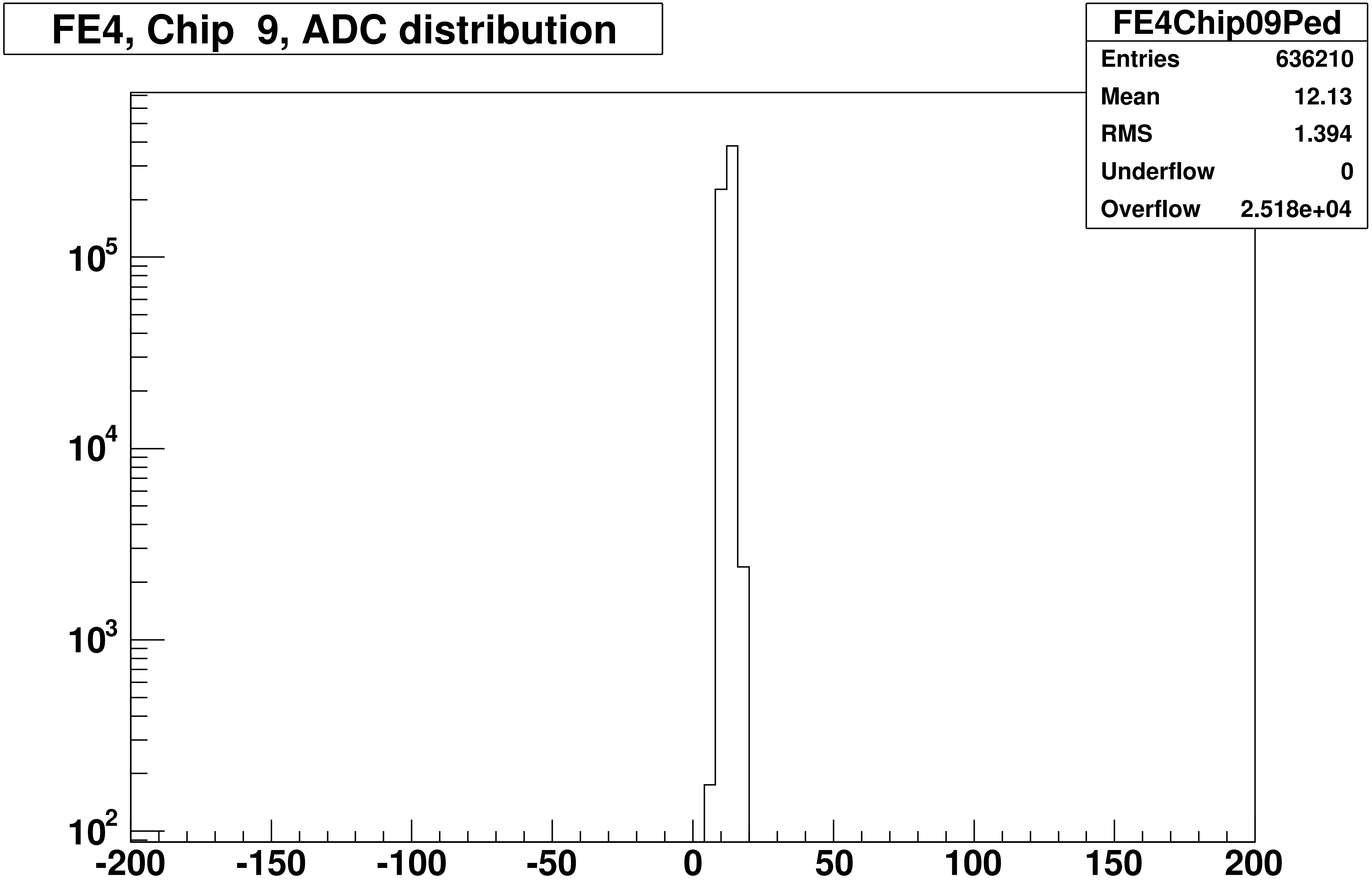
FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 636210
Mean 476.6
RMS 2338
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

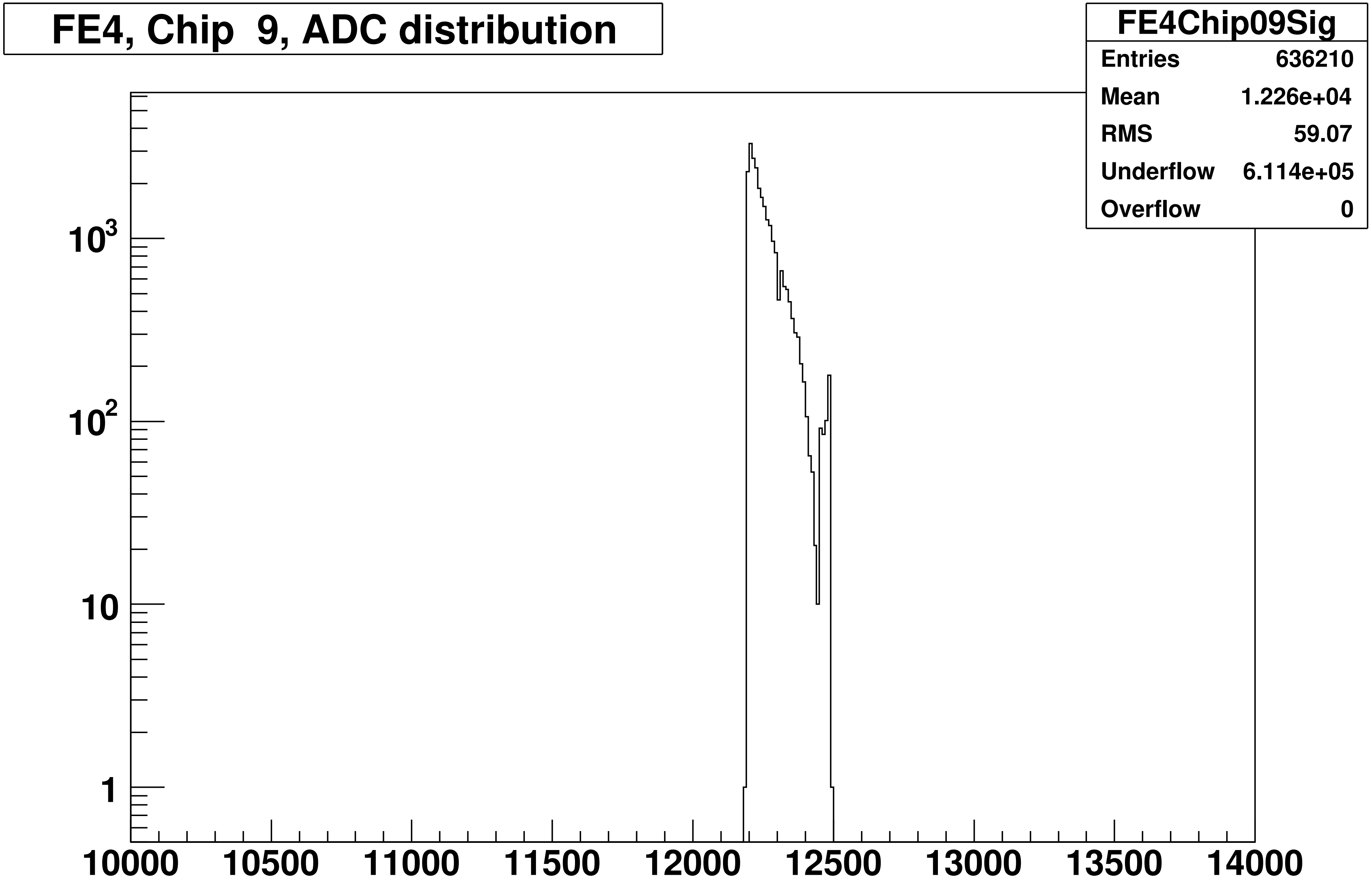

FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 636210
Mean 9.826
RMS 1.396
Underflow 0
Overflow 2.513e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

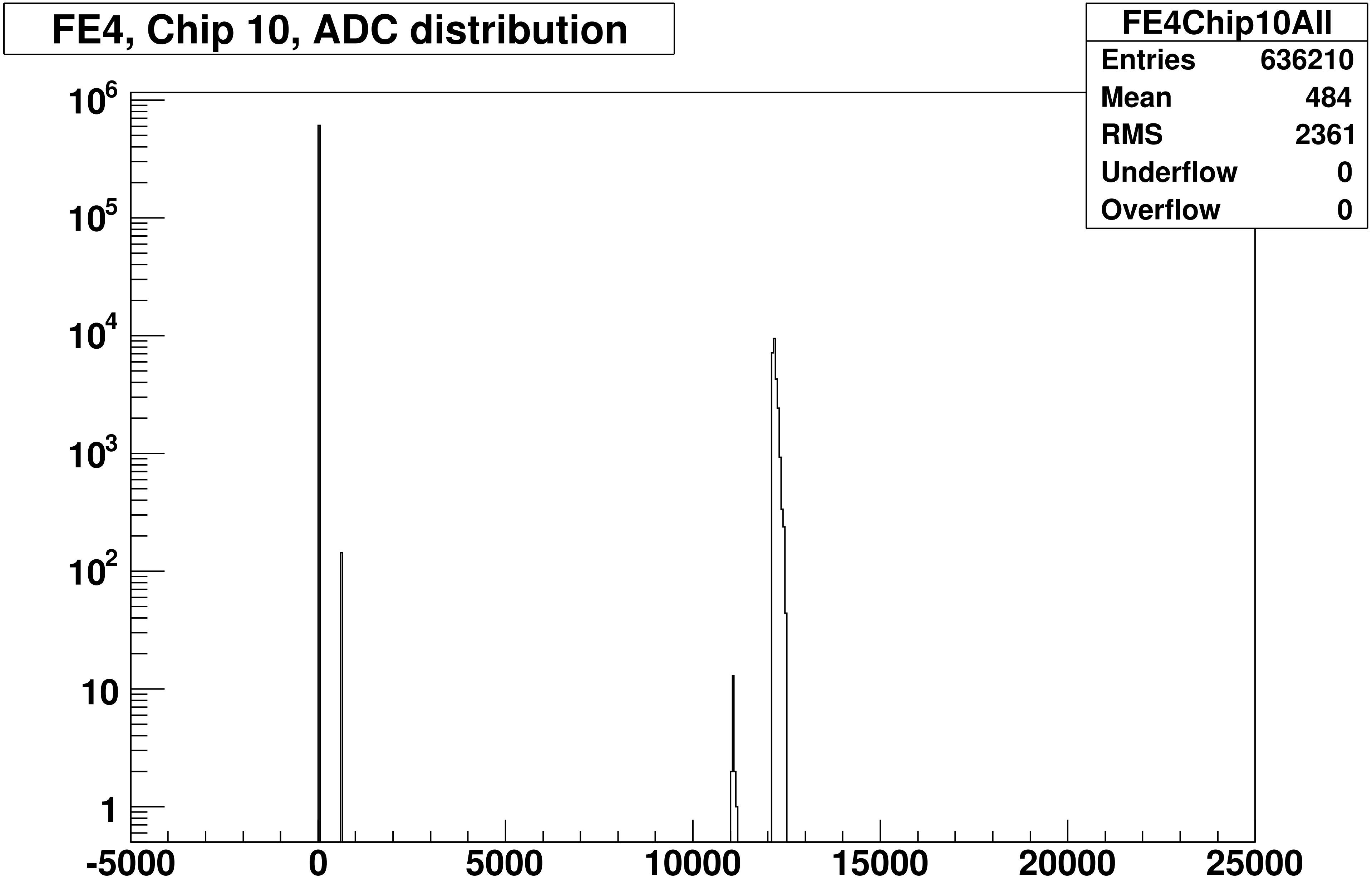


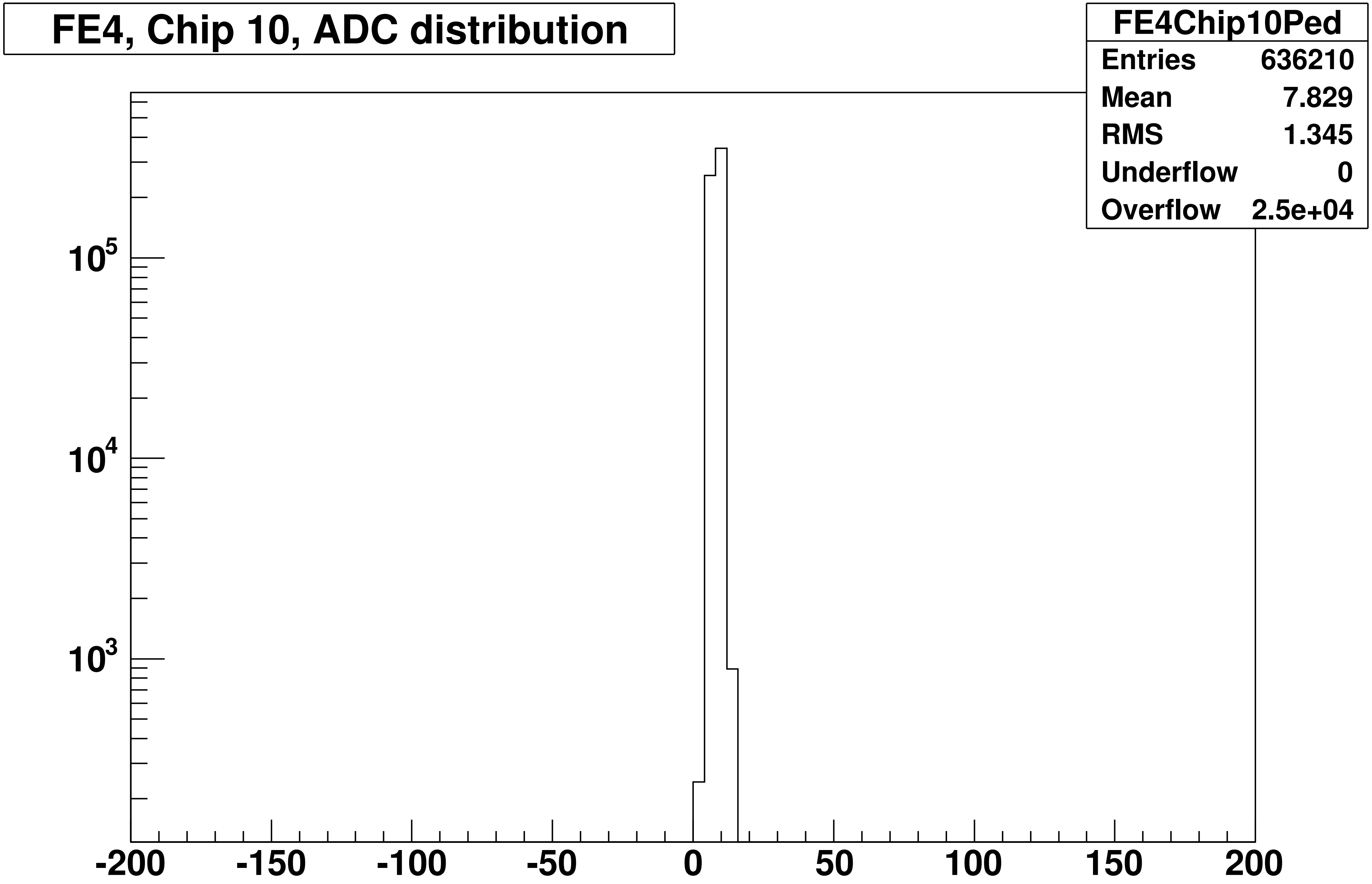
FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 636210
Mean 1.221e+04
RMS 51.06
Underflow 6.119e+05
Overflow 0
10^3
10^2
10000
10500
11000
11500
12000
12500
13000
13500
14000

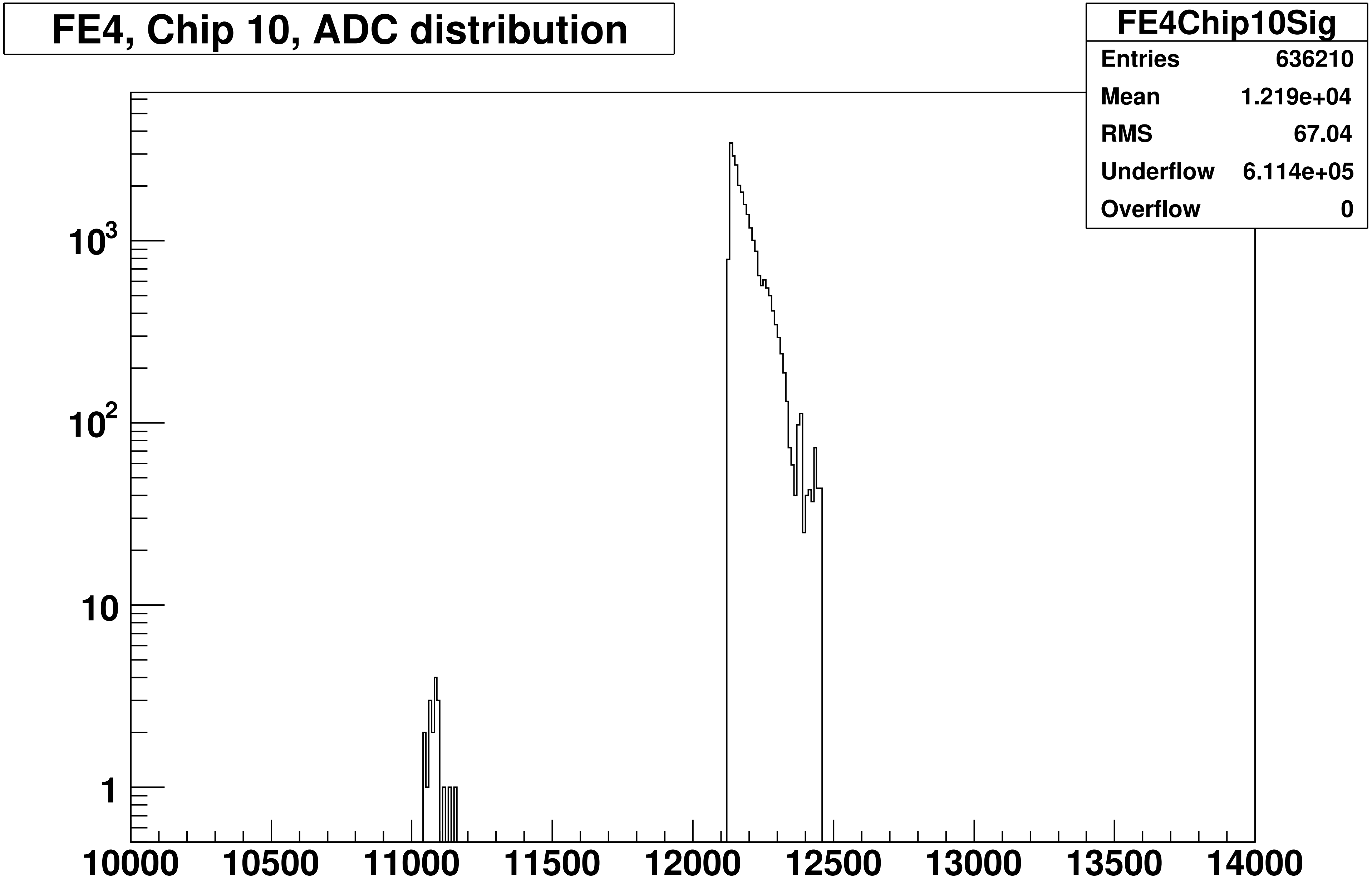

FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 636210
Mean 489.7
RMS 2370
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 636210
Mean 12.13
RMS 1.394
Underflow 0
Overflow 2.518e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

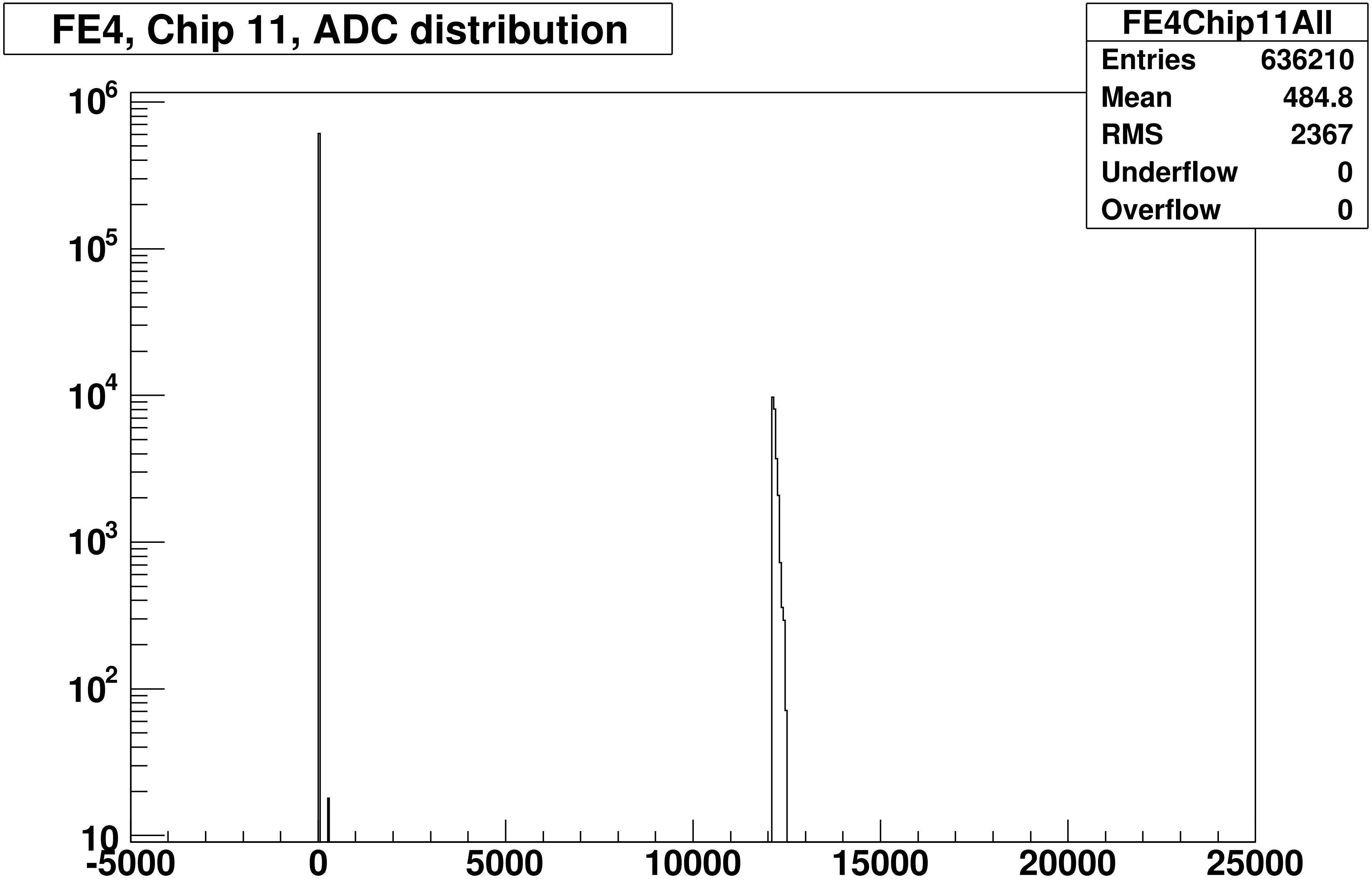

FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 636210
Mean 1.226e+04
RMS 59.07
Underflow 6.114e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 636210
Mean 484
RMS 2361
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

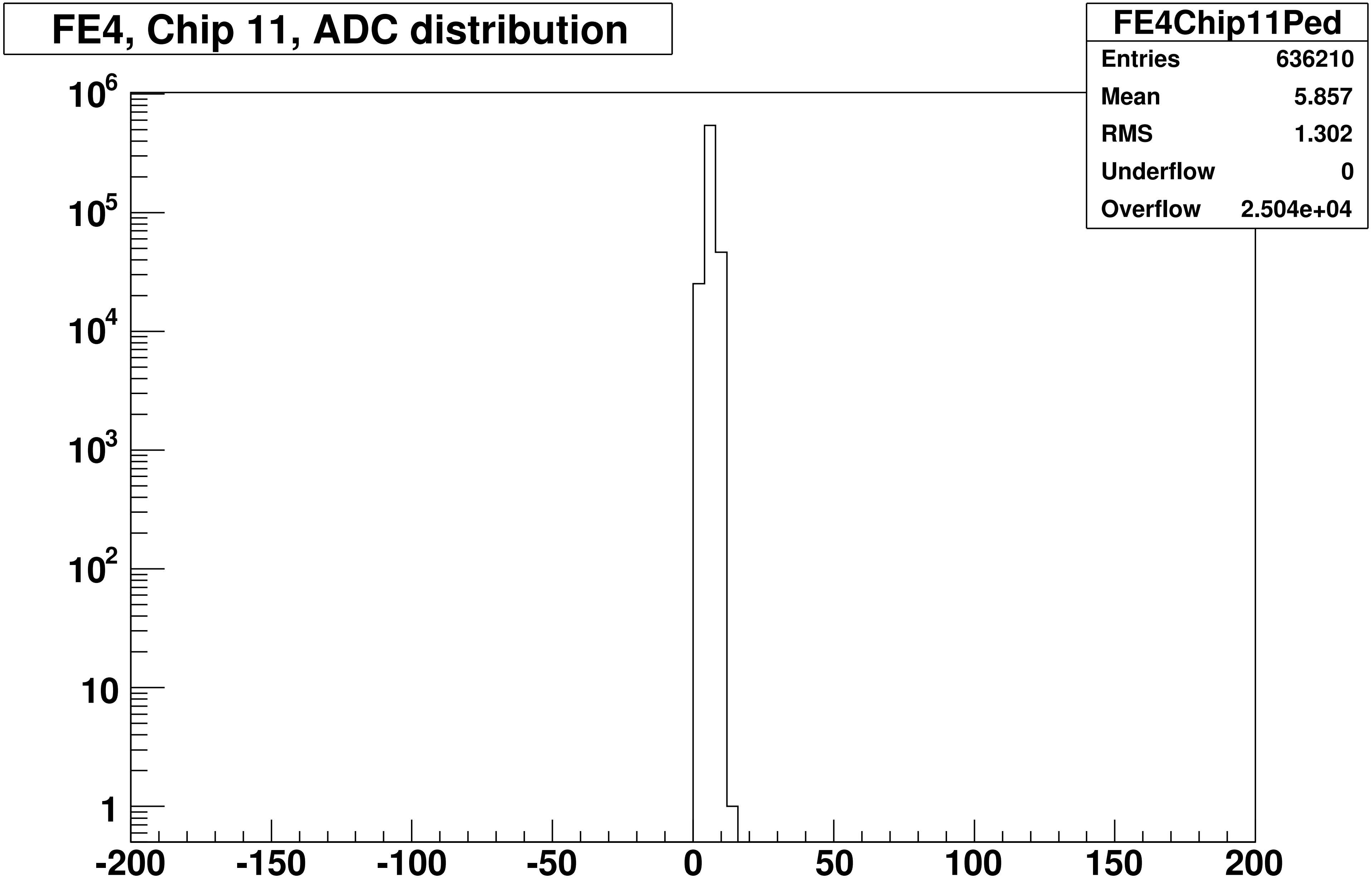

FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 636210
Mean 7.829
RMS 1.345
Underflow 0
Overflow 2.5e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

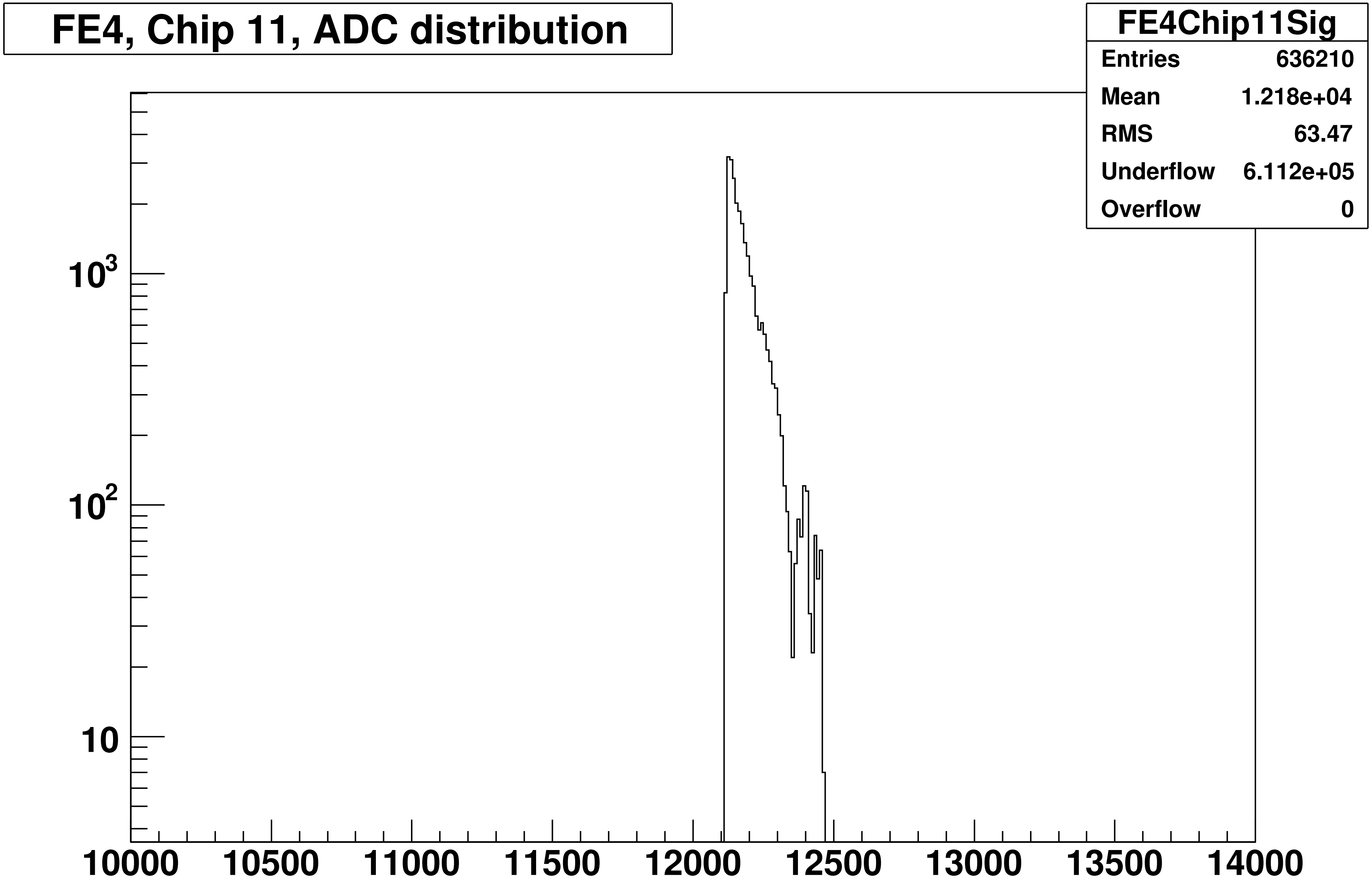


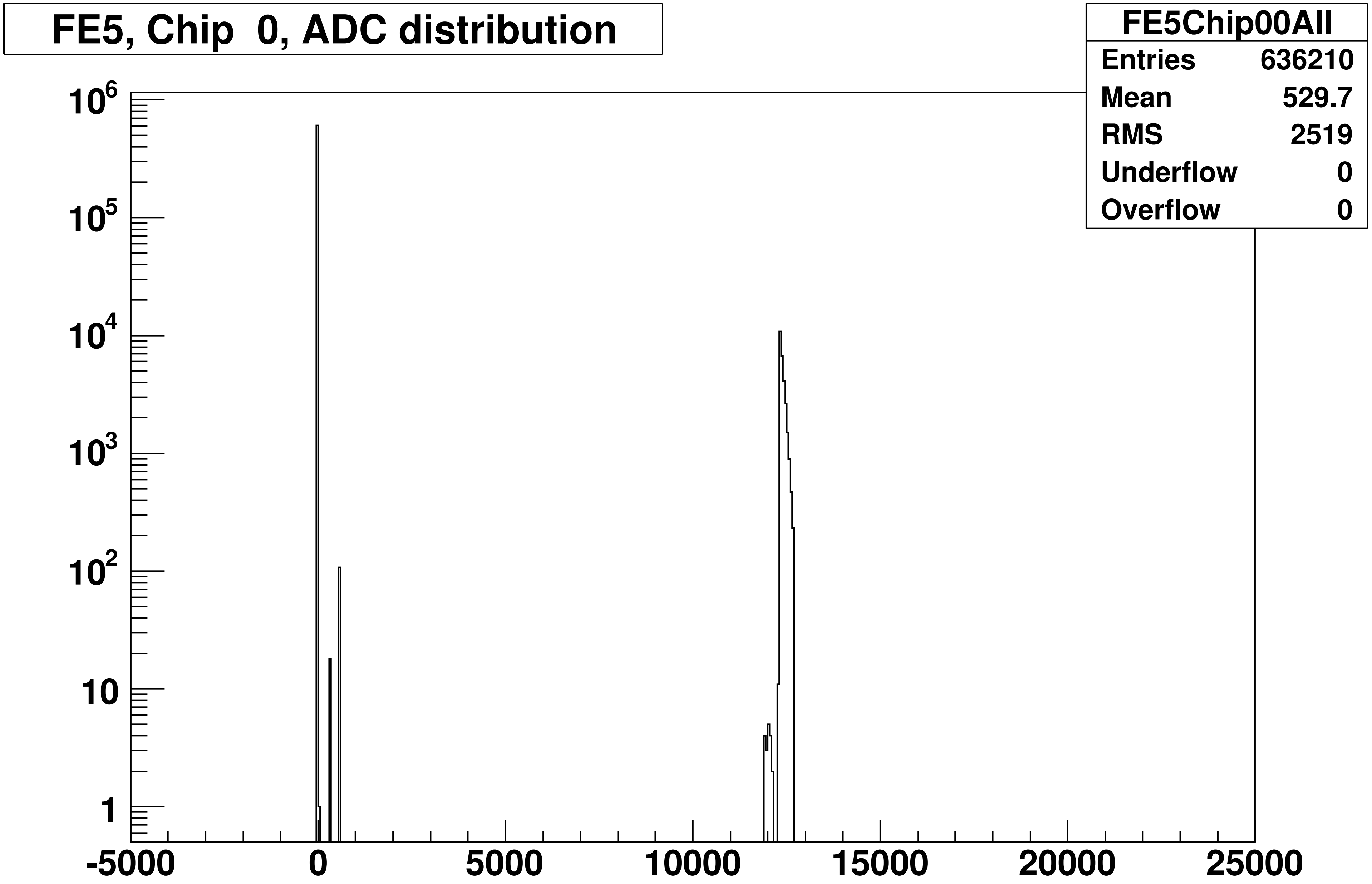
FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 636210
Mean 1.219e+04
RMS 67.04
Underflow 6.114e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

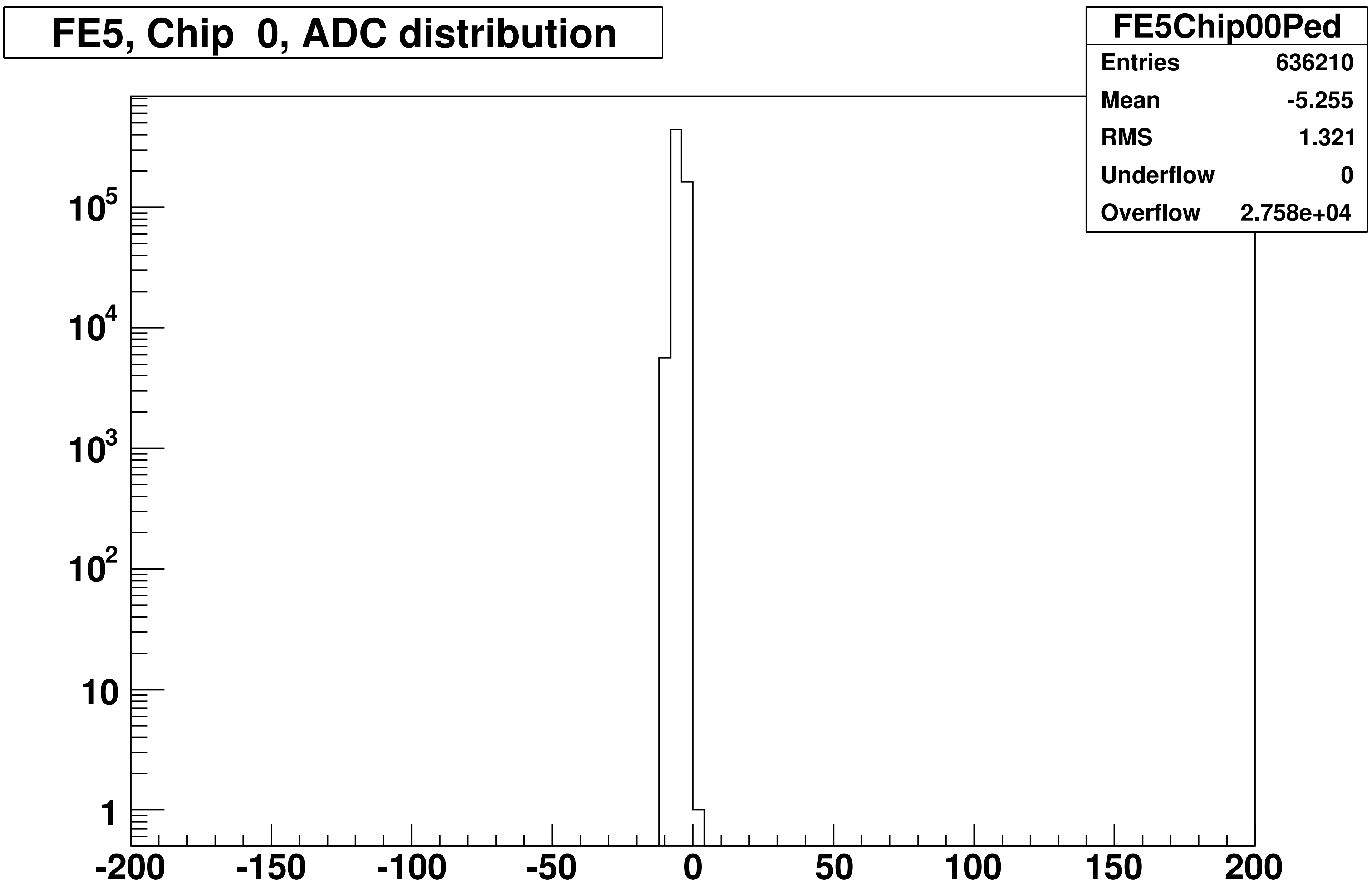


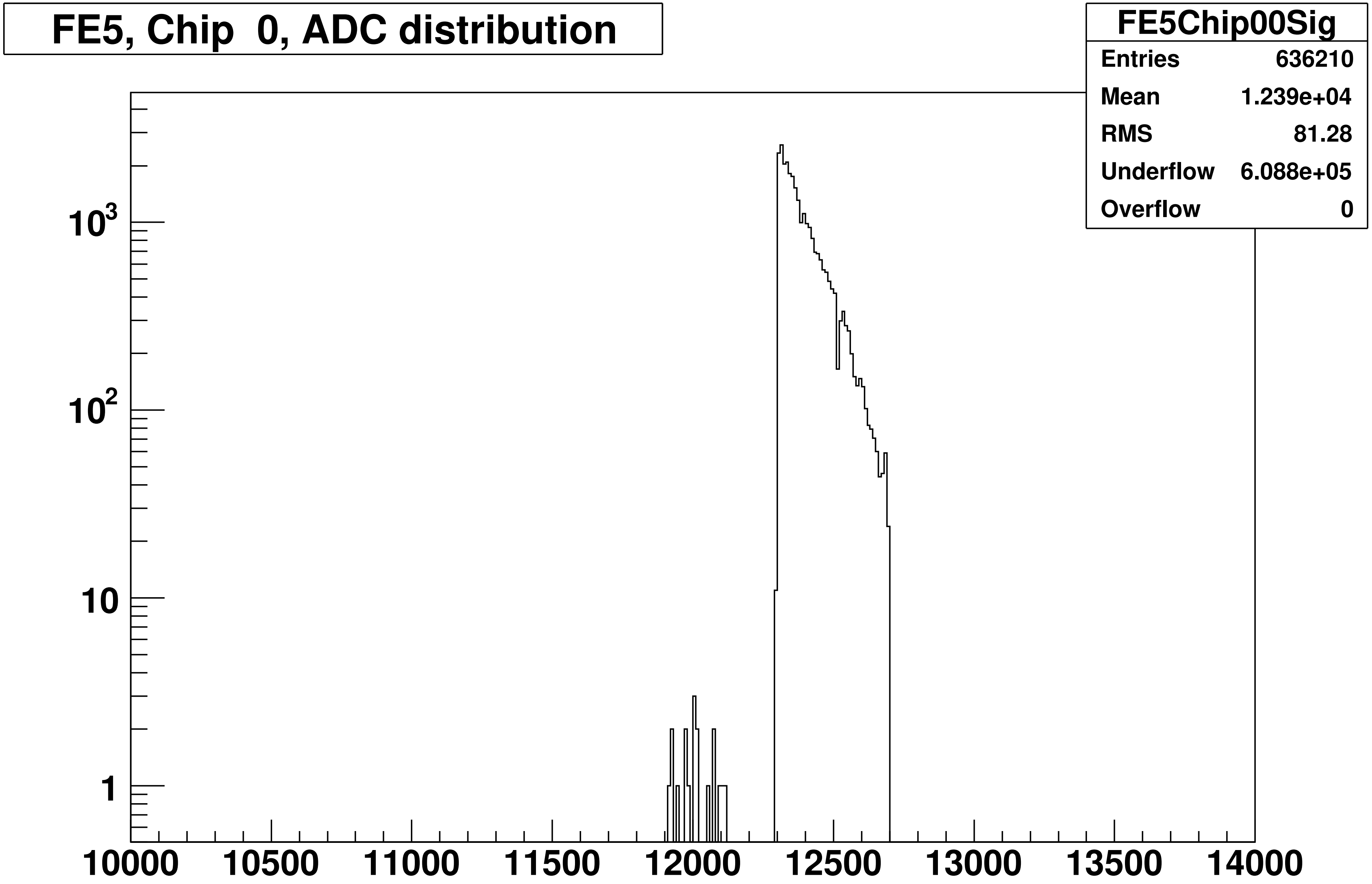
FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 636210
Mean 484.8
RMS 2367
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 636210
Mean 5.857
RMS 1.302
Underflow 0
Overflow 2.504e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

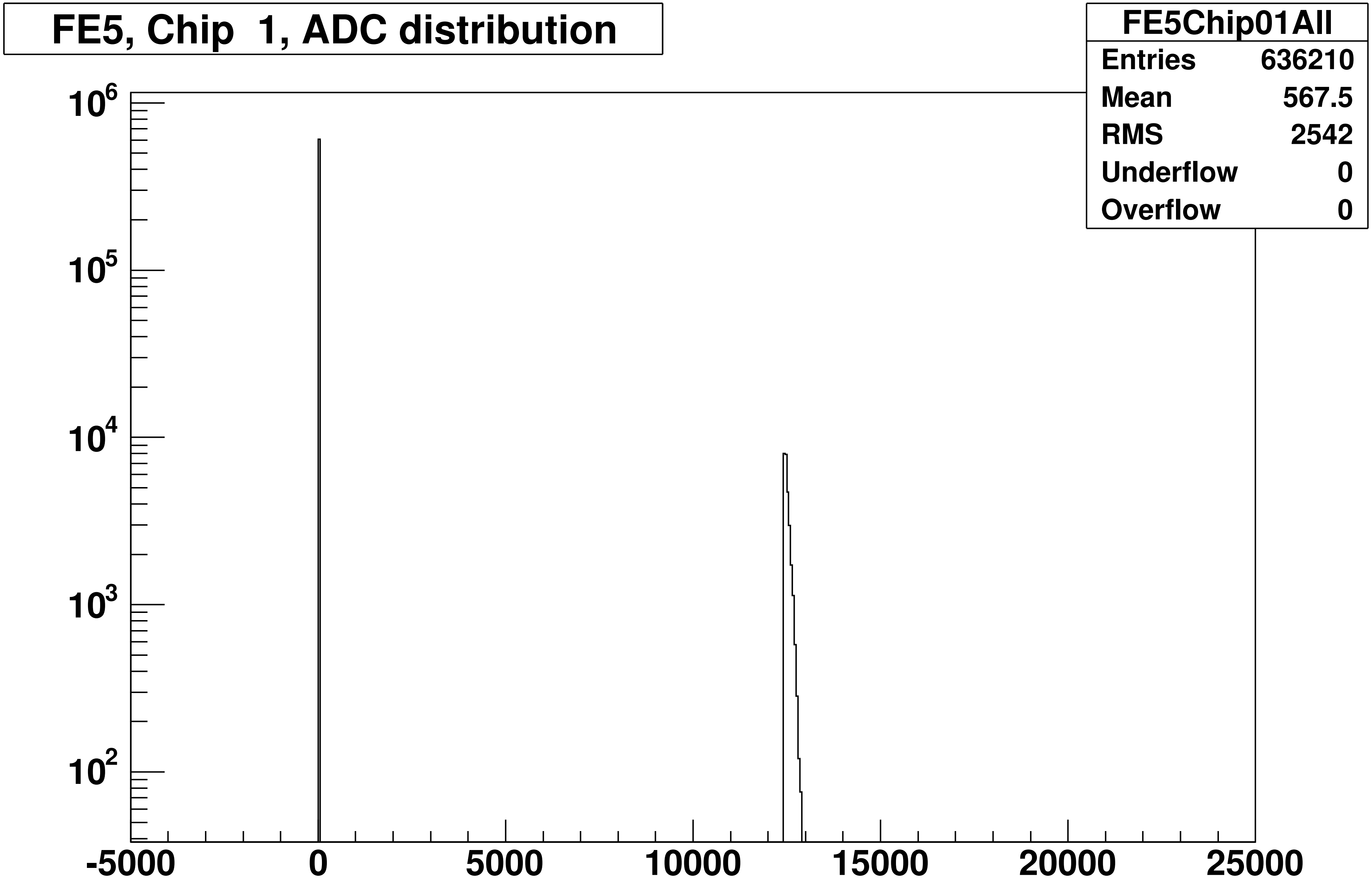

FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 636210
Mean 1.218e+04
RMS 63.47
Underflow 6.112e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

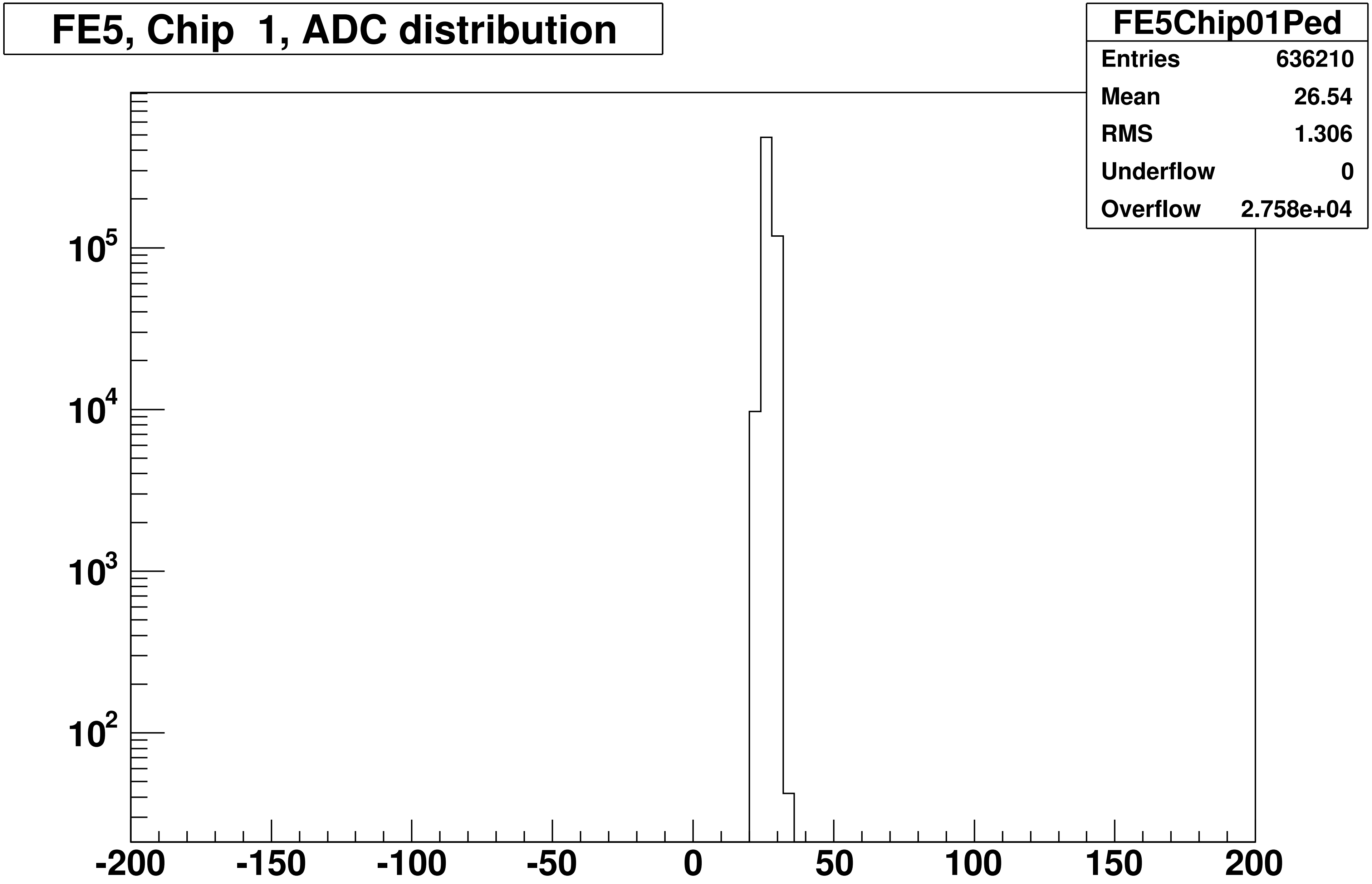

FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 636210
Mean 529.7
RMS 2519
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

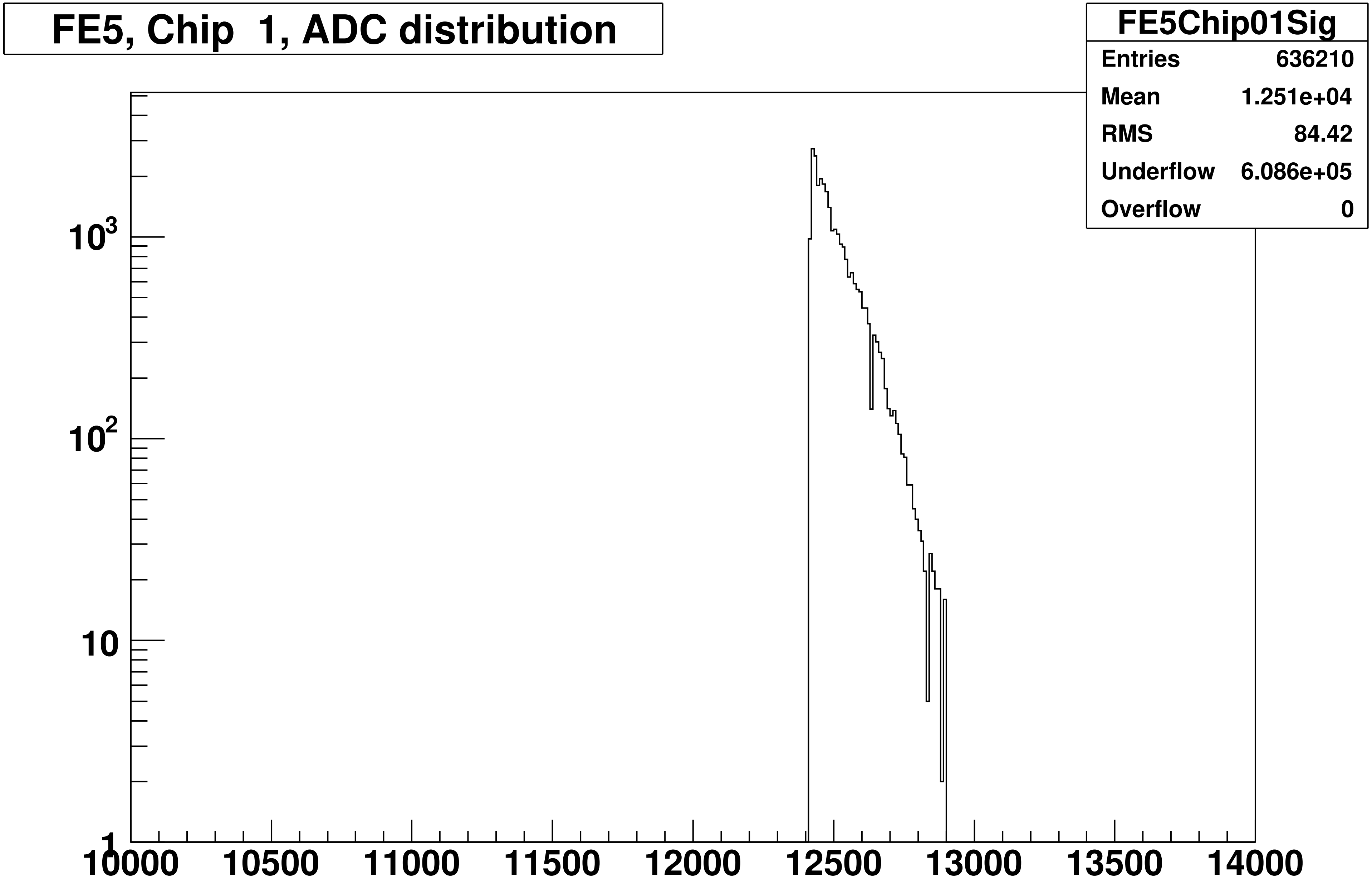

FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 636210
Mean -5.255
RMS 1.321
Underflow 0
Overflow 2.758e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

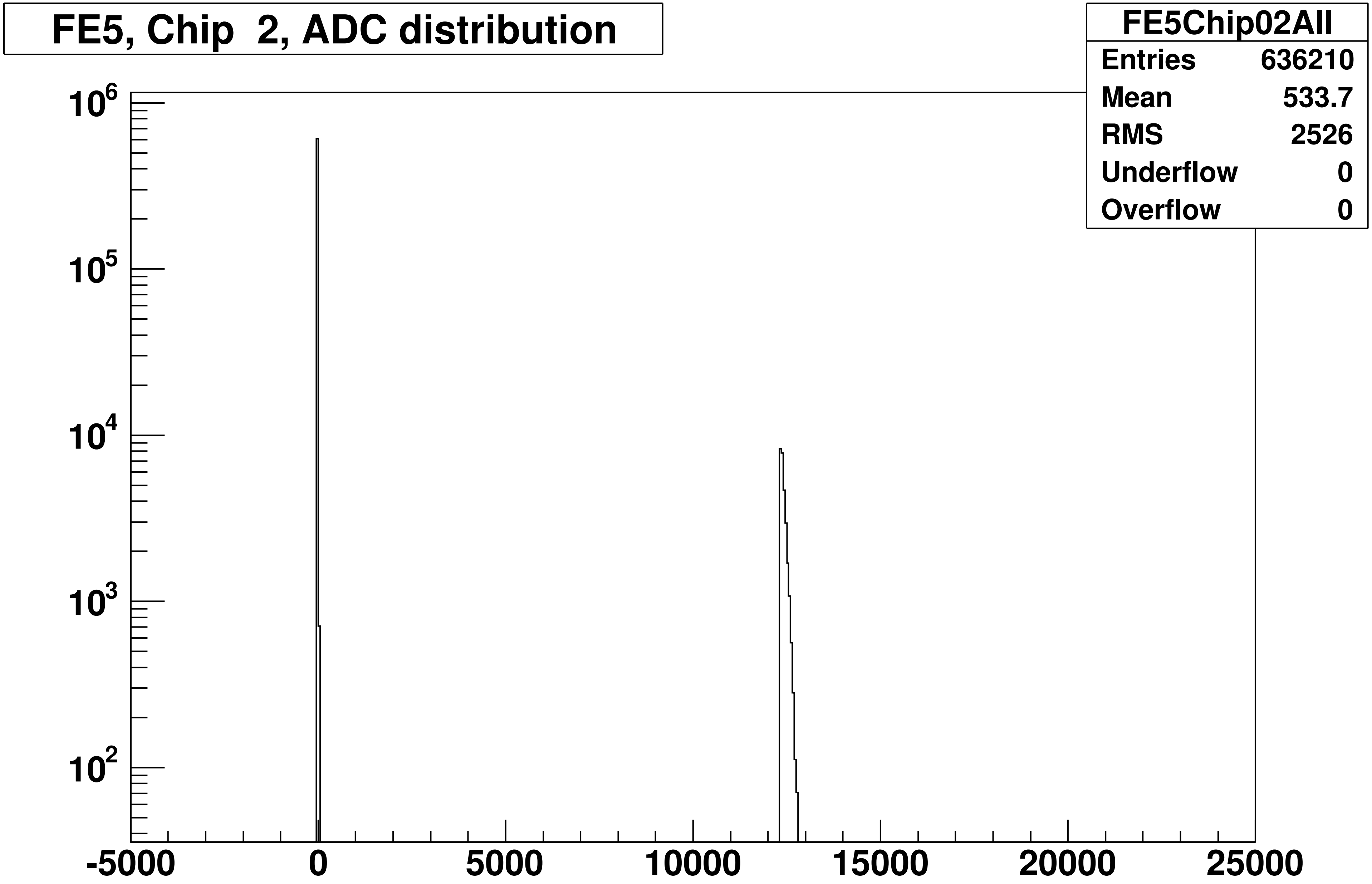


FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 636210
Mean 1.239e+04
RMS 81.28
Underflow 6.088e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

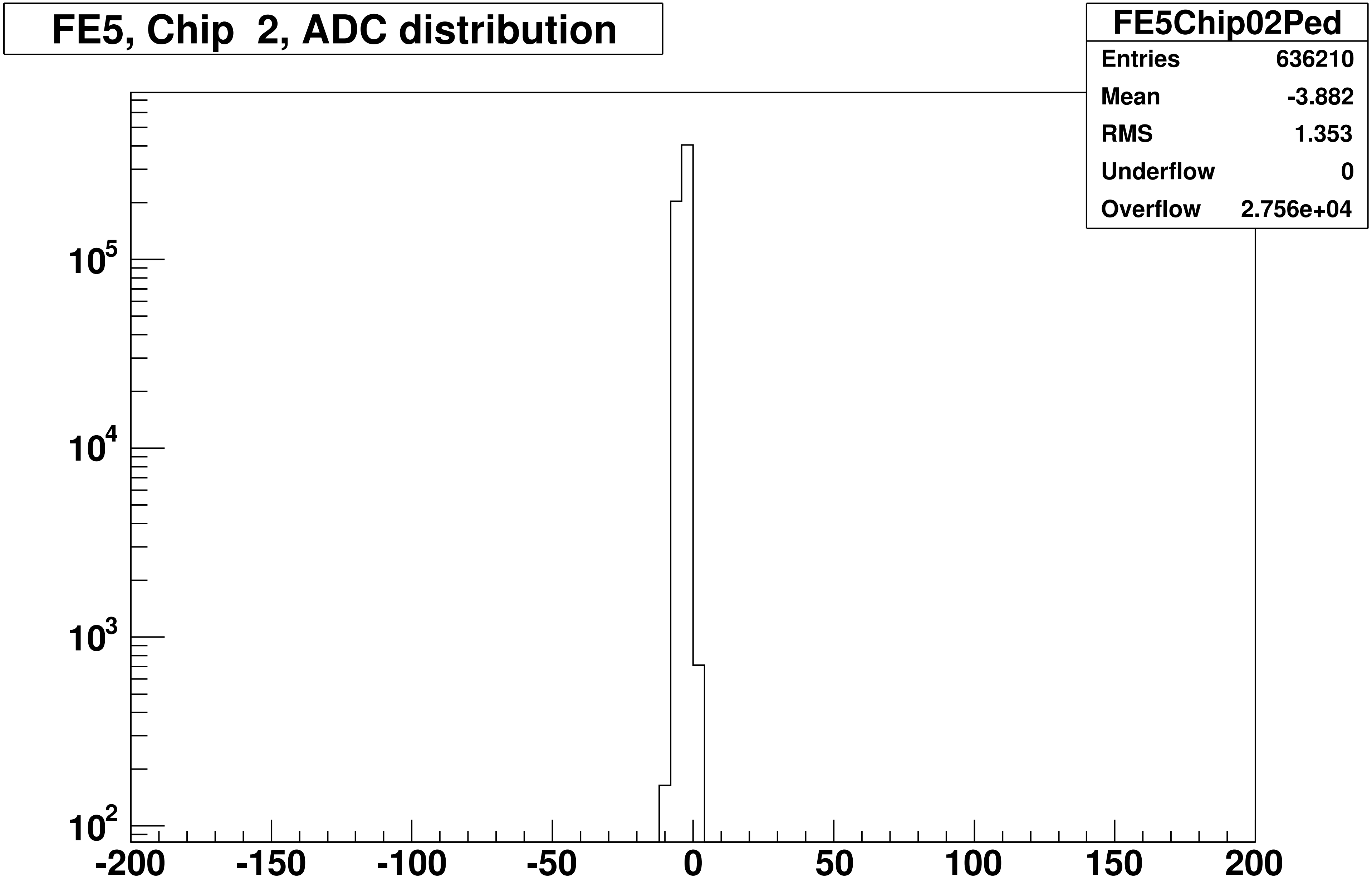

FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 636210
Mean 567.5
RMS 2542
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 636210
Mean 26.54
RMS 1.306
Underflow 0
Overflow 2.758e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

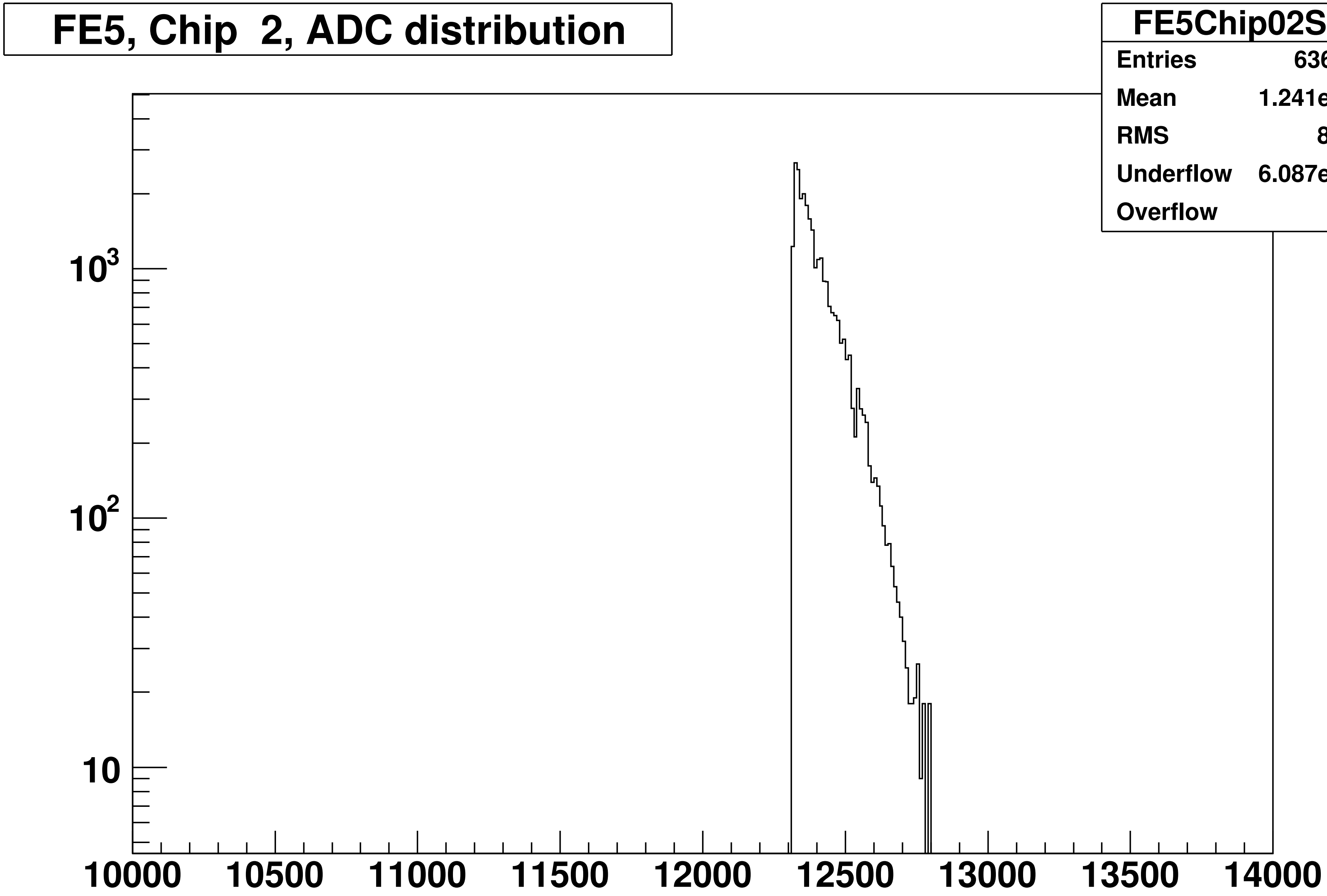

FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 636210
Mean 1.251e+04
RMS 84.42
Underflow 6.086e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000



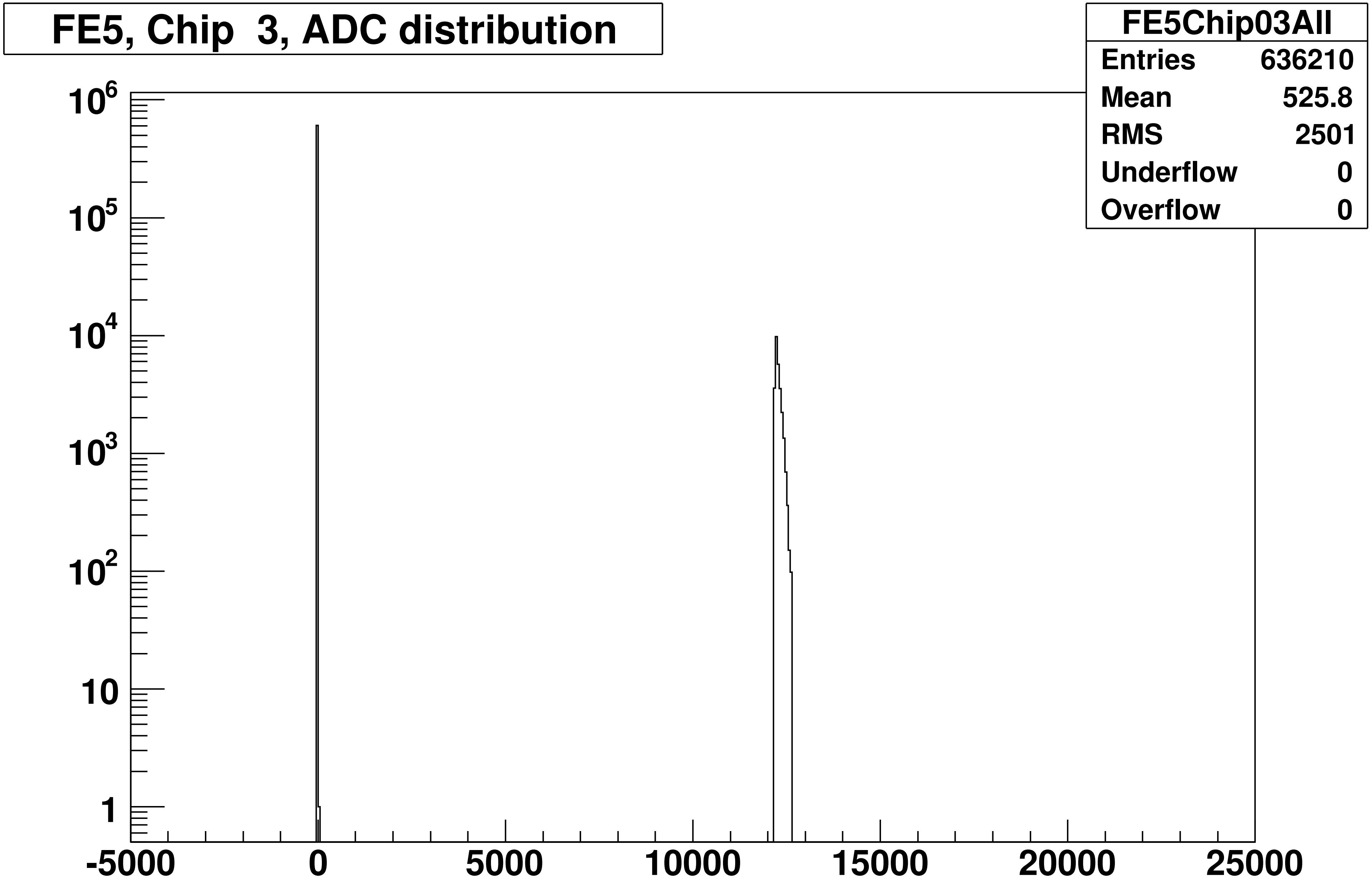
FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 636210
Mean 533.7
RMS 2526
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

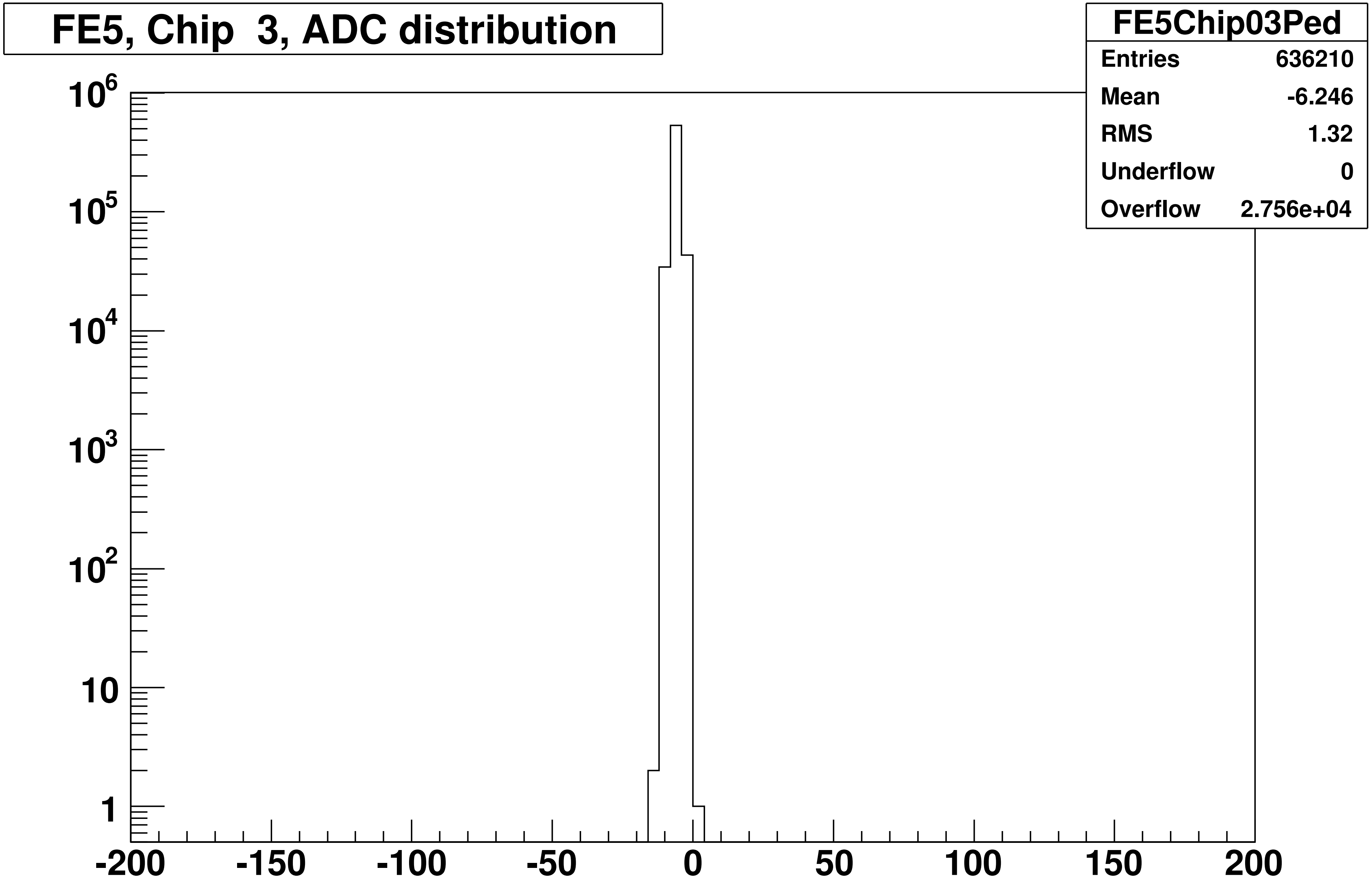


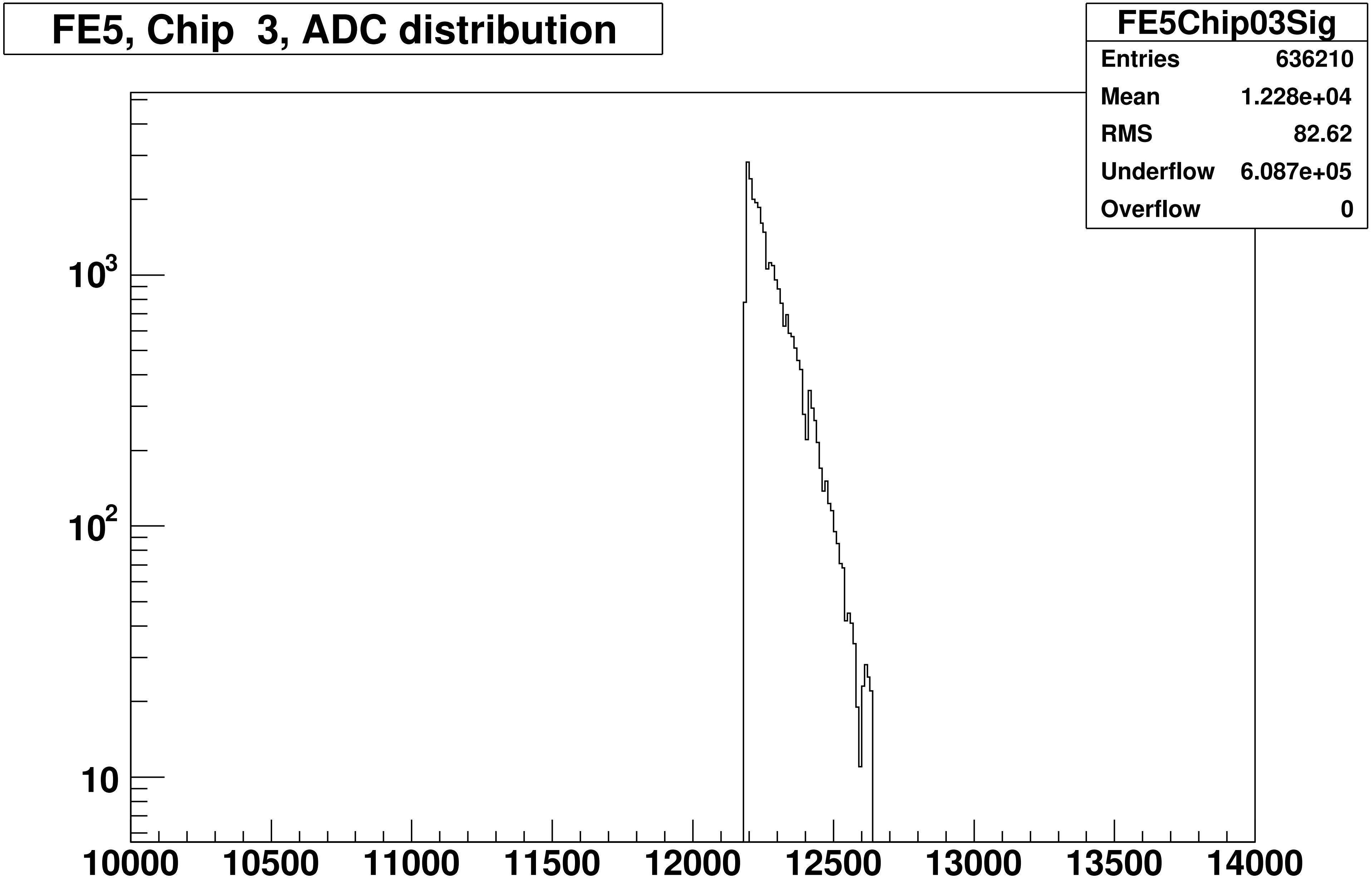
FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 636210
Mean -3.882
RMS 1.353
Underflow 0
Overflow 2.756e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



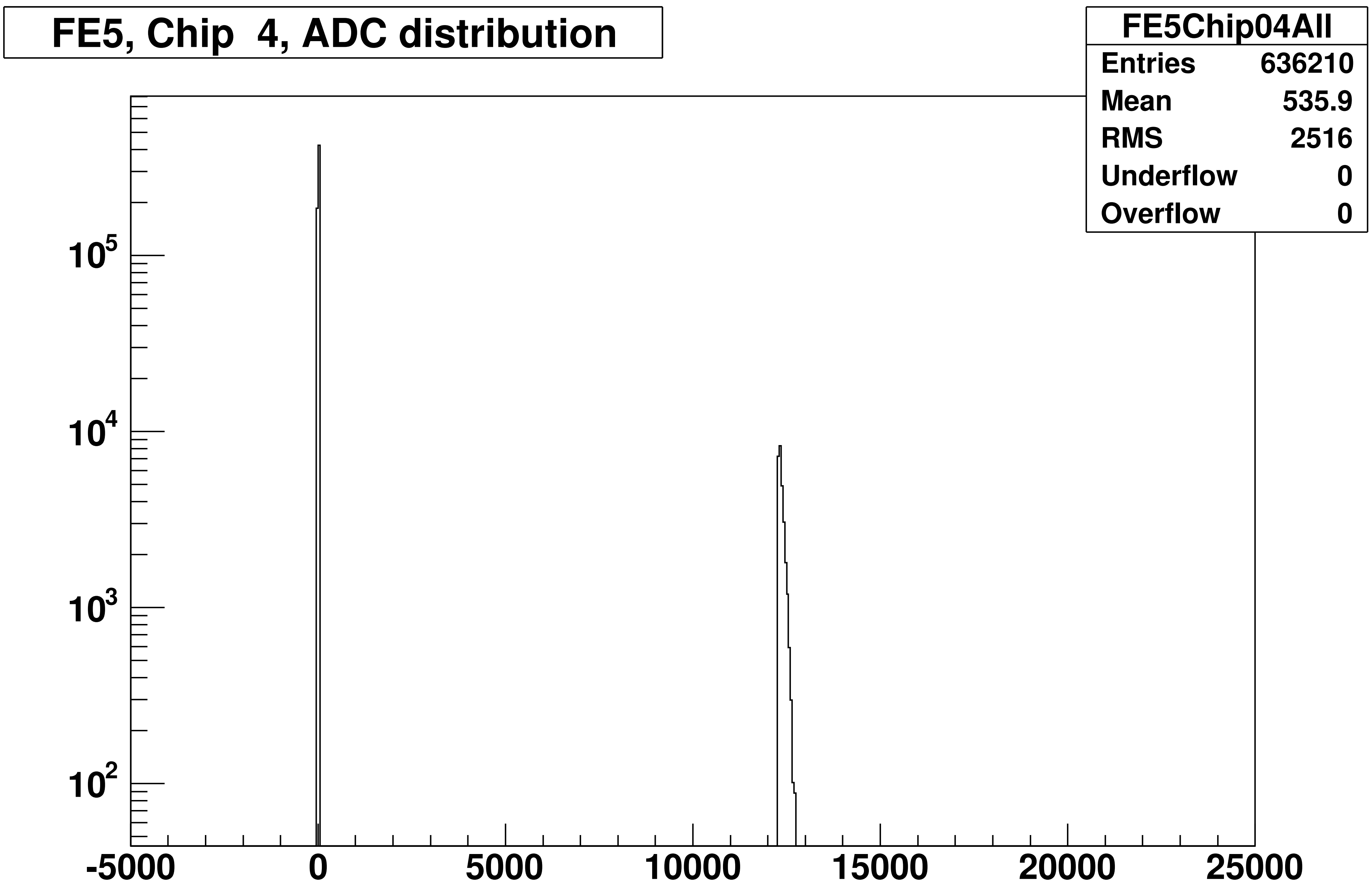
FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 636210
Mean 1.241e+04
RMS 83.81
Underflow 6.087e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

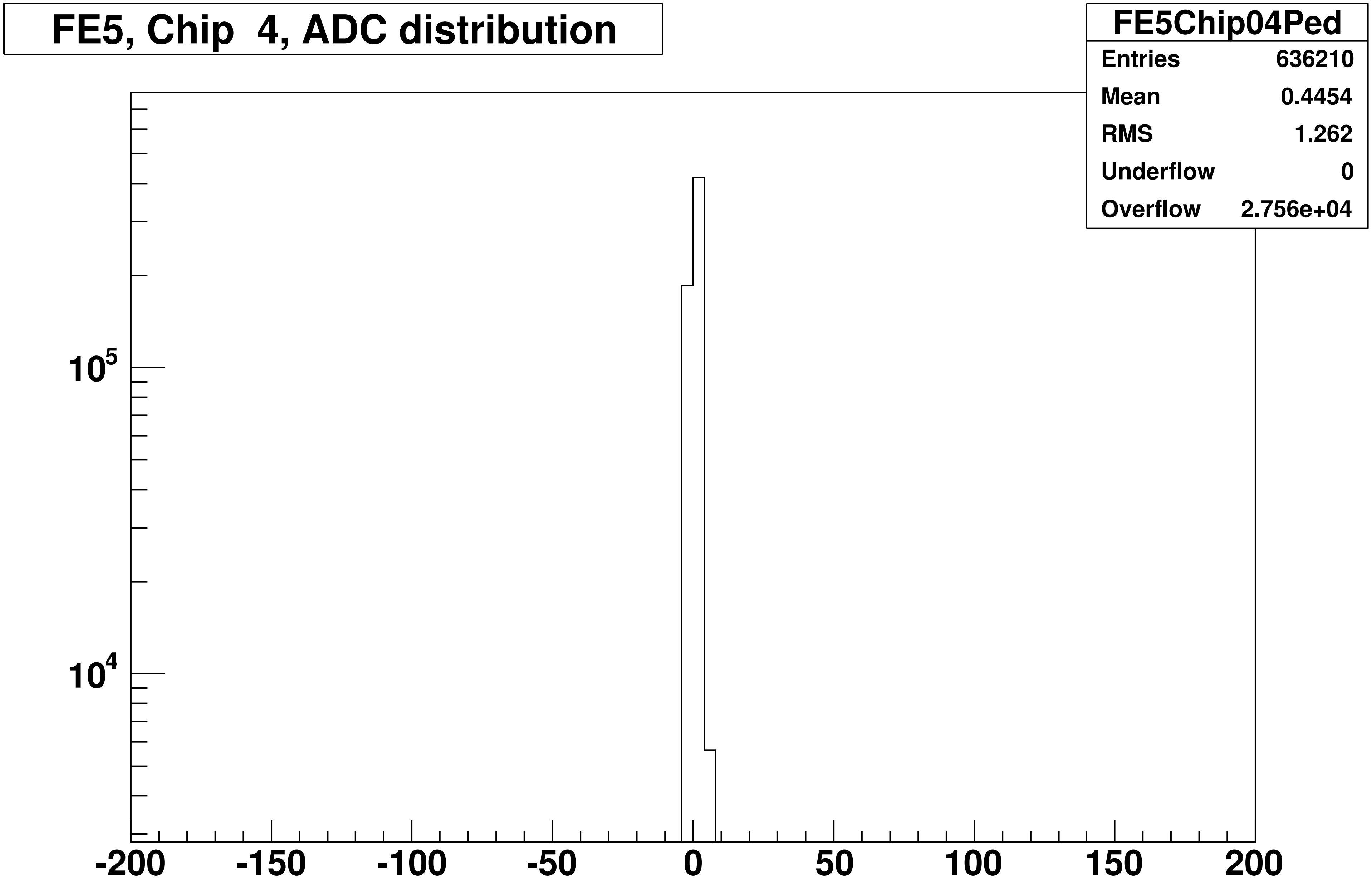

FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 636210
Mean 525.8
RMS 2501
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 636210
Mean -6.246
RMS 1.32
Underflow 0
Overflow 2.756e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

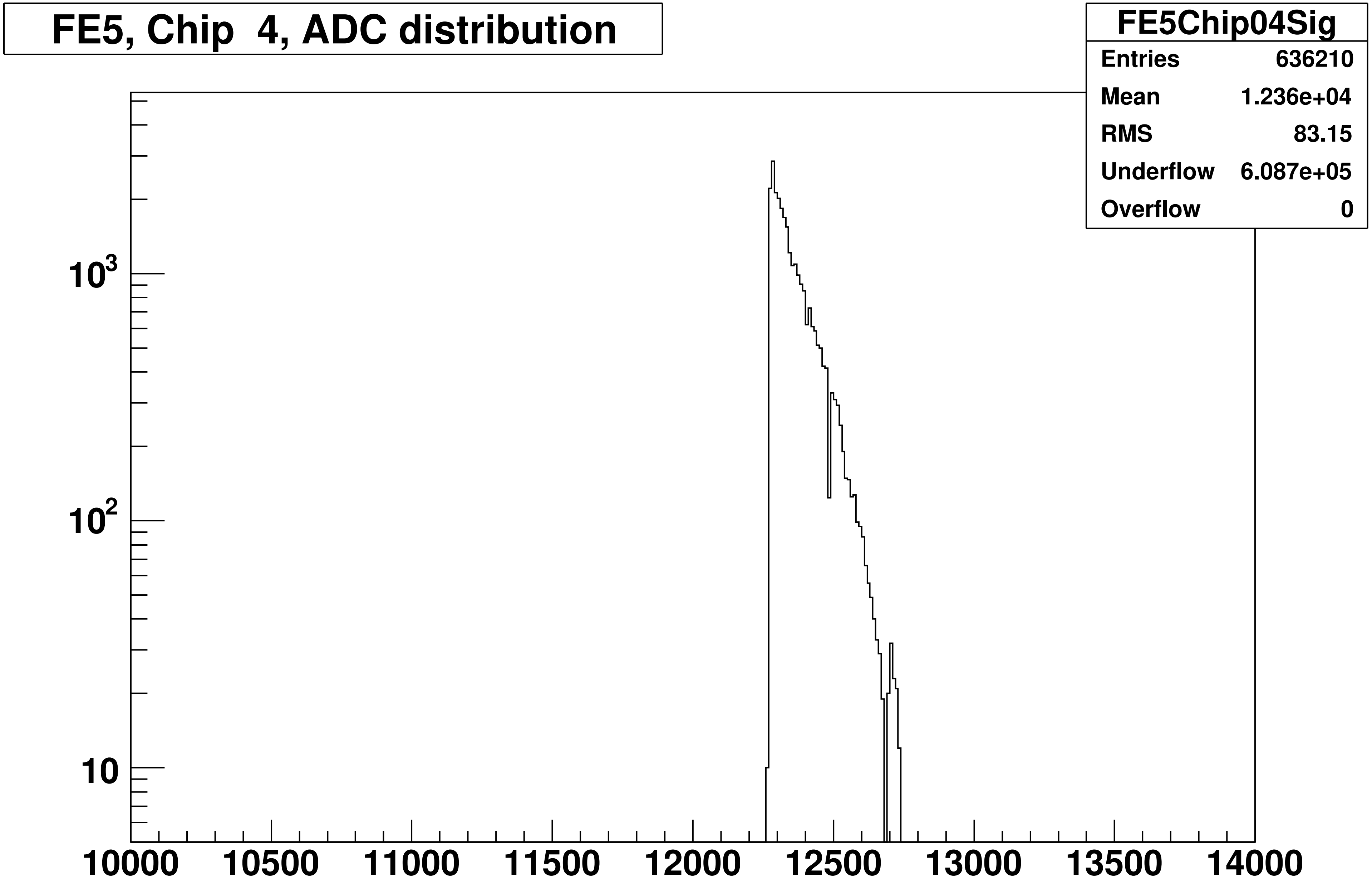


FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 636210
Mean 1.228e+04
RMS 82.62
Underflow 6.087e+05
Overflow 0
10³
10²
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

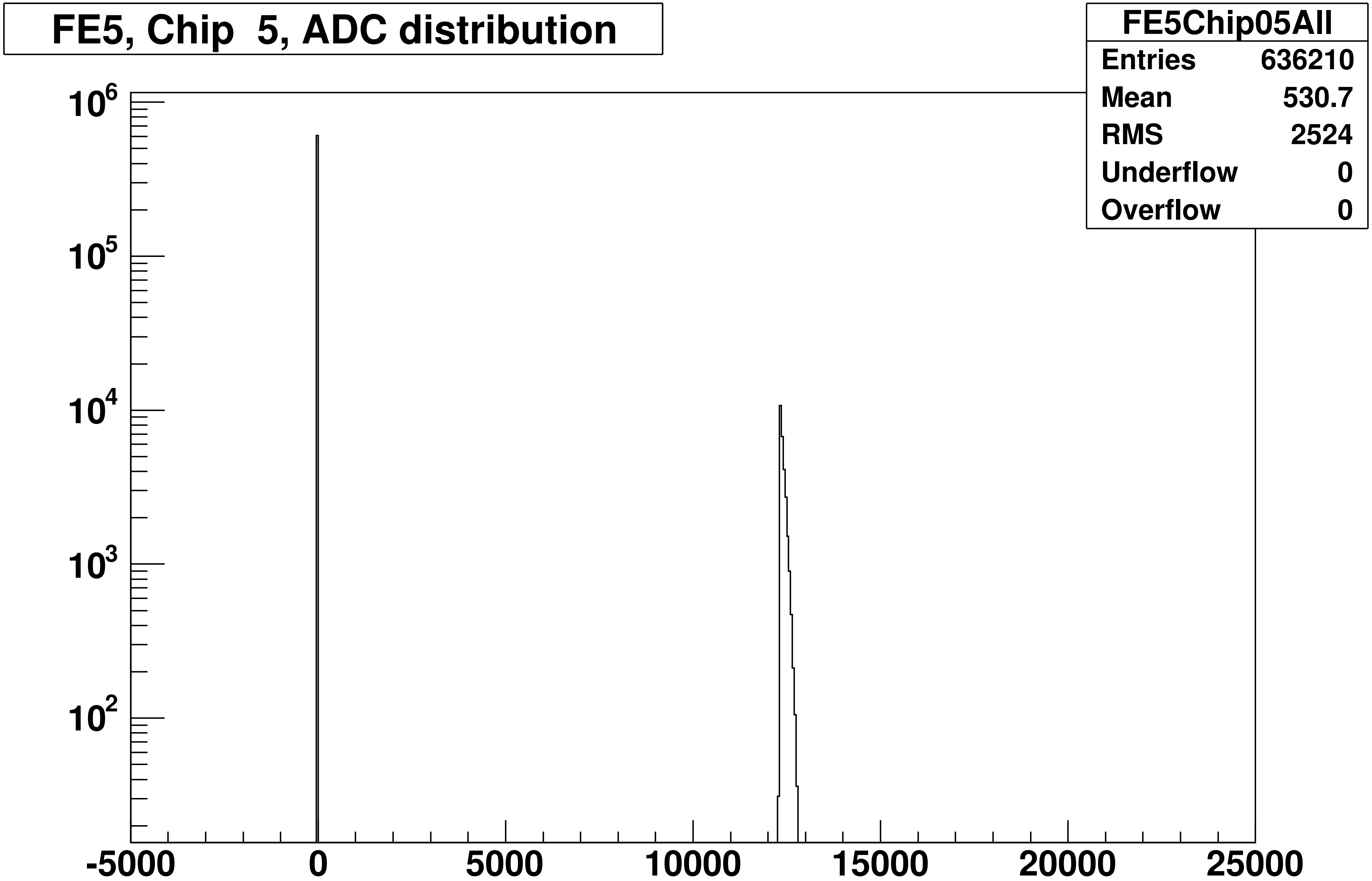

FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 636210
Mean 535.9
RMS 2516
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



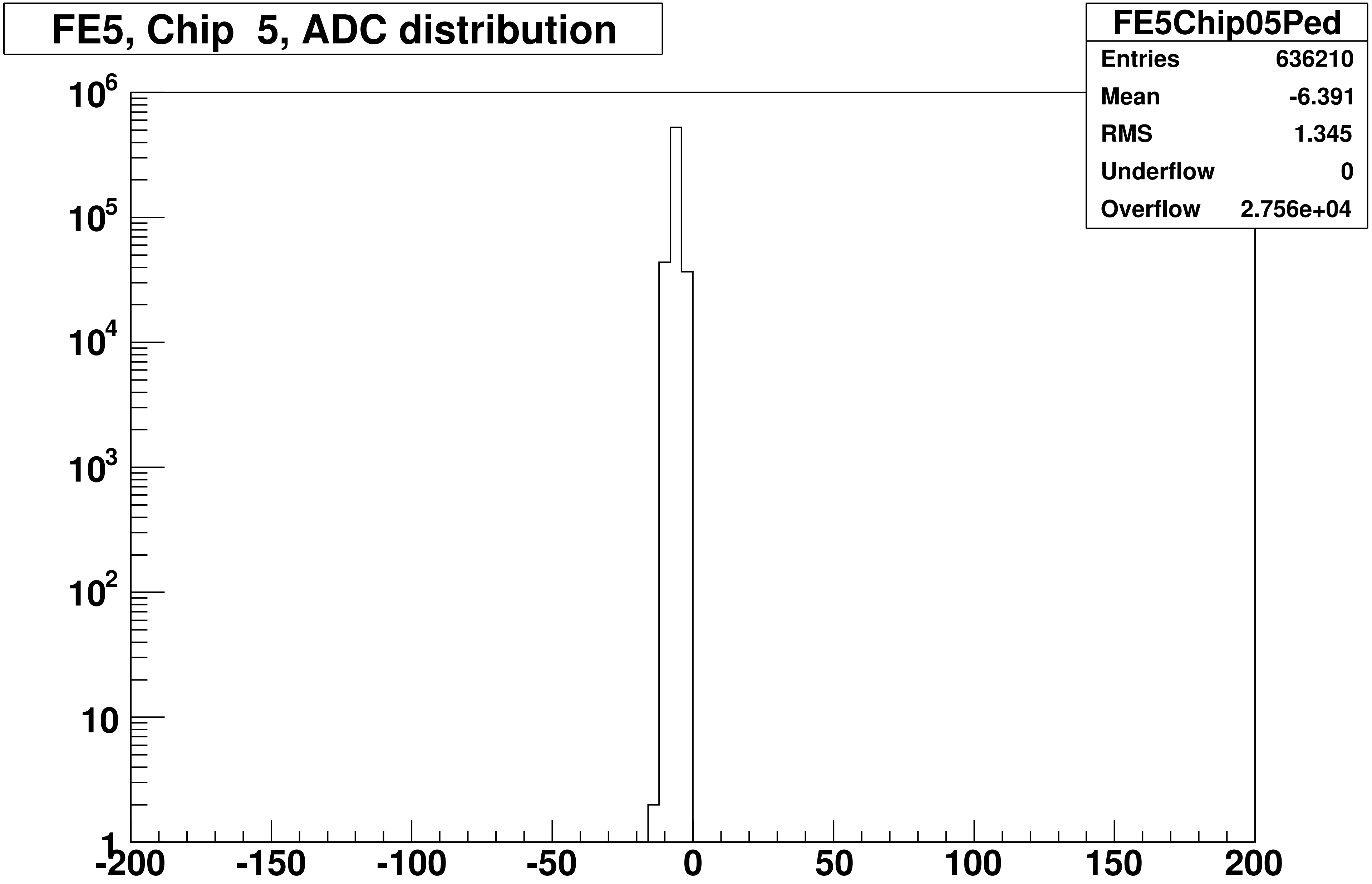
FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 636210
Mean 0.4454
RMS 1.262
Underflow 0
Overflow 2.756e+04
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200

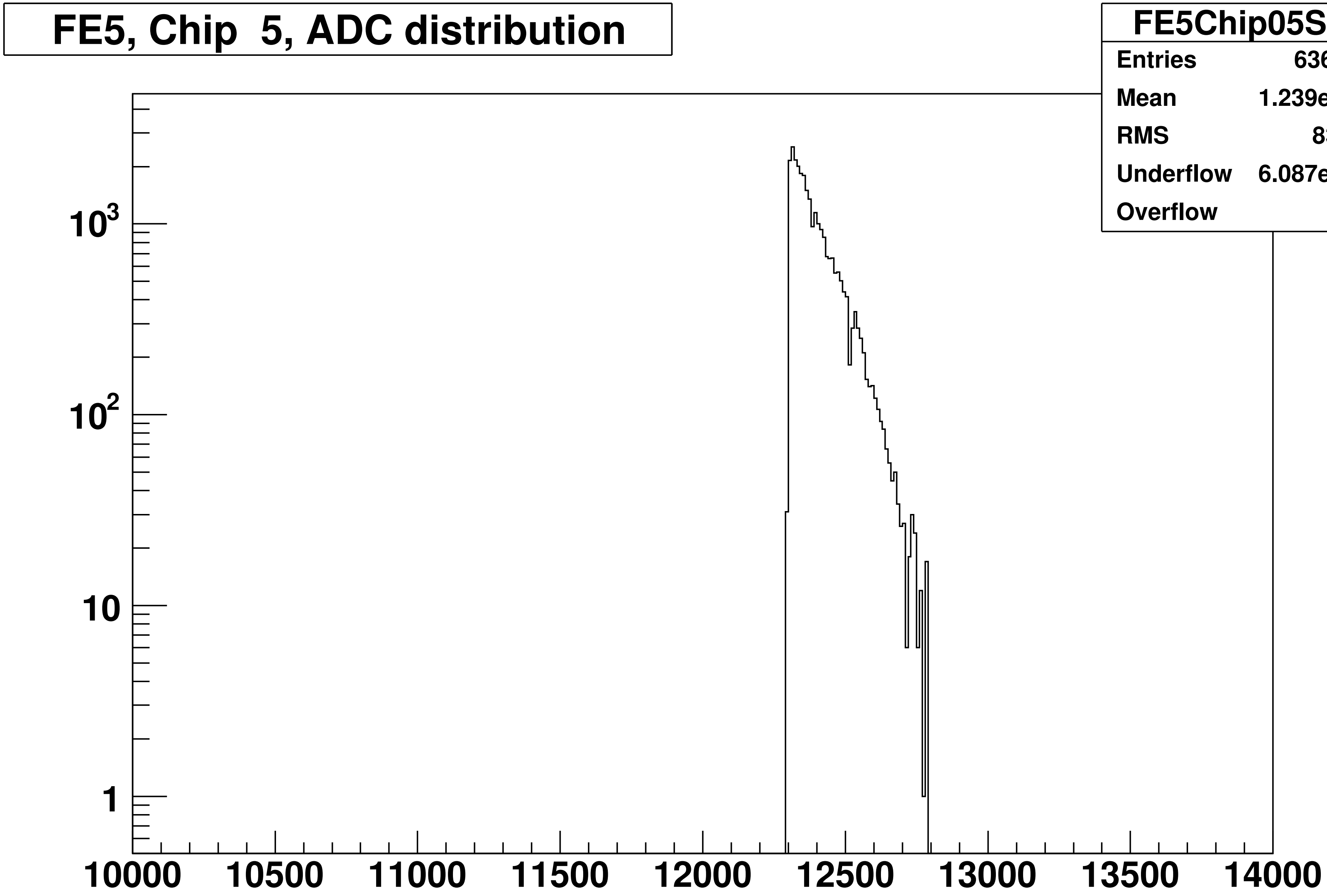


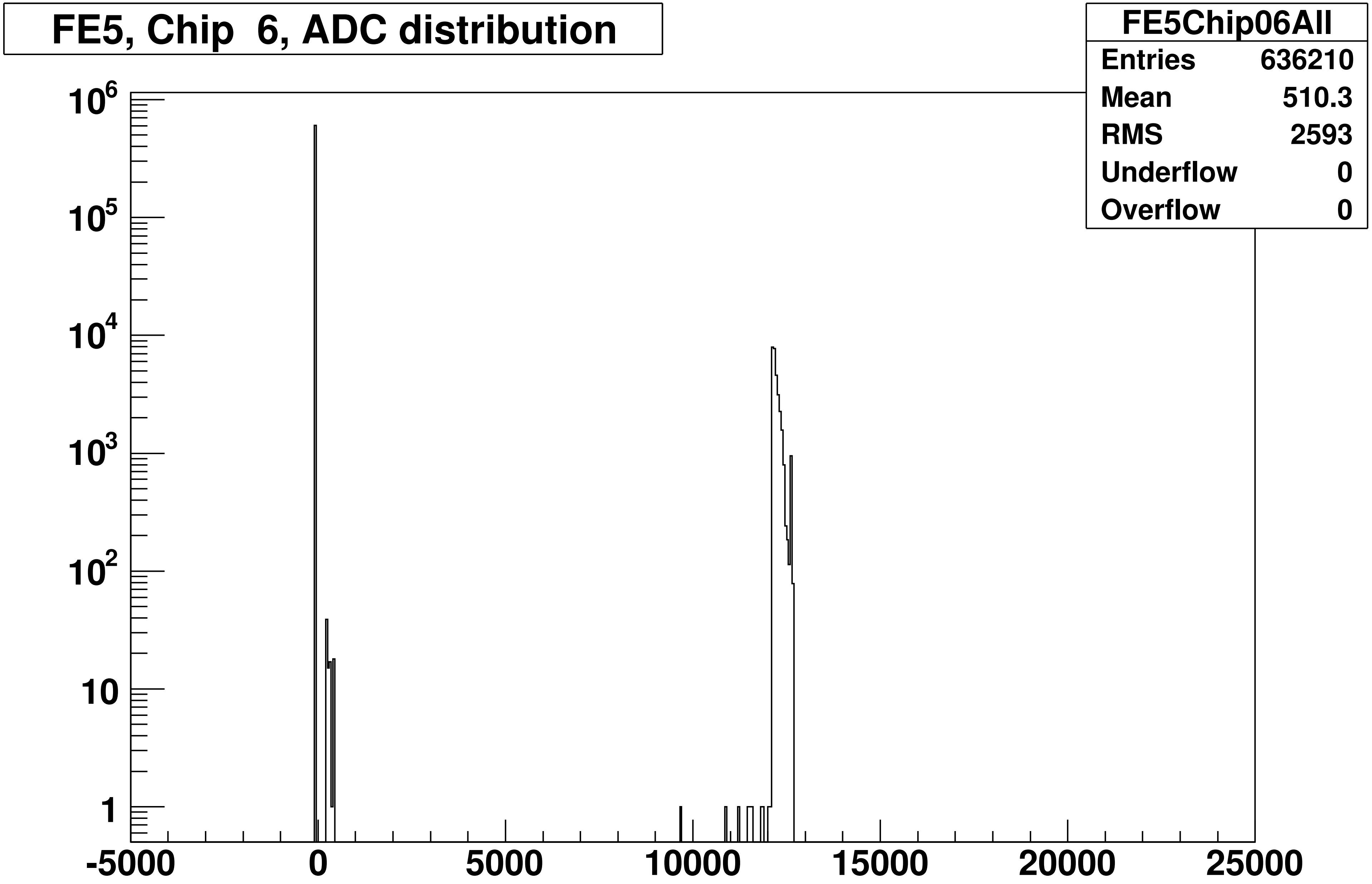
FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 636210
Mean 1.236e+04
RMS 83.15
Underflow 6.087e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

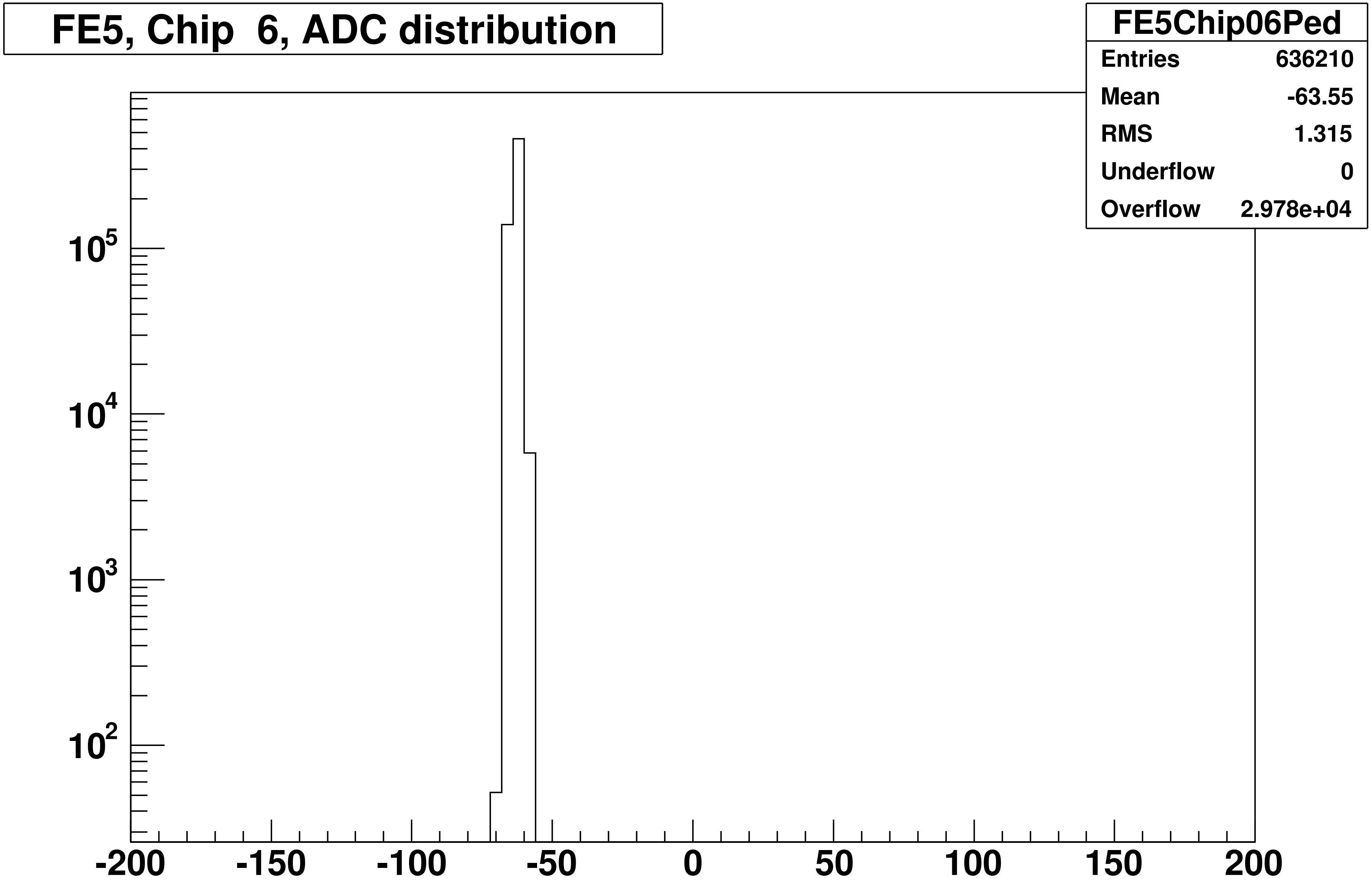


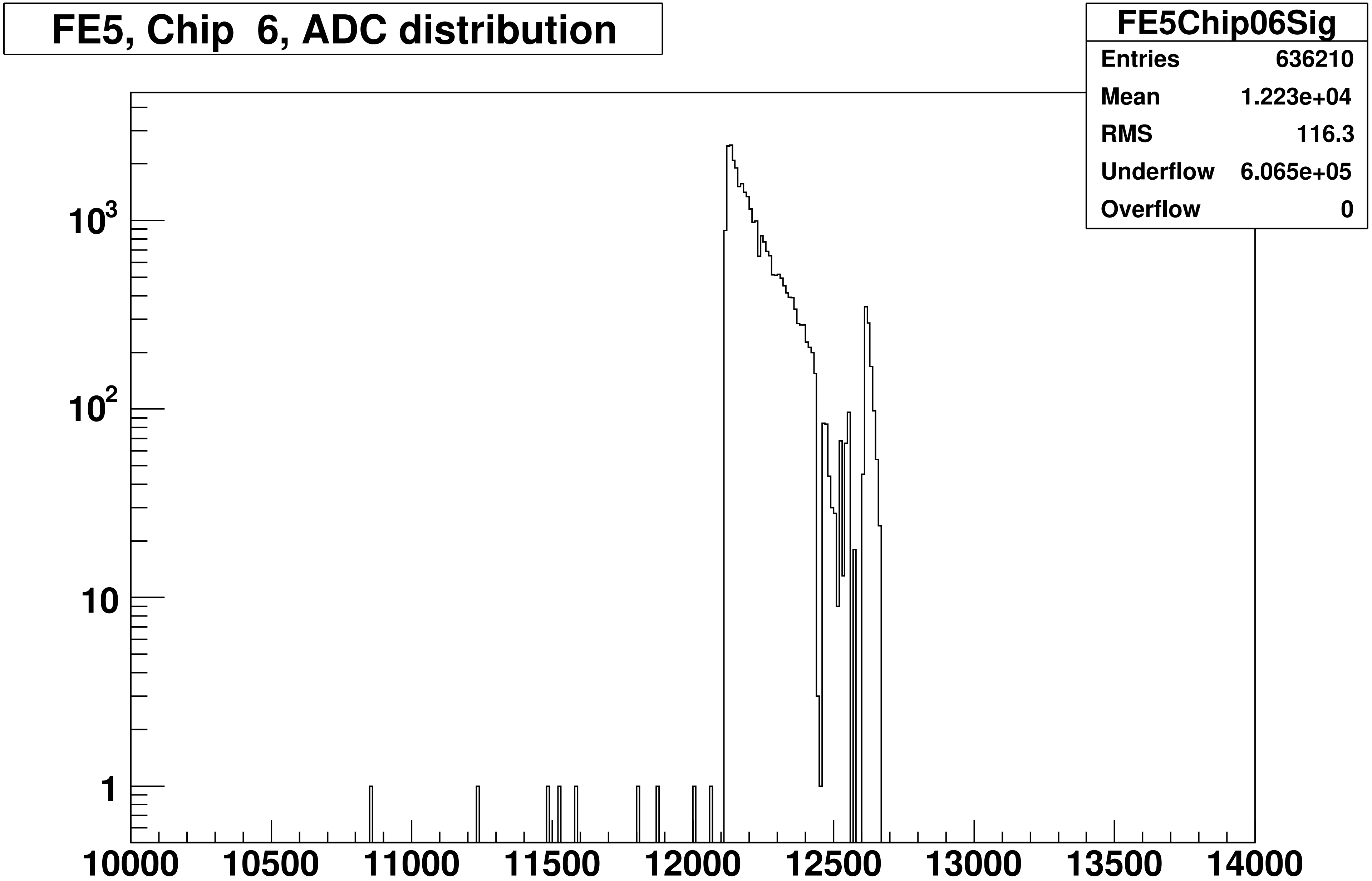
FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 636210
Mean 530.7
RMS 2524
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

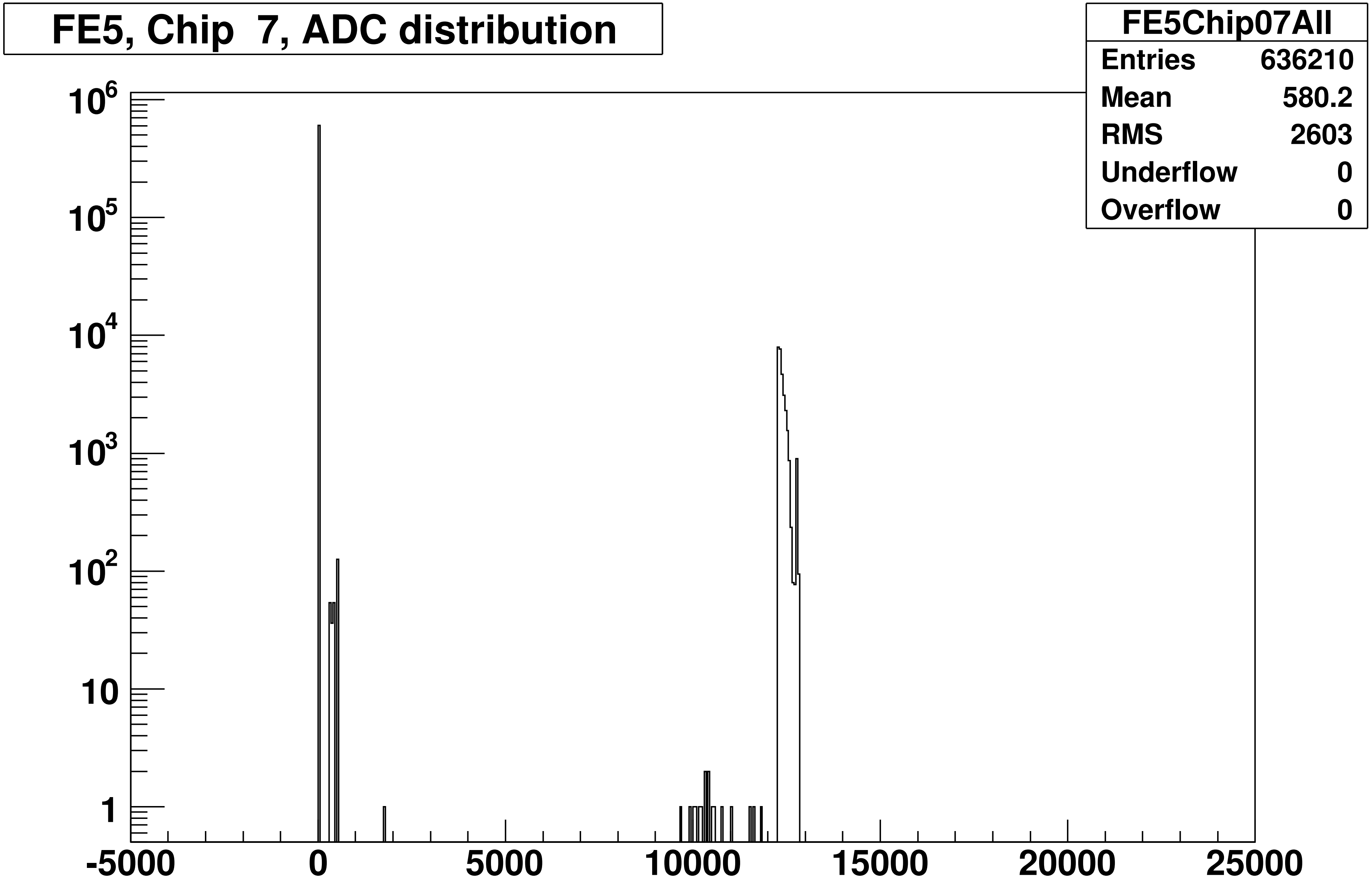

FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 636210
Mean -6.391
RMS 1.345
Underflow 0
Overflow 2.756e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

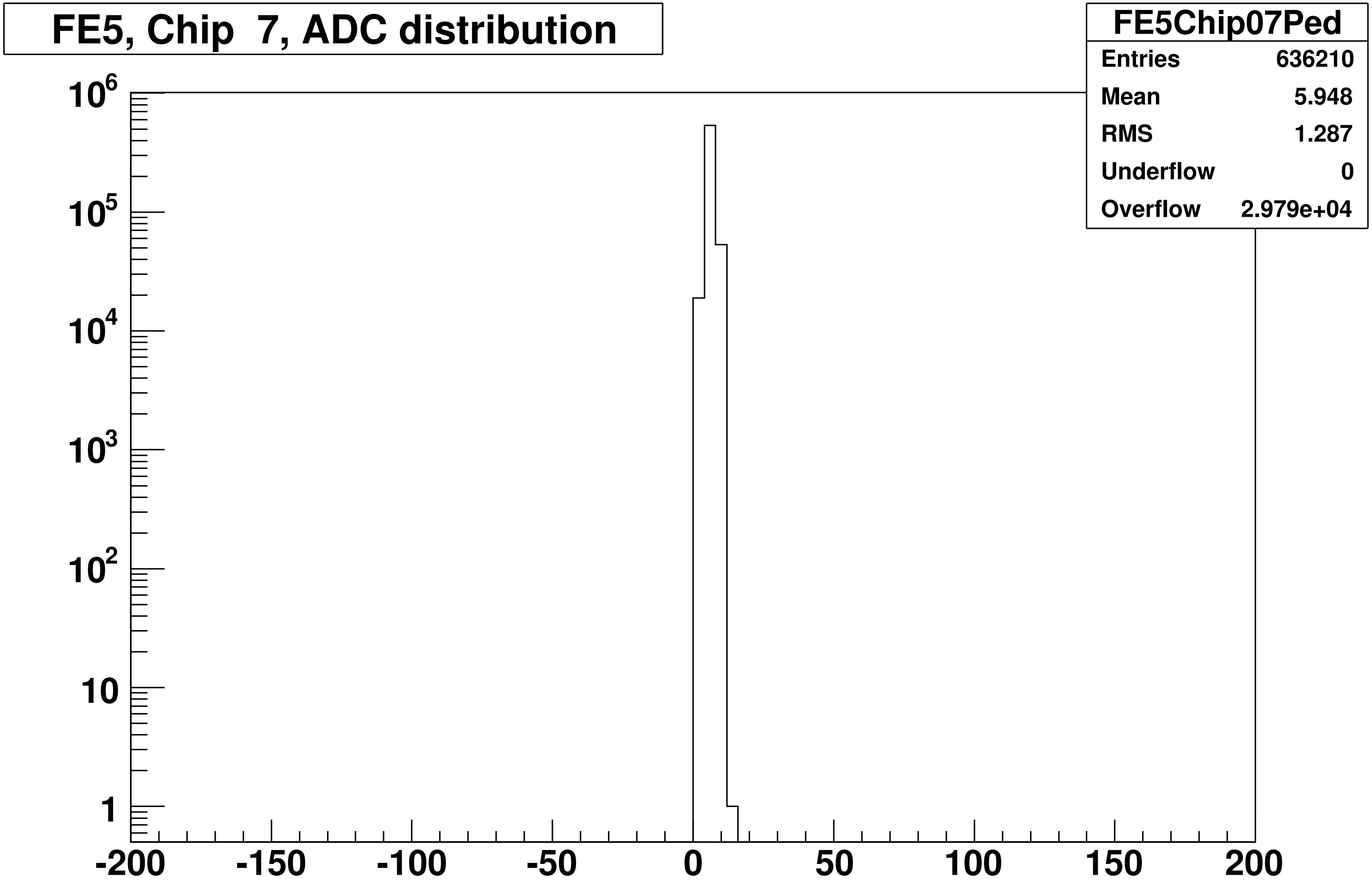

FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 636210
Mean 1.239e+04
RMS 83.76
Underflow 6.087e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

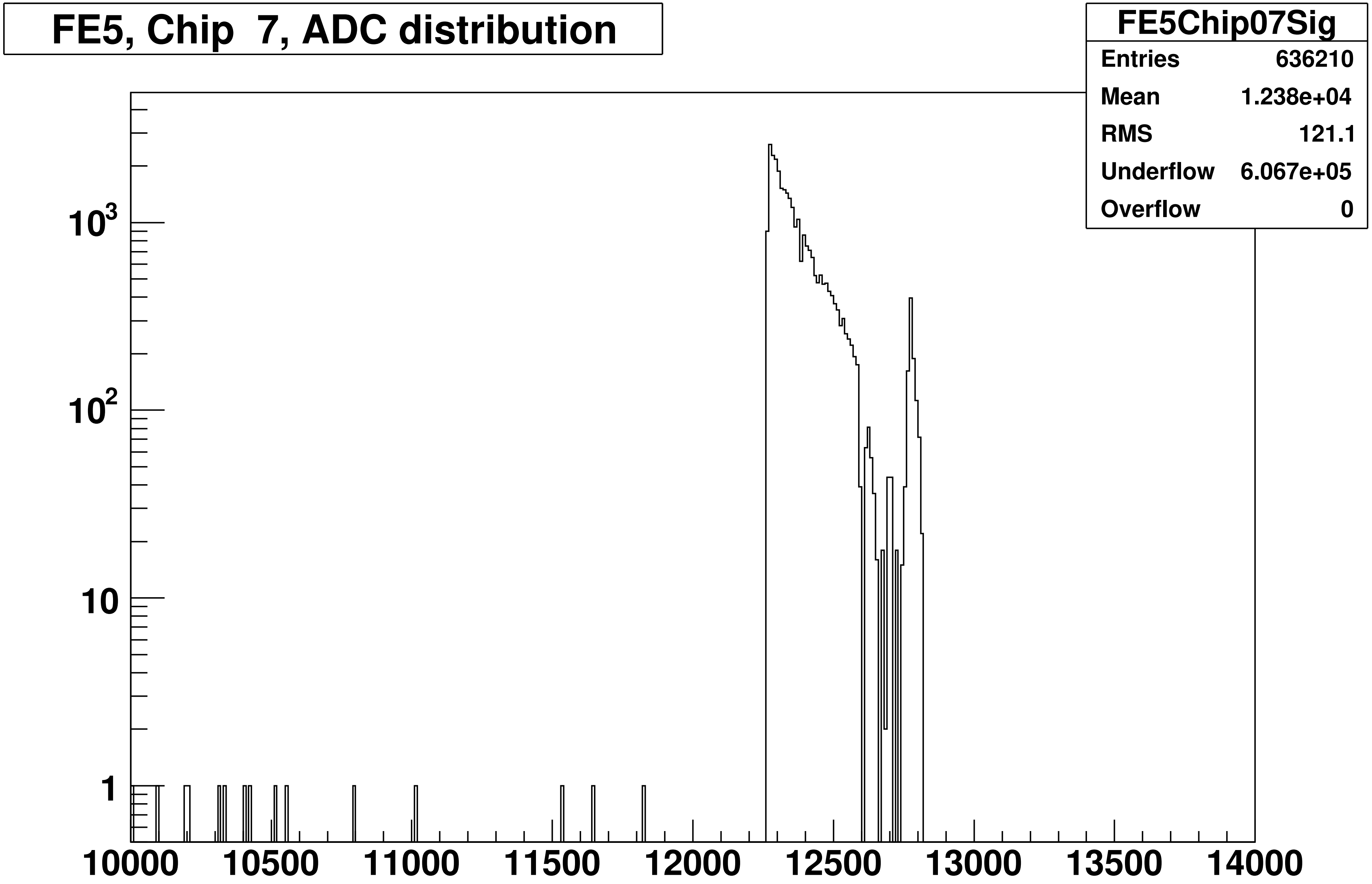

FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 636210
Mean 510.3
RMS 2593
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

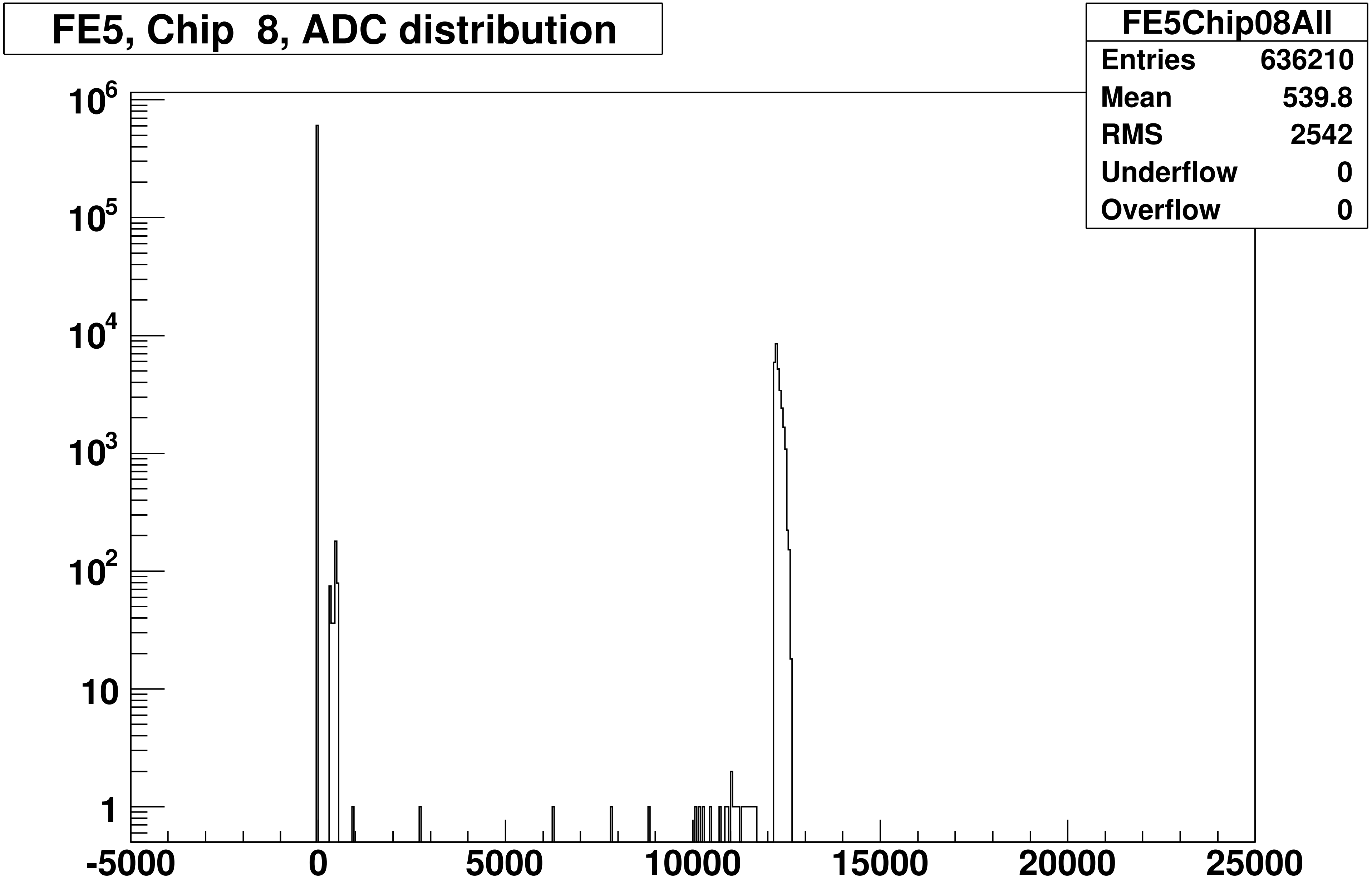

FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 636210
Mean -63.55
RMS 1.315
Underflow 0
Overflow 2.978e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 636210
Mean 1.223e+04
RMS 116.3
Underflow 6.065e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

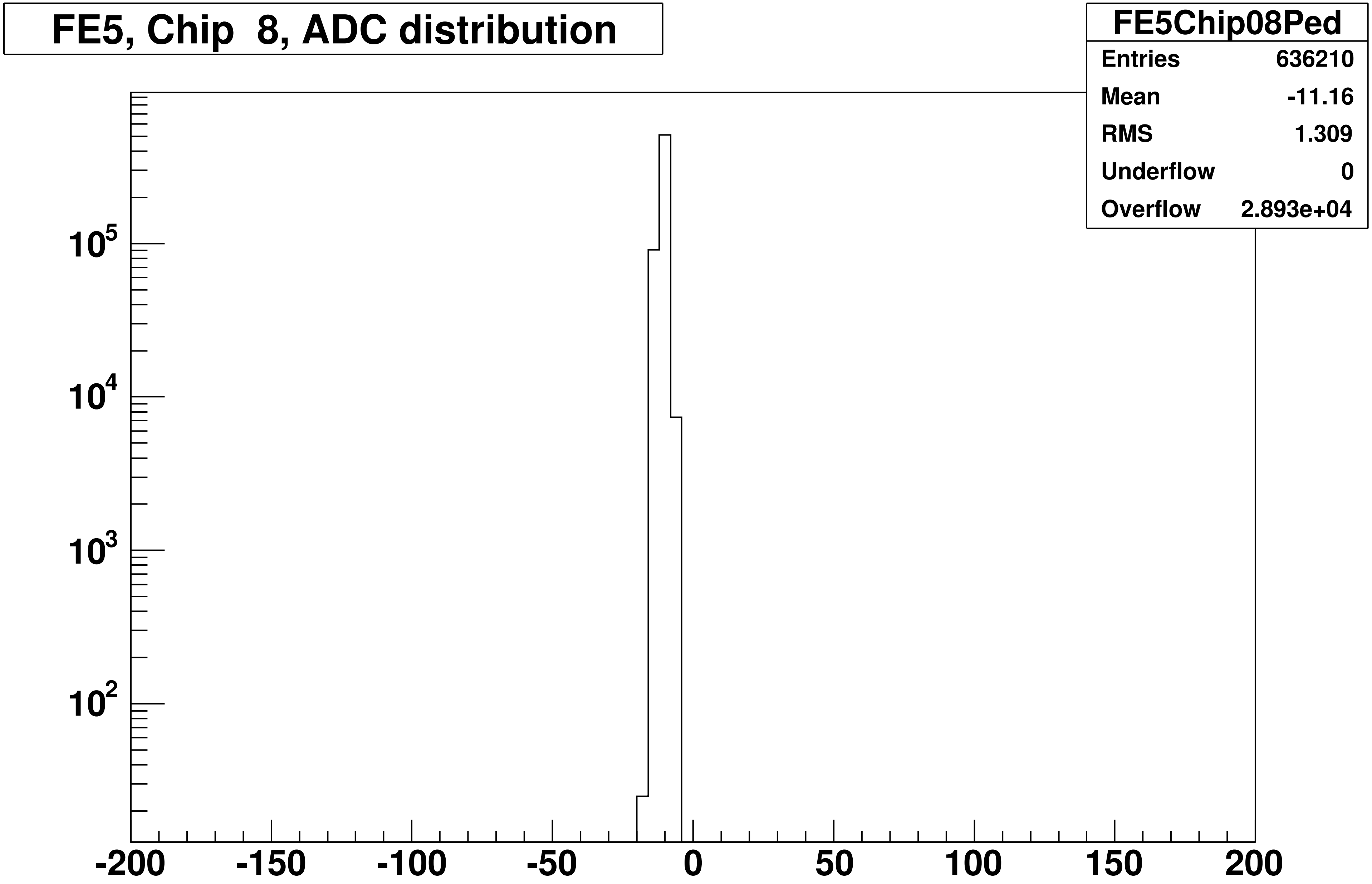

FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 636210
Mean 580.2
RMS 2603
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

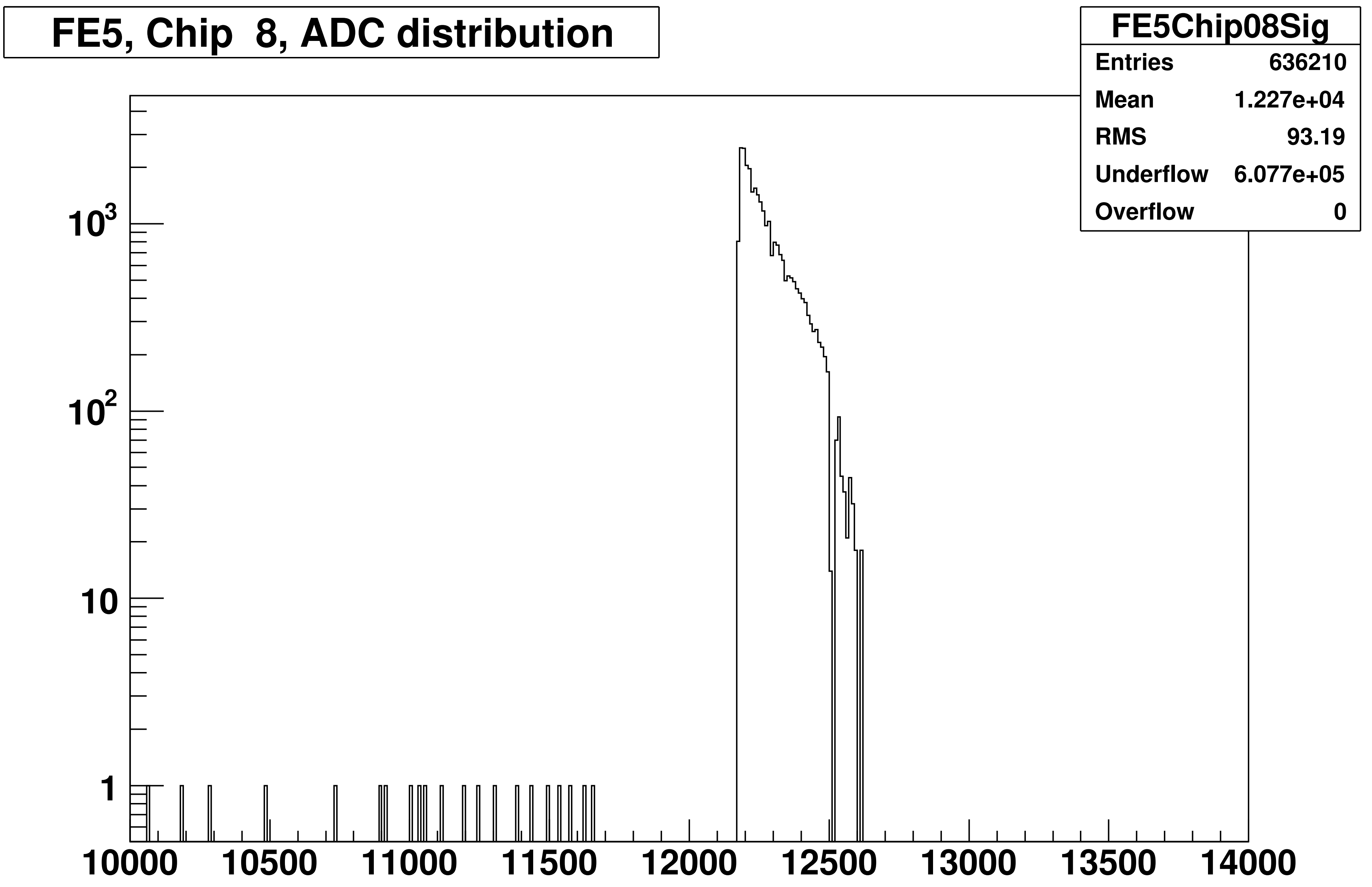

FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 636210
Mean 5.948
RMS 1.287
Underflow 0
Overflow 2.979e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 636210
Mean 1.238e+04
RMS 121.1
Underflow 6.067e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

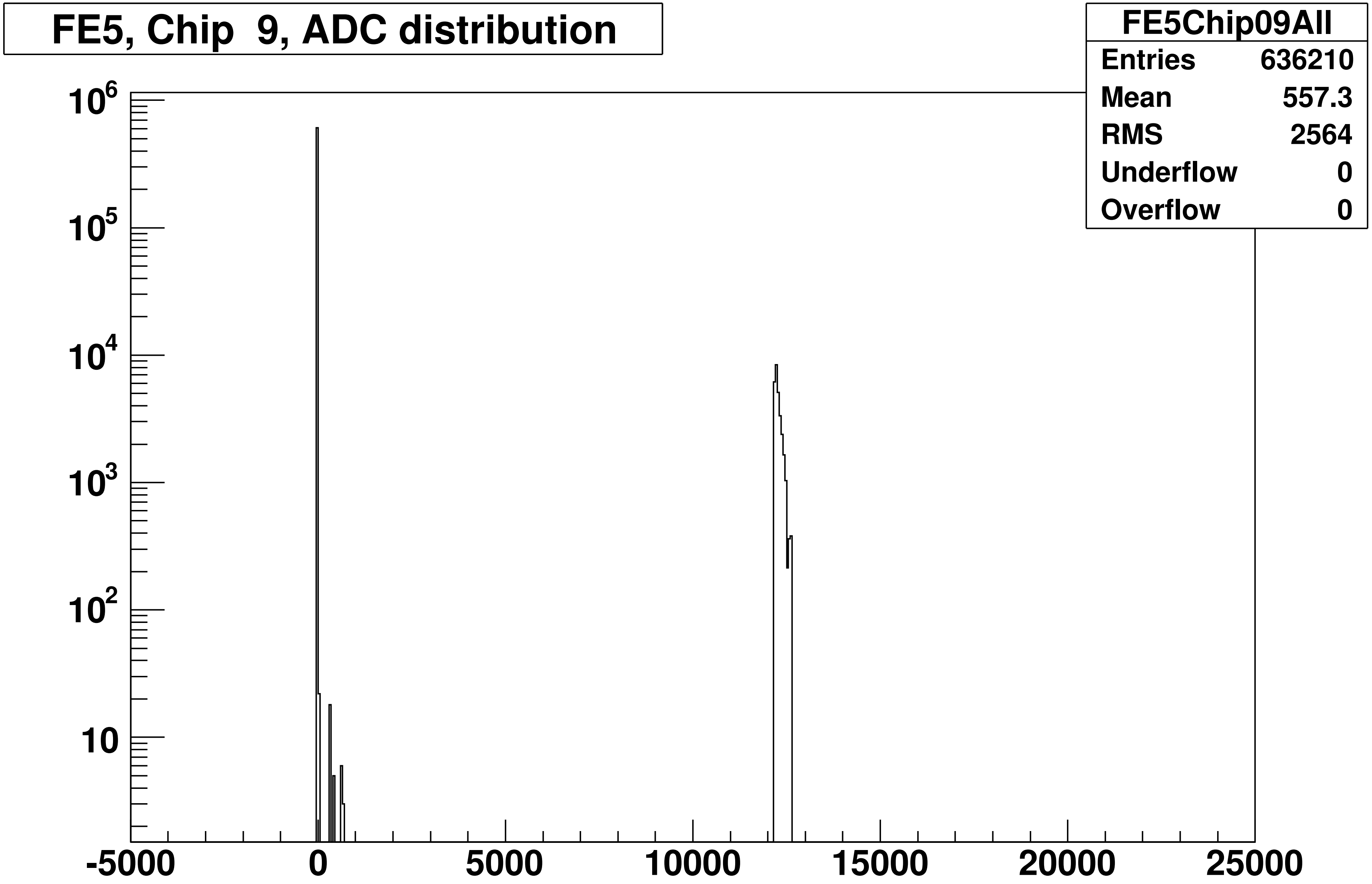


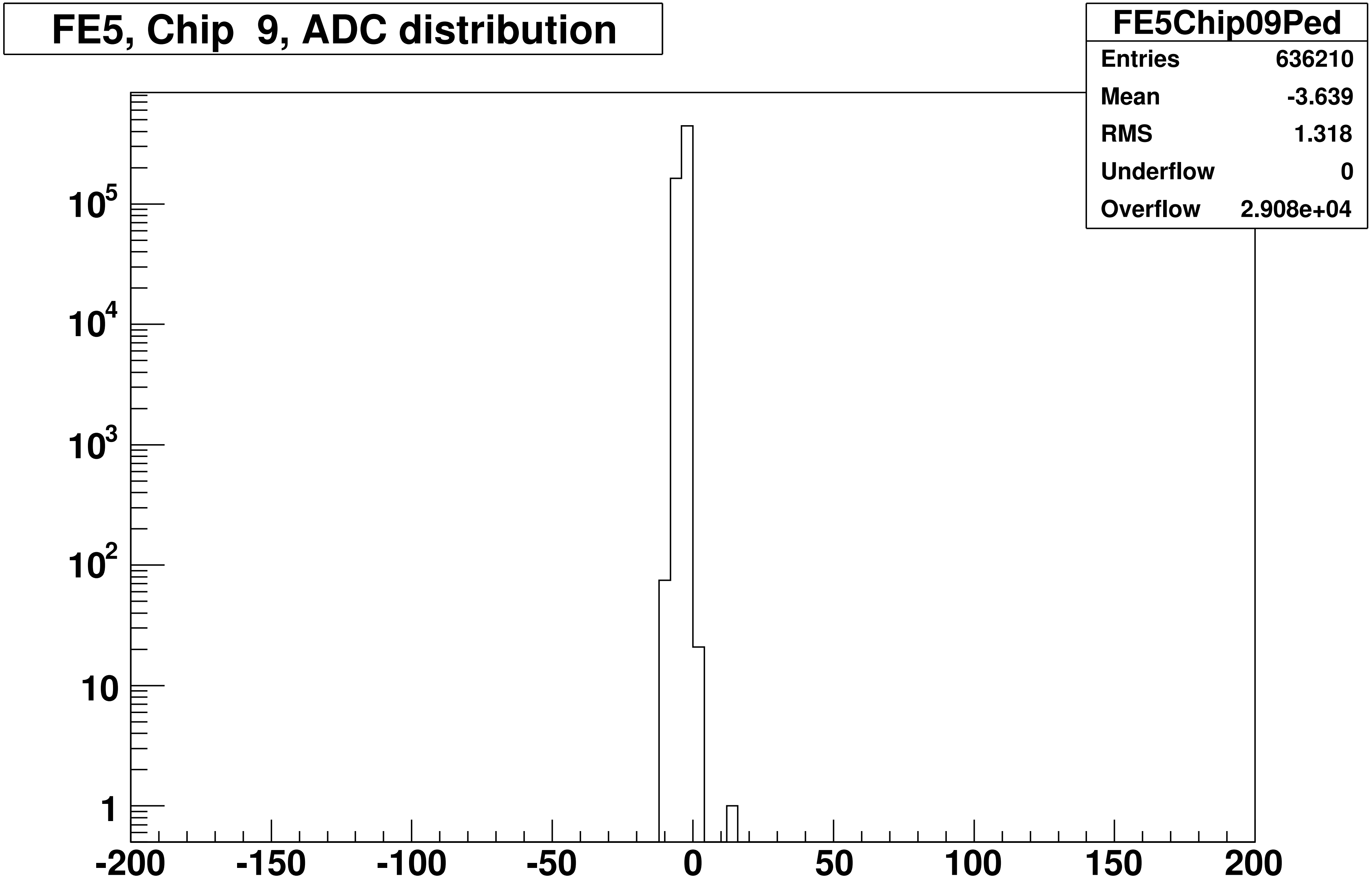
FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 636210
Mean 539.8
RMS 2542
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

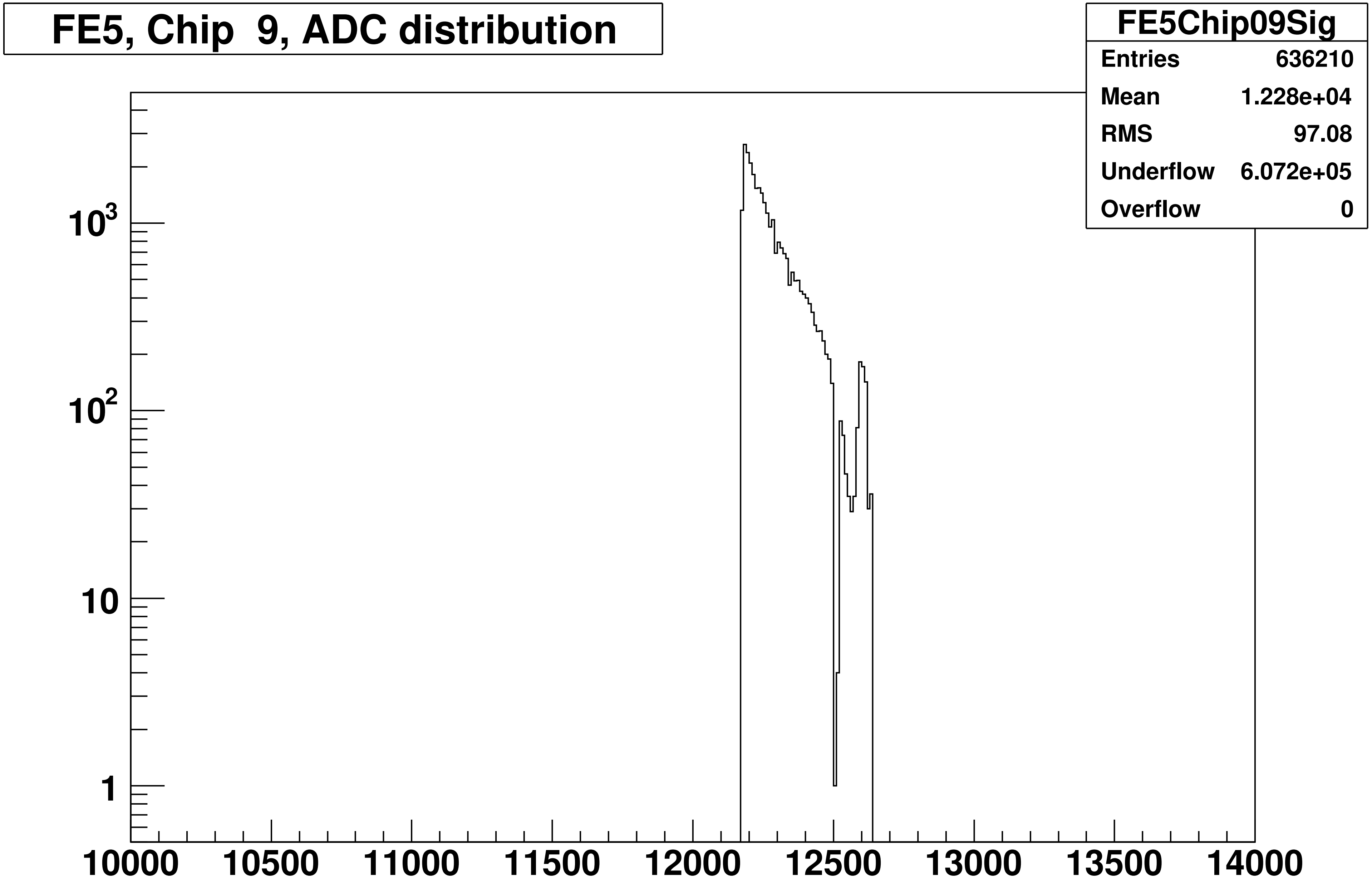

FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 636210
Mean -11.16
RMS 1.309
Underflow 0
Overflow 2.893e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



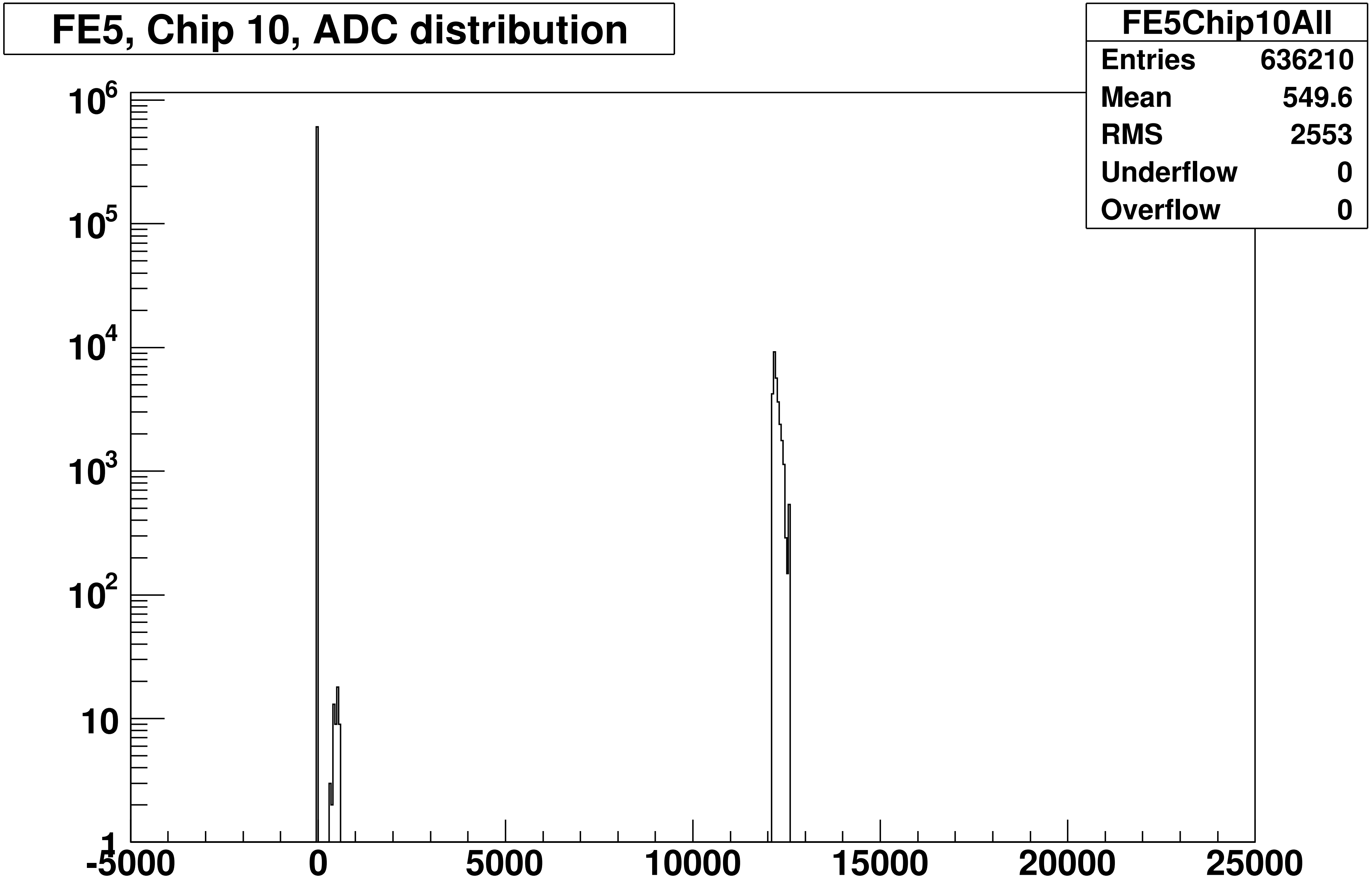
FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 636210
Mean 1.227e+04
RMS 93.19
Underflow 6.077e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 636210
Mean 557.3
RMS 2564
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

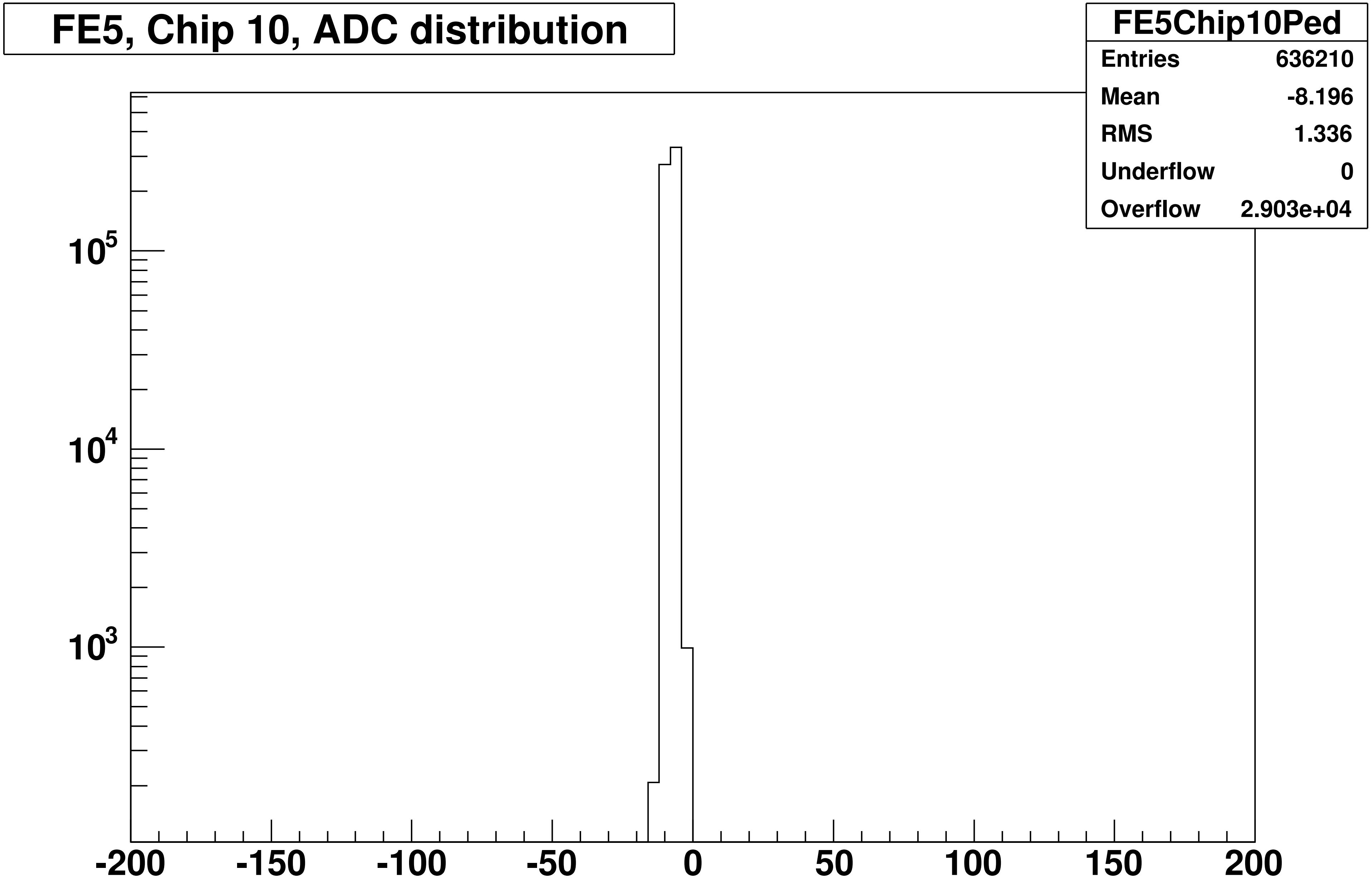

FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 636210
Mean -3.639
RMS 1.318
Underflow 0
Overflow 2.908e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



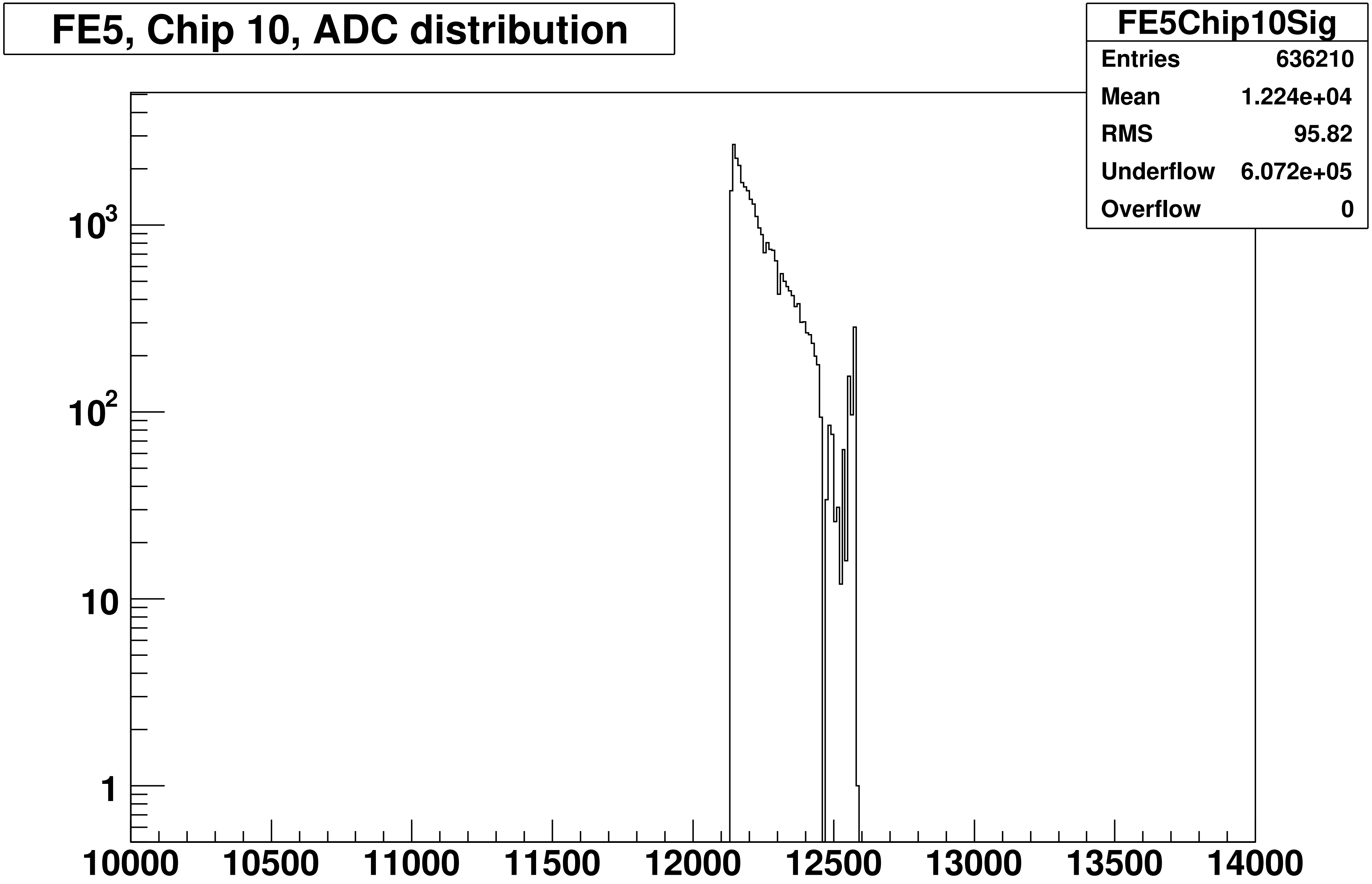
FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 636210
Mean 1.228e+04
RMS 97.08
Underflow 6.072e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

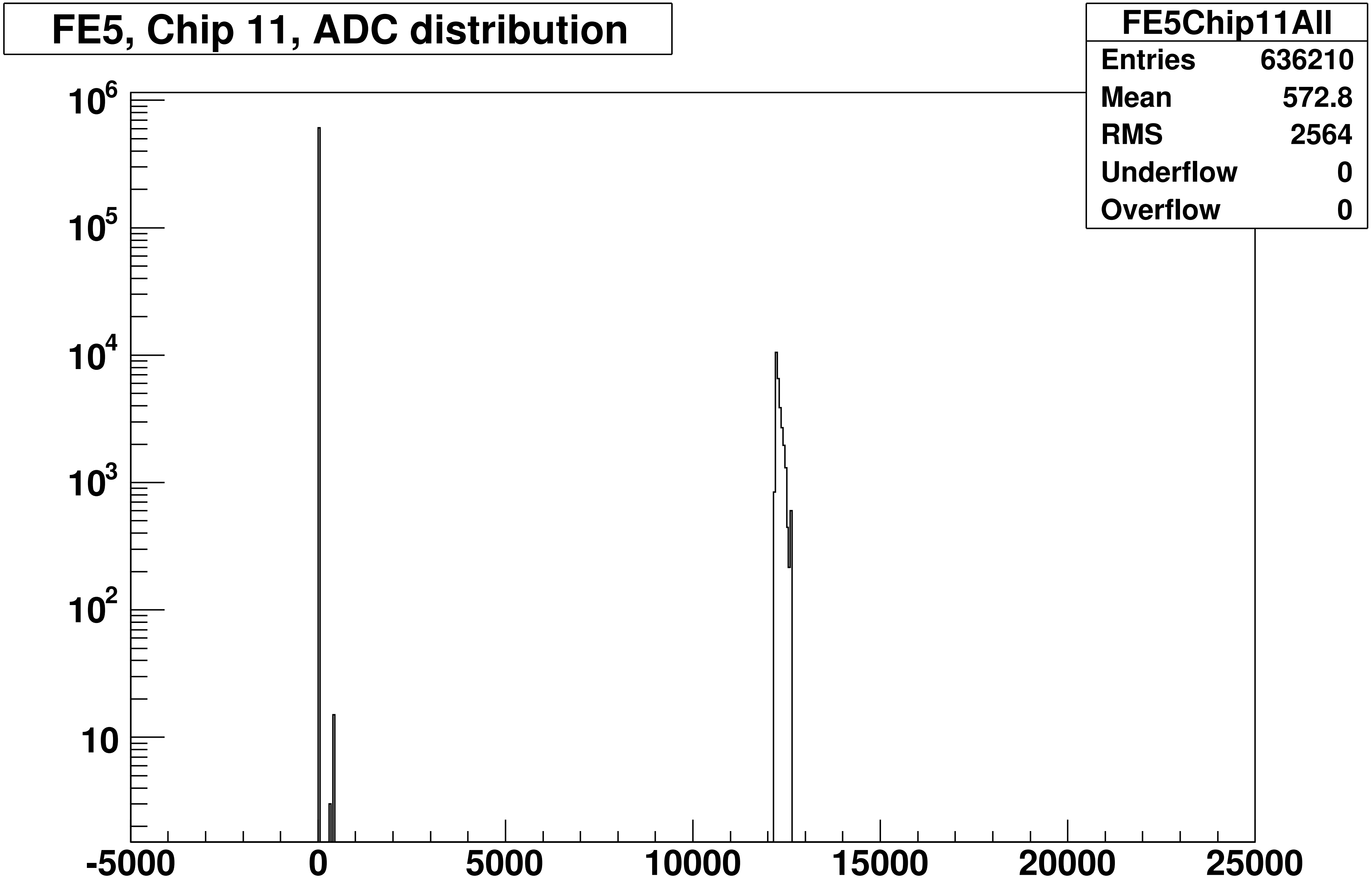


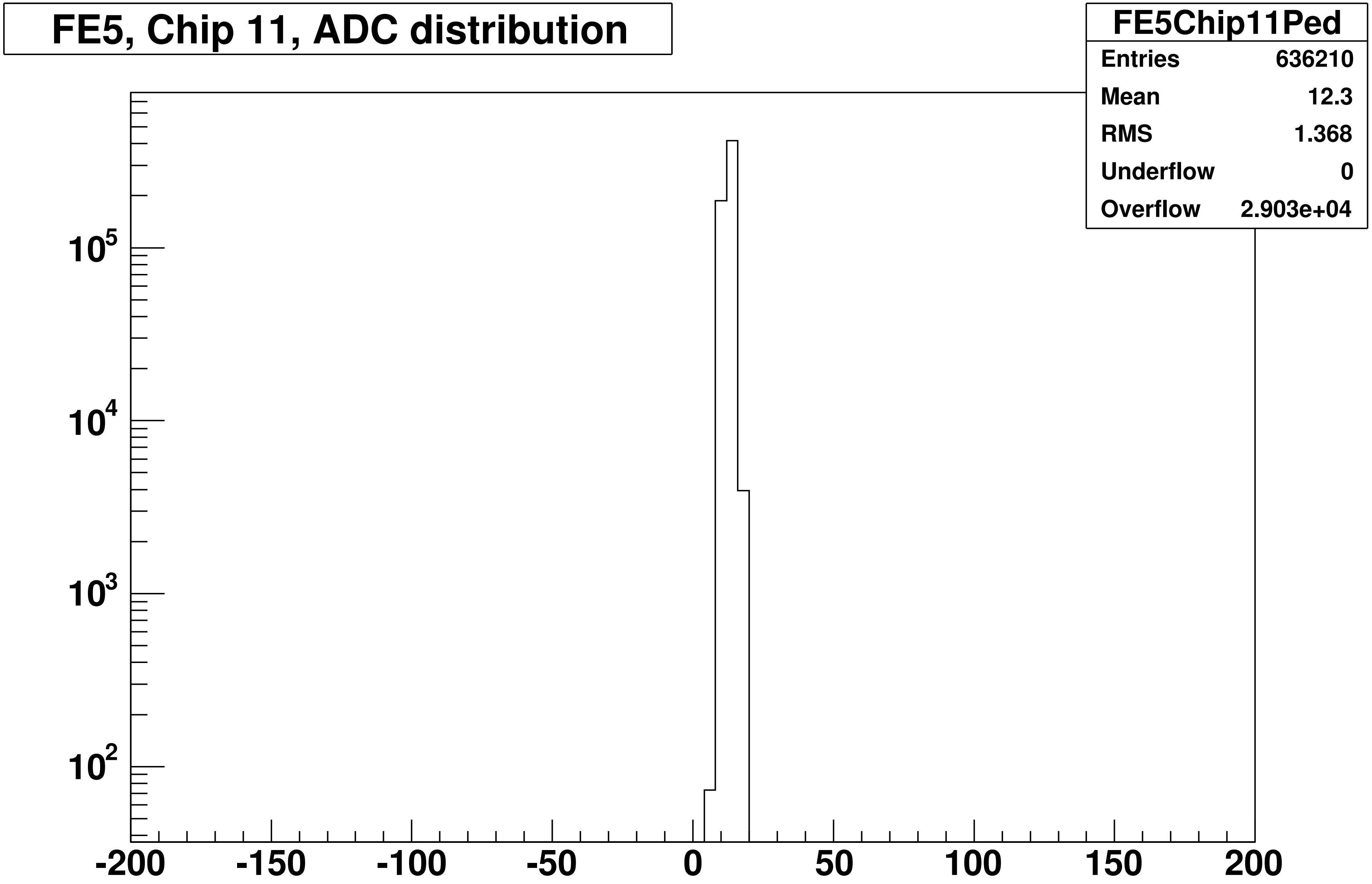
FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 636210
Mean 549.6
RMS 2553
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



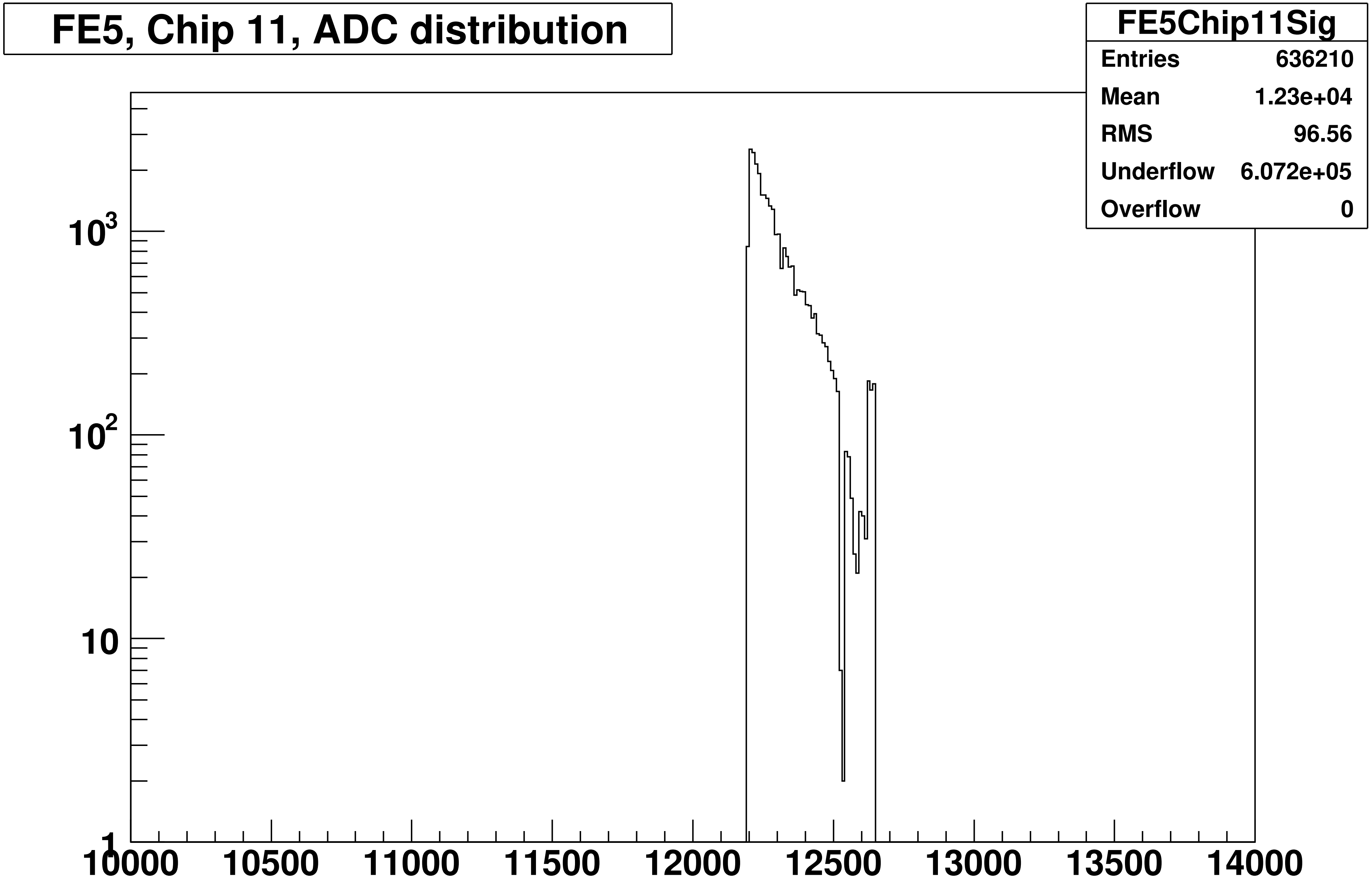
FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 636210
Mean -8.196
RMS 1.336
Underflow 0
Overflow 2.903e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

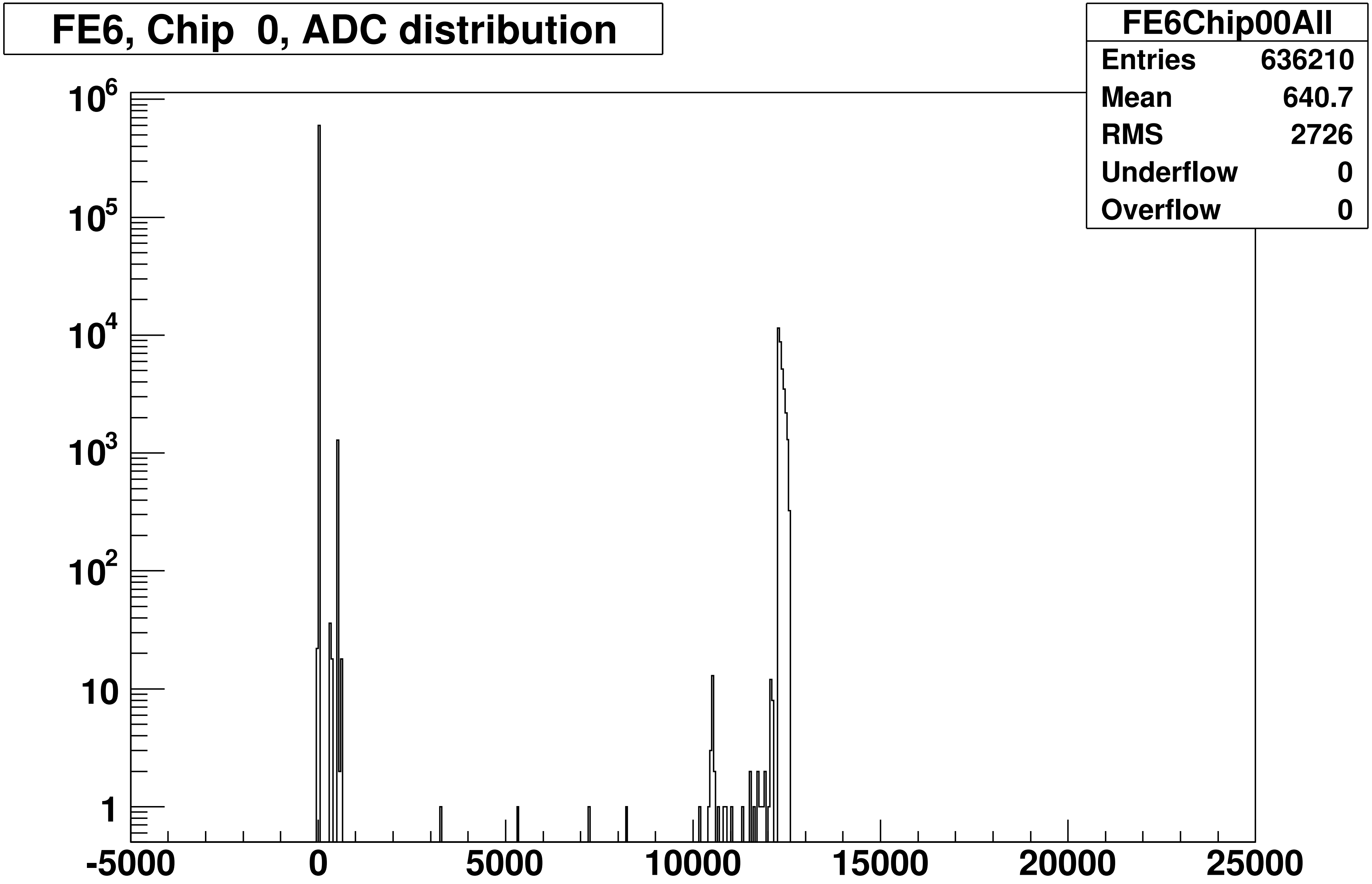

FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 636210
Mean 1.224e+04
RMS 95.82
Underflow 6.072e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

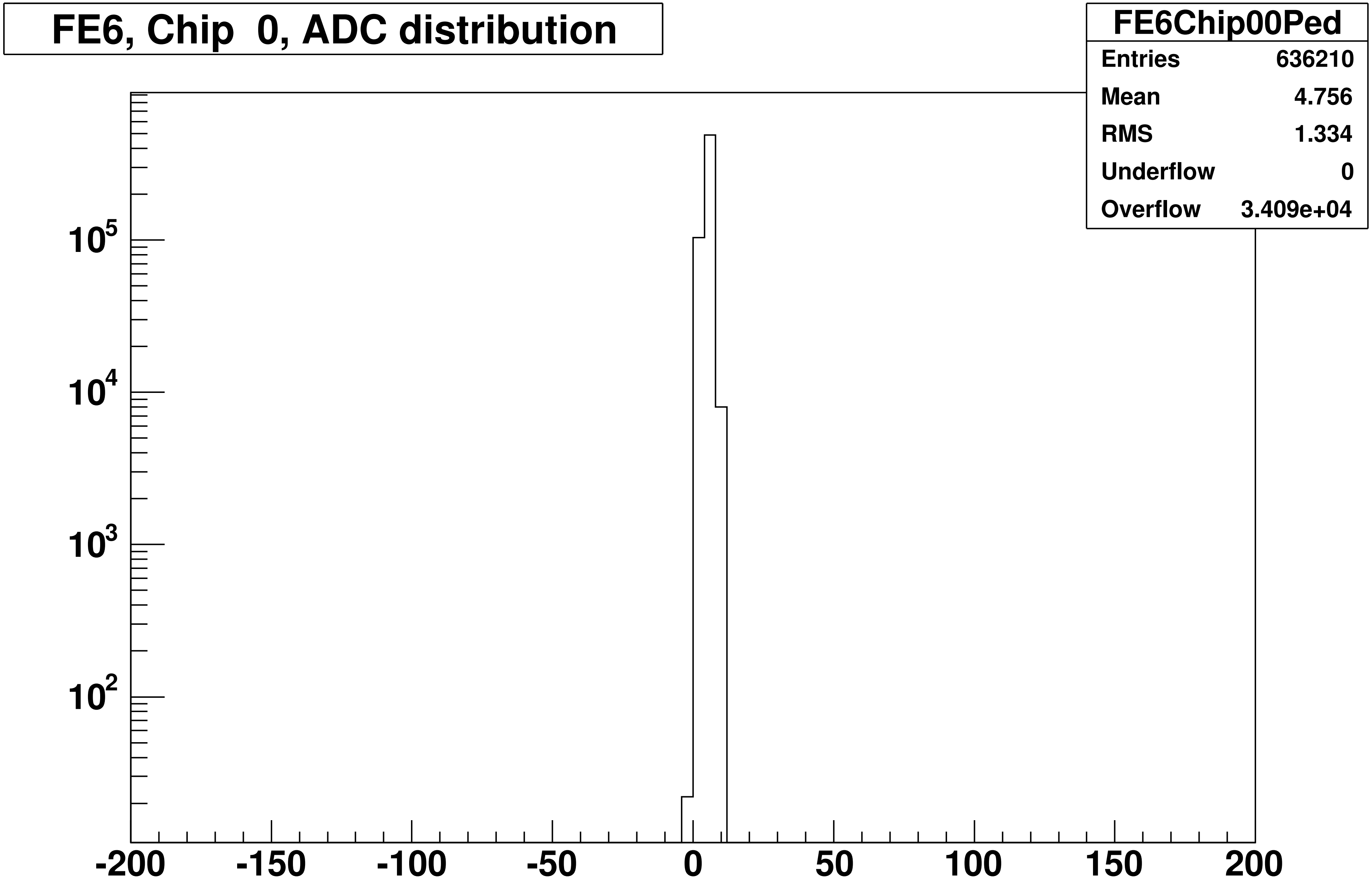

FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 636210
Mean 572.8
RMS 2564
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

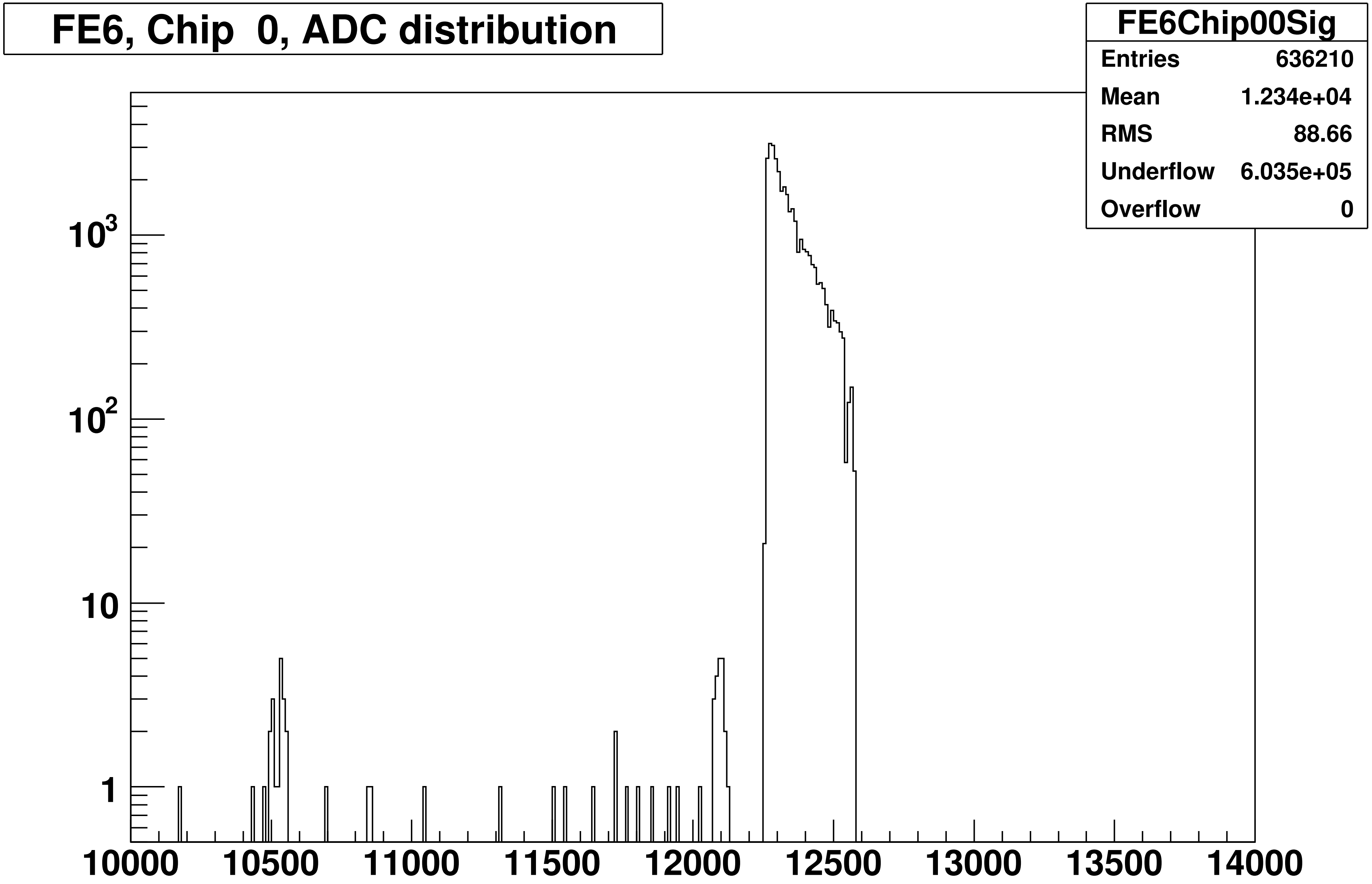


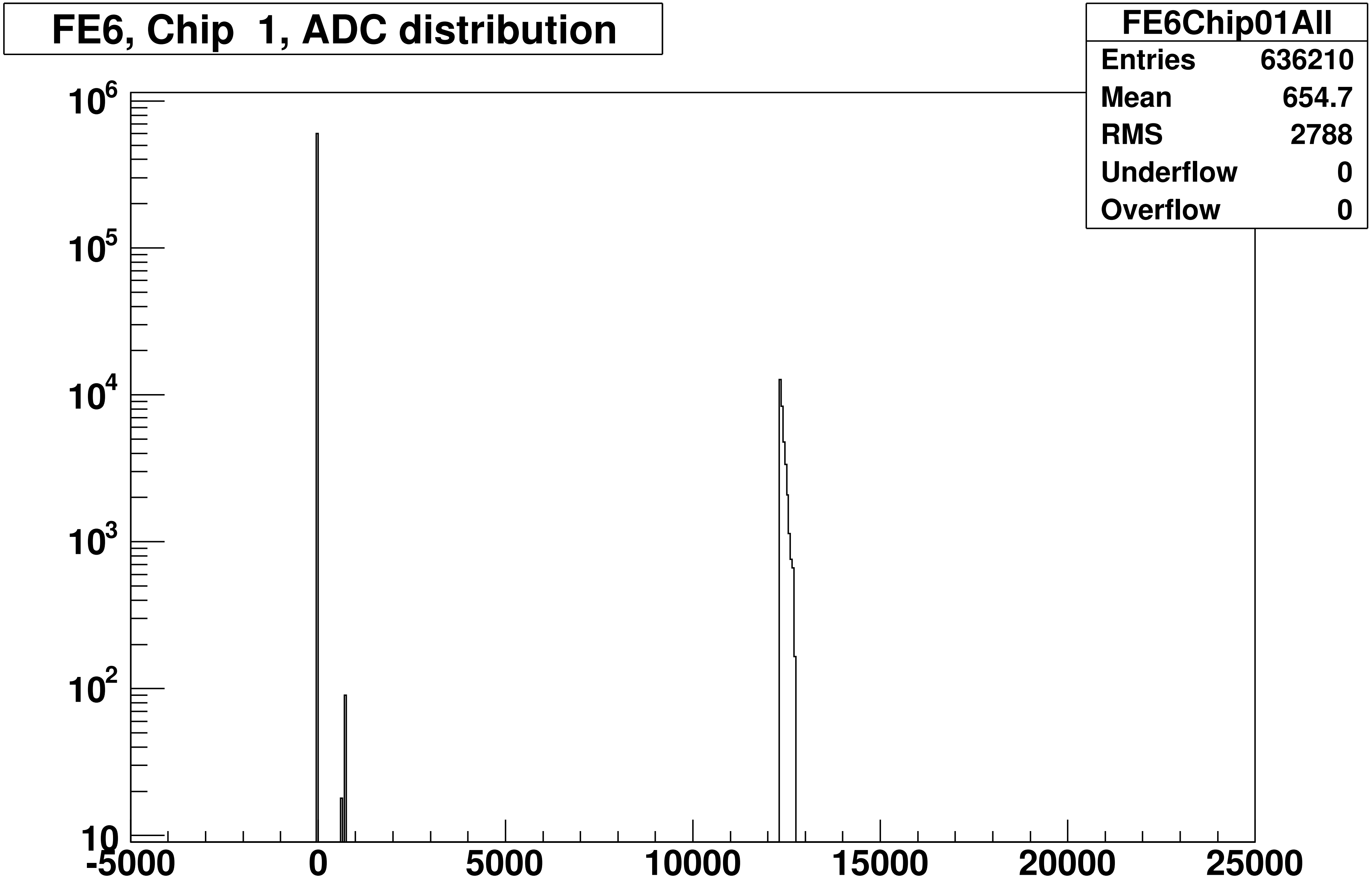
FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 636210
Mean 12.3
RMS 1.368
Underflow 0
Overflow 2.903e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 636210
Mean 1.23e+04
RMS 96.56
Underflow 6.072e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

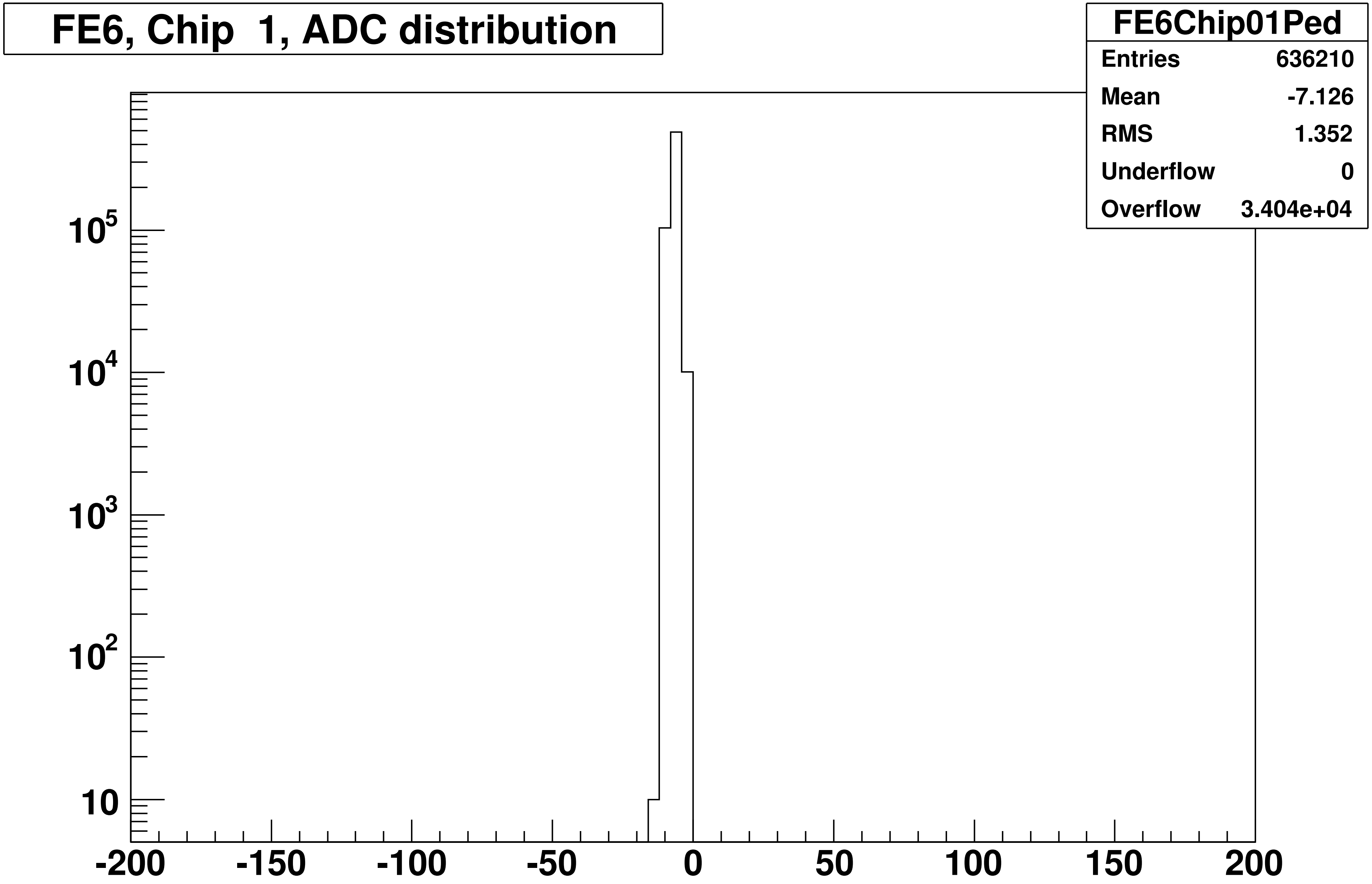

FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 636210
Mean 640.7
RMS 2726
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 636210
Mean 4.756
RMS 1.334
Underflow 0
Overflow 3.409e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

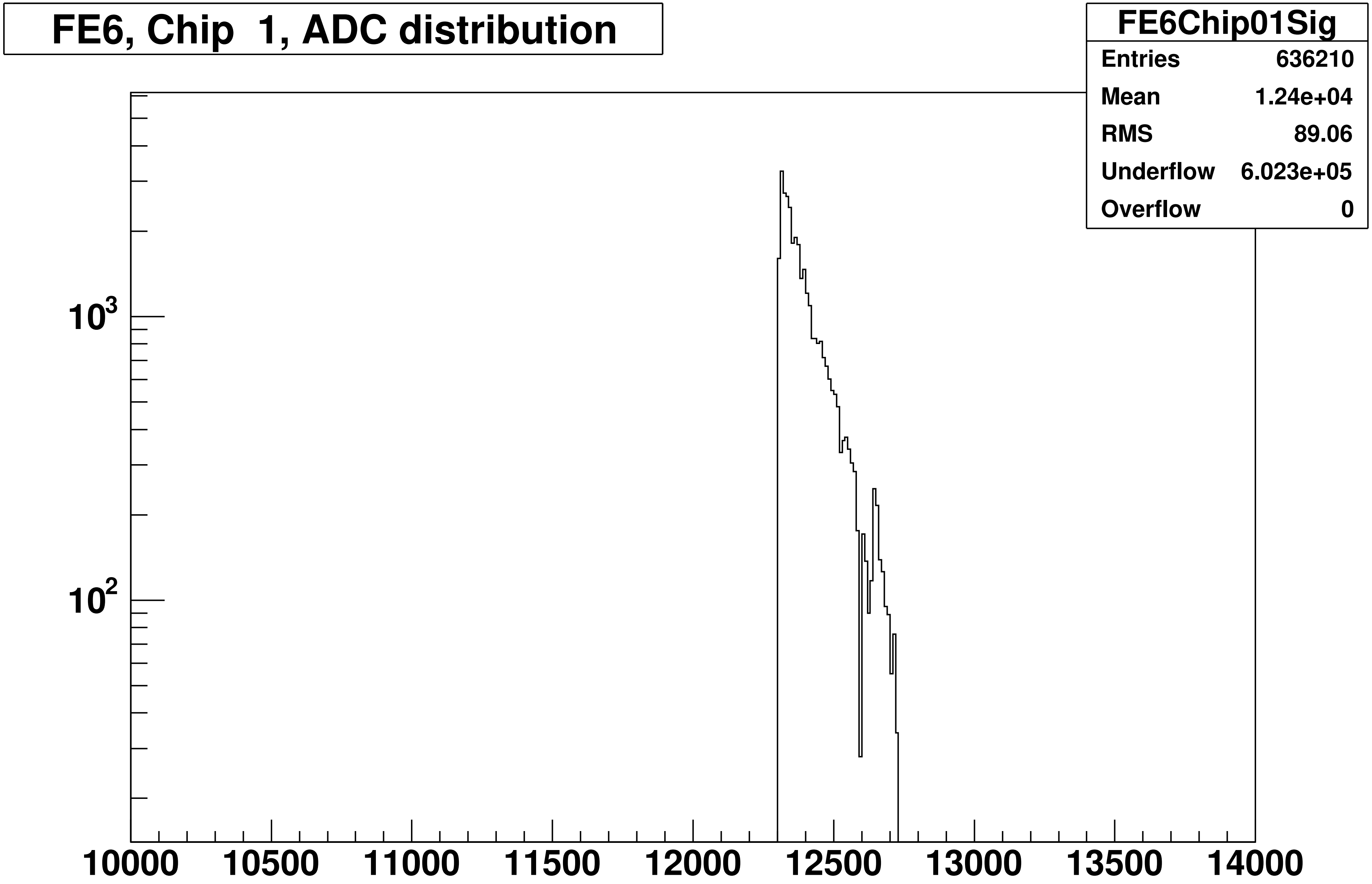

FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 636210
Mean 1.234e+04
RMS 88.66
Underflow 6.035e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000



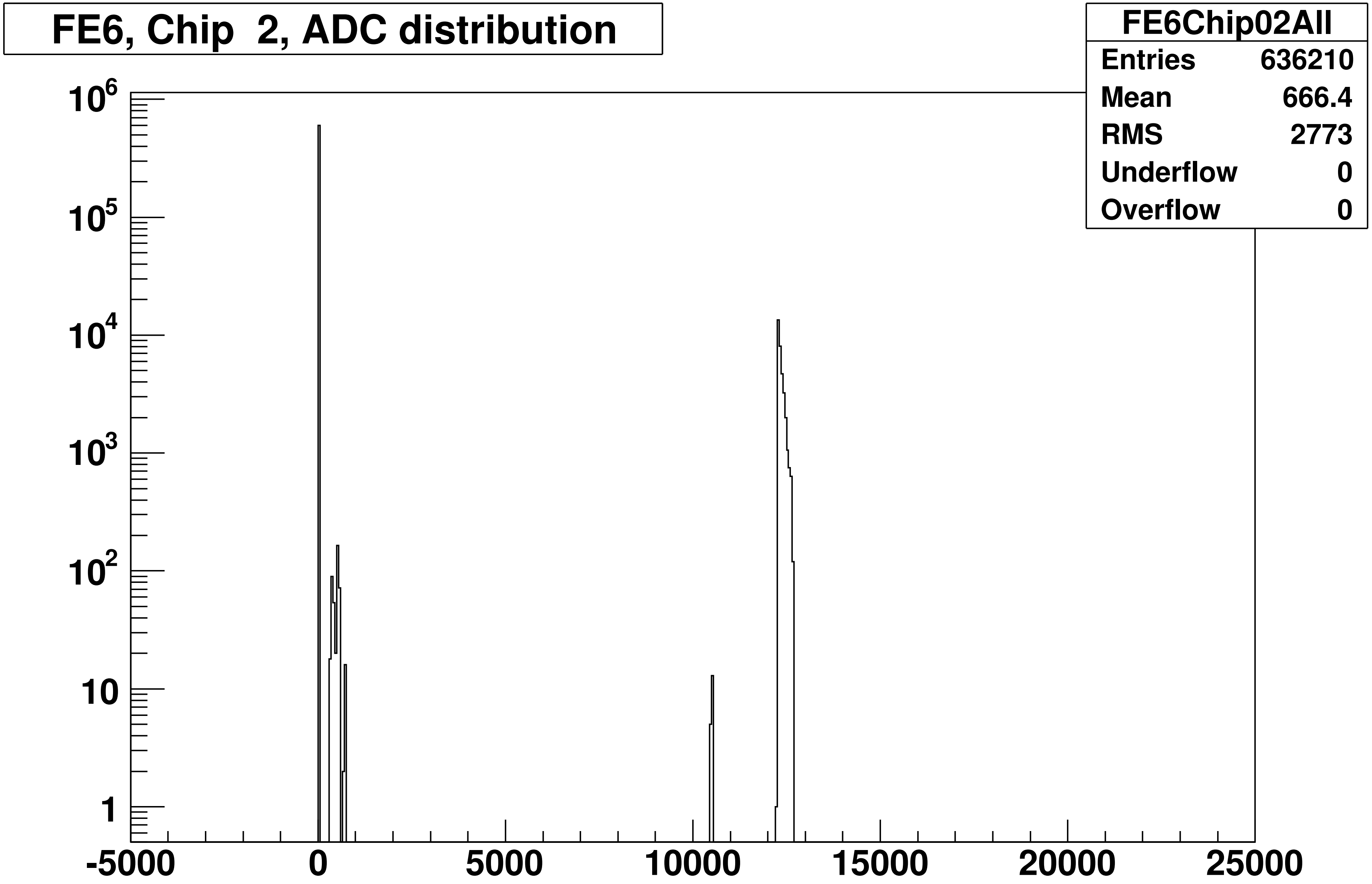
FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 636210
Mean 654.7
RMS 2788
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

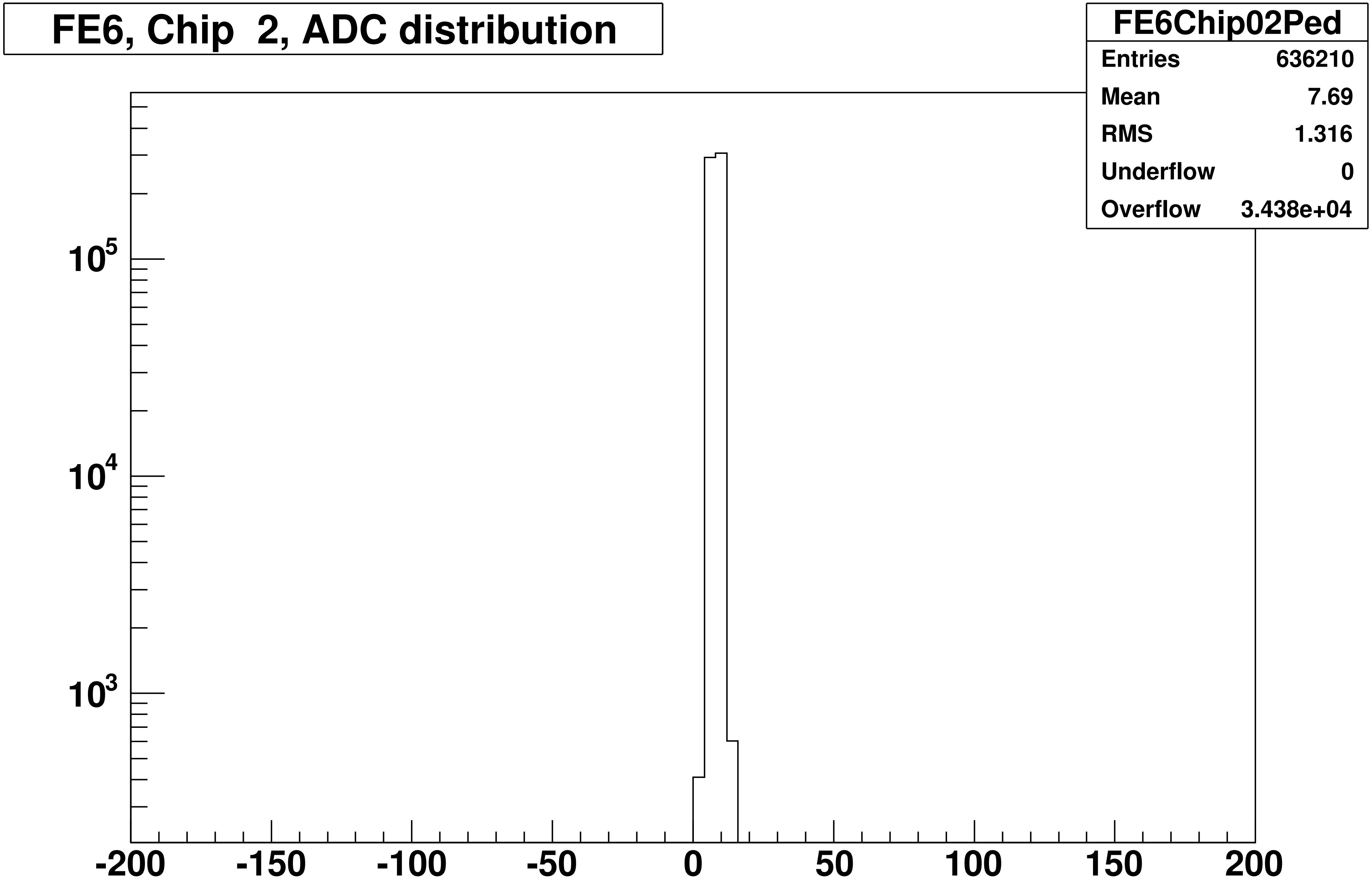


FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 636210
Mean -7.126
RMS 1.352
Underflow 0
Overflow 3.404e+04
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200

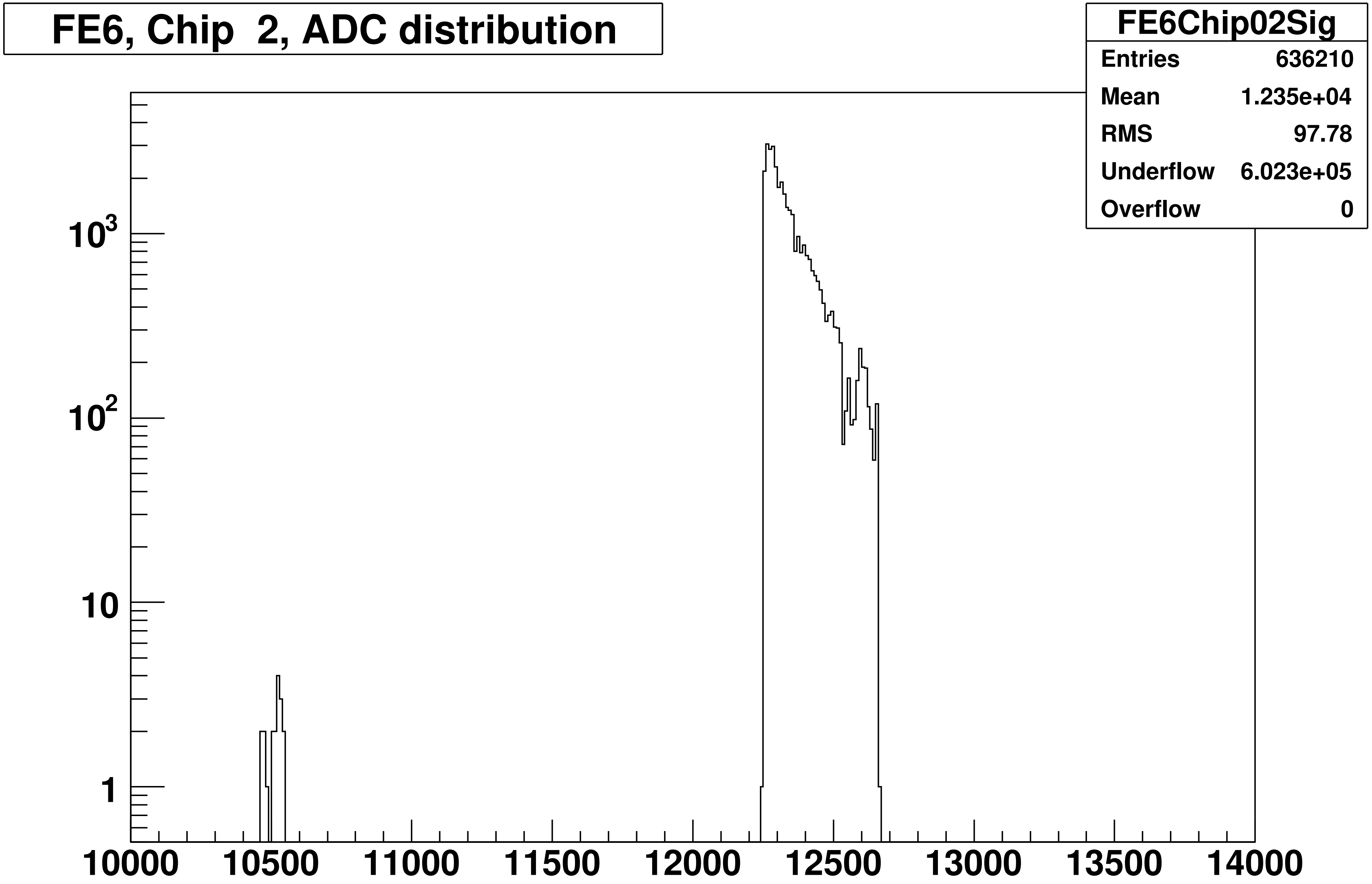


FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 636210
Mean 1.24e+04
RMS 89.06
Underflow 6.023e+05
Overflow 0
10^3
10^2
10000
10500
11000
11500
12000
12500
13000
13500
14000

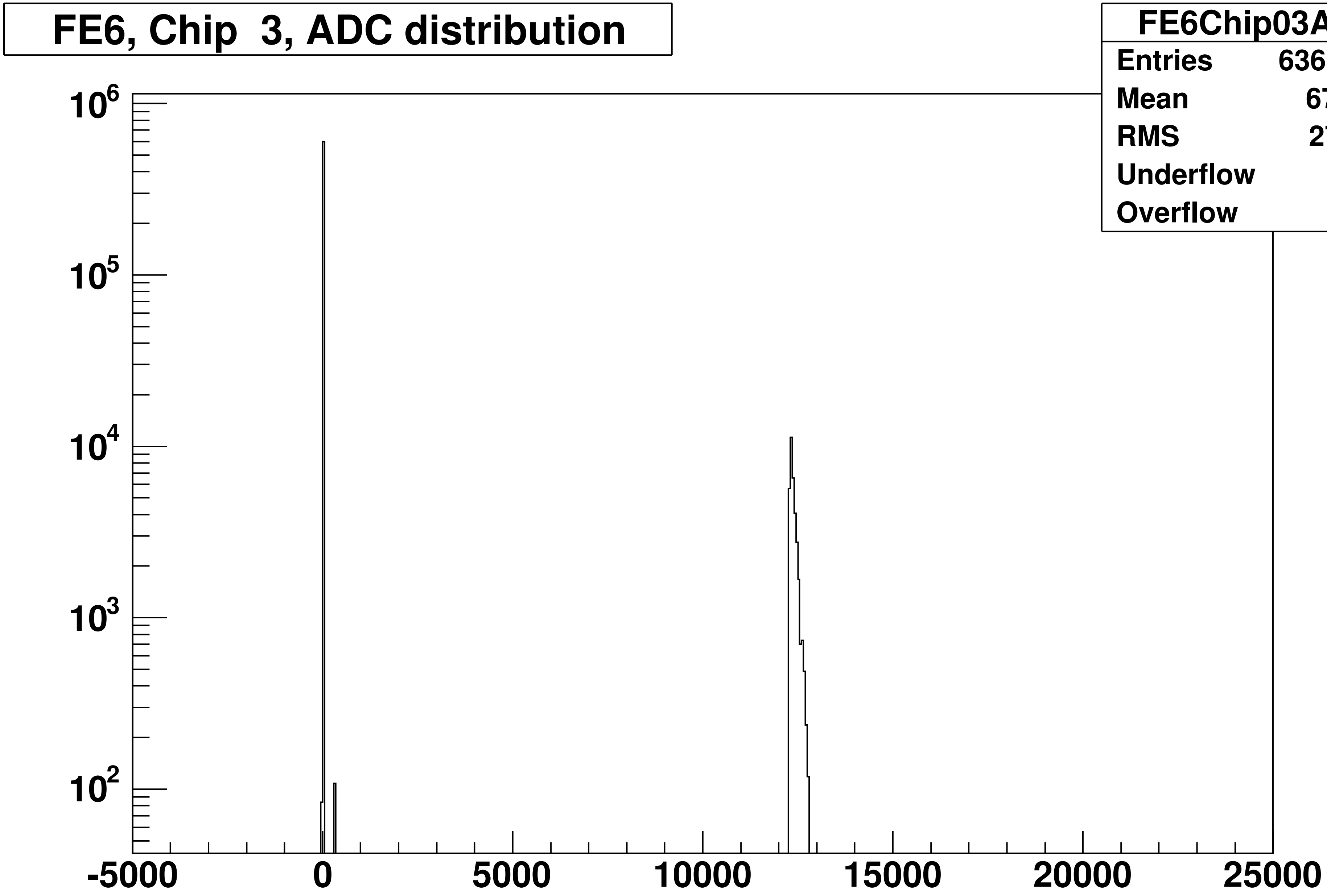

FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 636210
Mean 666.4
RMS 2773
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

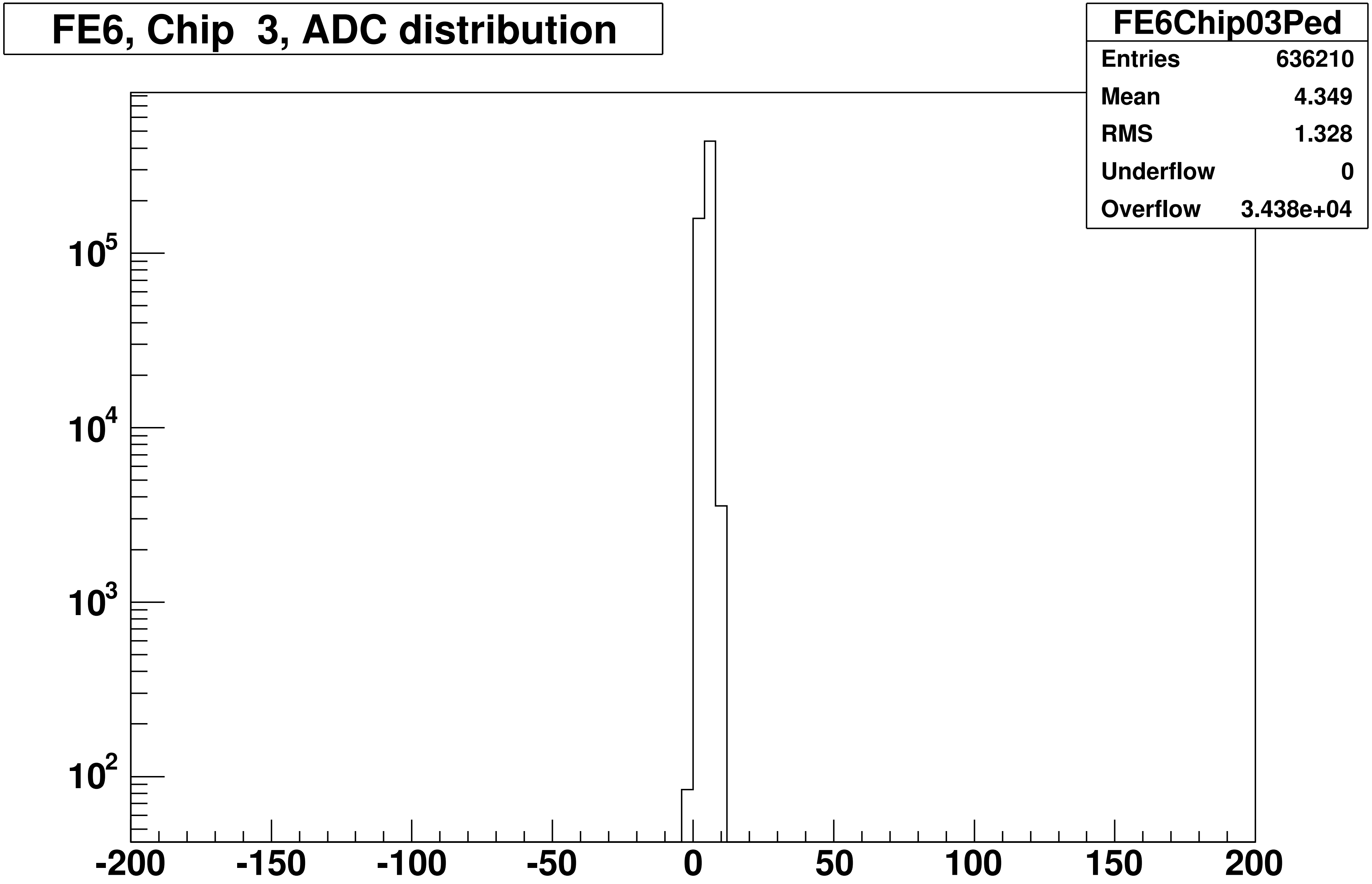


FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 636210
Mean 7.69
RMS 1.316
Underflow 0
Overflow 3.438e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

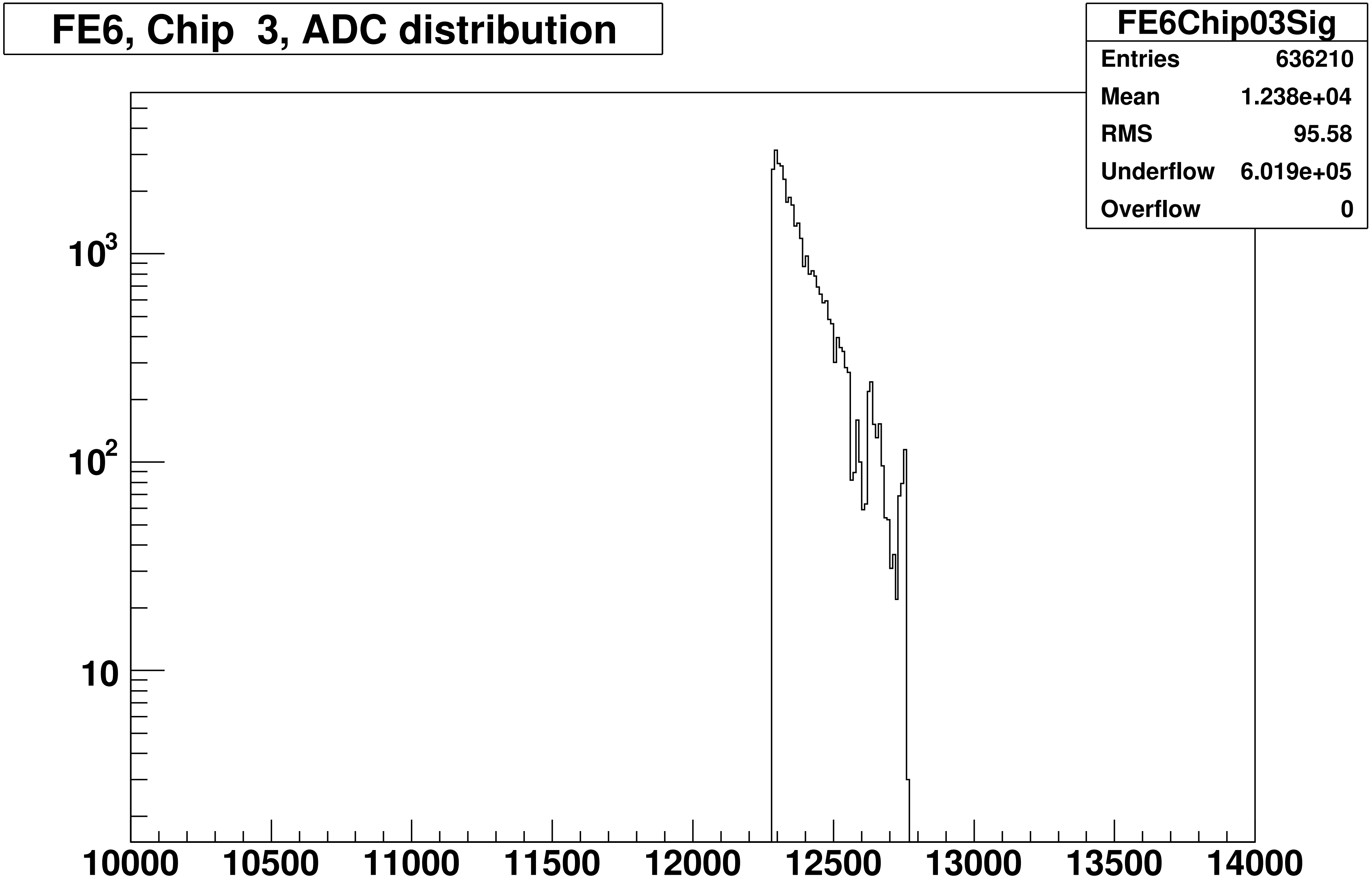


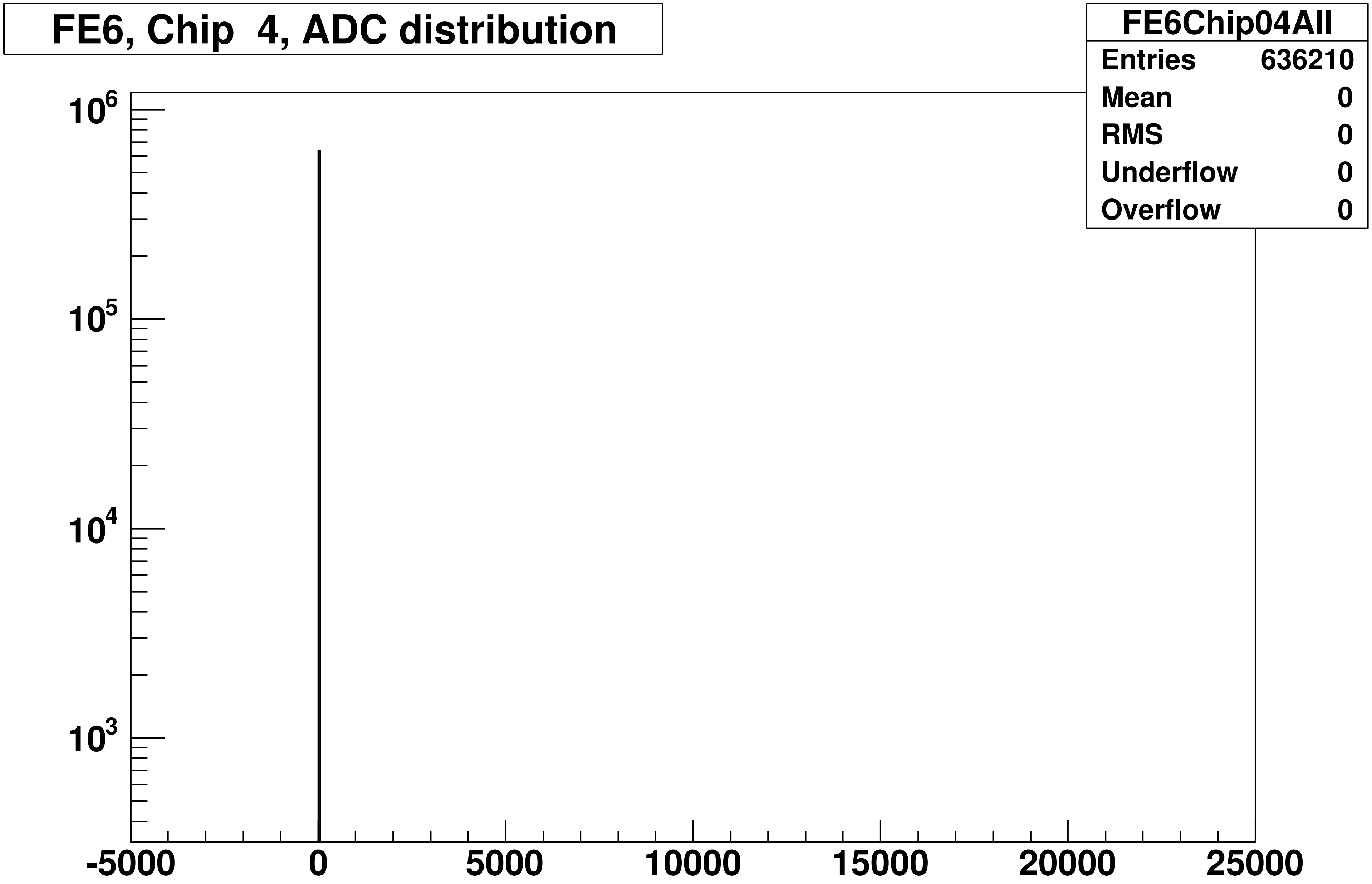
FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 636210
Mean 1.235e+04
RMS 97.78
Underflow 6.023e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

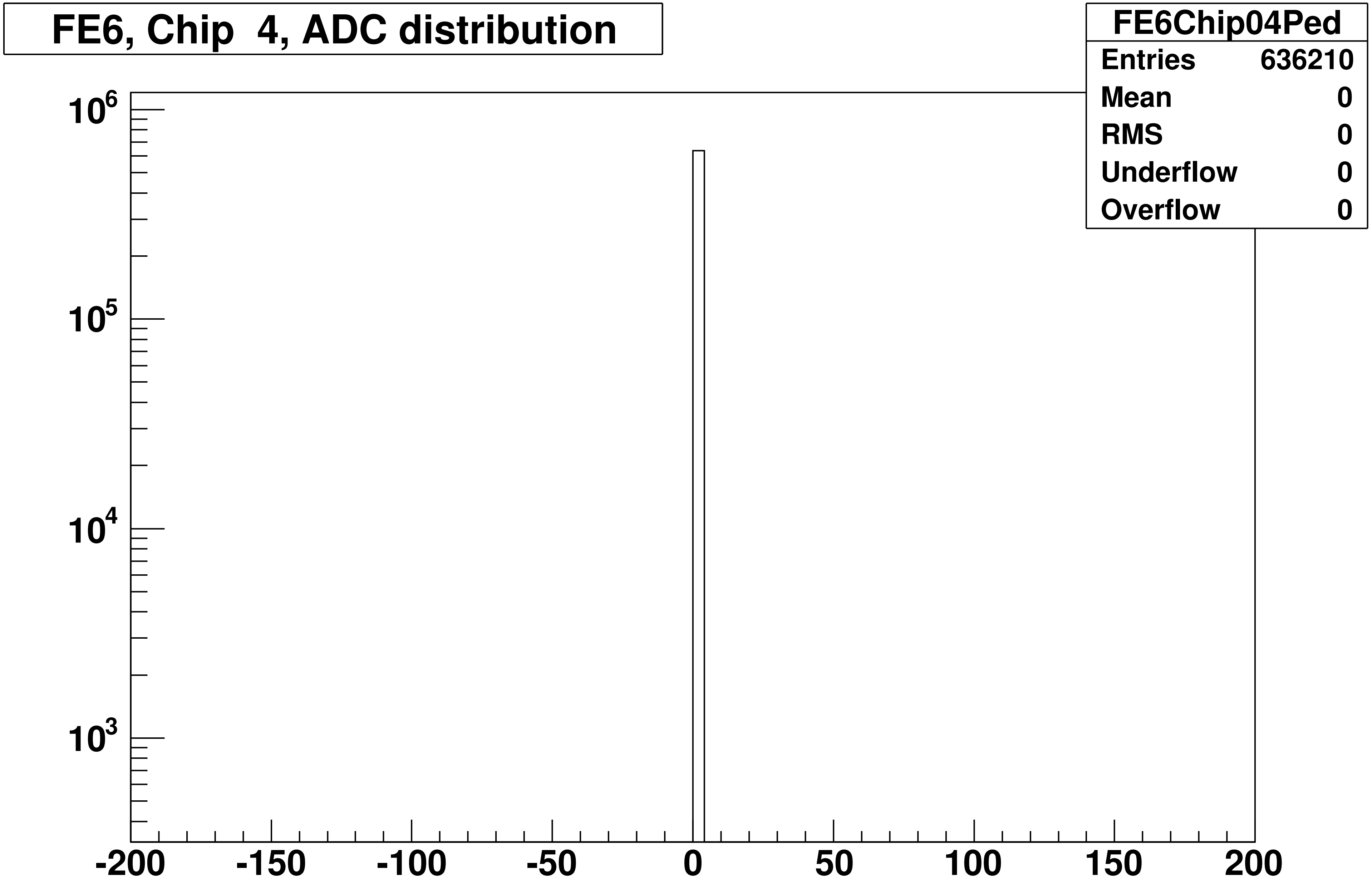

FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 636210
Mean 671.1
RMS 2794
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

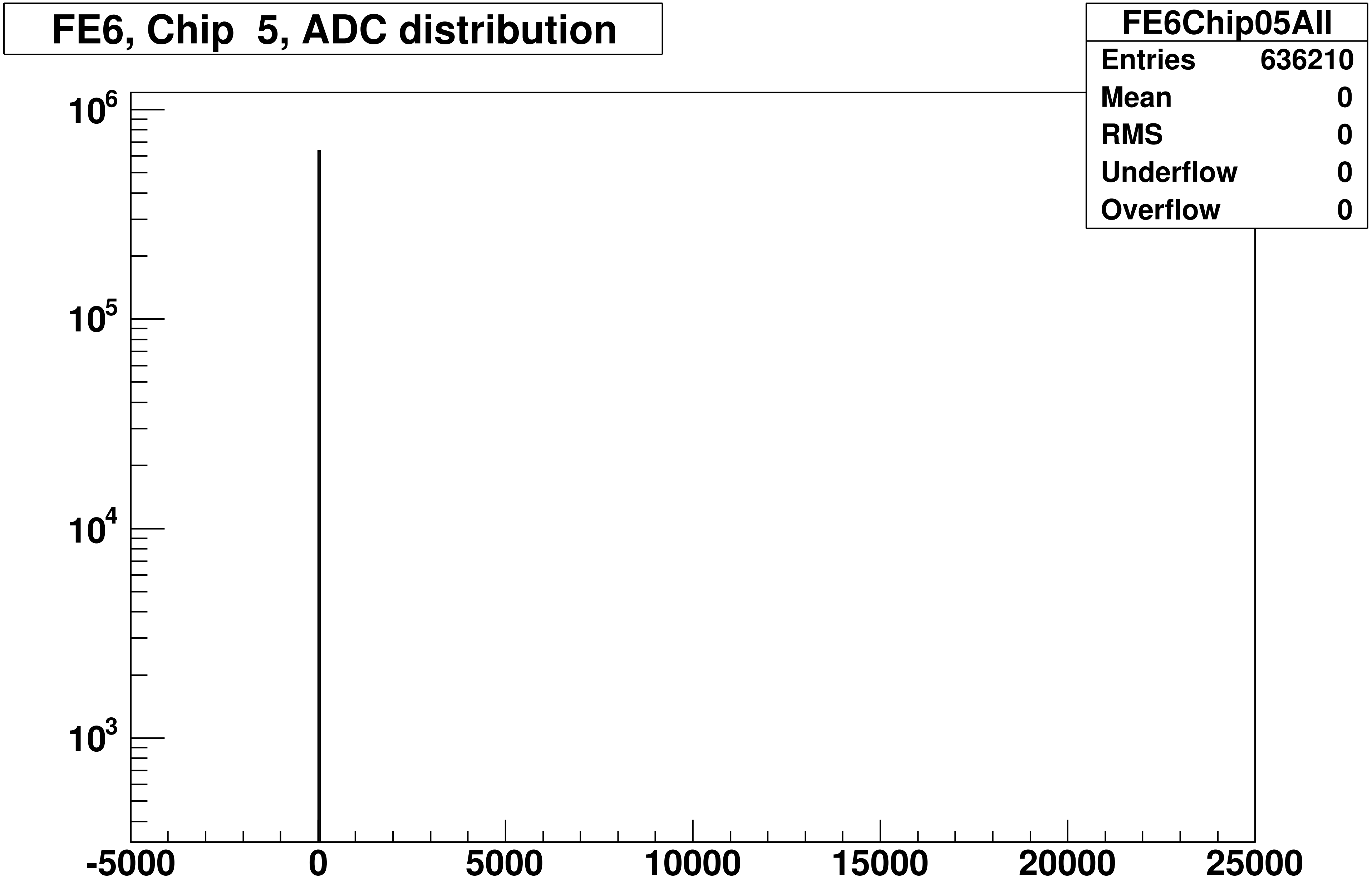


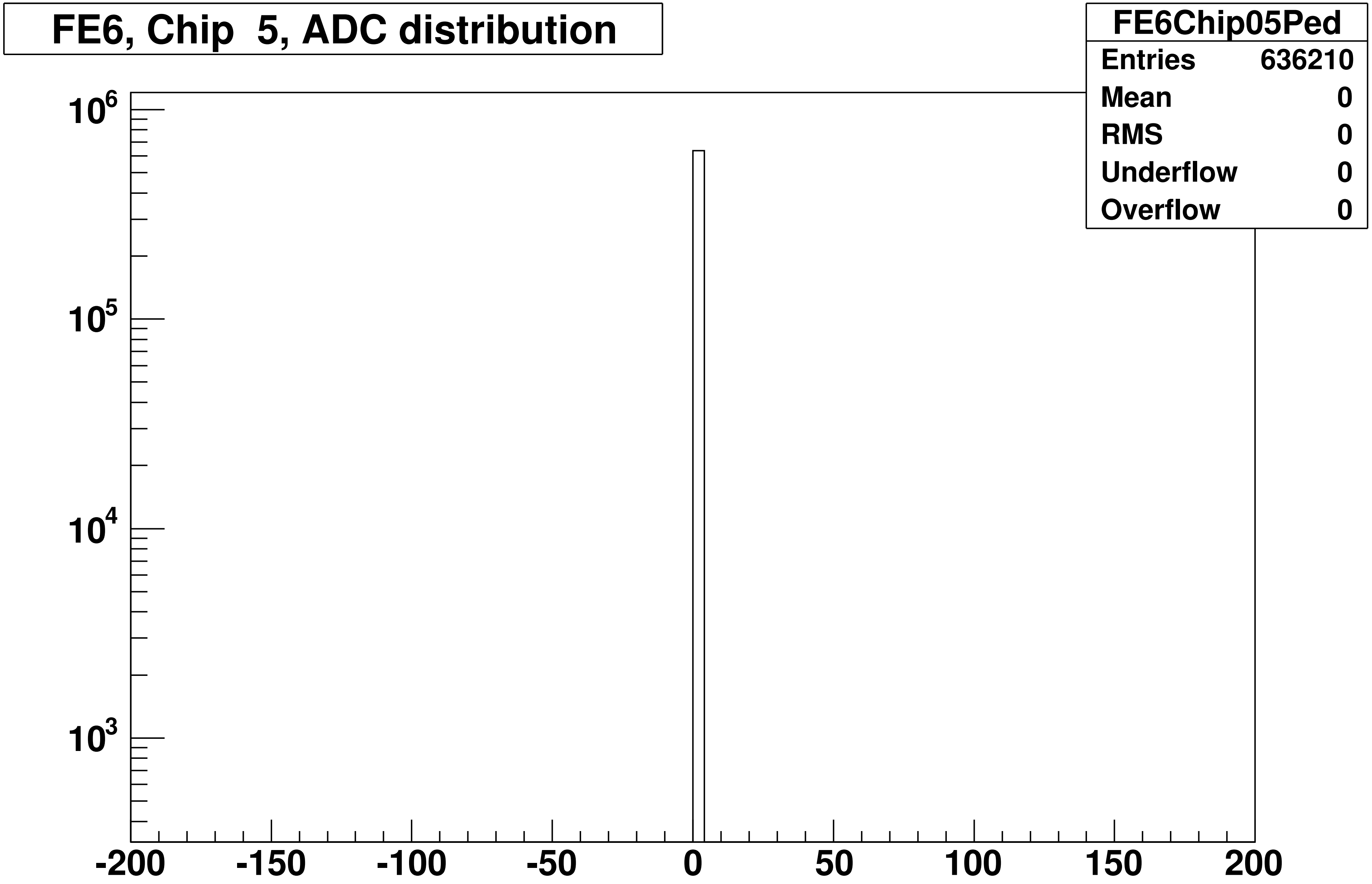
FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 636210
Mean 4.349
RMS 1.328
Underflow 0
Overflow 3.438e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

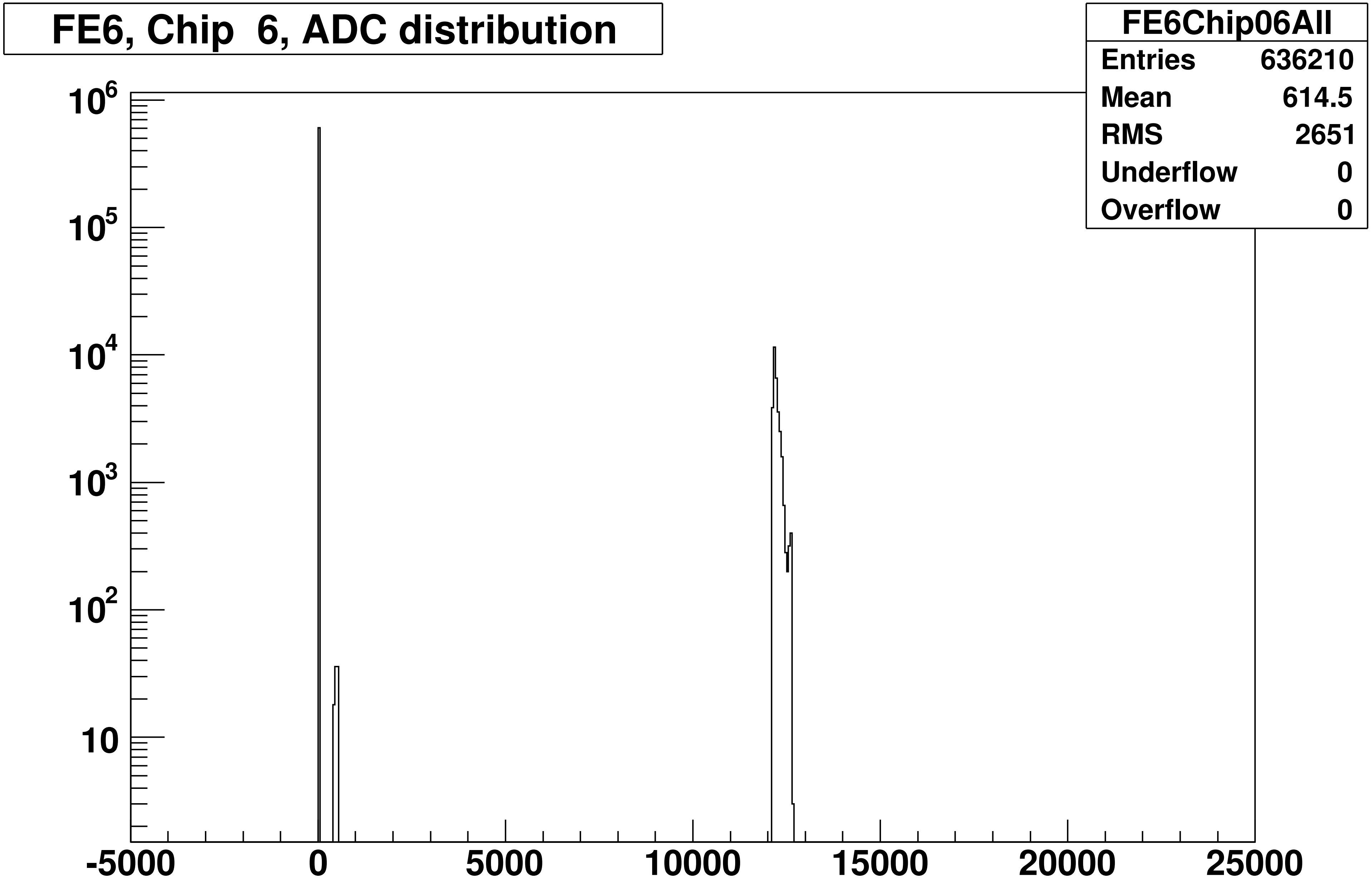

FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 636210
Mean 1.238e+04
RMS 95.58
Underflow 6.019e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000

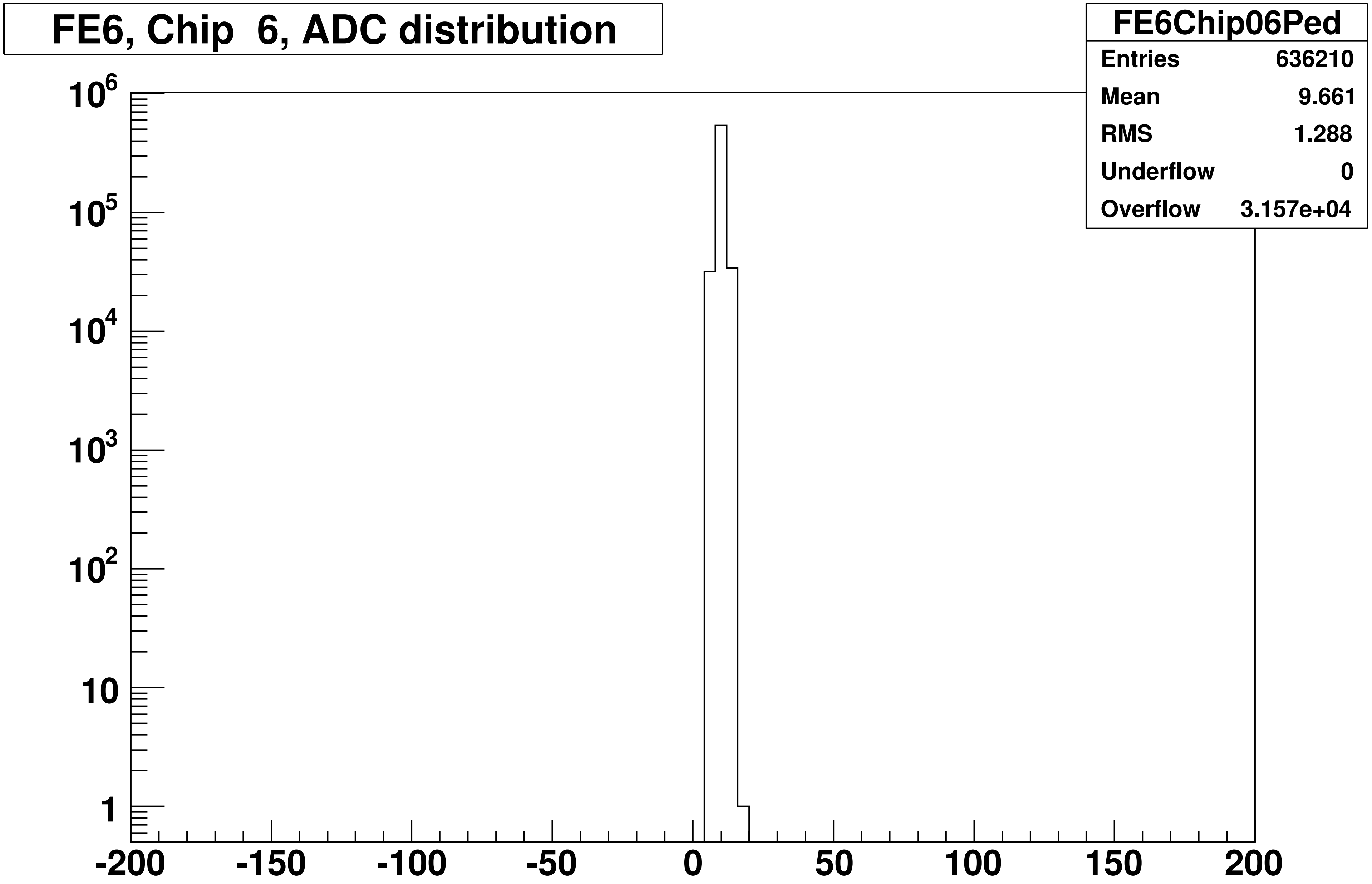

FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 636210
Mean 0
RMS 0
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 636210
Mean 0
RMS 0
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

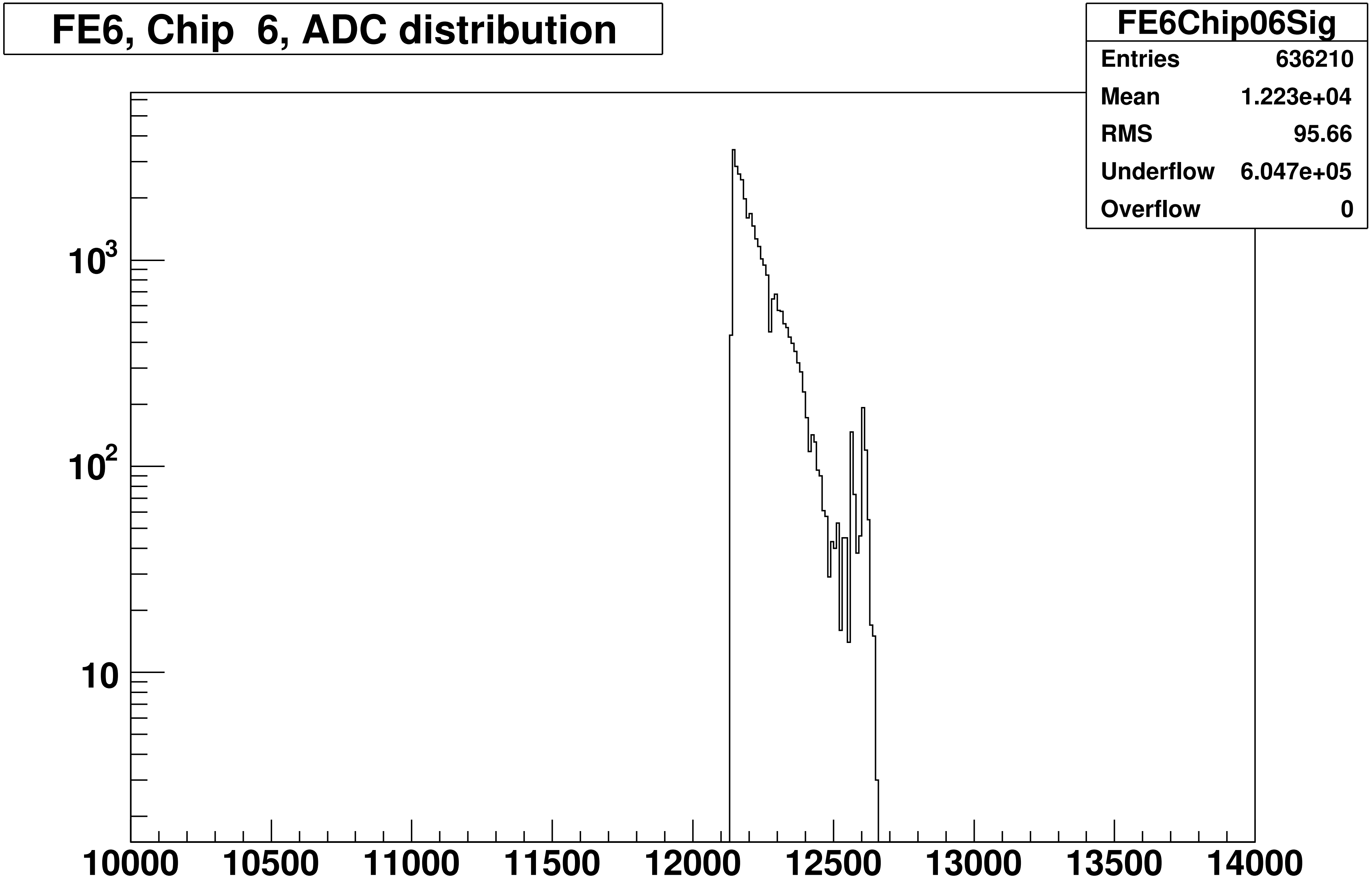

FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 636210
Mean 0
RMS 0
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 636210
Mean 0
RMS 0
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

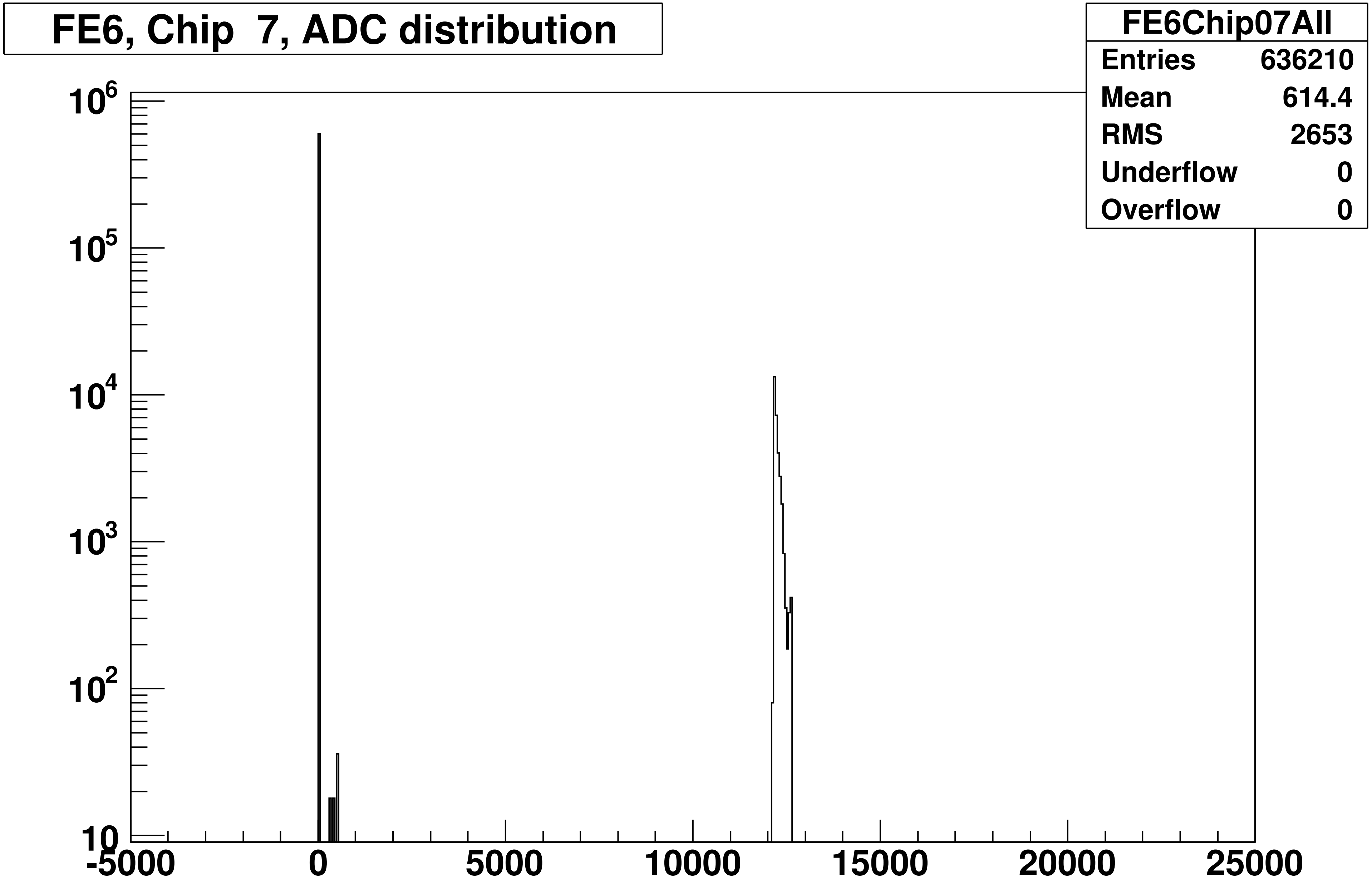

FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 636210
Mean 614.5
RMS 2651
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

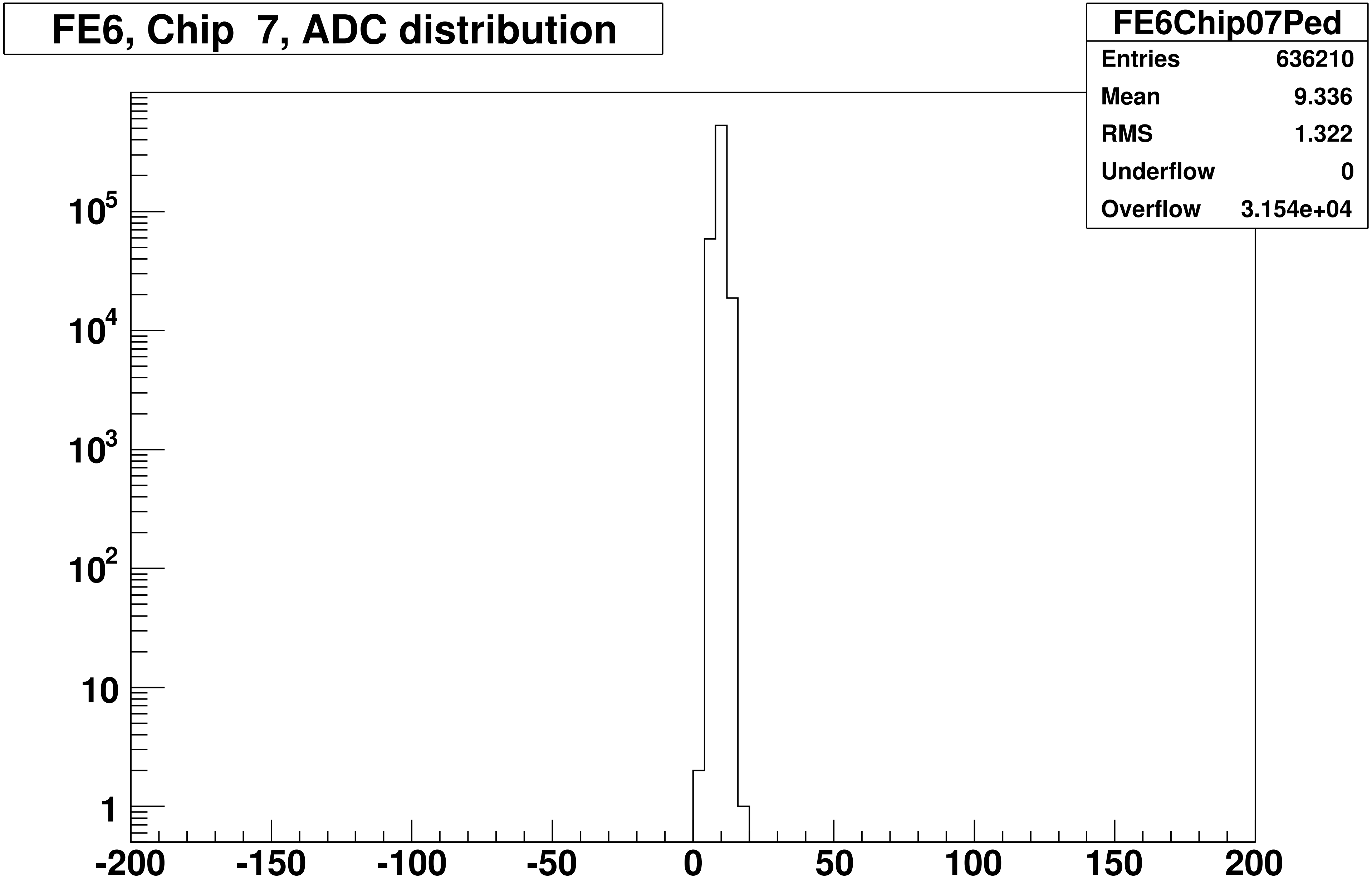


FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 636210
Mean 9.661
RMS 1.288
Underflow 0
Overflow 3.157e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

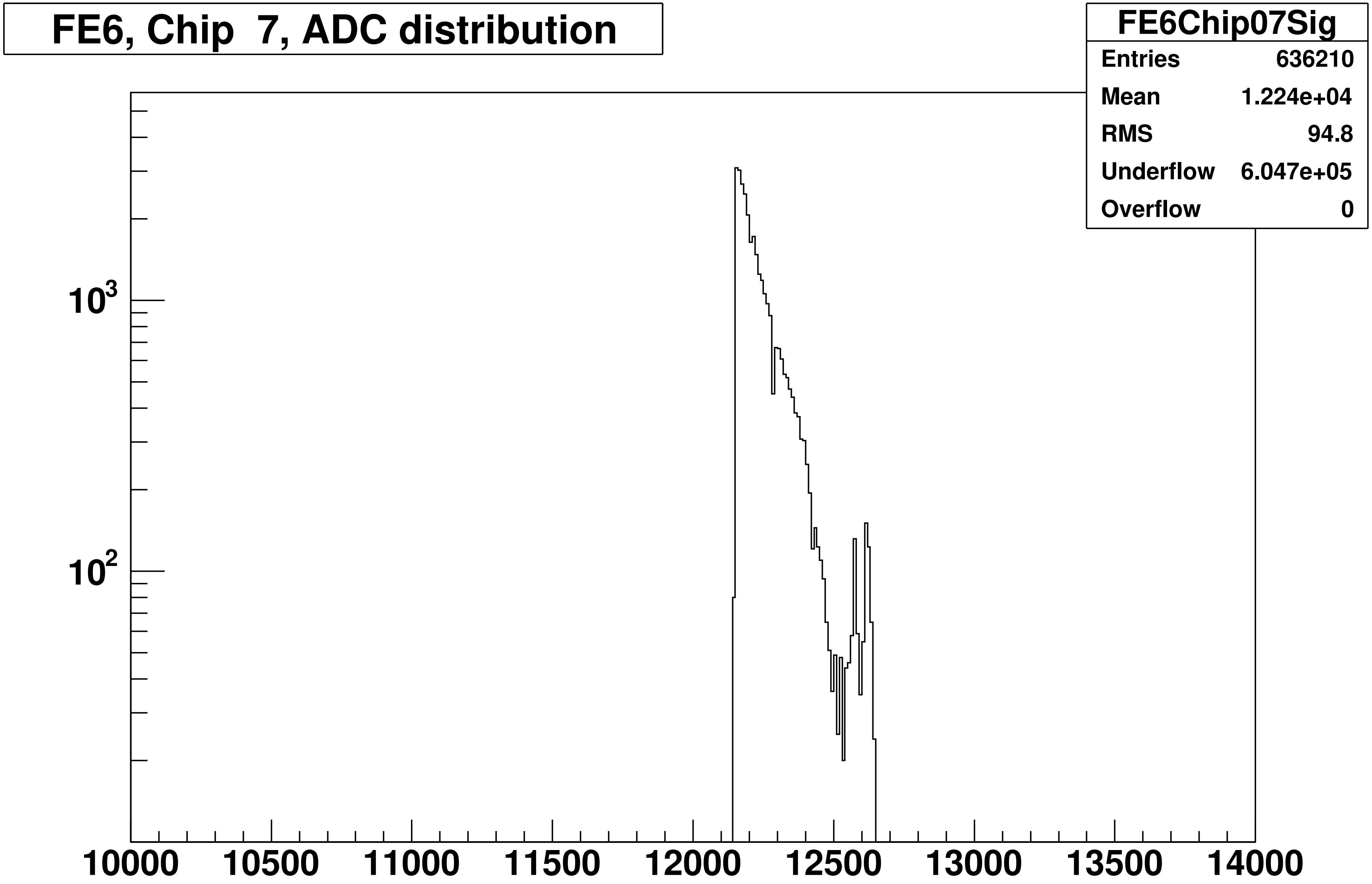


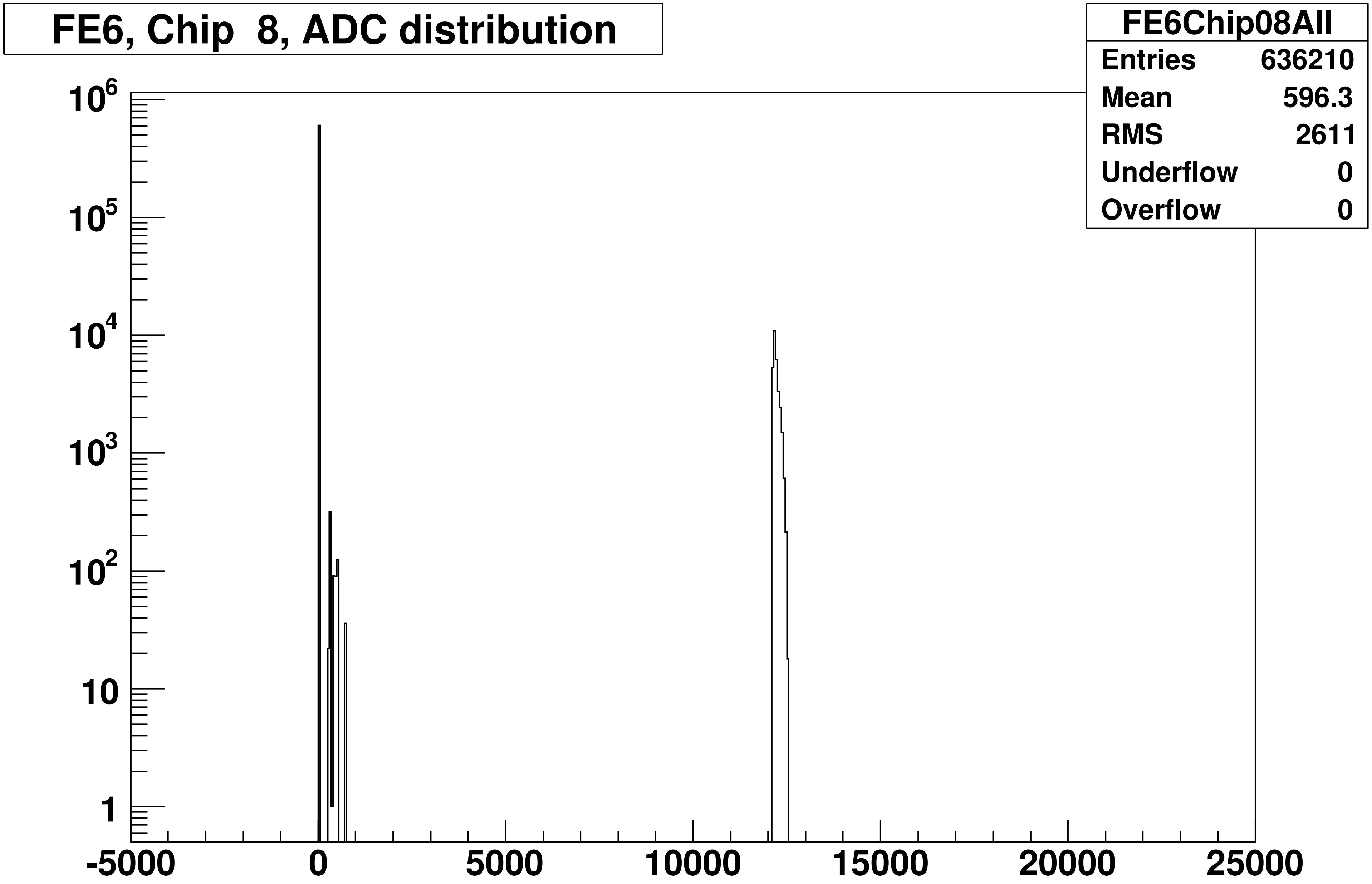
FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 636210
Mean 1.223e+04
RMS 95.66
Underflow 6.047e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 636210
Mean 614.4
RMS 2653
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

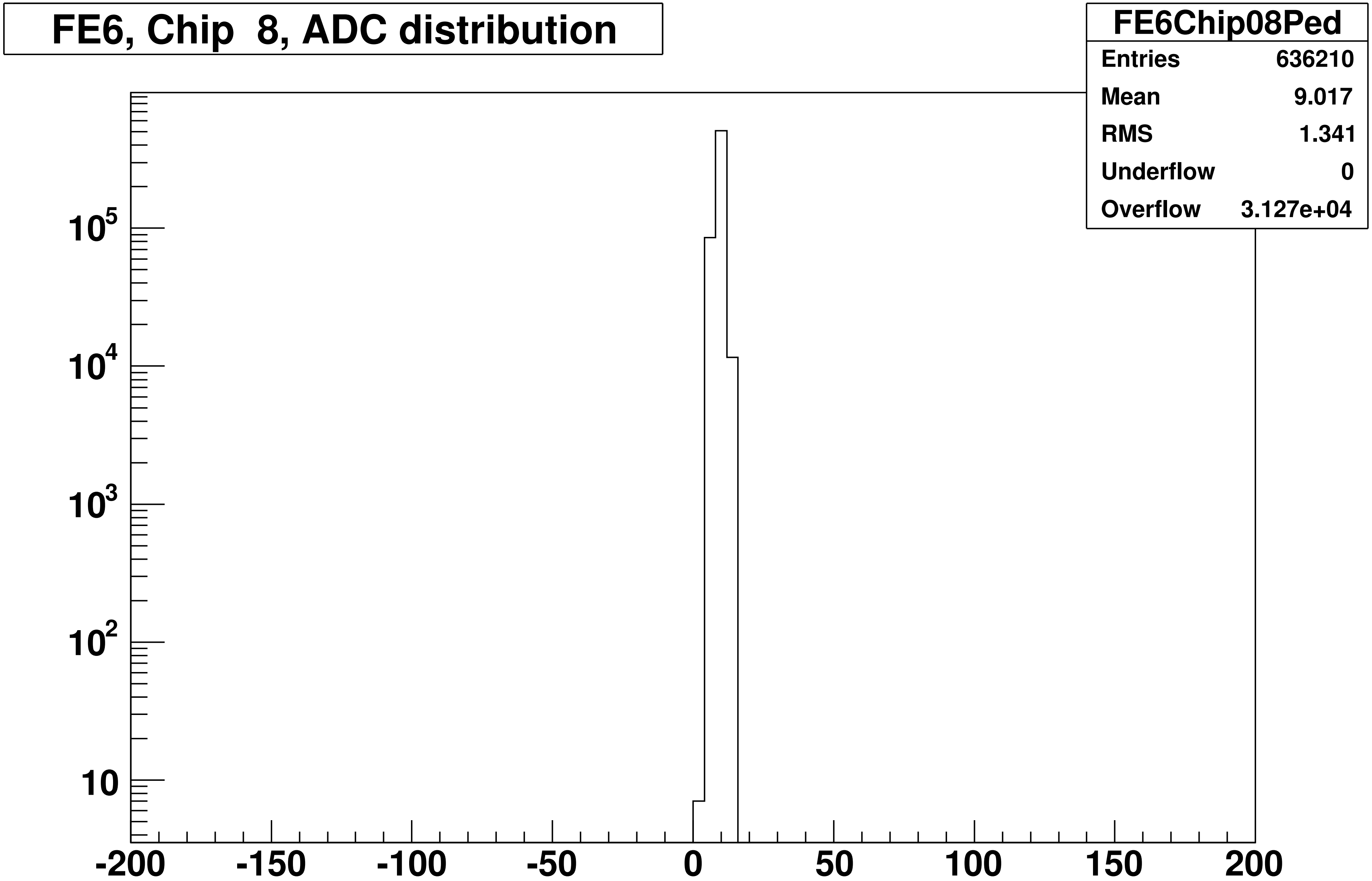


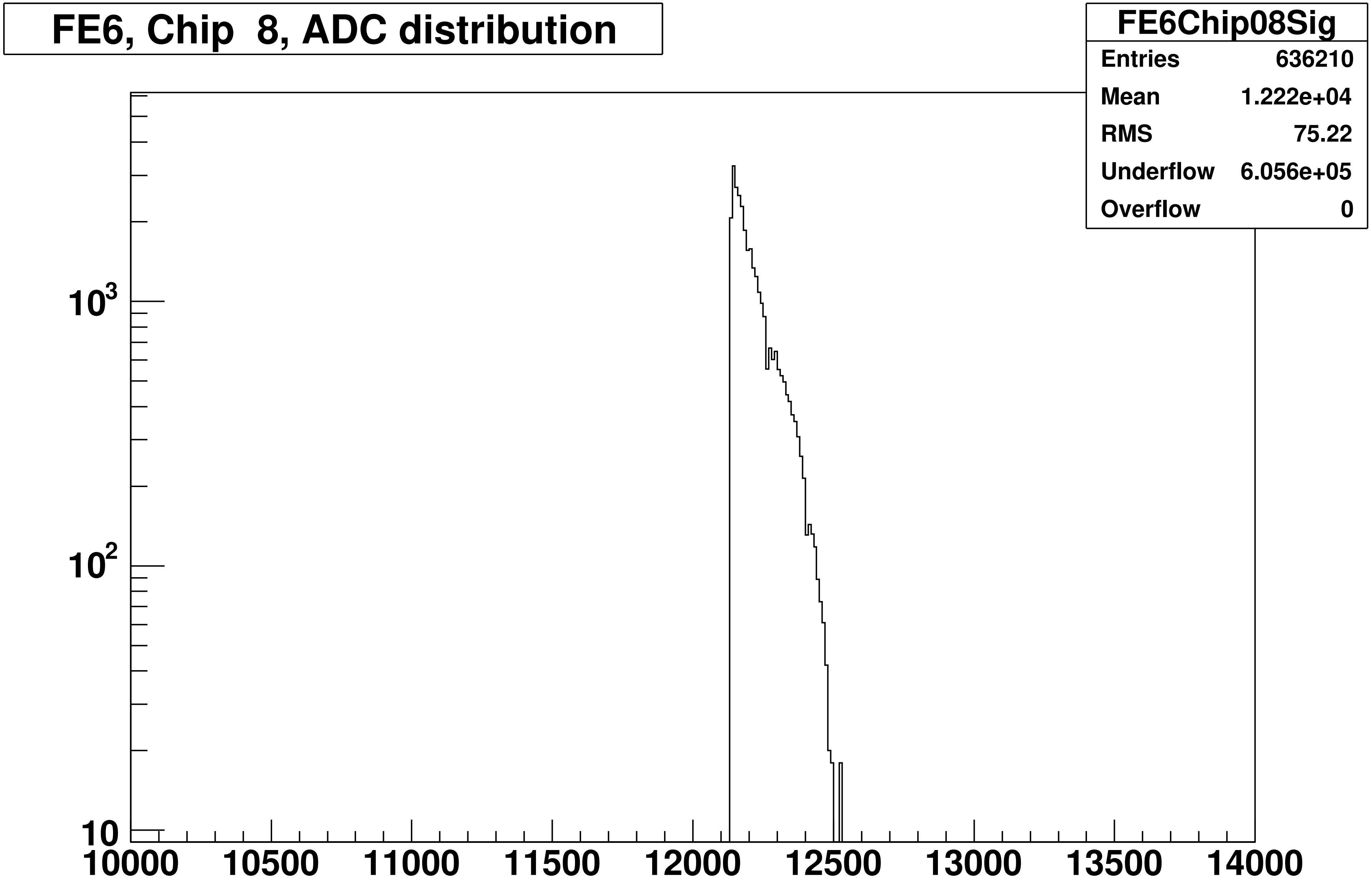
FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 636210
Mean 9.336
RMS 1.322
Underflow 0
Overflow 3.154e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

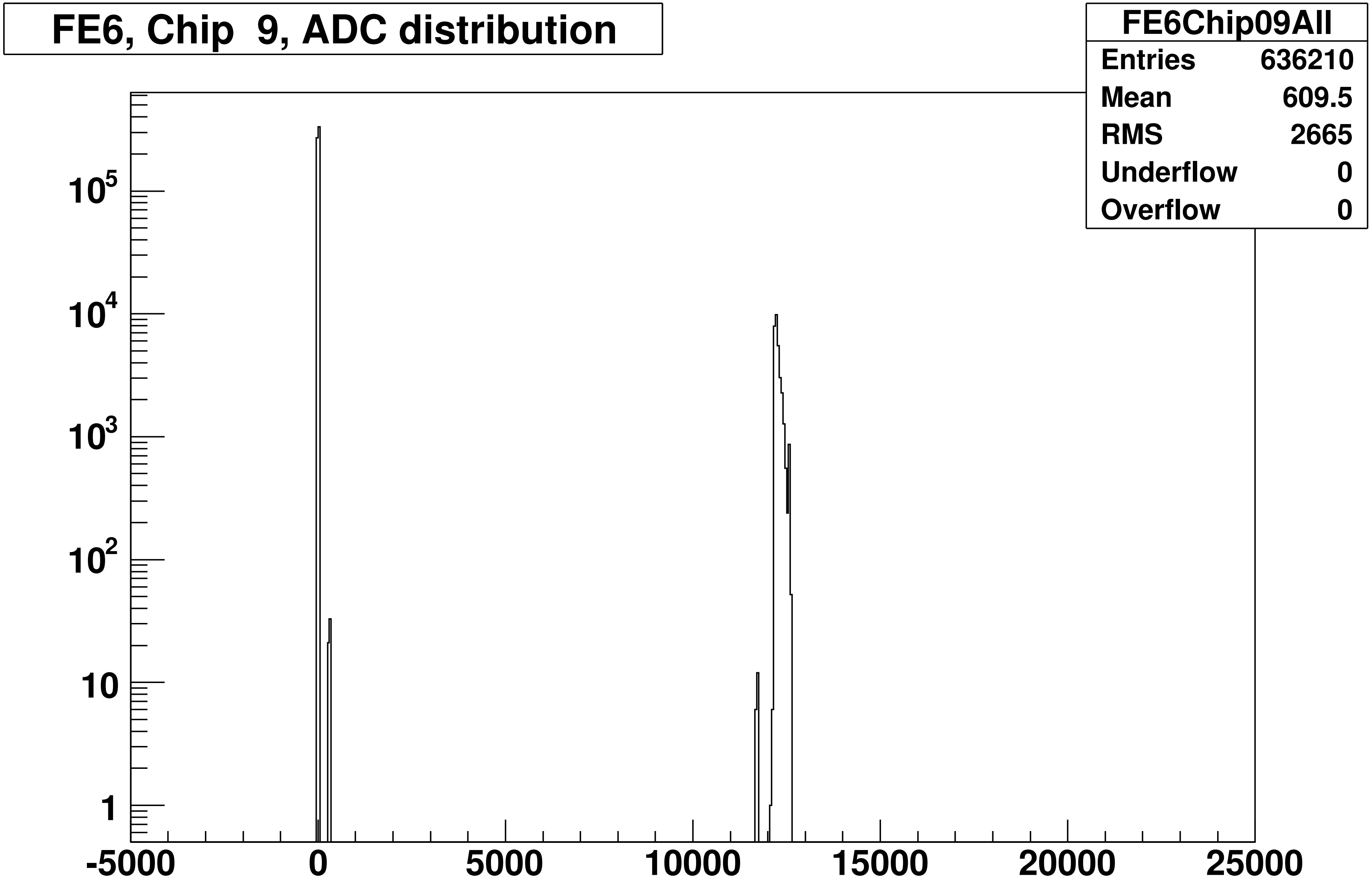

FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 636210
Mean 1.224e+04
RMS 94.8
Underflow 6.047e+05
Overflow 0
10^3
10^2
10000
10500
11000
11500
12000
12500
13000
13500
14000

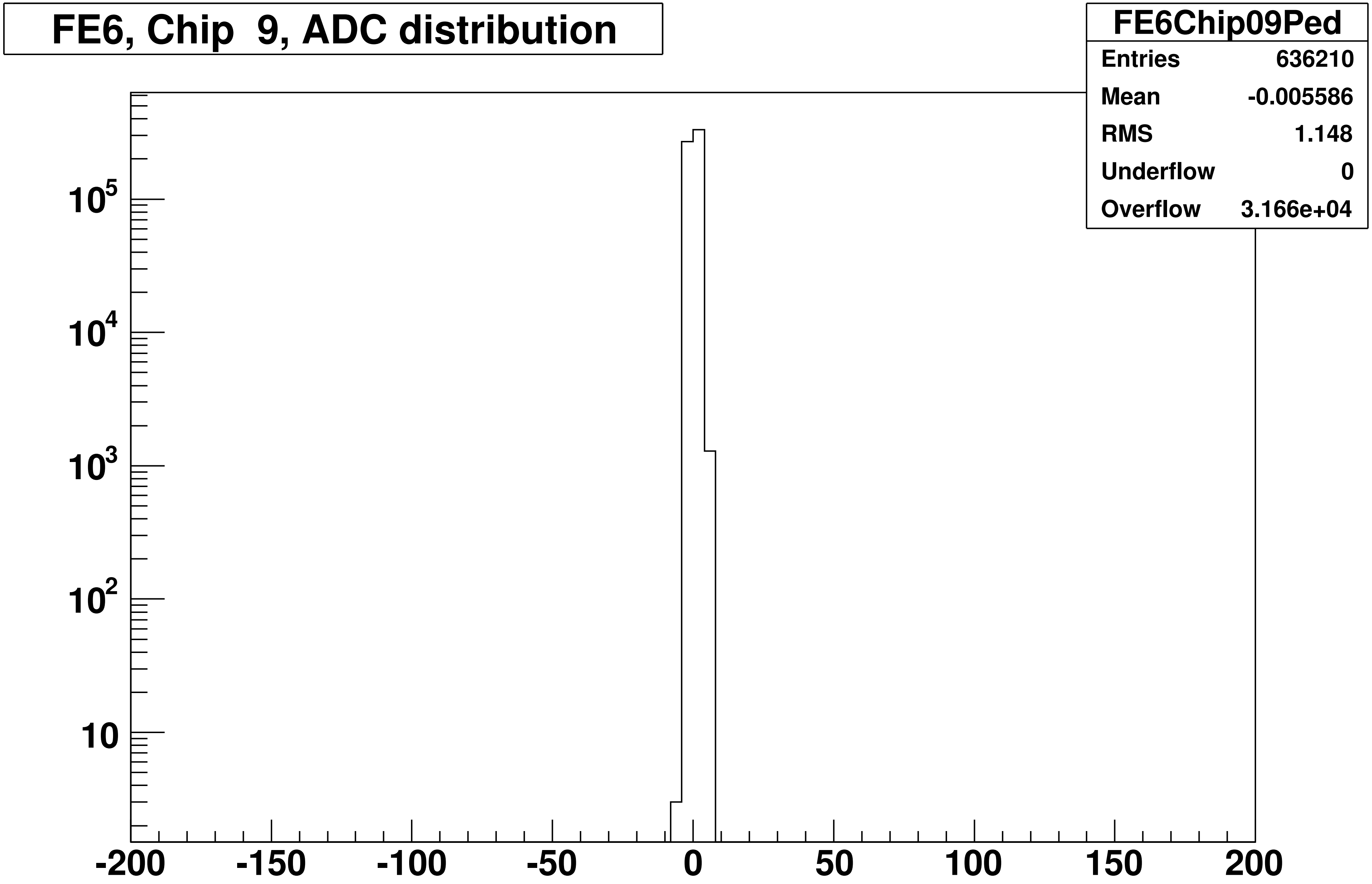


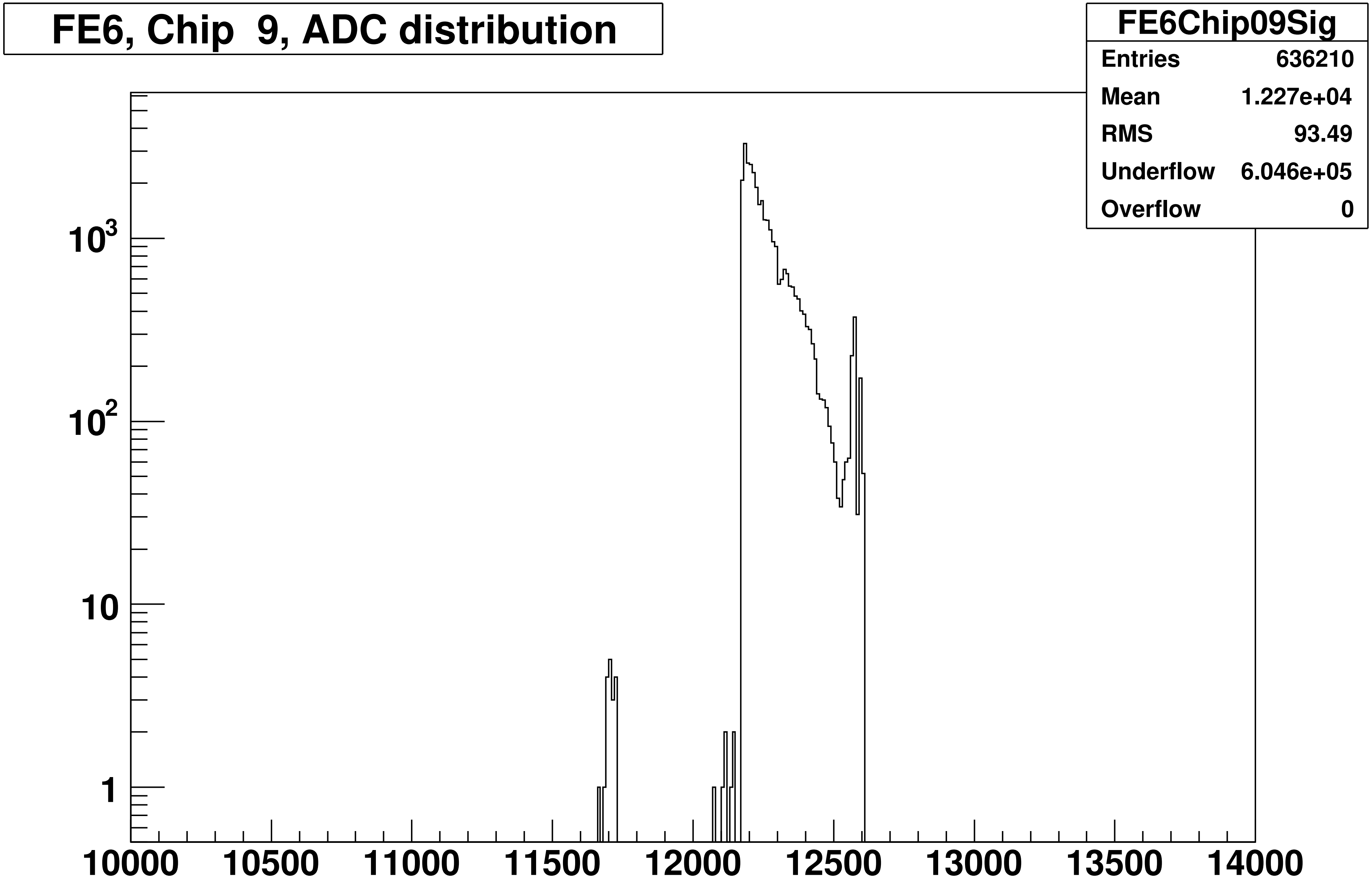
FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 636210
Mean 596.3
RMS 2611
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

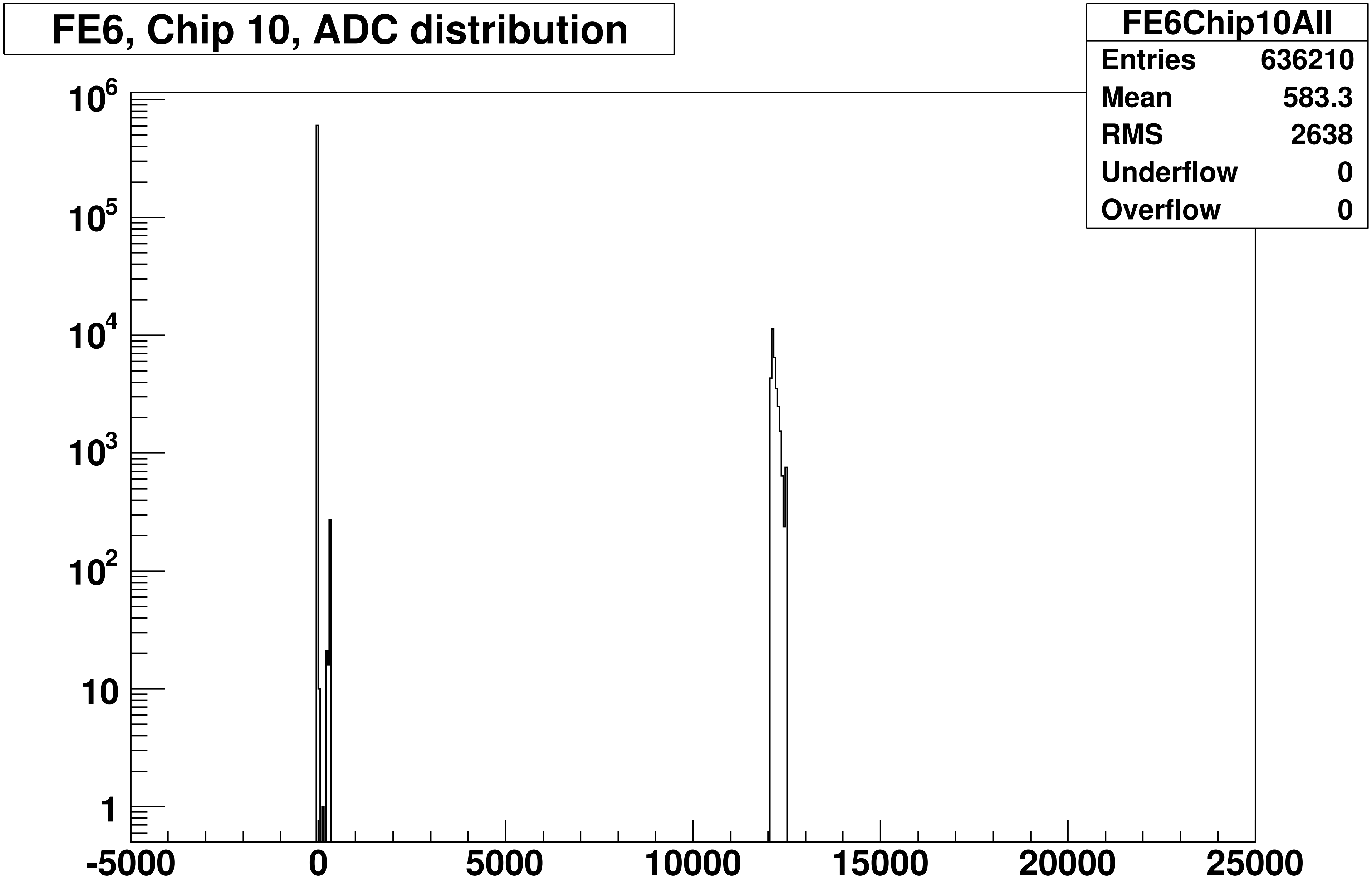

FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 636210
Mean 9.017
RMS 1.341
Underflow 0
Overflow 3.127e+04
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200

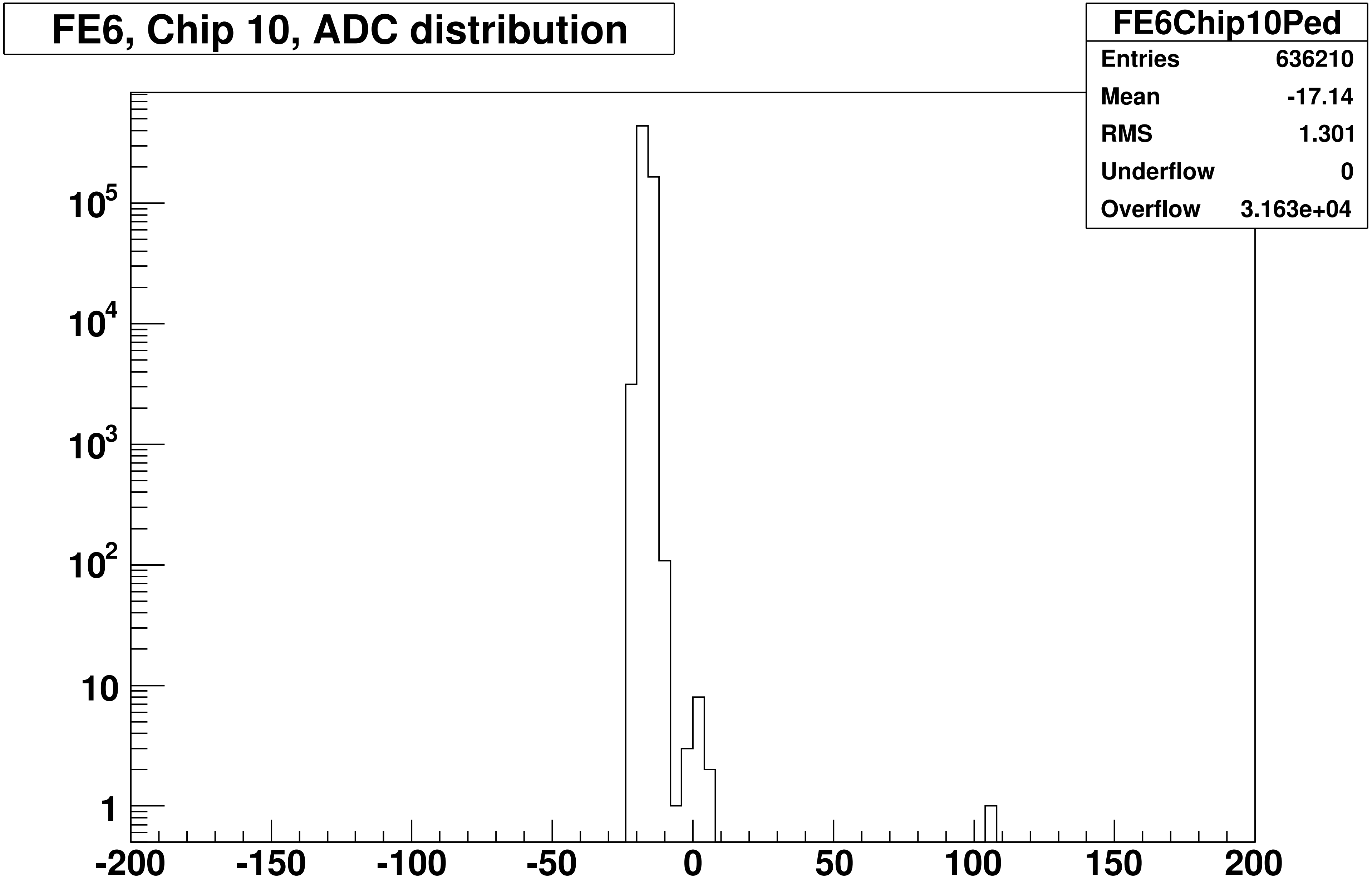

FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 636210
Mean 1.222e+04
RMS 75.22
Underflow 6.056e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 636210
Mean 609.5
RMS 2665
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

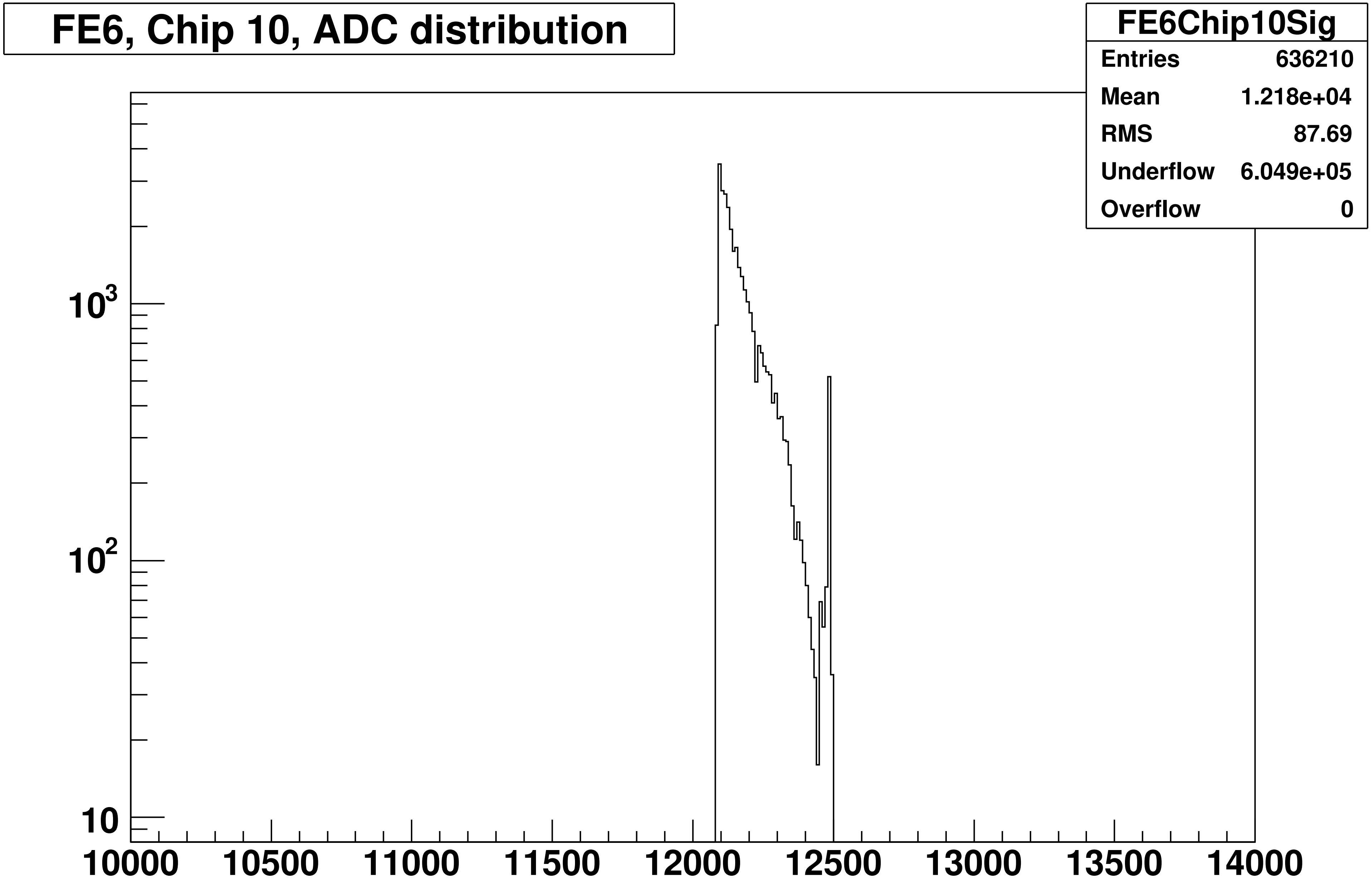

FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 636210
Mean -0.005586
RMS 1.148
Underflow 0
Overflow 3.166e+04
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200

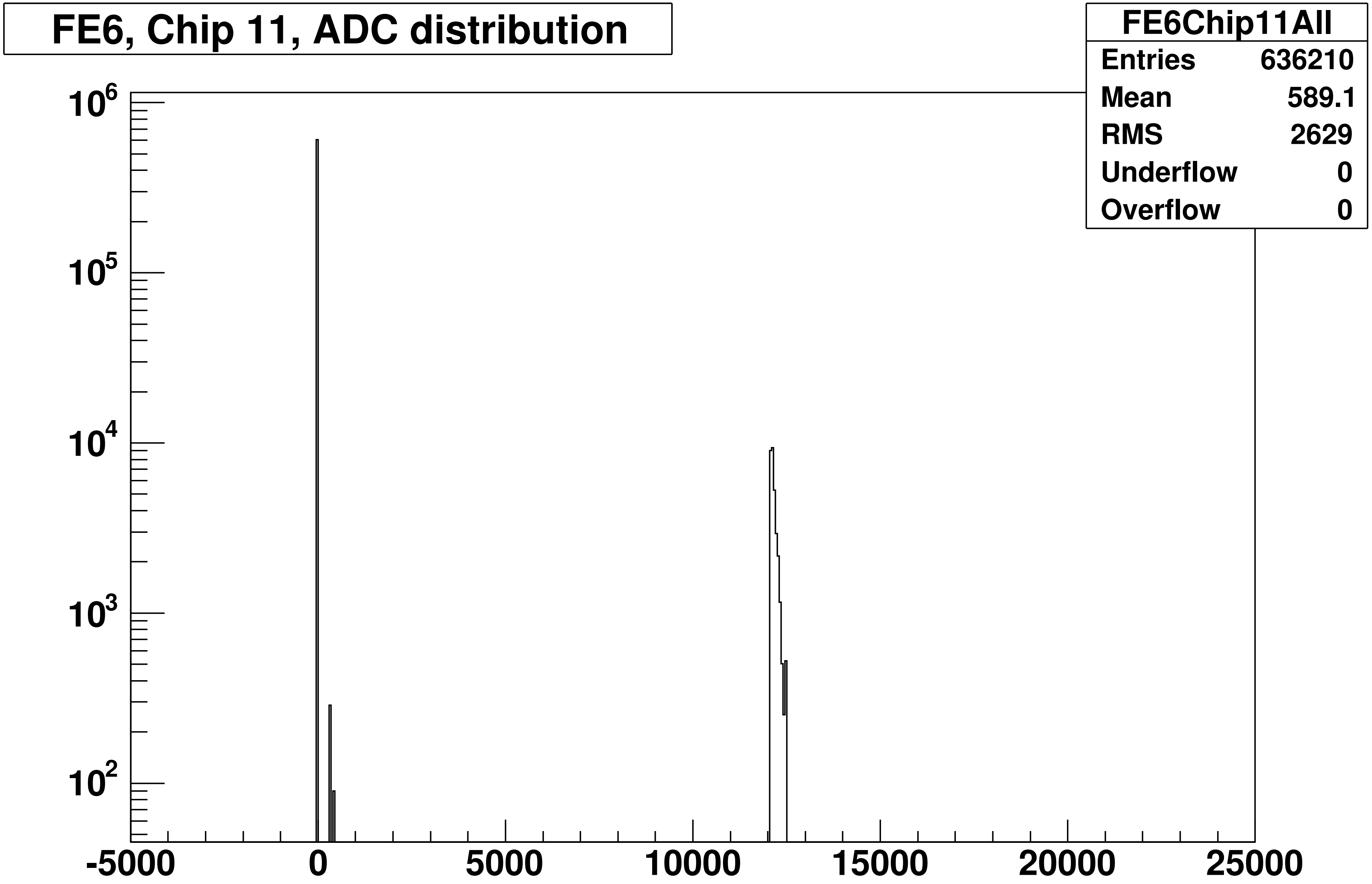

FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 636210
Mean 1.227e+04
RMS 93.49
Underflow 6.046e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

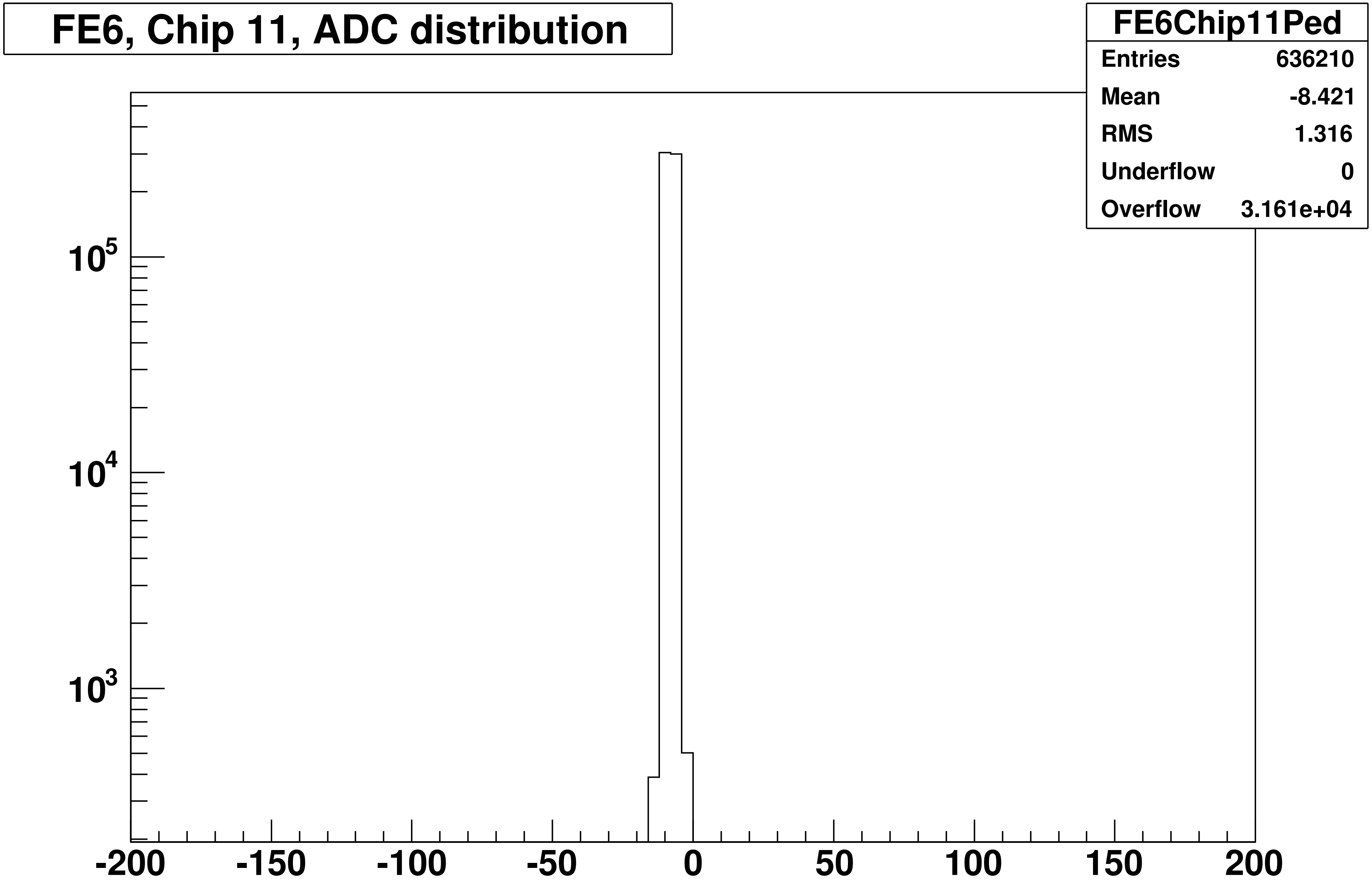

FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 636210
Mean 583.3
RMS 2638
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



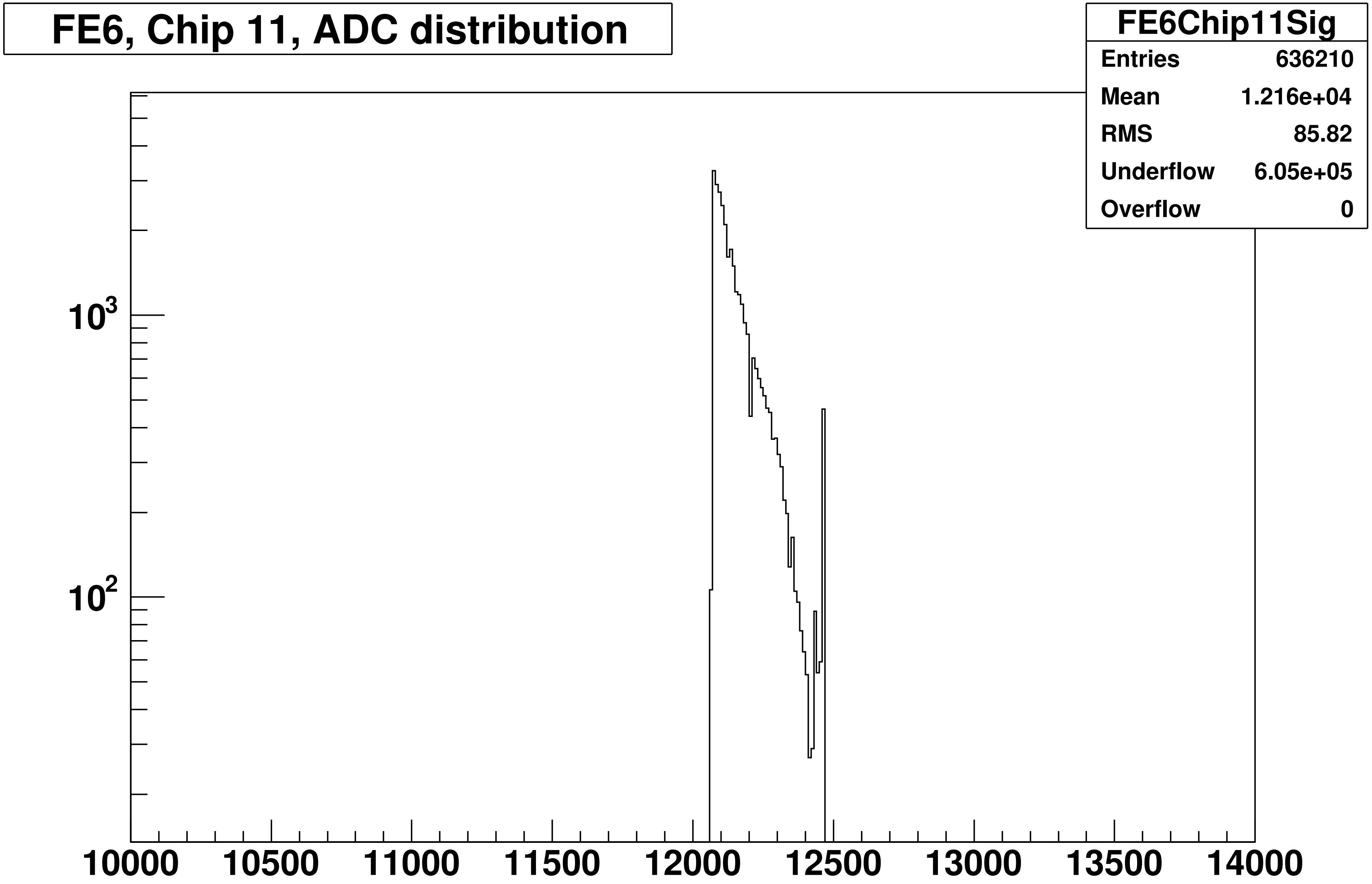
FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 636210
Mean -17.14
RMS 1.301
Underflow 0
Overflow 3.163e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

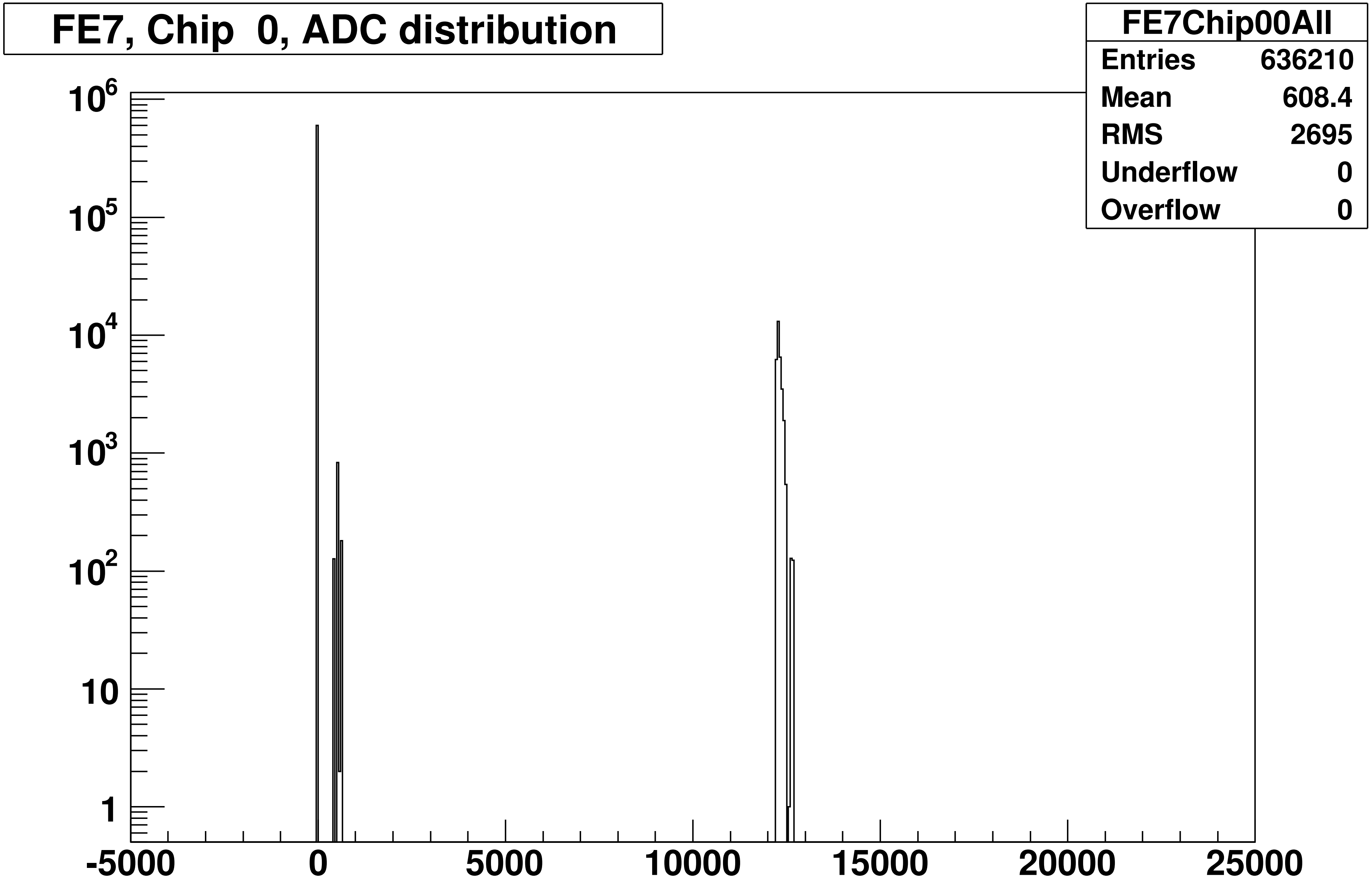

FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 636210
Mean 1.218e+04
RMS 87.69
Underflow 6.049e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 636210
Mean 589.1
RMS 2629
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

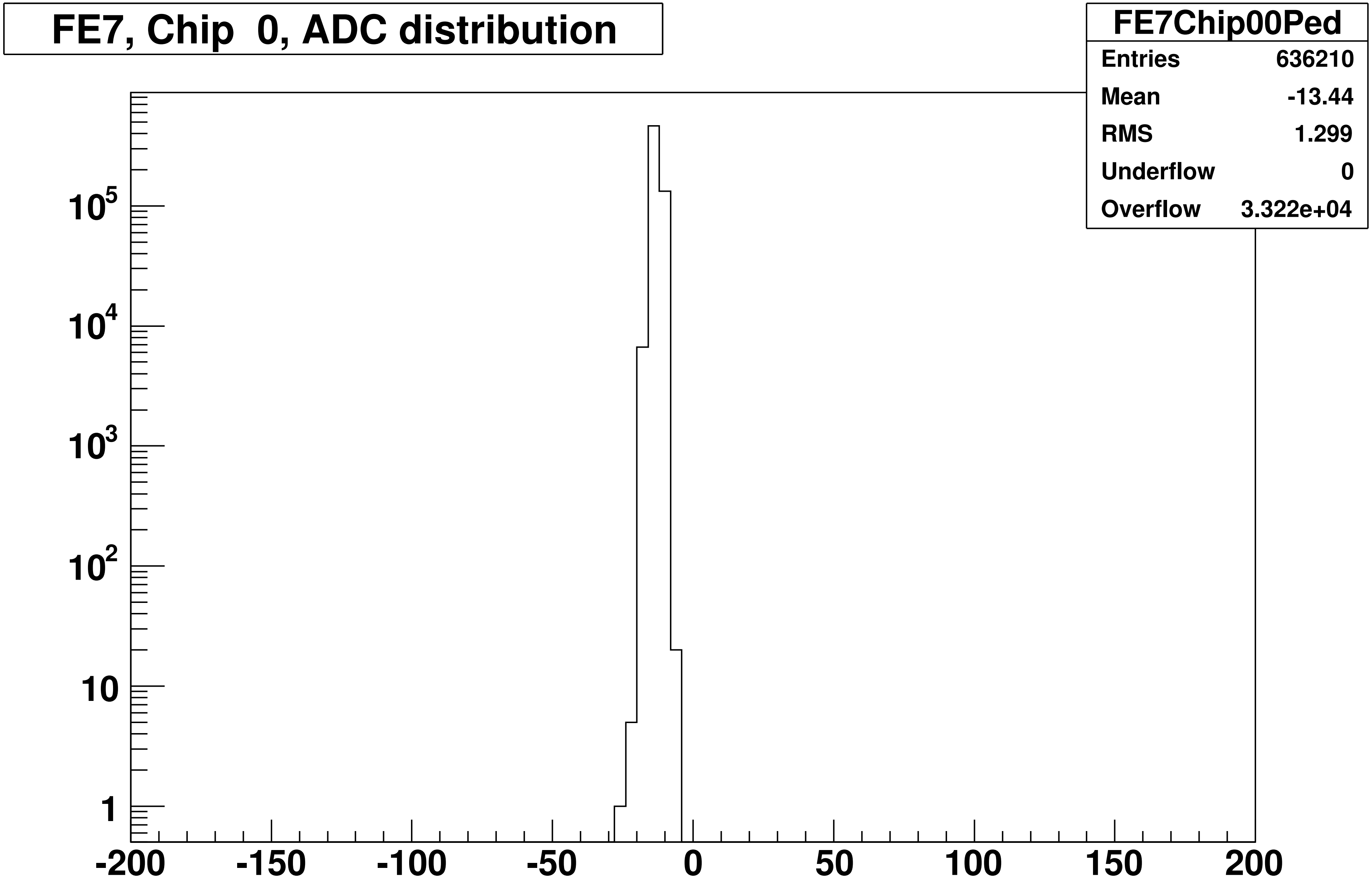


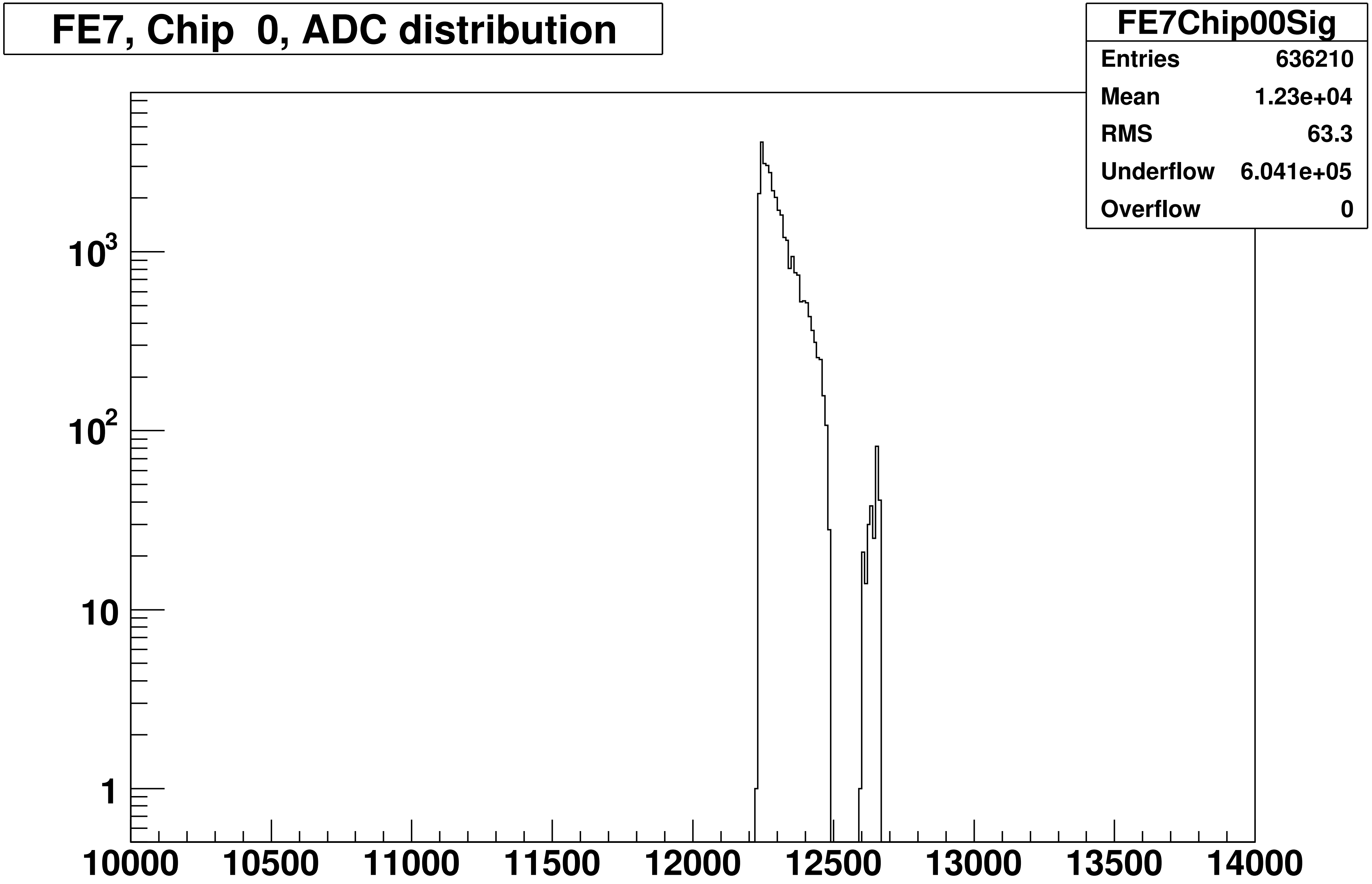
FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 636210
Mean -8.421
RMS 1.316
Underflow 0
Overflow 3.161e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 636210
Mean 1.216e+04
RMS 85.82
Underflow 6.05e+05
Overflow 0
10^3
10^2
10000
10500
11000
11500
12000
12500
13000
13500
14000

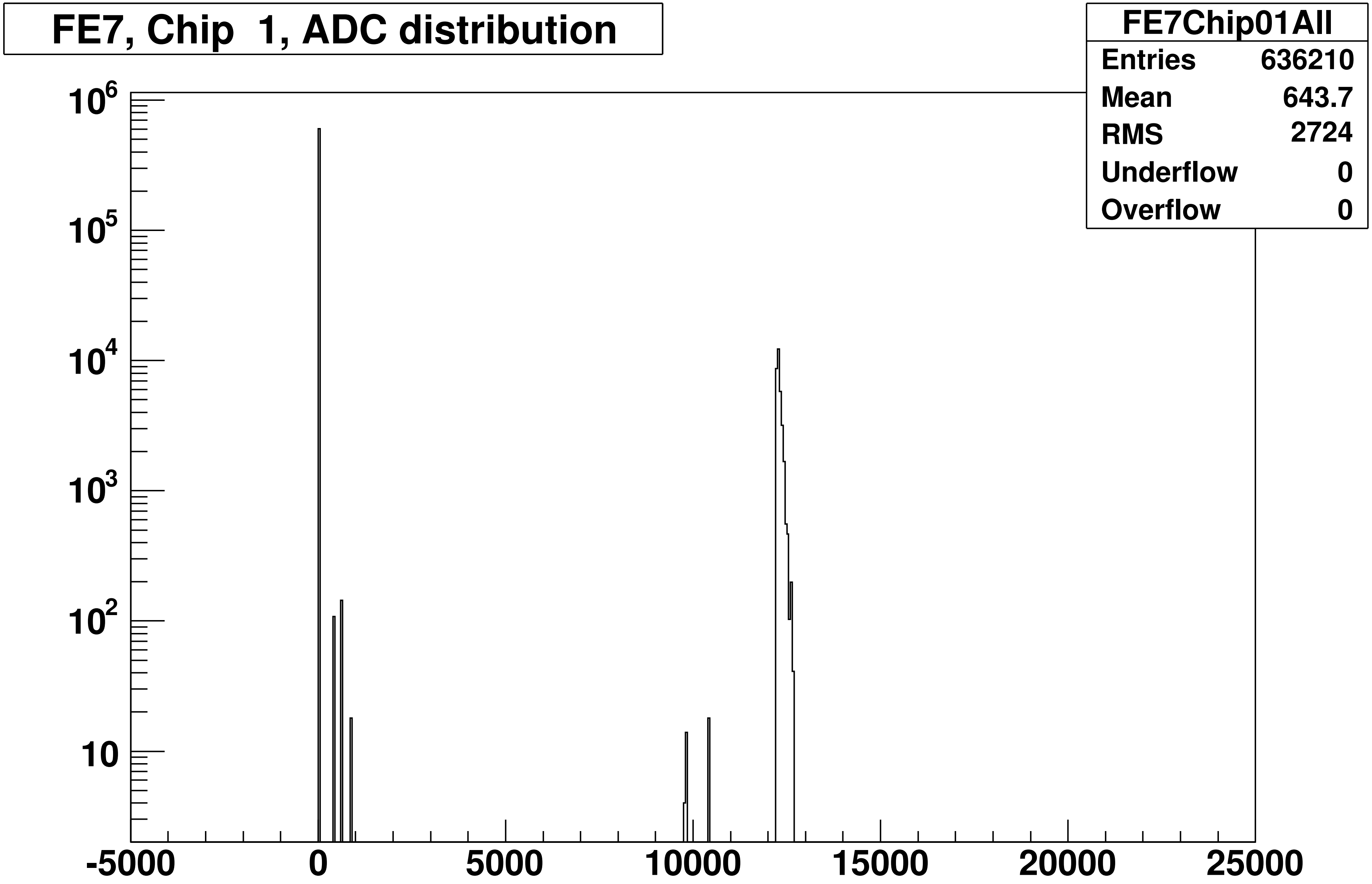


FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 636210
Mean 608.4
RMS 2695
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

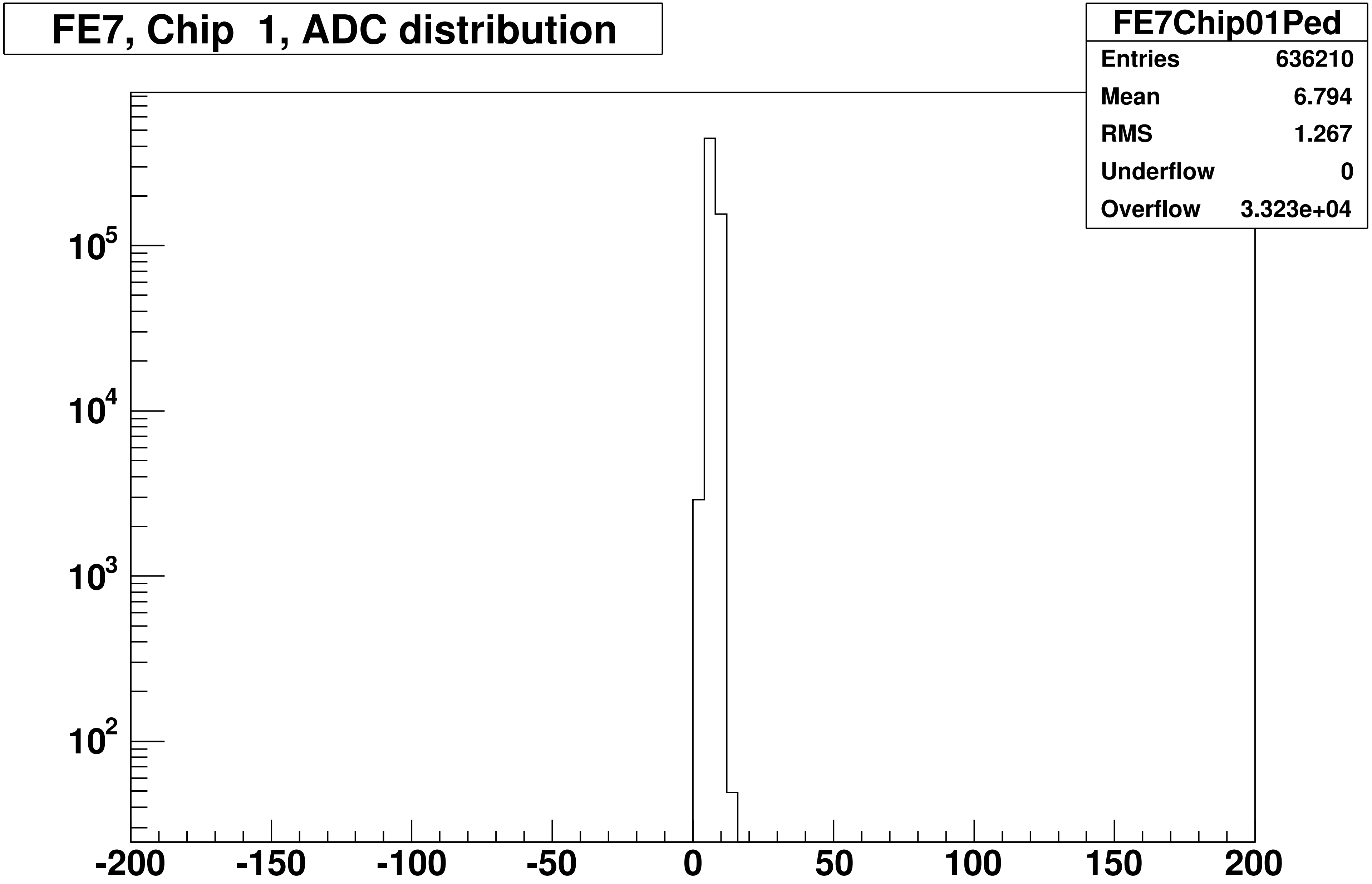

FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 636210
Mean -13.44
RMS 1.299
Underflow 0
Overflow 3.322e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

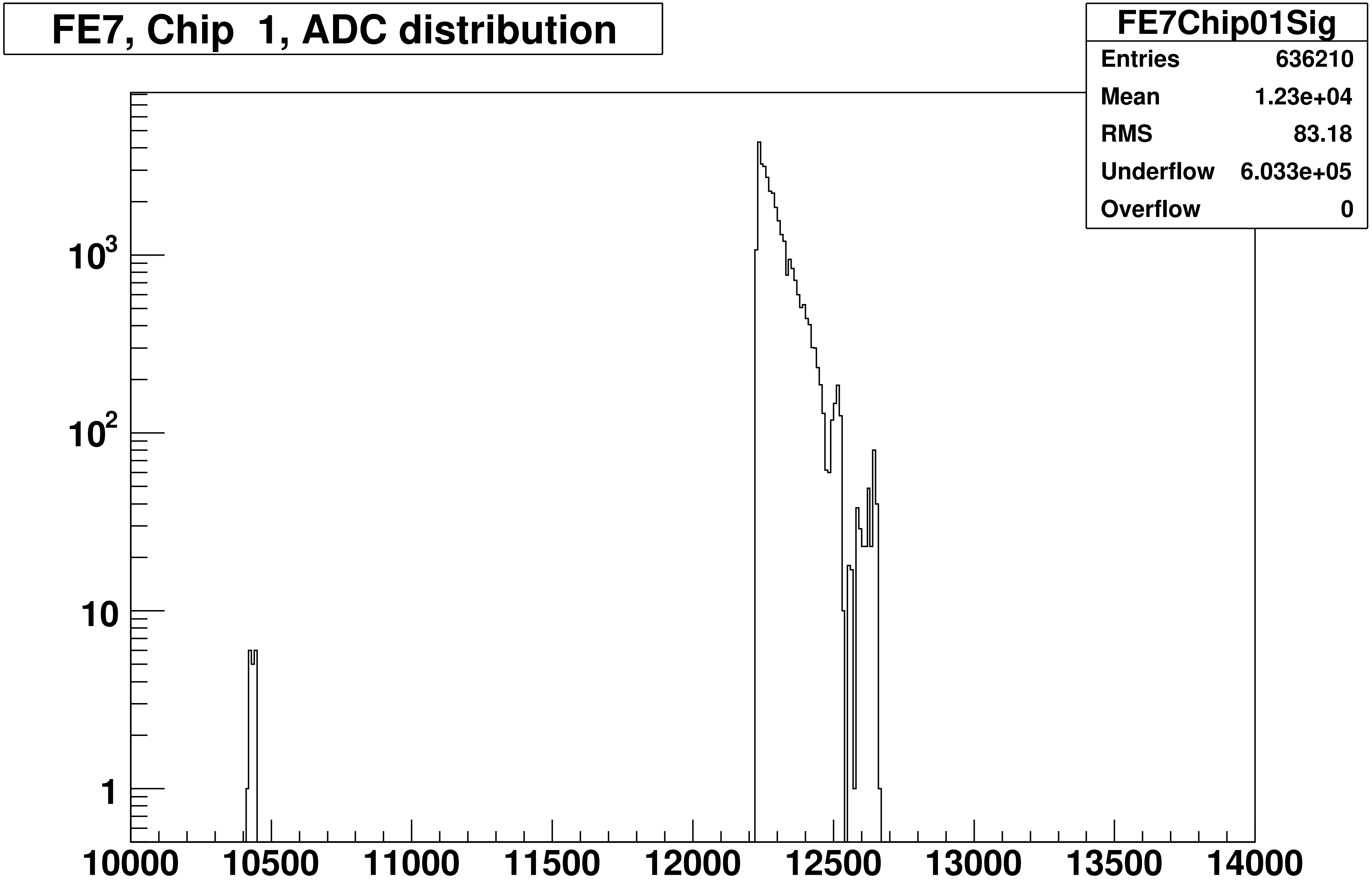


FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 636210
Mean 1.23e+04
RMS 63.3
Underflow 6.041e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

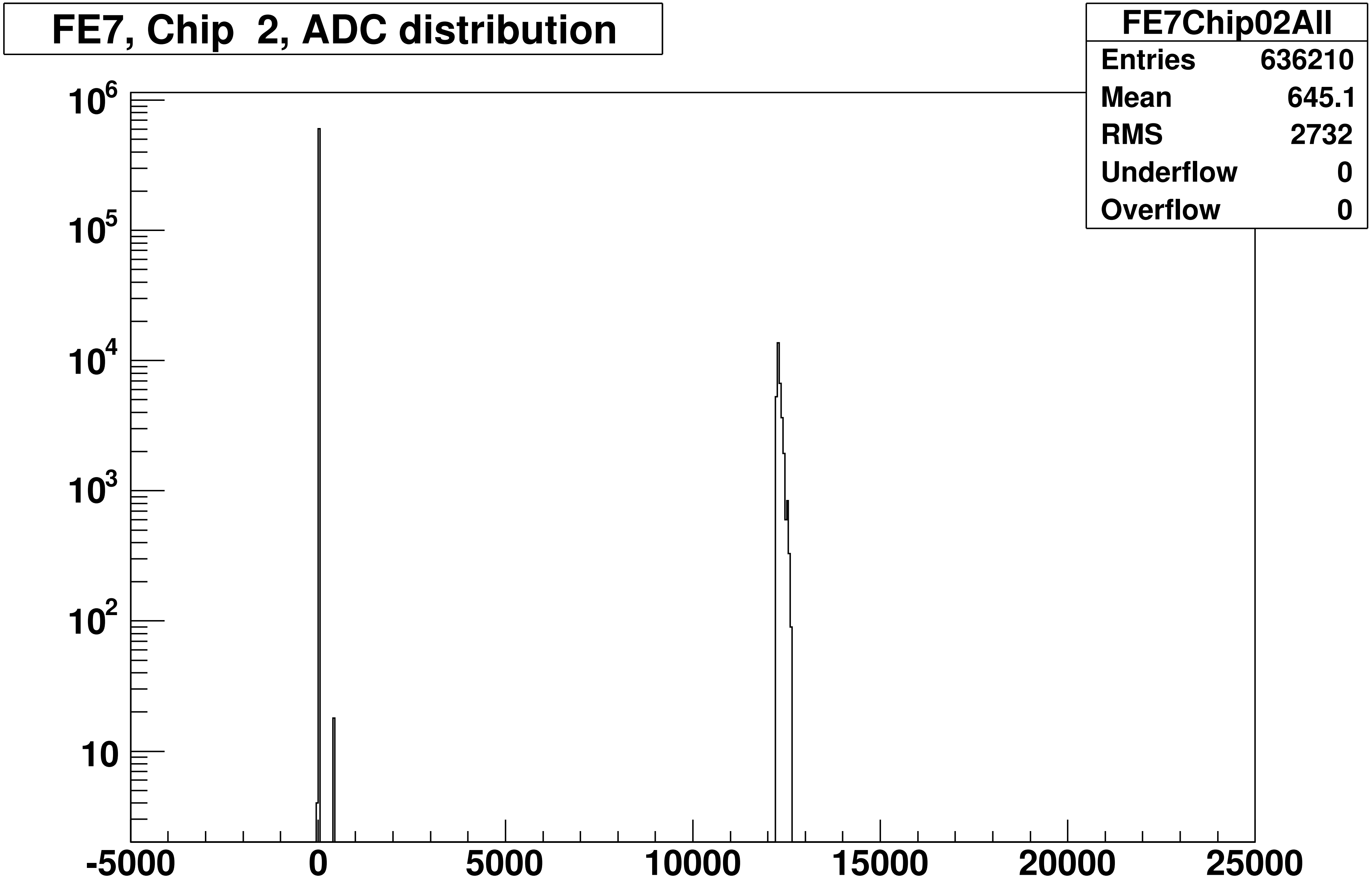


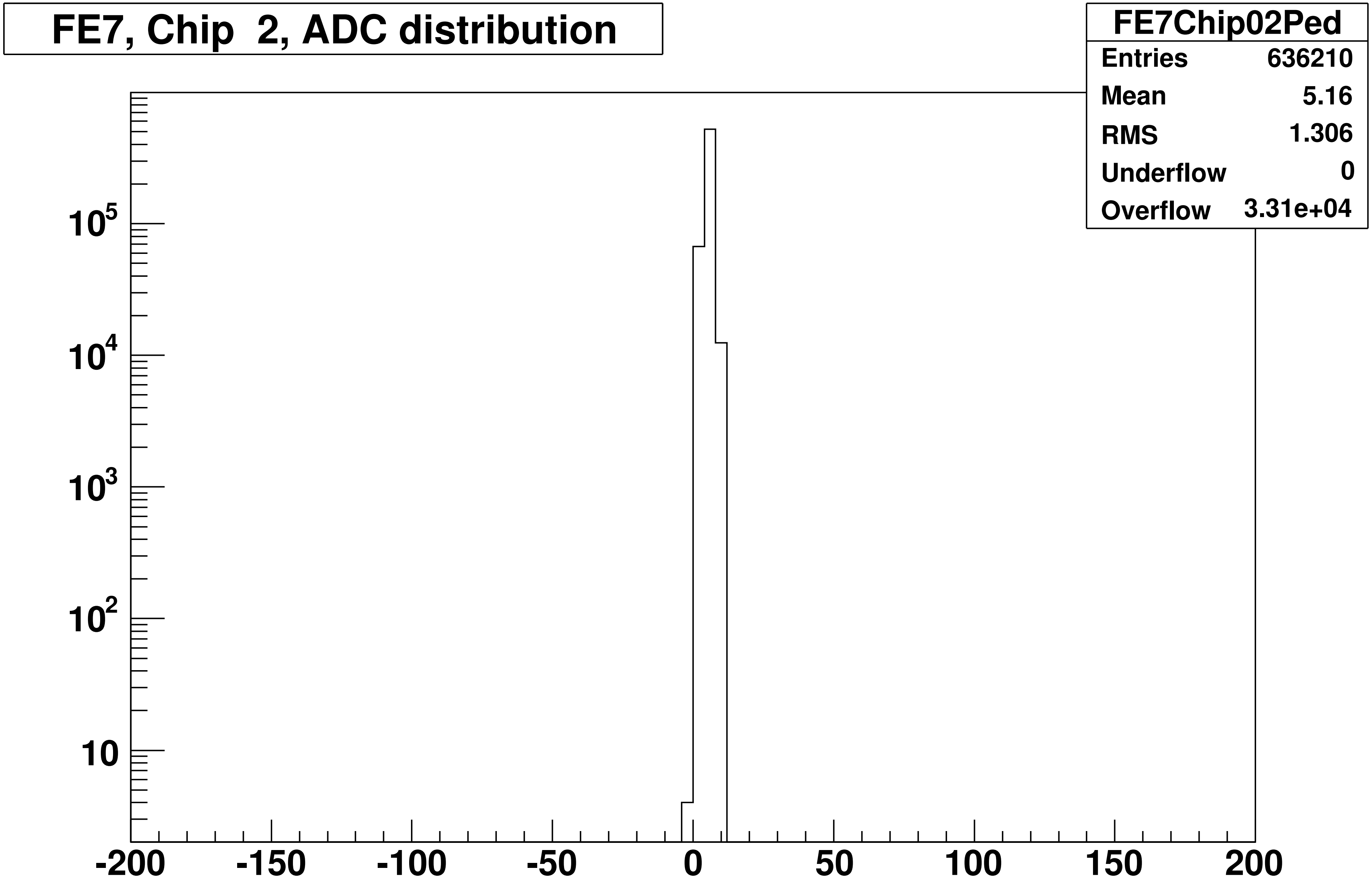
FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 636210
Mean 643.7
RMS 2724
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

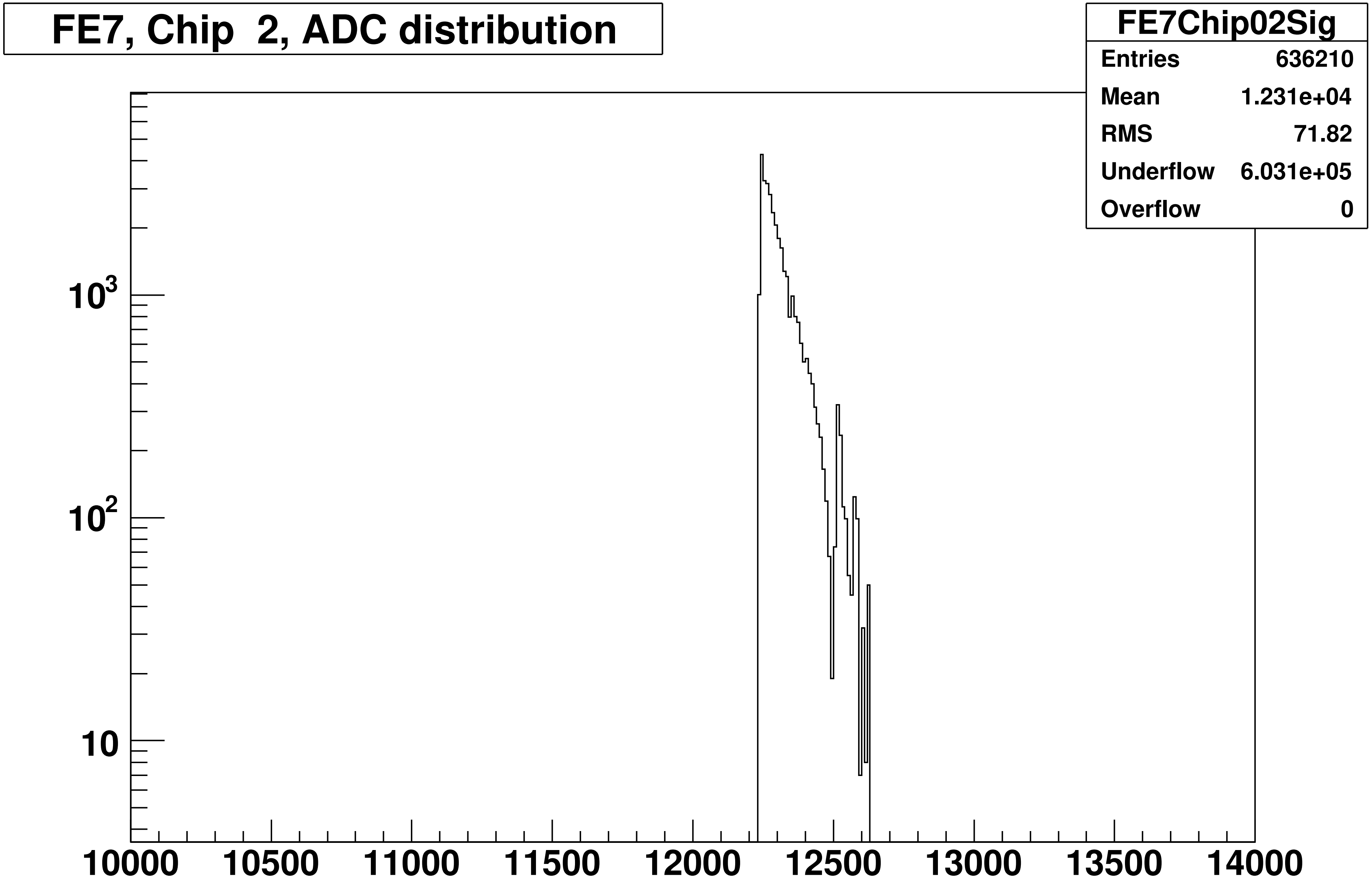

FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 636210
Mean 6.794
RMS 1.267
Underflow 0
Overflow 3.323e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



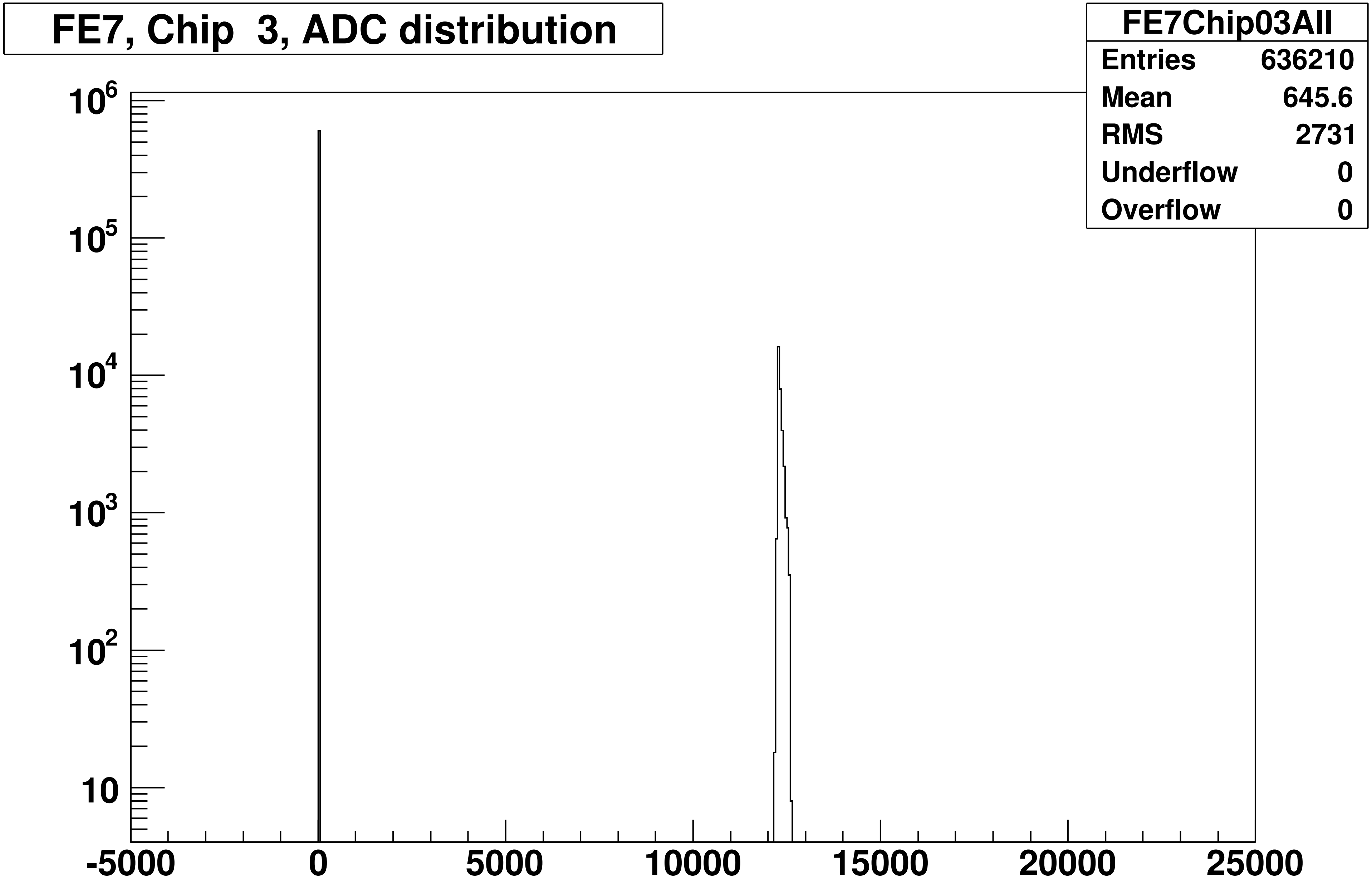
FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 636210
Mean 1.23e+04
RMS 83.18
Underflow 6.033e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

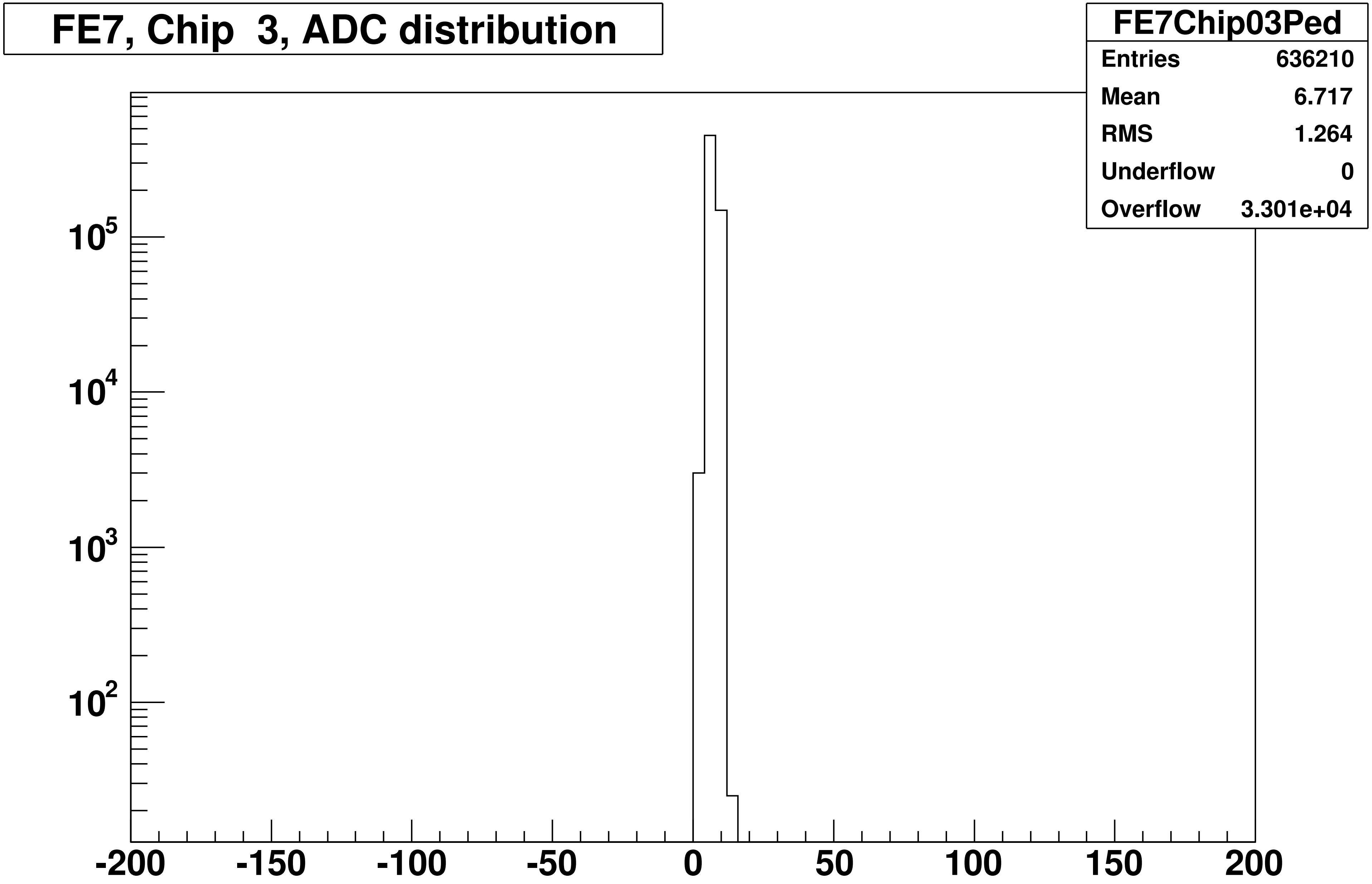

FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 636210
Mean 645.1
RMS 2732
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

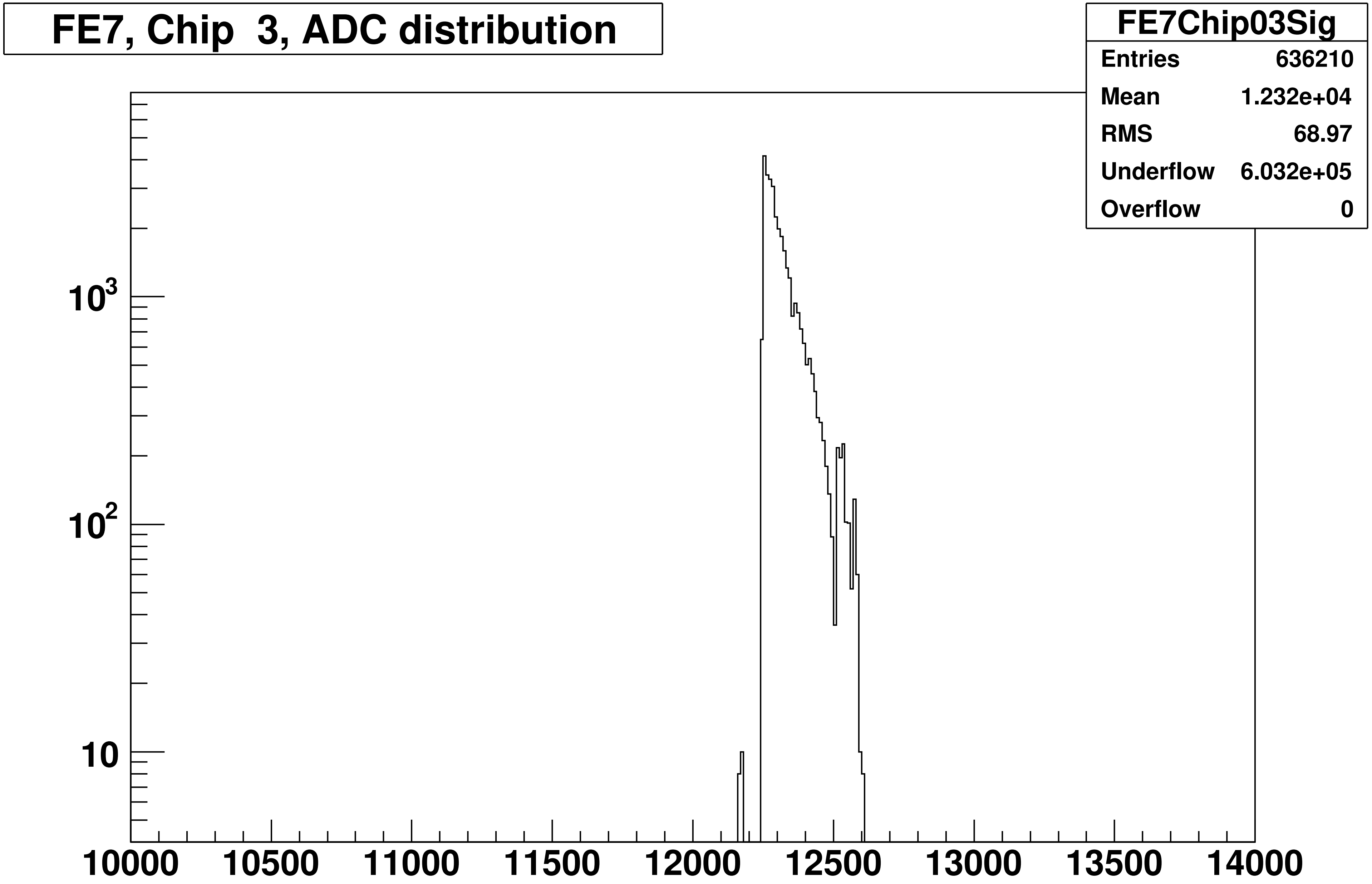

FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 636210
Mean 5.16
RMS 1.306
Underflow 0
Overflow 3.31e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



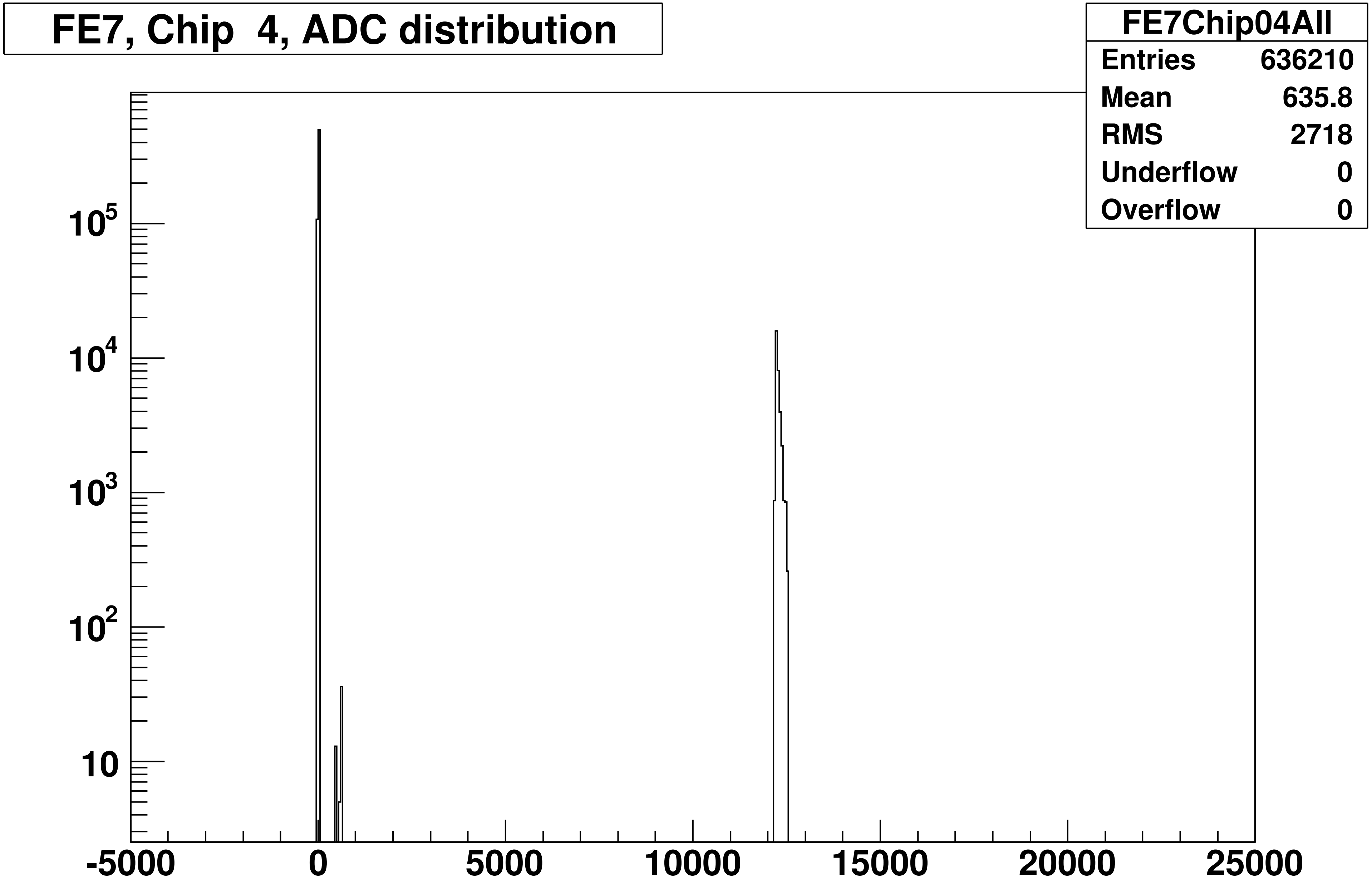
FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 636210
Mean 1.231e+04
RMS 71.82
Underflow 6.031e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 636210
Mean 645.6
RMS 2731
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

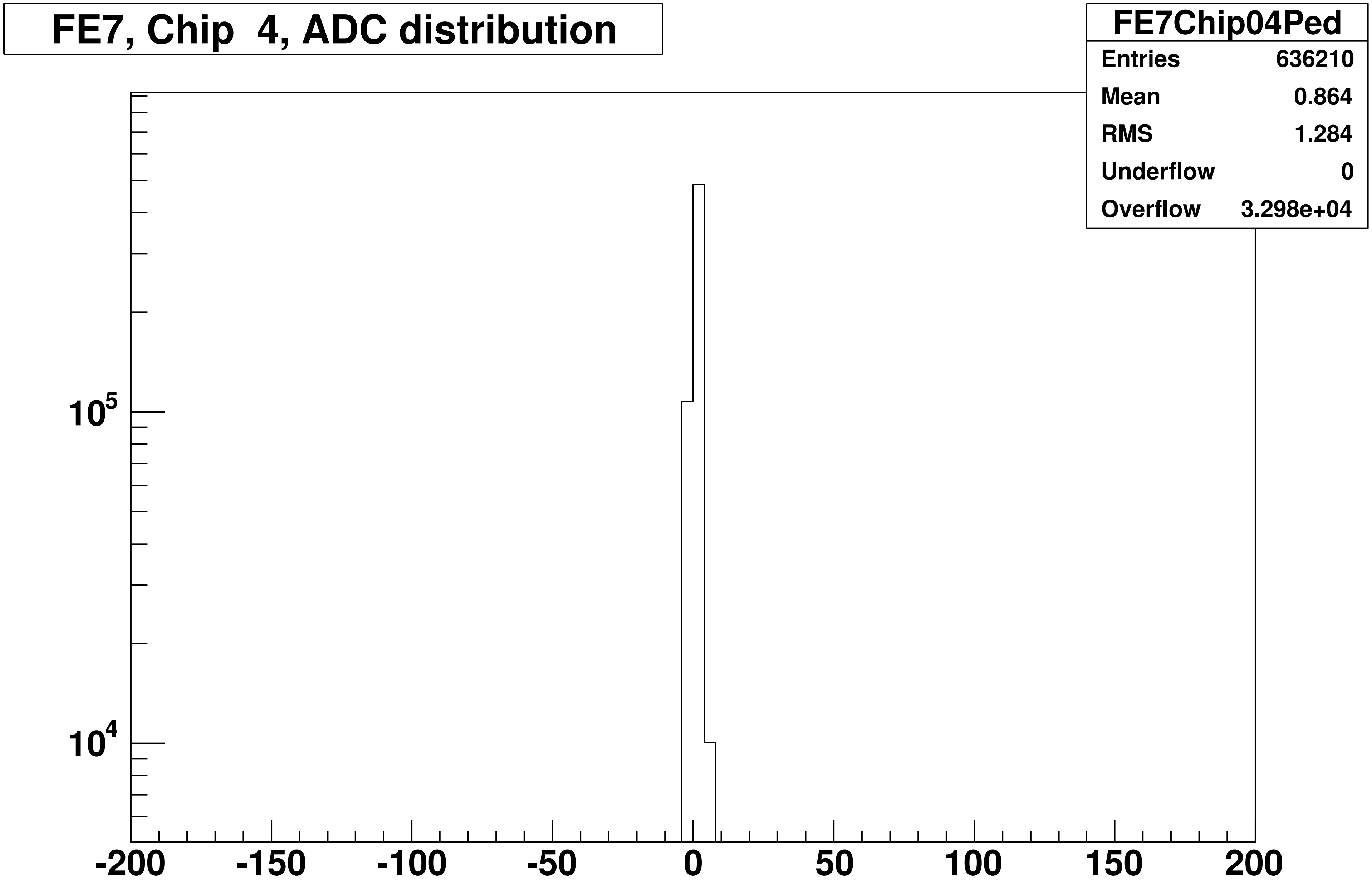

FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 636210
Mean 6.717
RMS 1.264
Underflow 0
Overflow 3.301e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

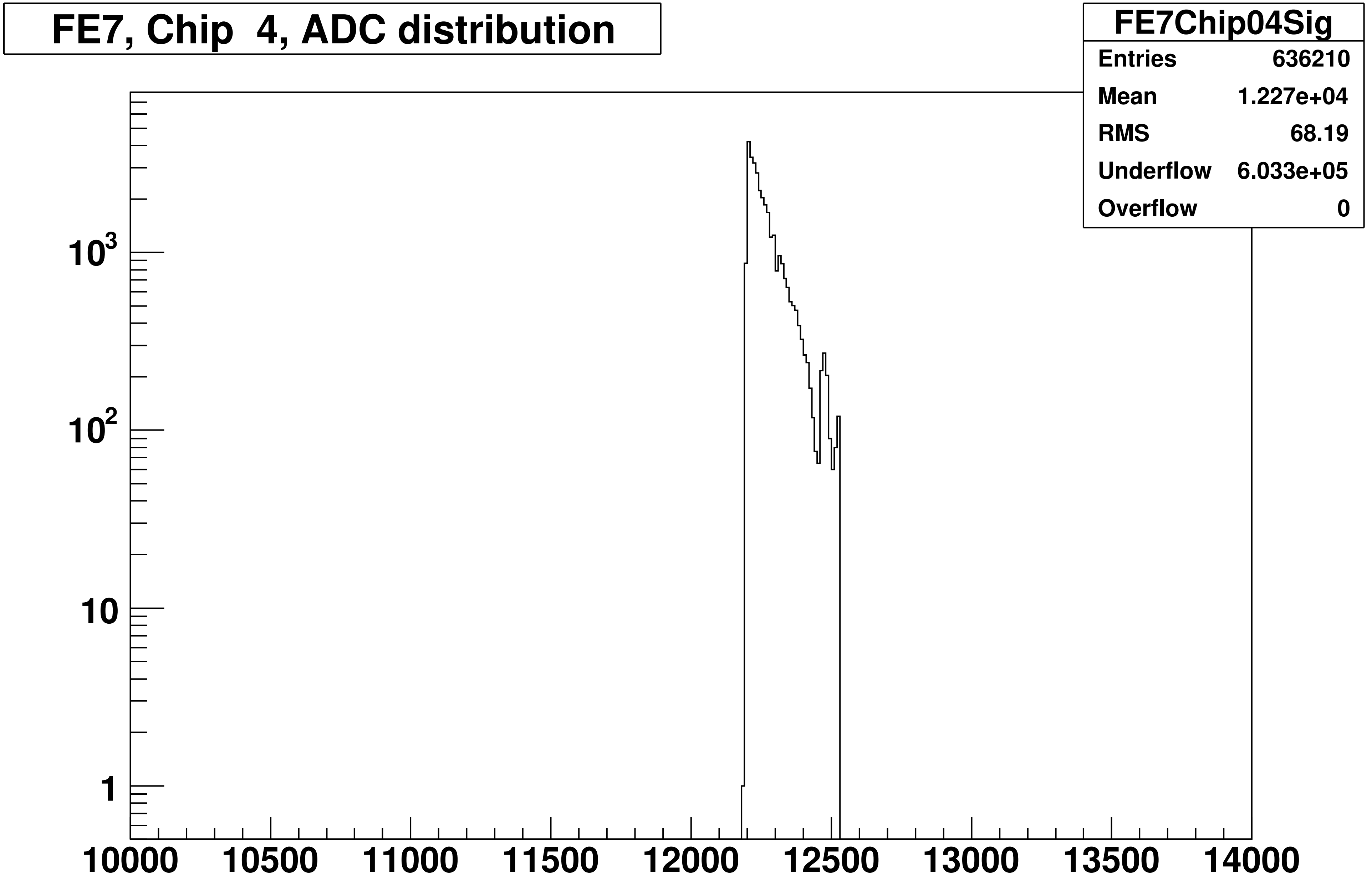


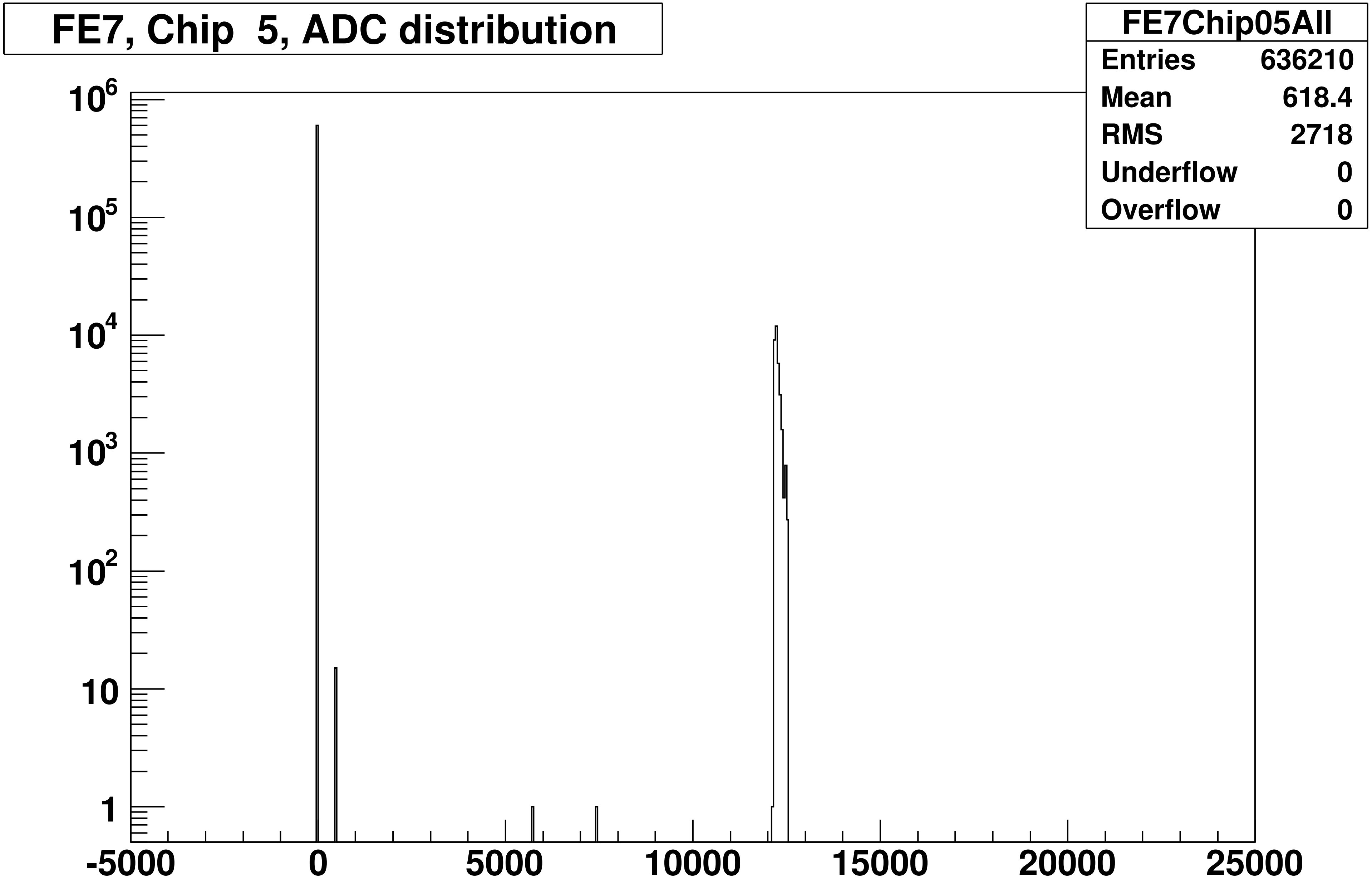
FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 636210
Mean 1.232e+04
RMS 68.97
Underflow 6.032e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000

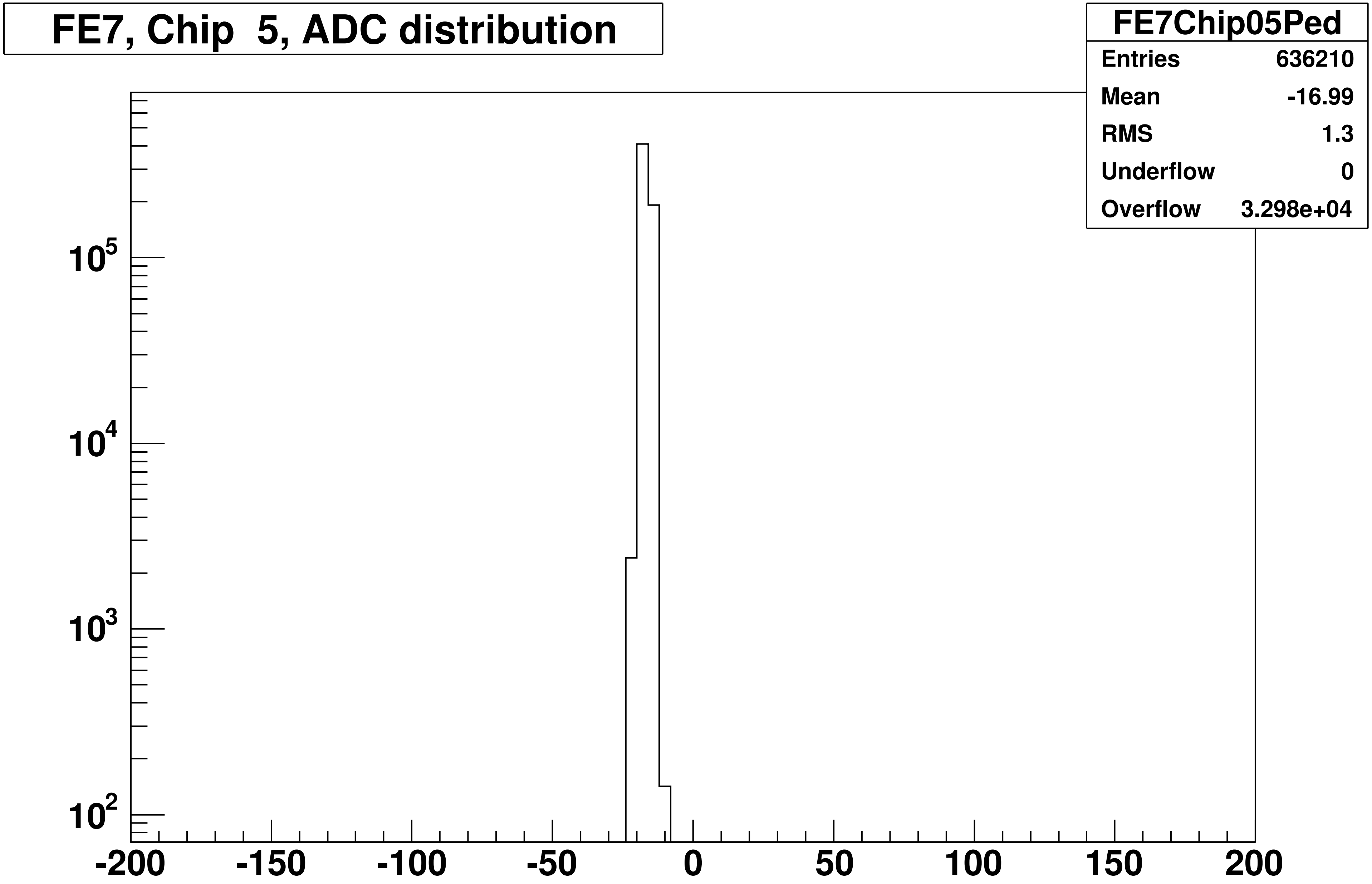


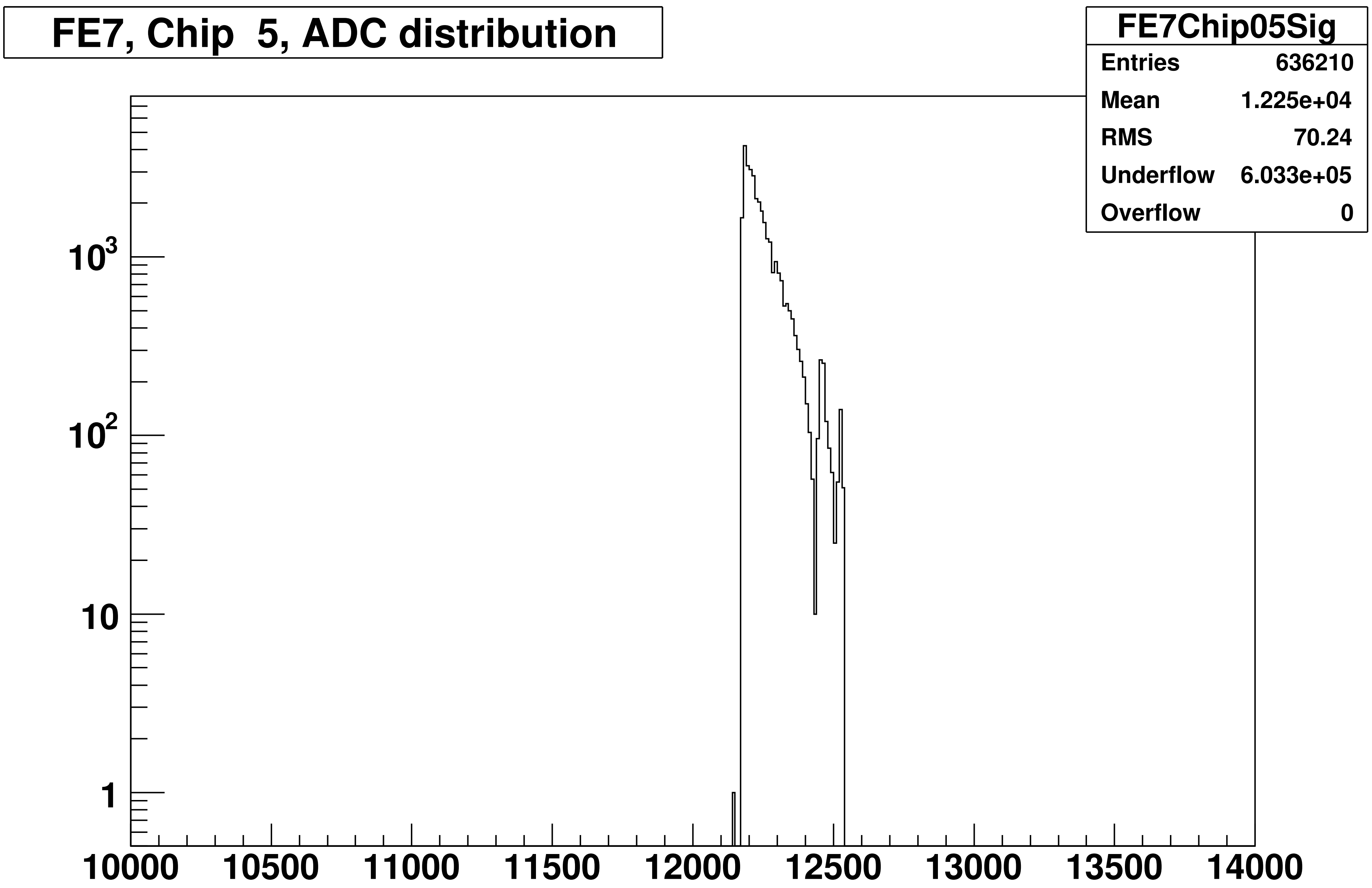
FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 636210
Mean 635.8
RMS 2718
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

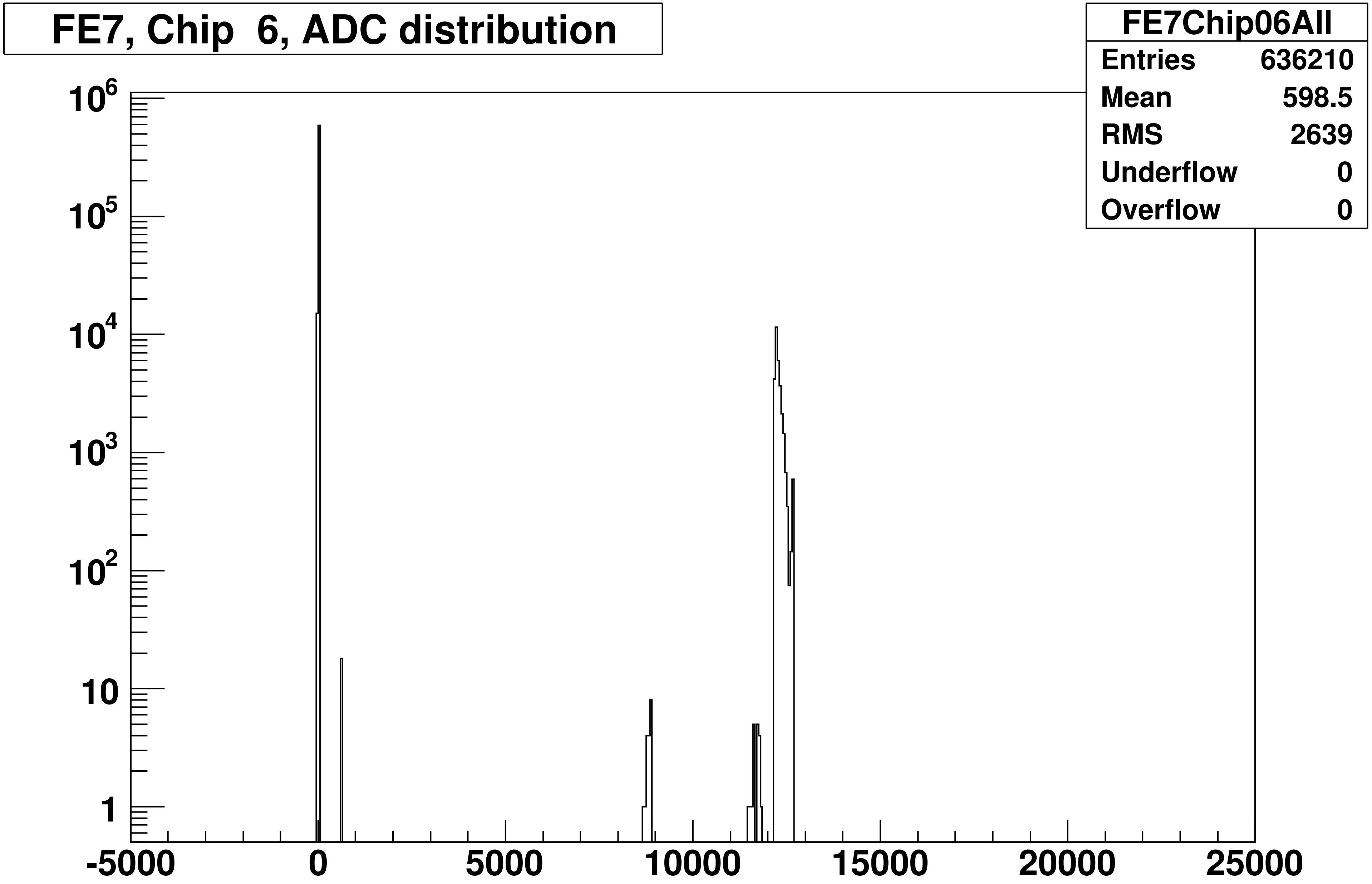

FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 636210
Mean 0.864
RMS 1.284
Underflow 0
Overflow 3.298e+04
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 636210
Mean 1.227e+04
RMS 68.19
Underflow 6.033e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

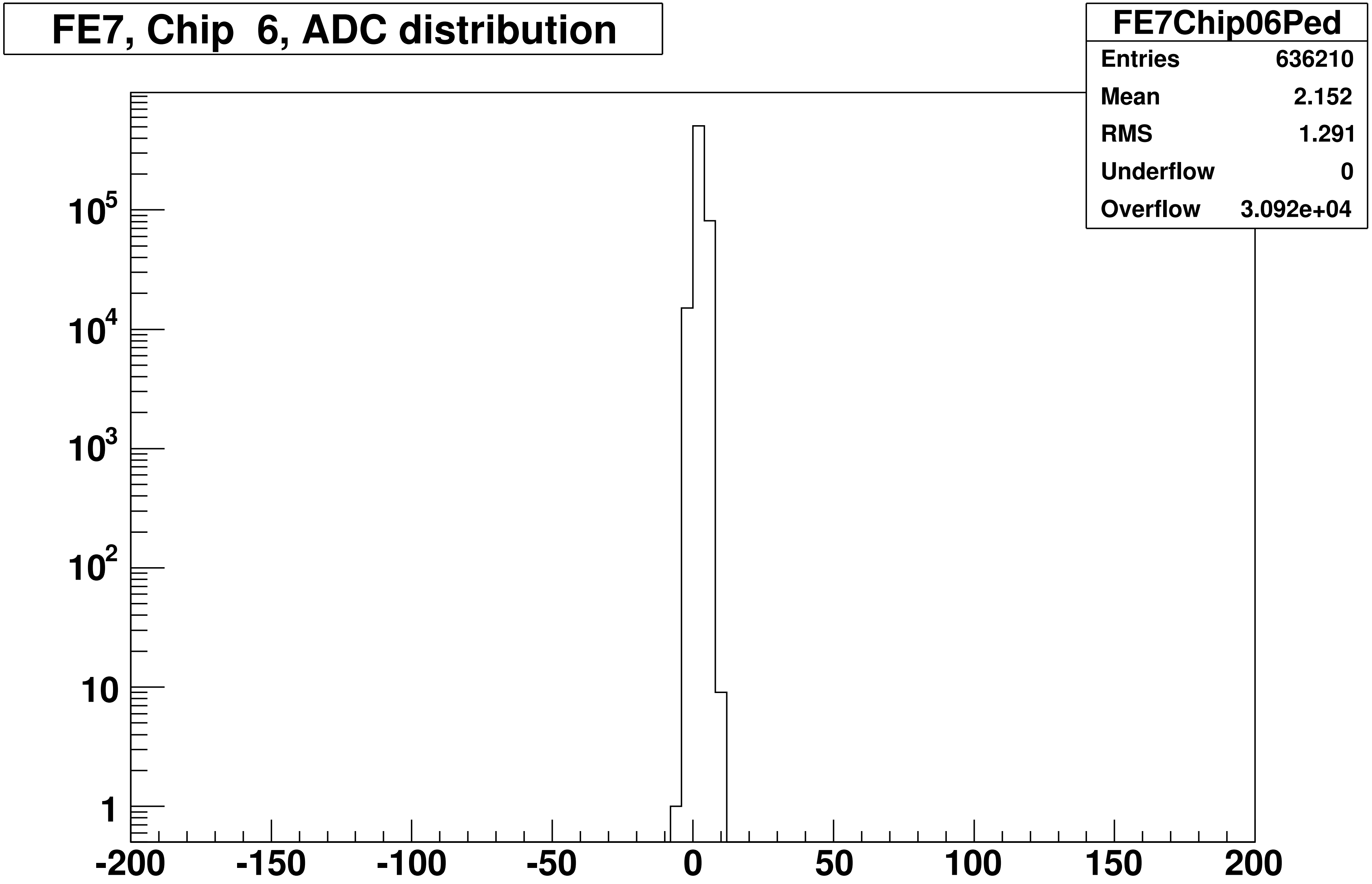

FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 636210
Mean 618.4
RMS 2718
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 636210
Mean -16.99
RMS 1.3
Underflow 0
Overflow 3.298e+04
10^5
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200

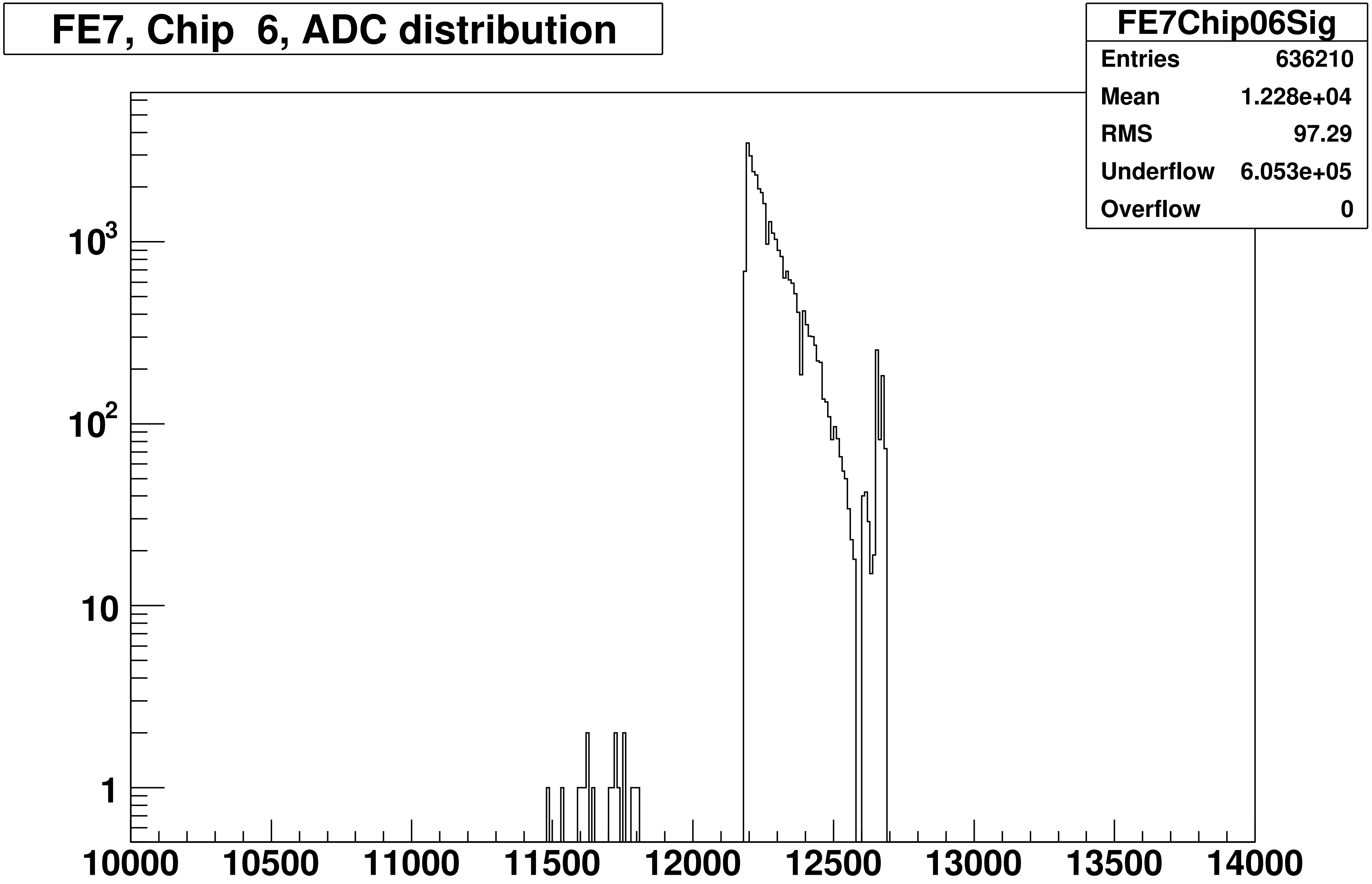

FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 636210
Mean 1.225e+04
RMS 70.24
Underflow 6.033e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000



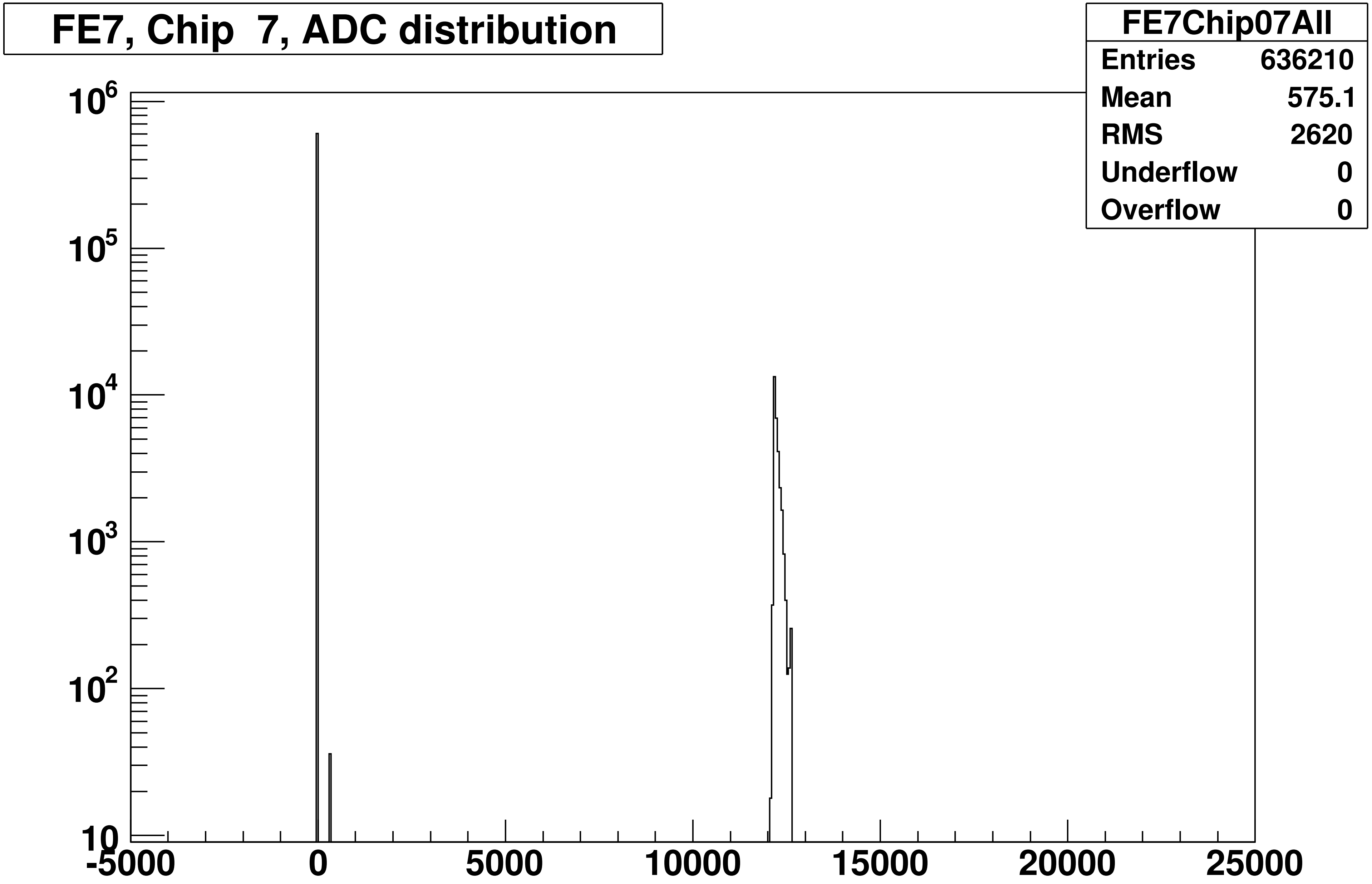
FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 636210
Mean 598.5
RMS 2639
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



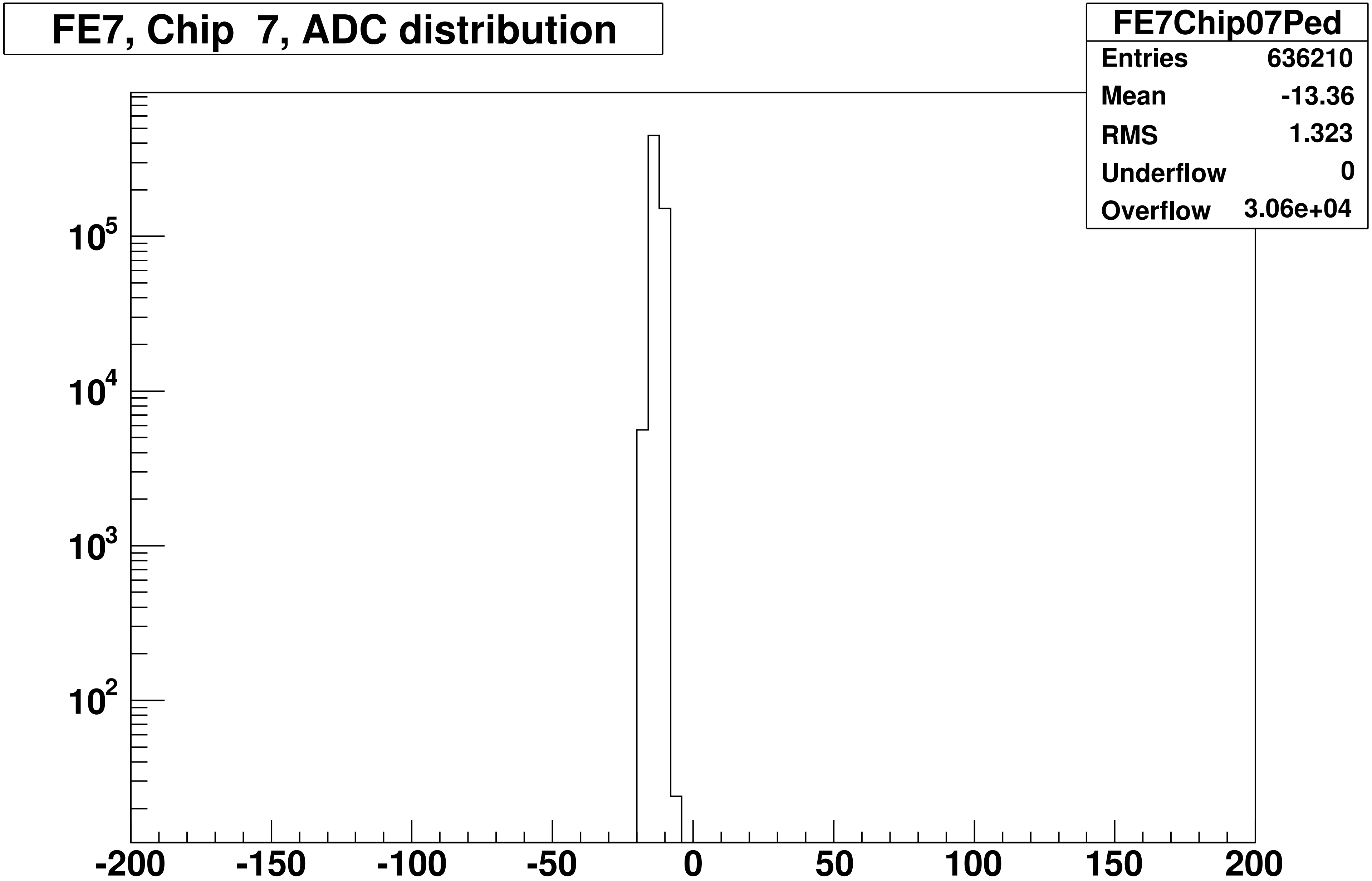
FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 636210
Mean 2.152
RMS 1.291
Underflow 0
Overflow 3.092e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



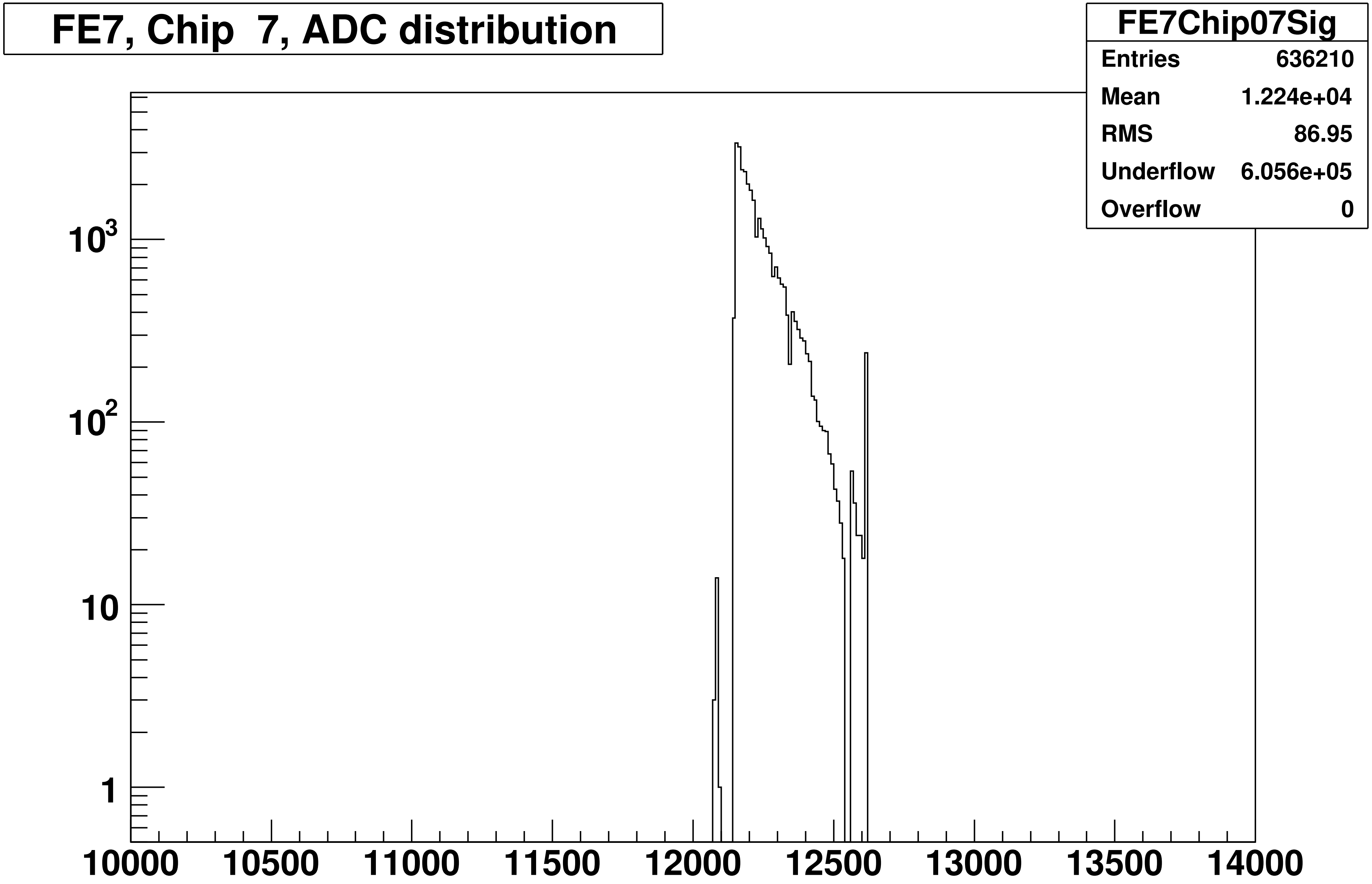
FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 636210
Mean 1.228e+04
RMS 97.29
Underflow 6.053e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000



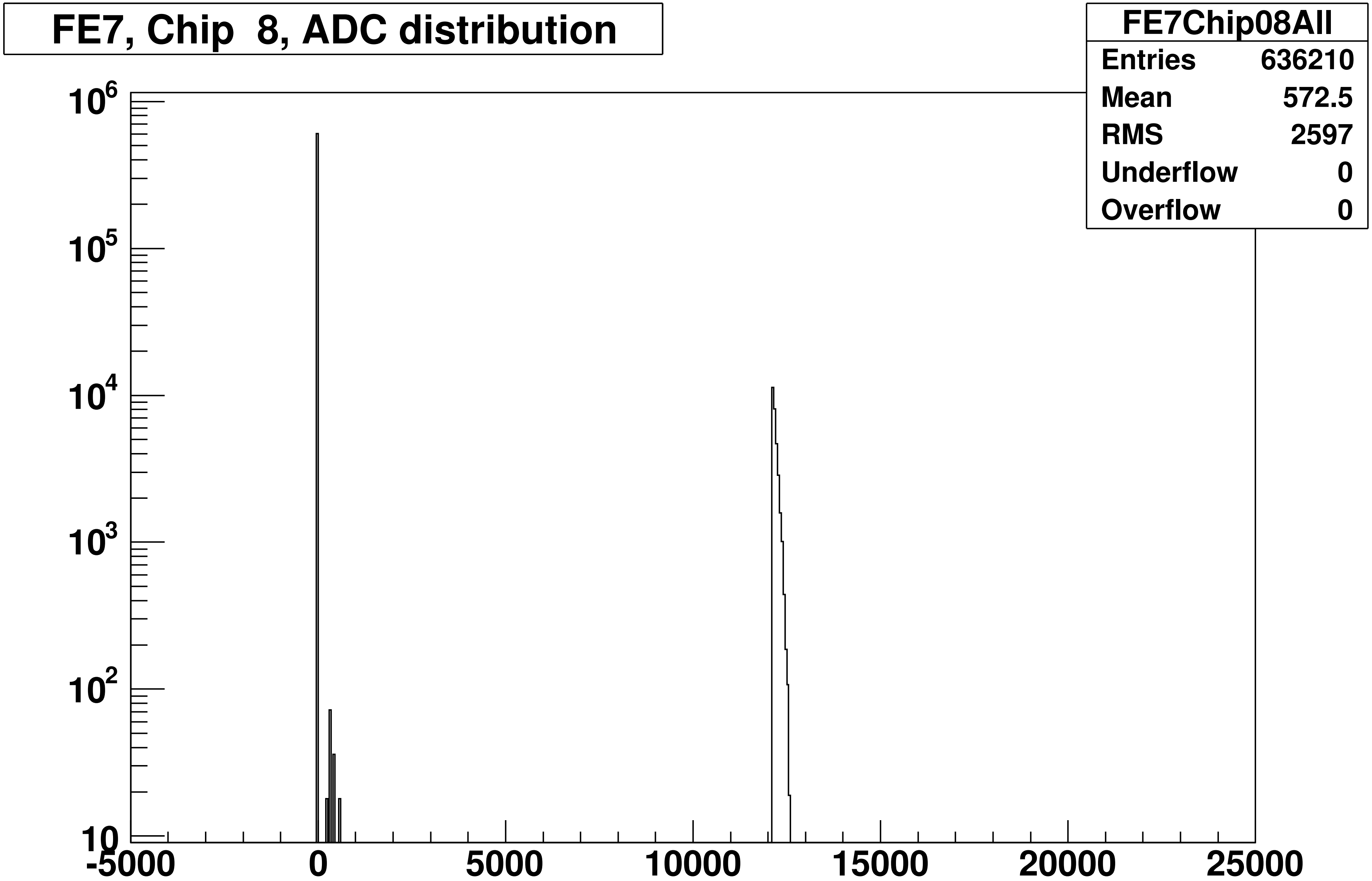
FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 636210
Mean 575.1
RMS 2620
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



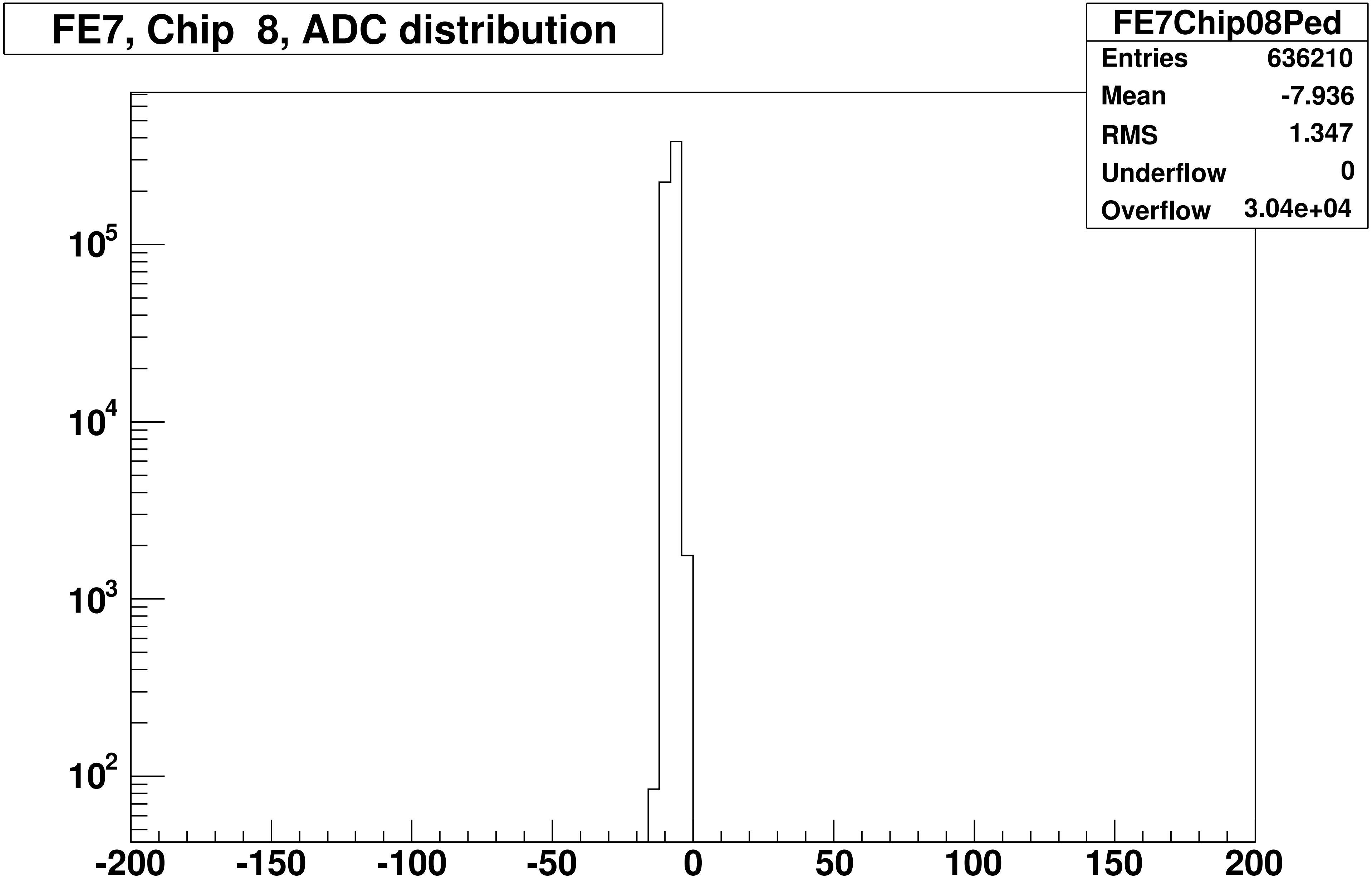
FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 636210
Mean -13.36
RMS 1.323
Underflow 0
Overflow 3.06e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

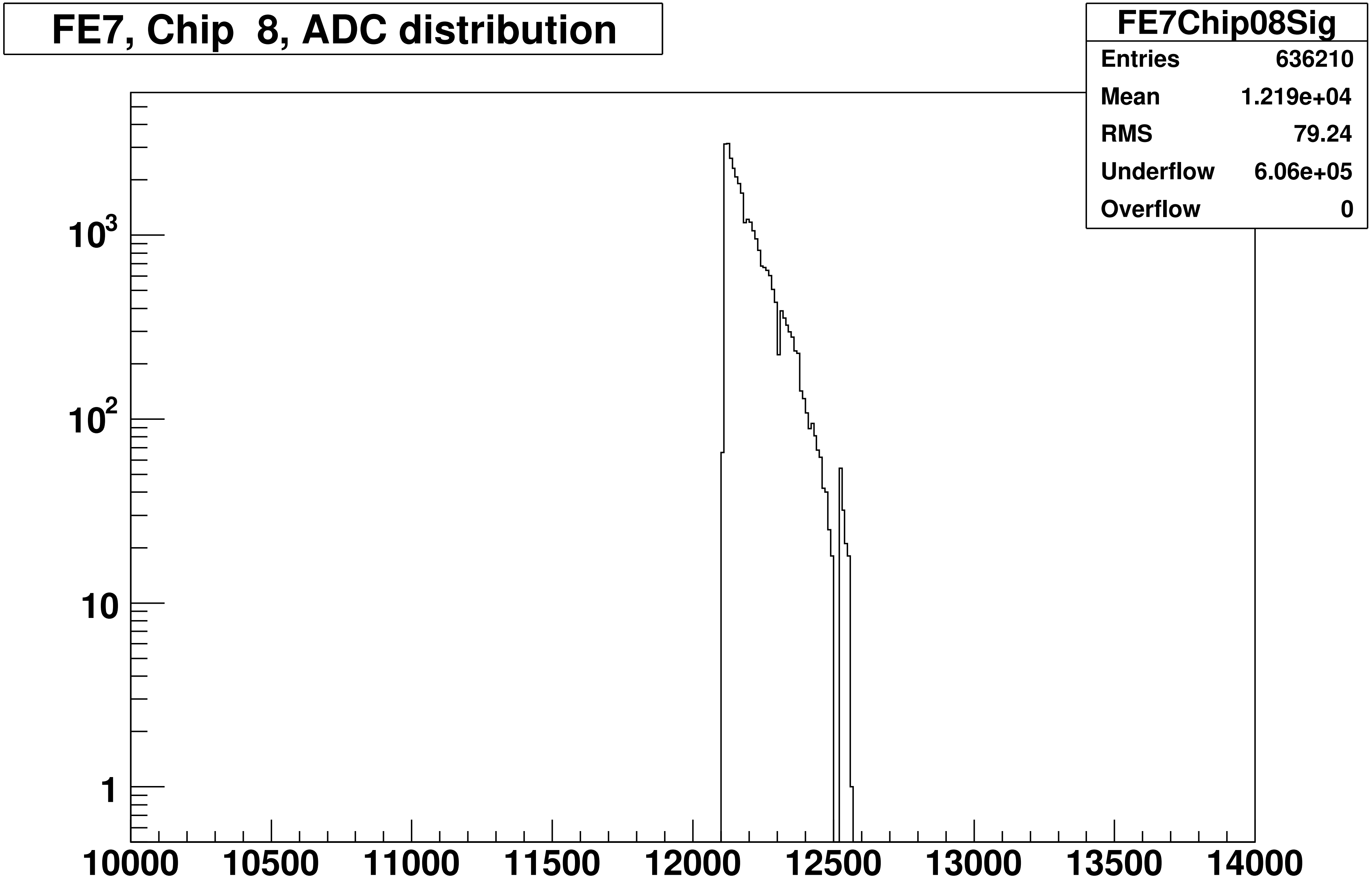


FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 636210
Mean 1.224e+04
RMS 86.95
Underflow 6.056e+05
Overflow 0
10³
10²
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

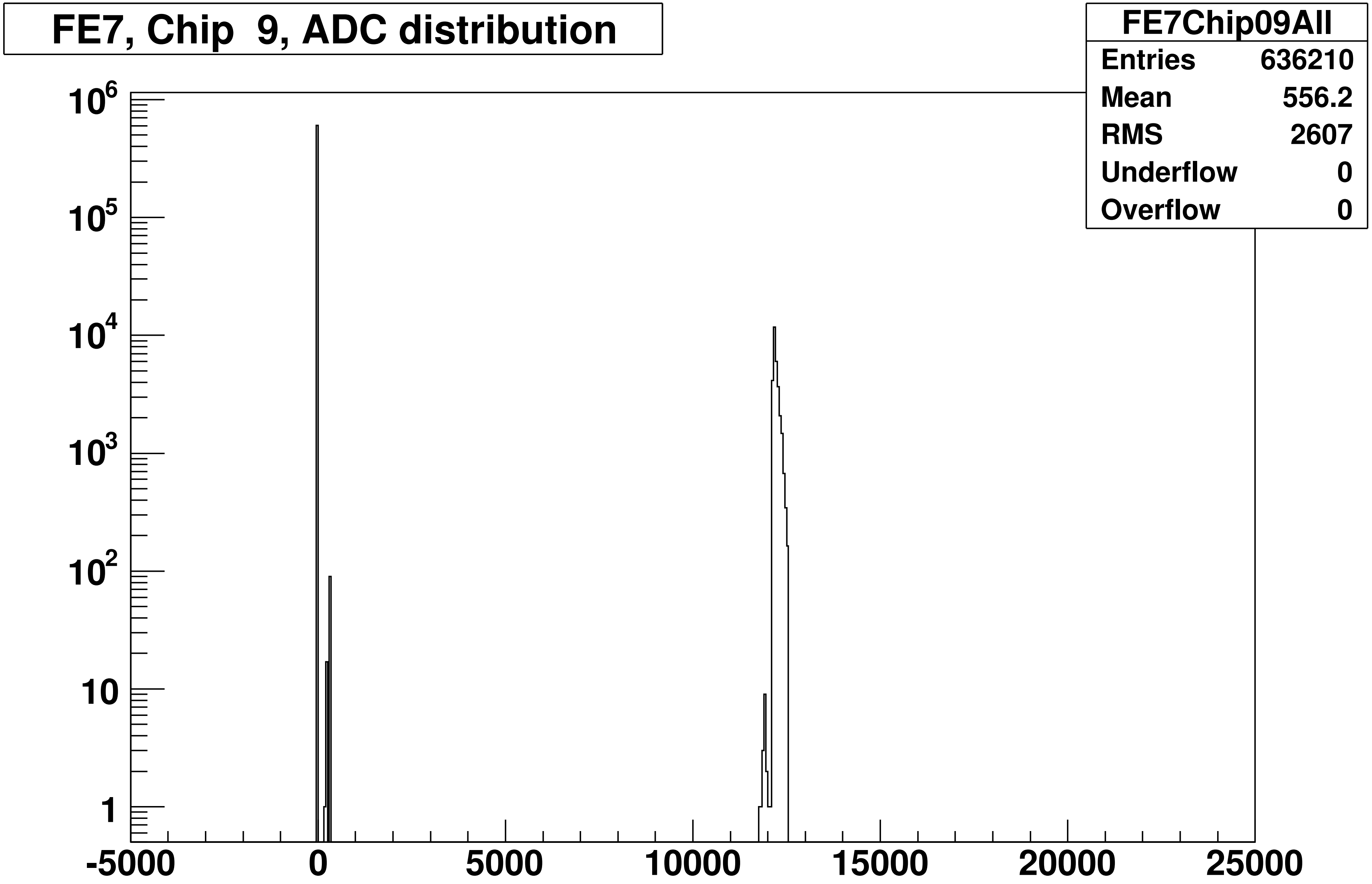


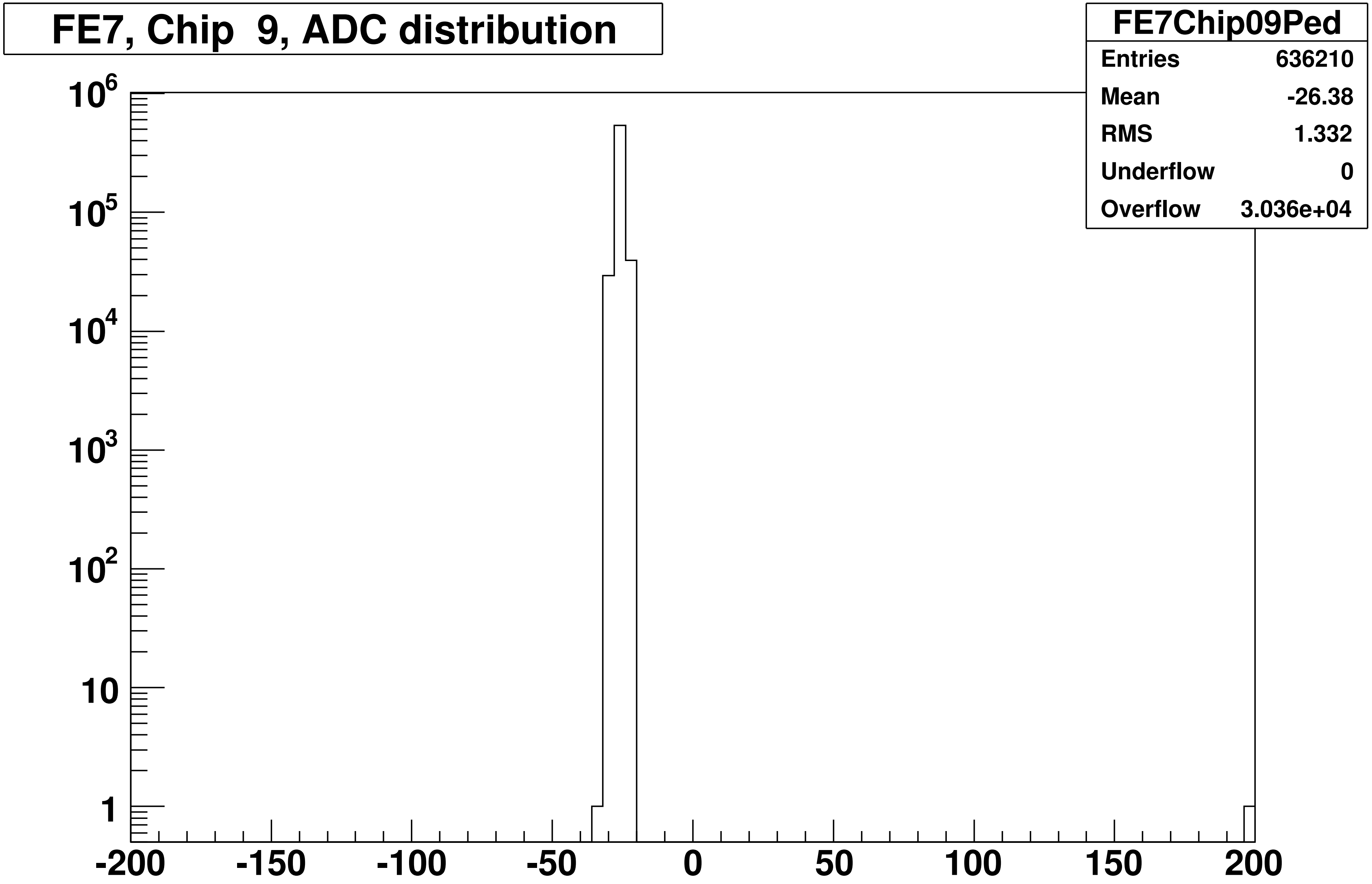
FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 636210
Mean 572.5
RMS 2597
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 636210
Mean -7.936
RMS 1.347
Underflow 0
Overflow 3.04e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

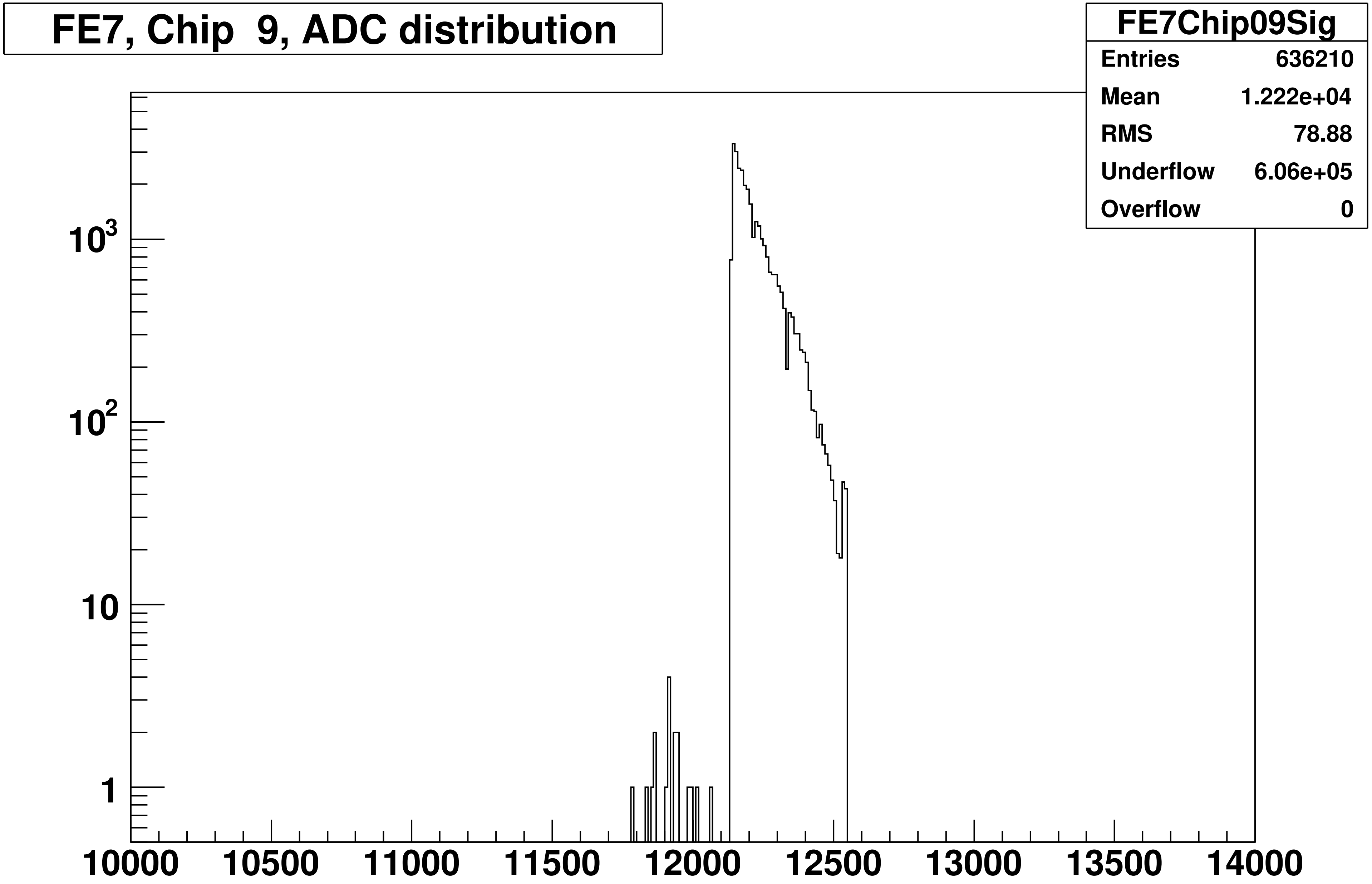


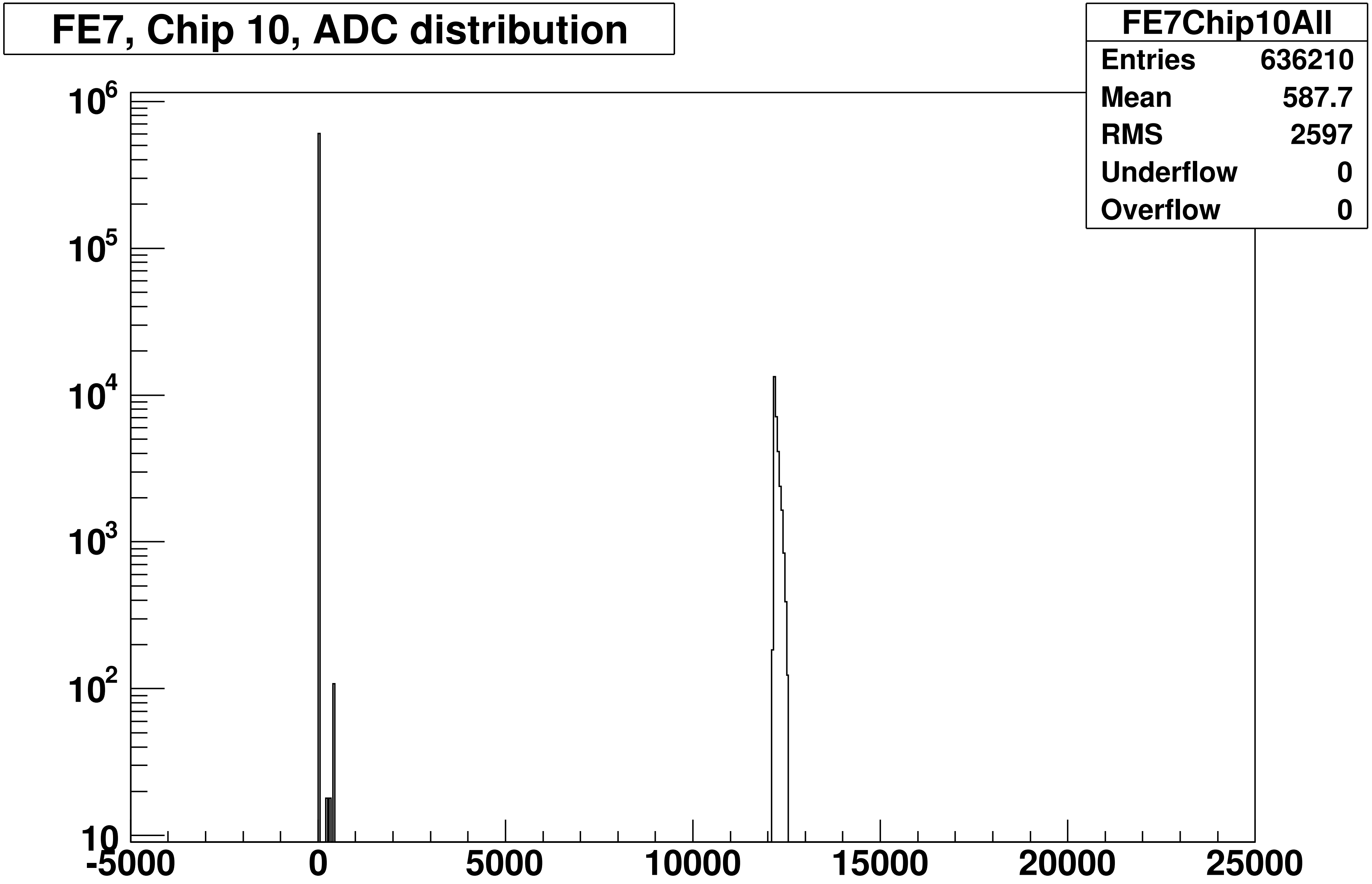
FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 636210
Mean 1.219e+04
RMS 79.24
Underflow 6.06e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

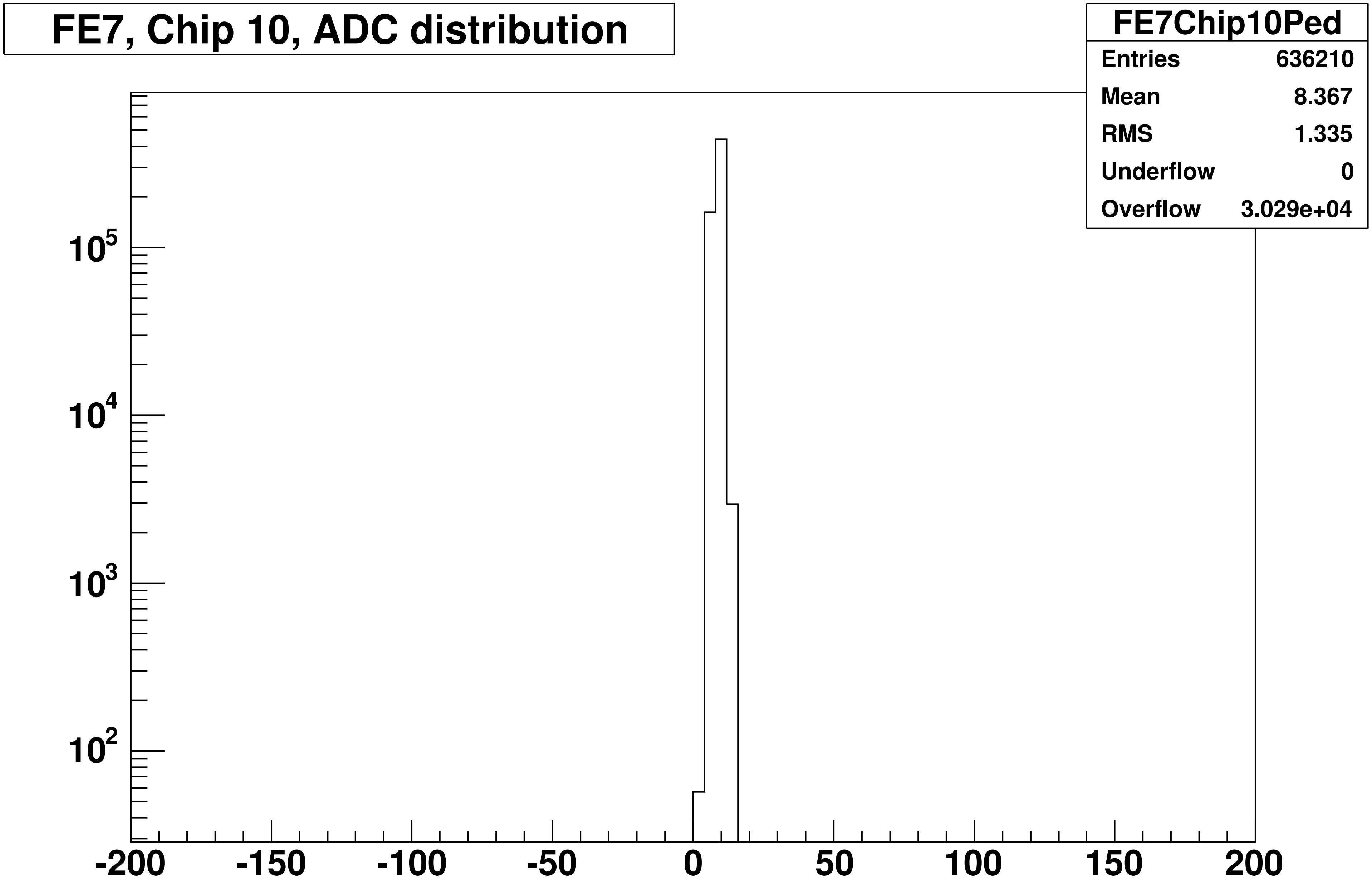

FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 636210
Mean 556.2
RMS 2607
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

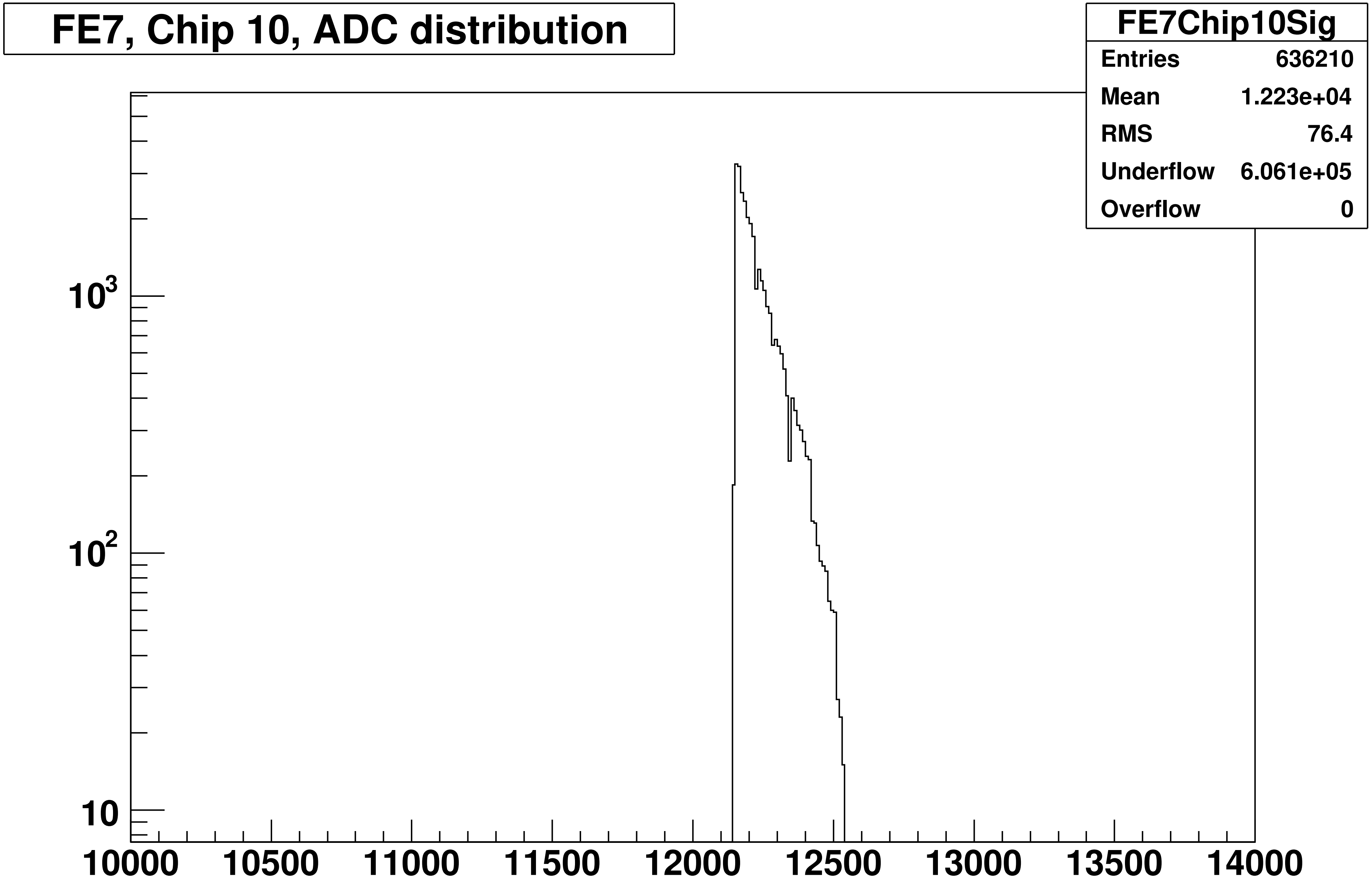


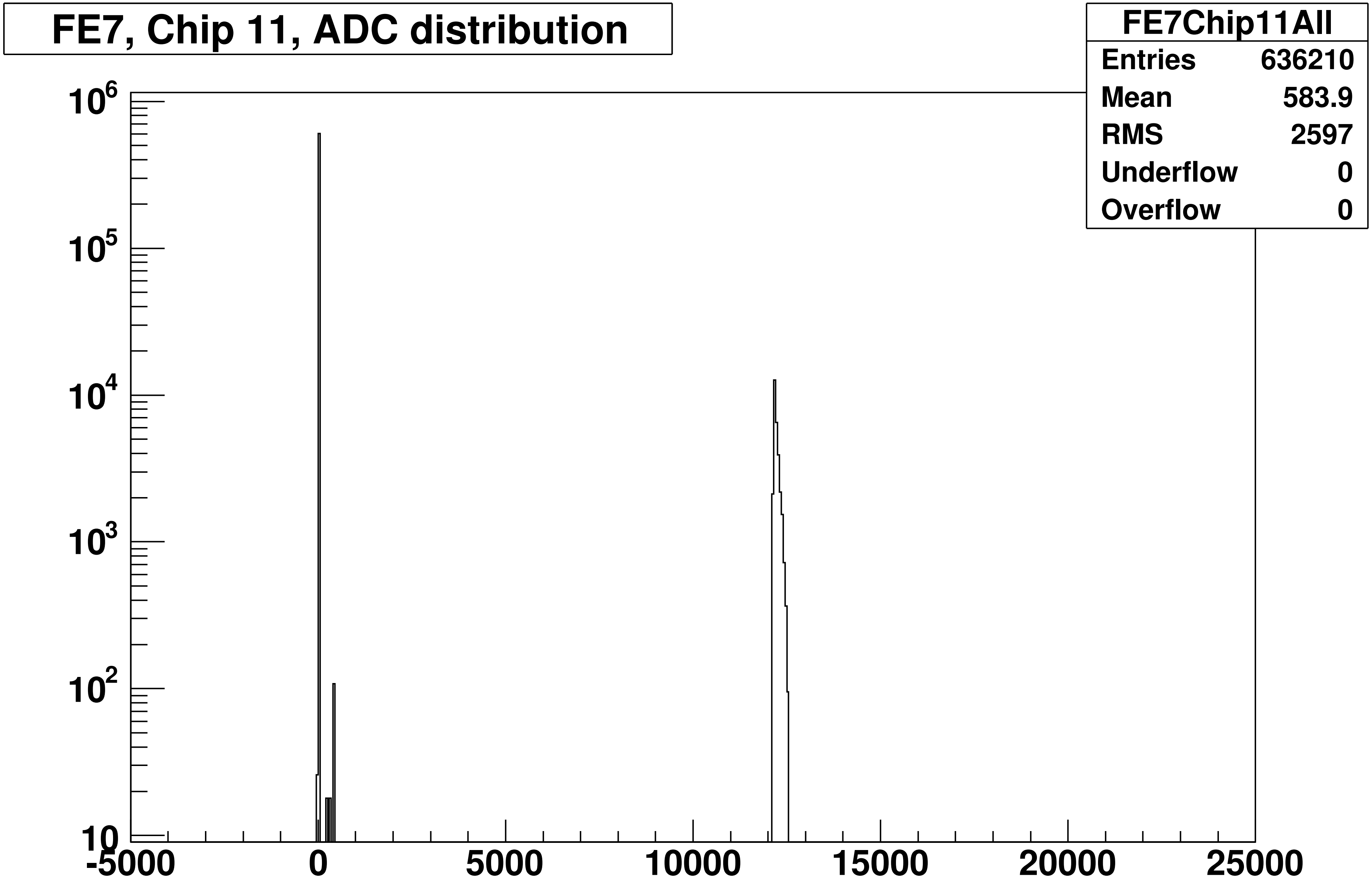
FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 636210
Mean -26.38
RMS 1.332
Underflow 0
Overflow 3.036e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

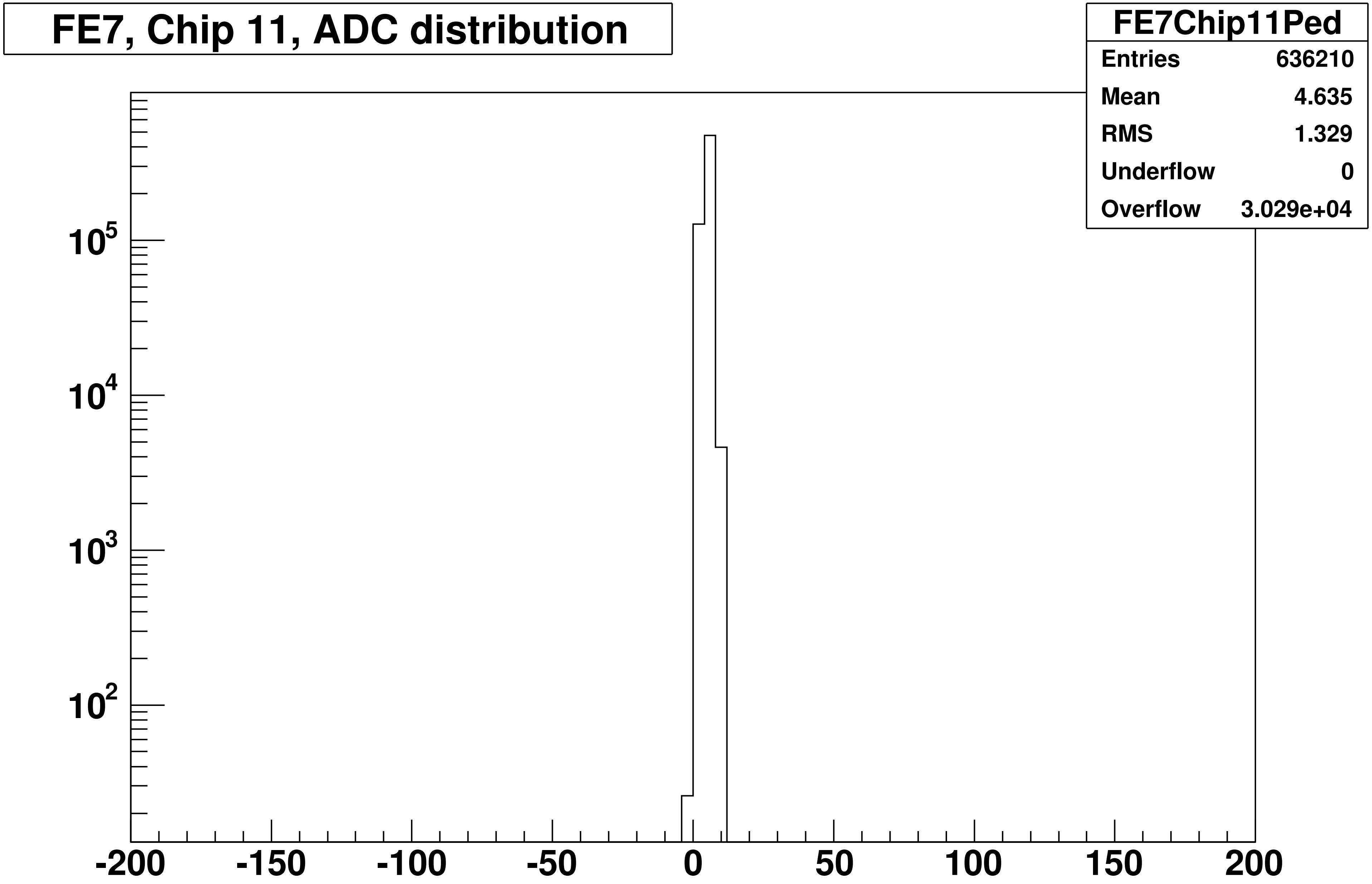

FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 636210
Mean 1.222e+04
RMS 78.88
Underflow 6.06e+05
Overflow 0
1
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000

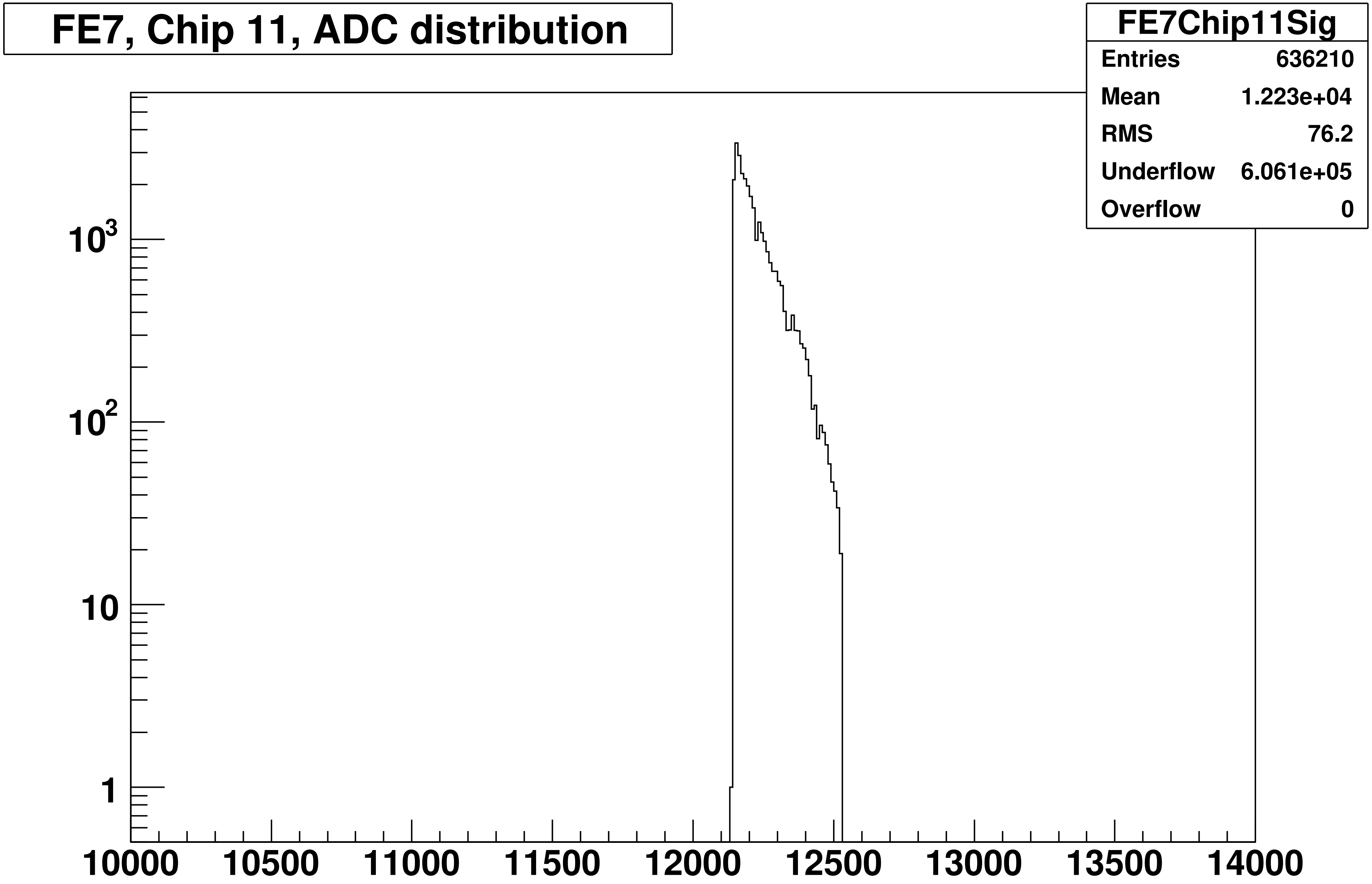

FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 636210
Mean 587.7
RMS 2597
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

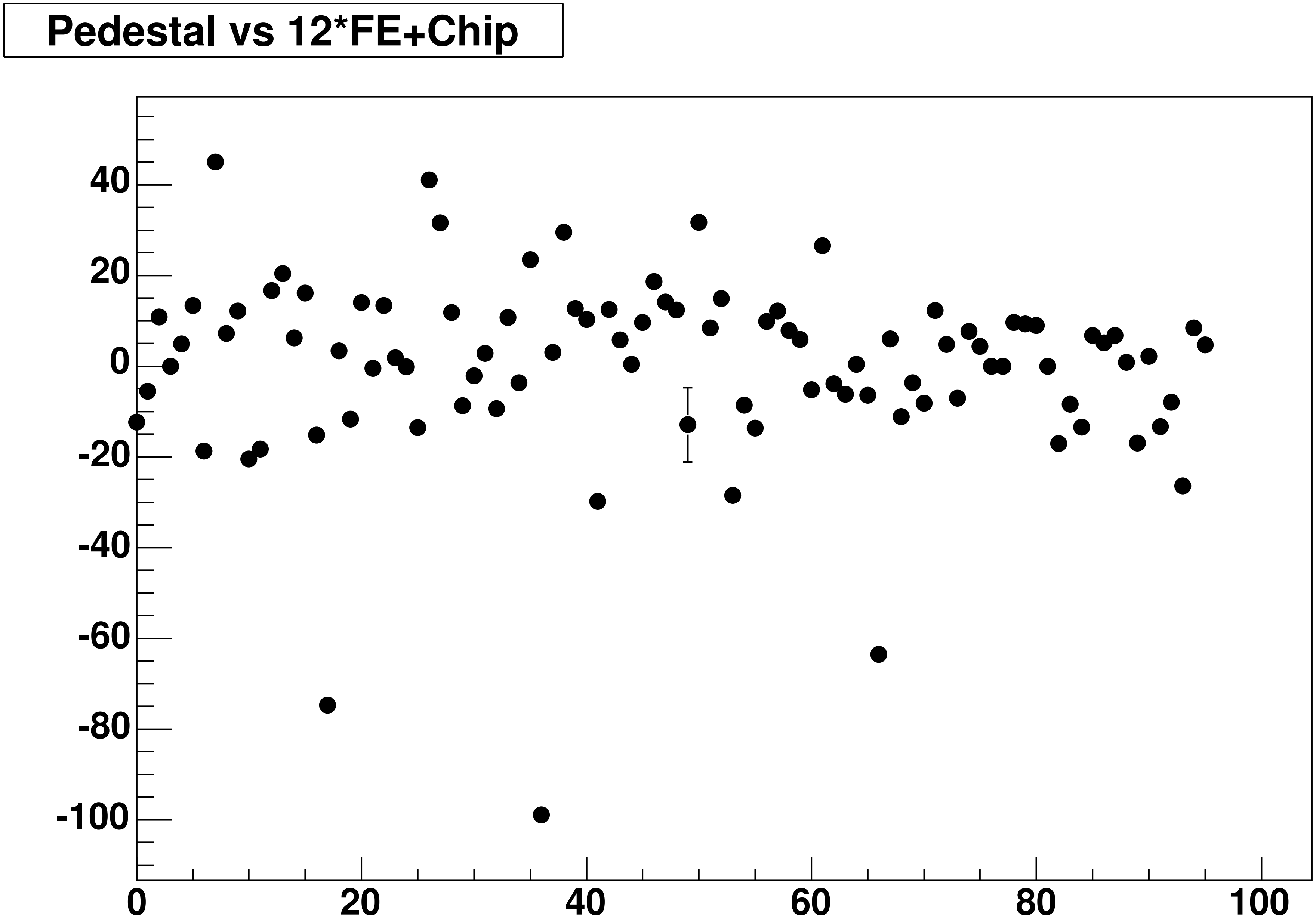

FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 636210
Mean 8.367
RMS 1.335
Underflow 0
Overflow 3.029e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 636210
Mean 1.223e+04
RMS 76.4
Underflow 6.061e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000

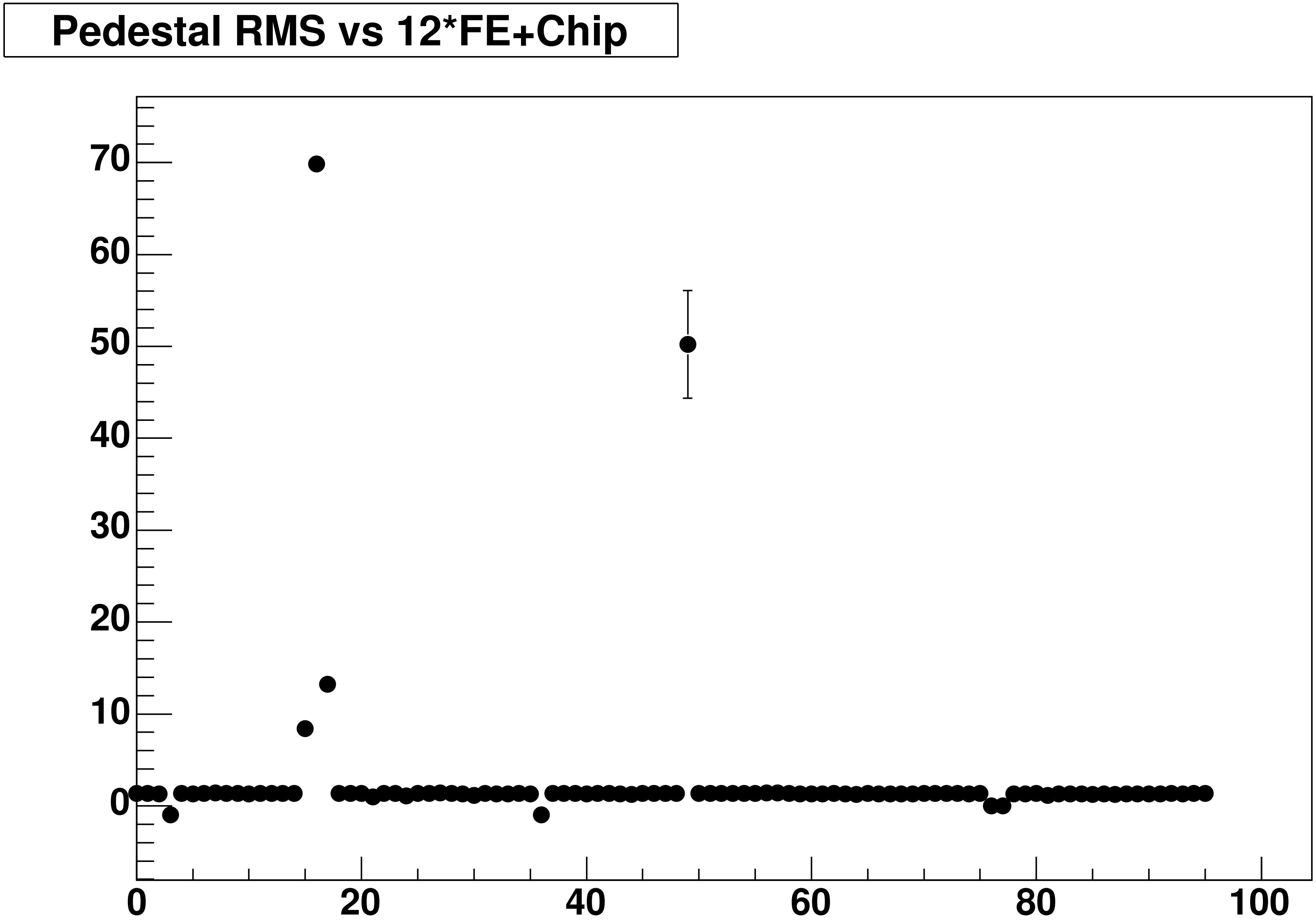

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 636210
Mean 583.9
RMS 2597
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 636210
Mean 4.635
RMS 1.329
Underflow 0
Overflow 3.029e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

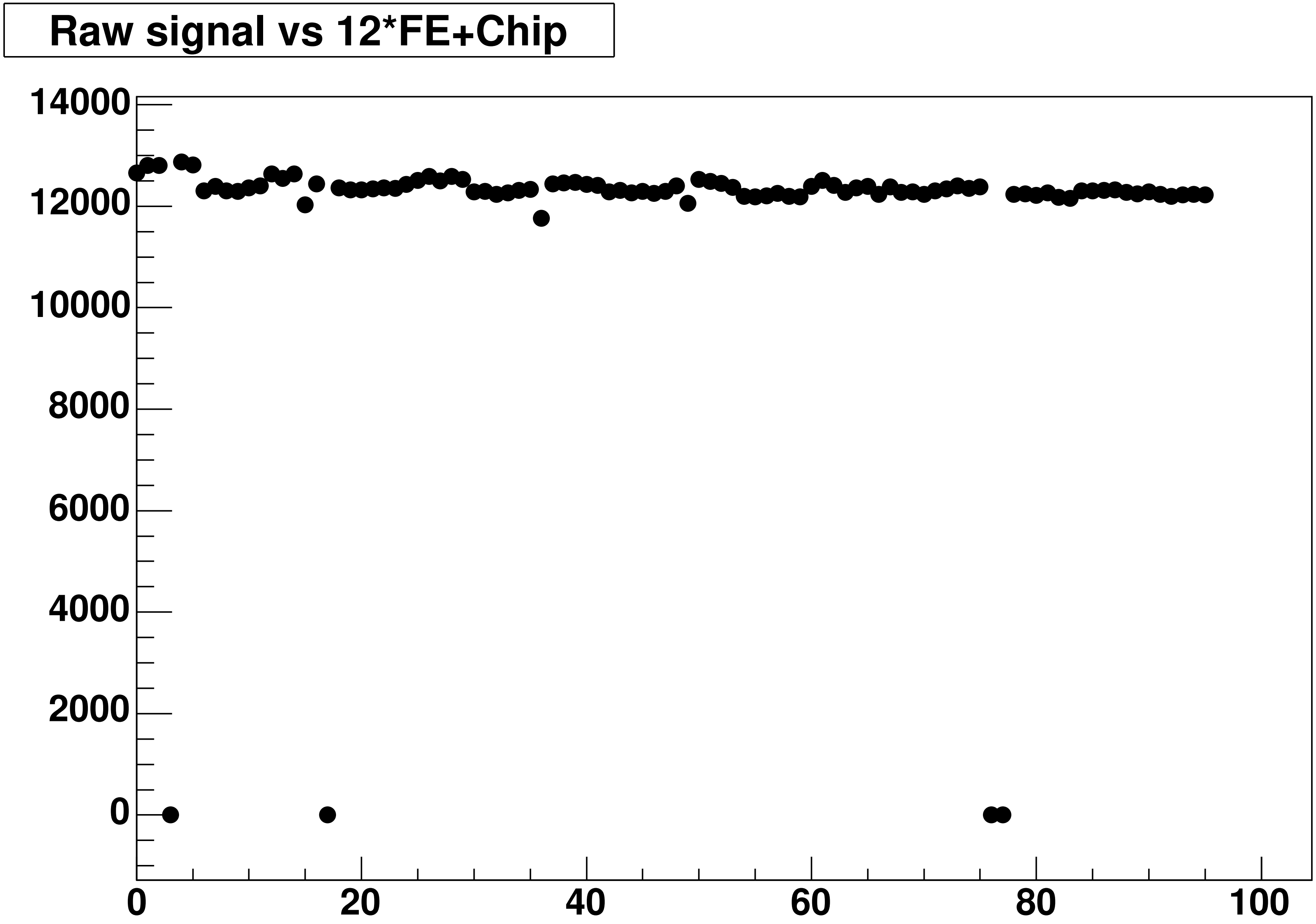

FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 636210
Mean 1.223e+04
RMS 76.2
Underflow 6.061e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000



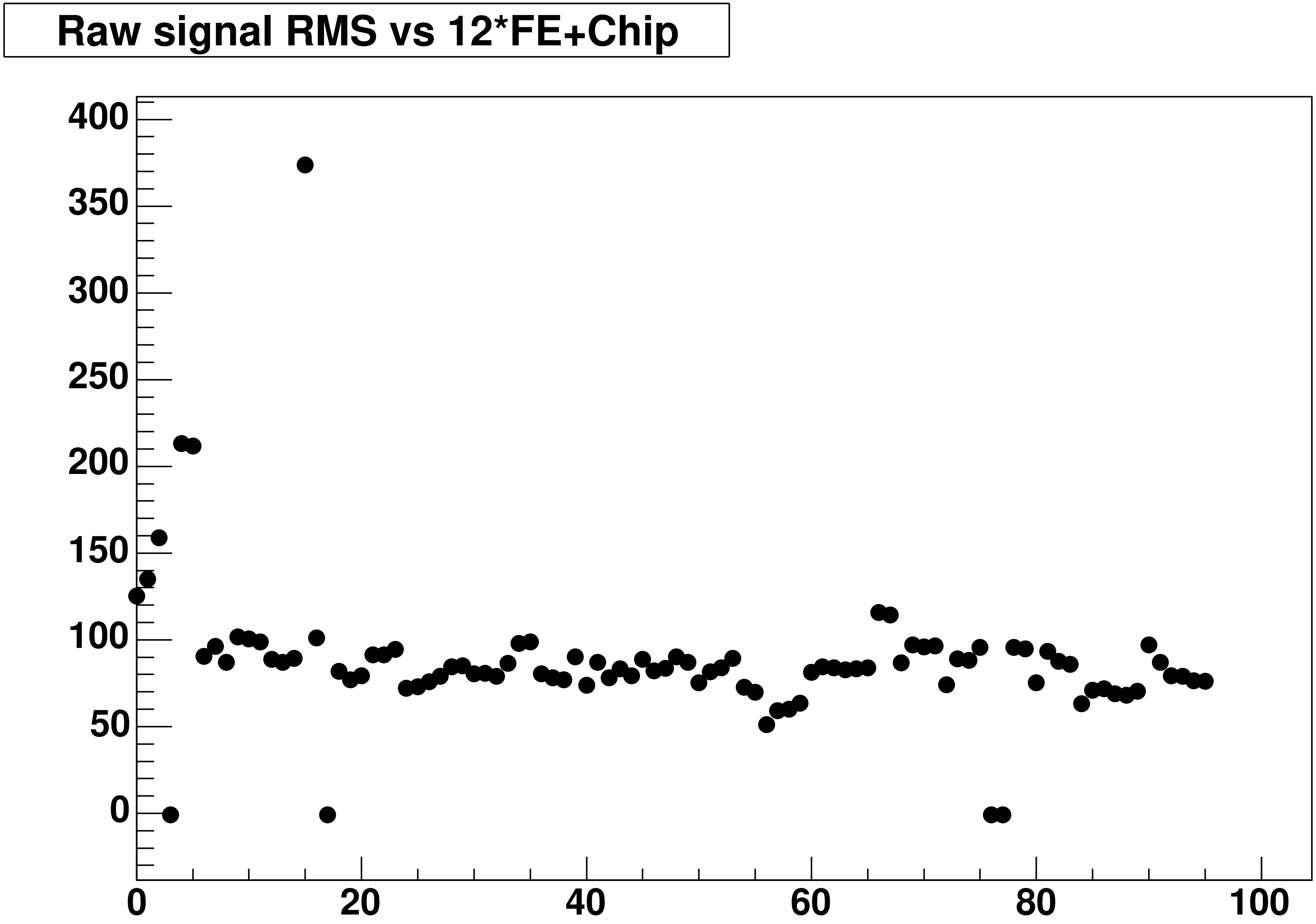
Pedestal vs 12*FE+Chip
40
20
0
-20
-40
-60
-80
-100
0
20
40
60
80
100

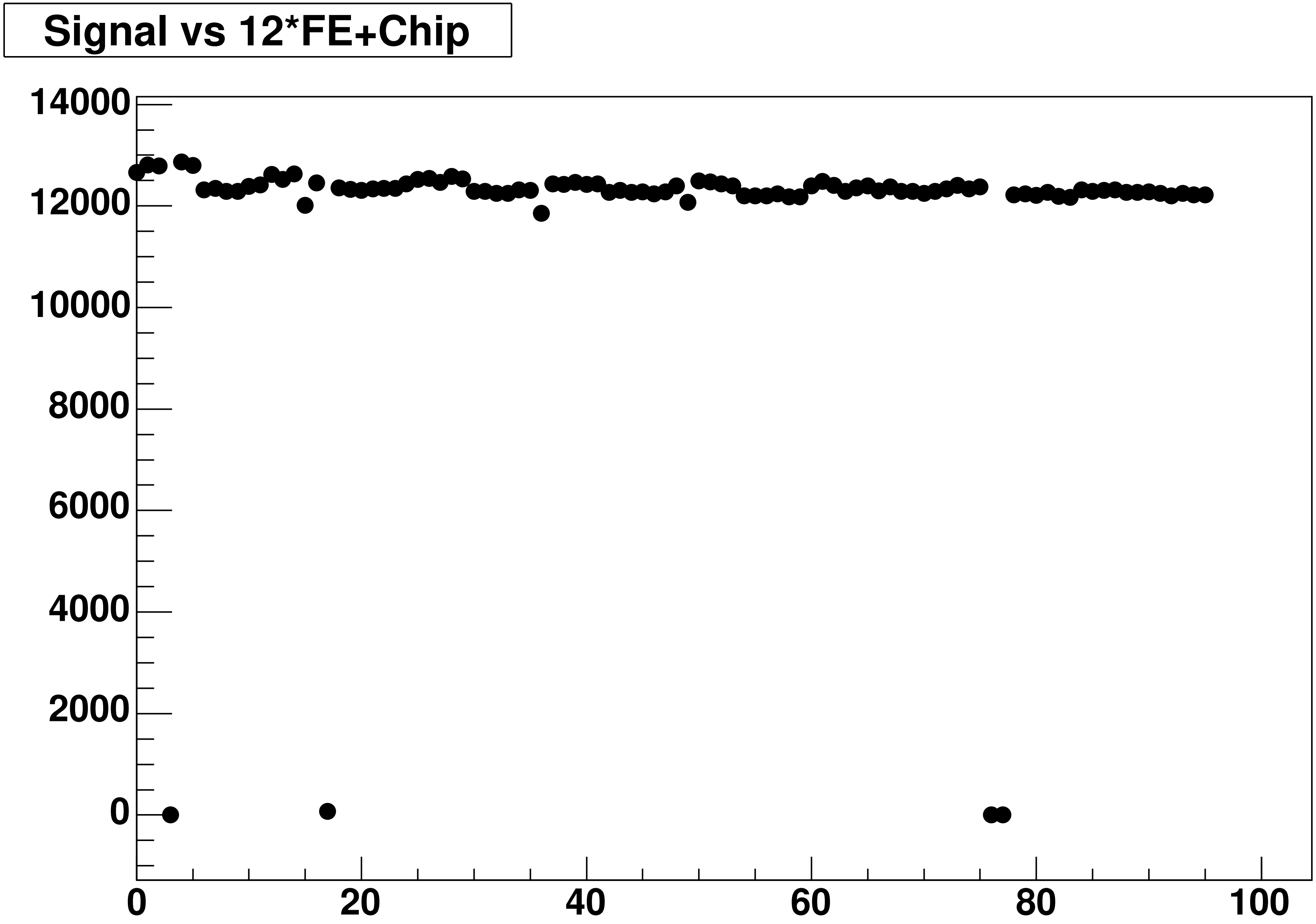


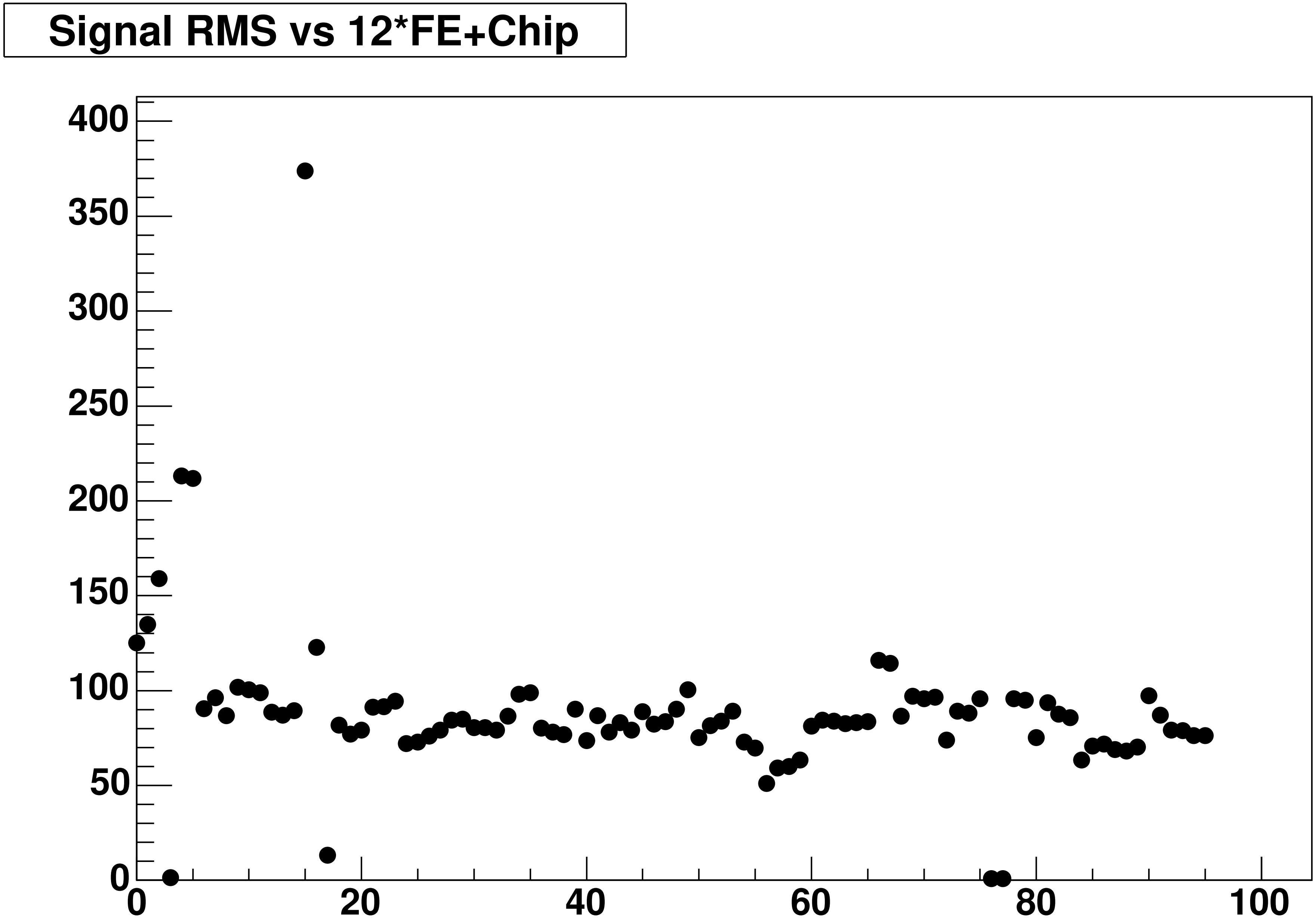
Pedestal RMS vs 12*FE+Chip
70
60
50
40
30
20
10
0
0
20
40
60
80
100



Raw signal vs 12*FE+Chip
14000
12000
10000
8000
6000
4000
2000
0
0
20
40
60
80
100


Raw signal RMS vs 12*FE+Chip
400
350
300
250
200
150
100
50
0
0
20
40
60
80
100


Signal vs 12*FE+Chip
14000
12000
10000
8000
6000
4000
2000
0
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
0
50
100
150
200
250
300
350
400
0
20
40
60
80
100