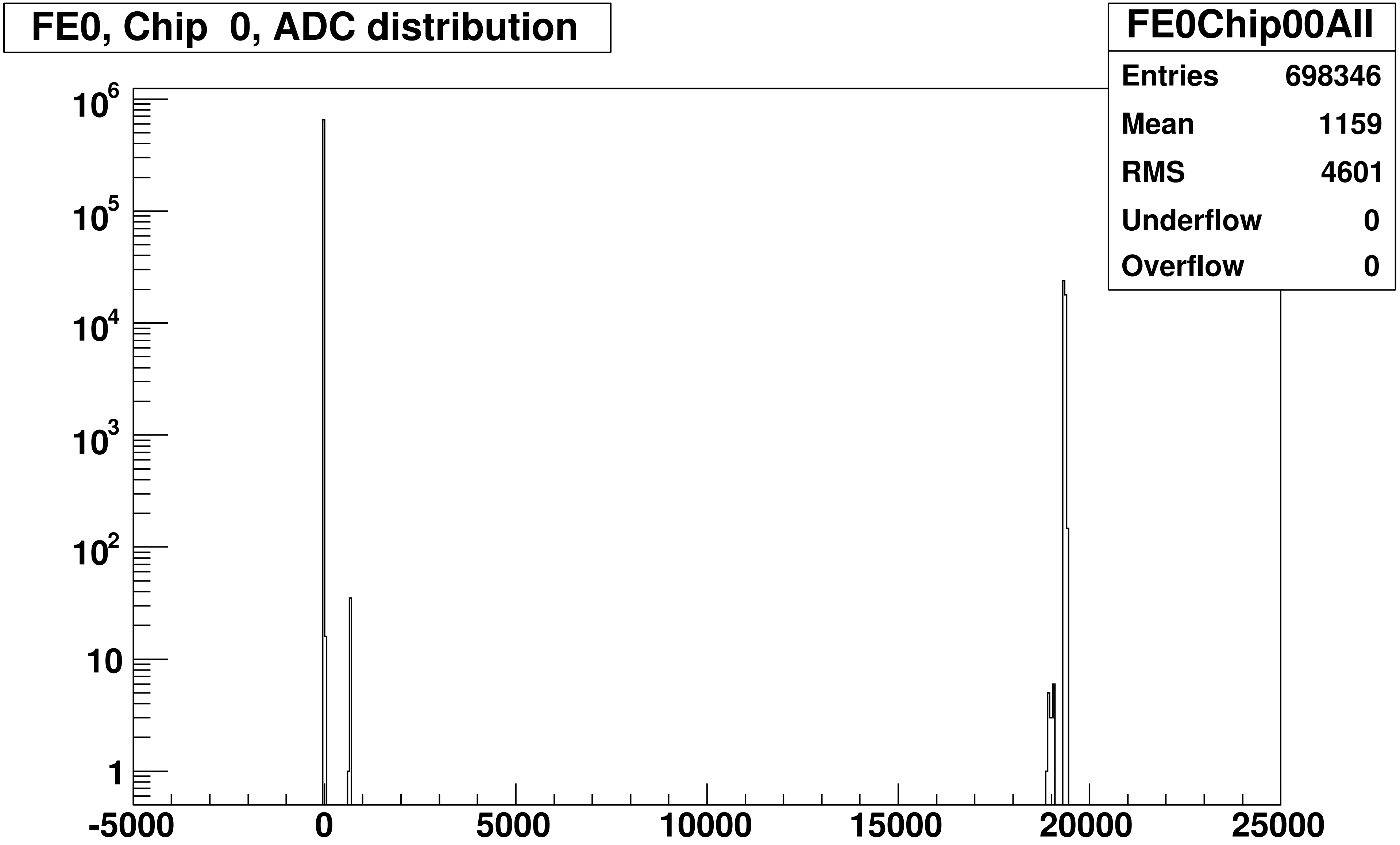
FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 698346
Mean 1159
RMS 4601
Underflow 0
Overflow 0
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10^3
10^2
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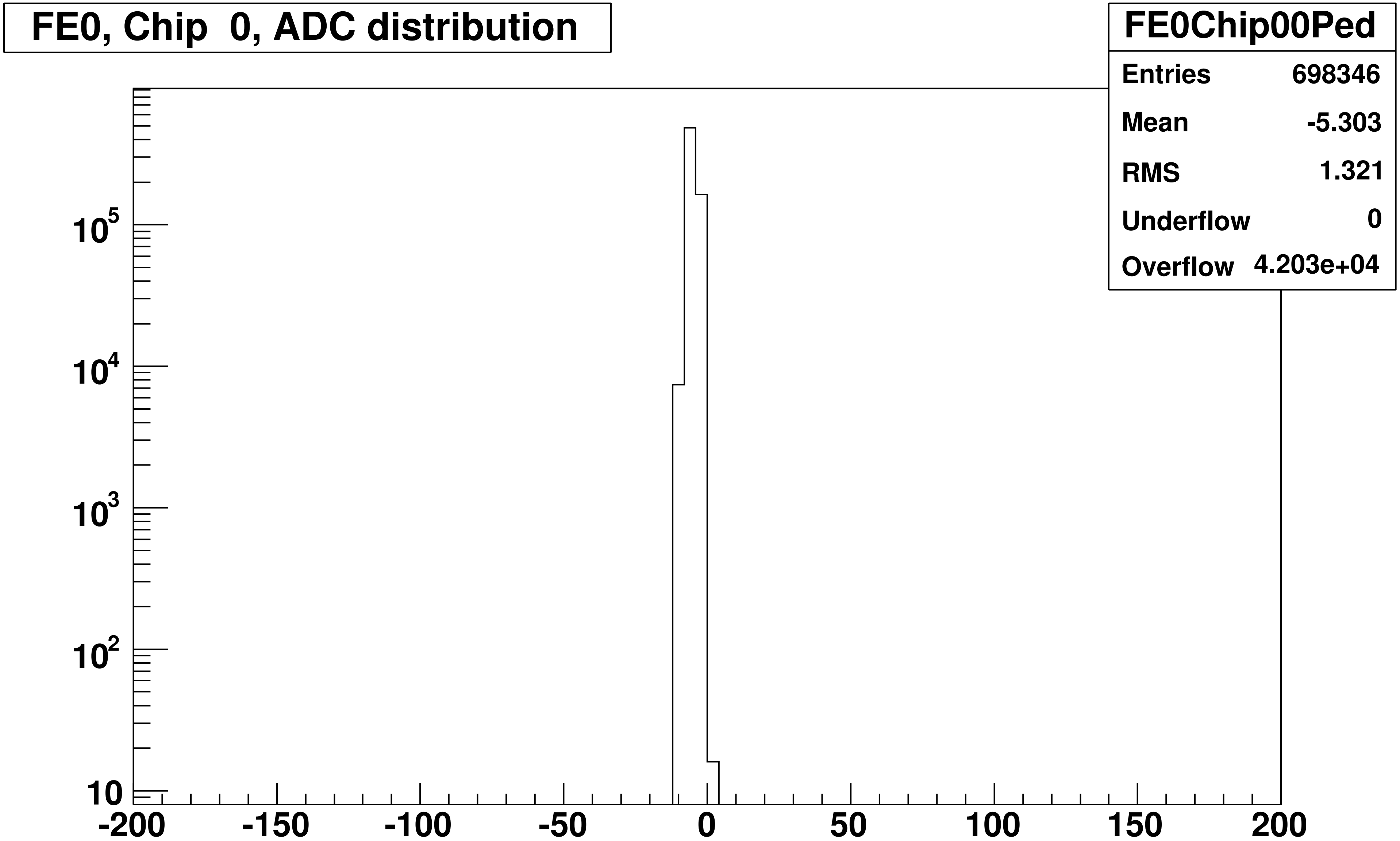
FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 698346
Mean -5.303
RMS 1.321
Underflow 0
Overflow 4.203e+04
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10^3
10^4
10^5
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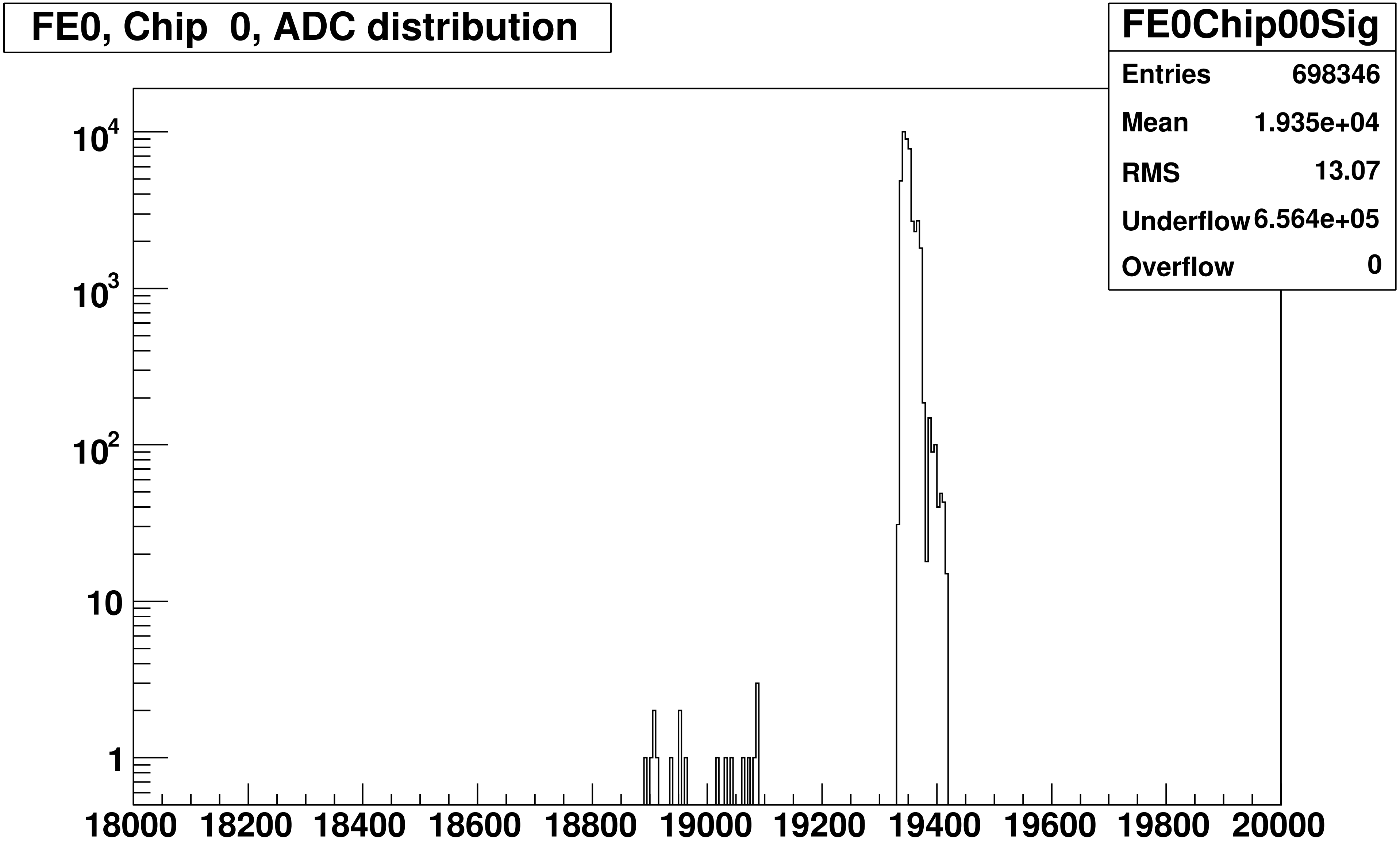
FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 698346
Mean 1.935e+04
RMS 13.07
Underflow 6.564e+05
Overflow 0
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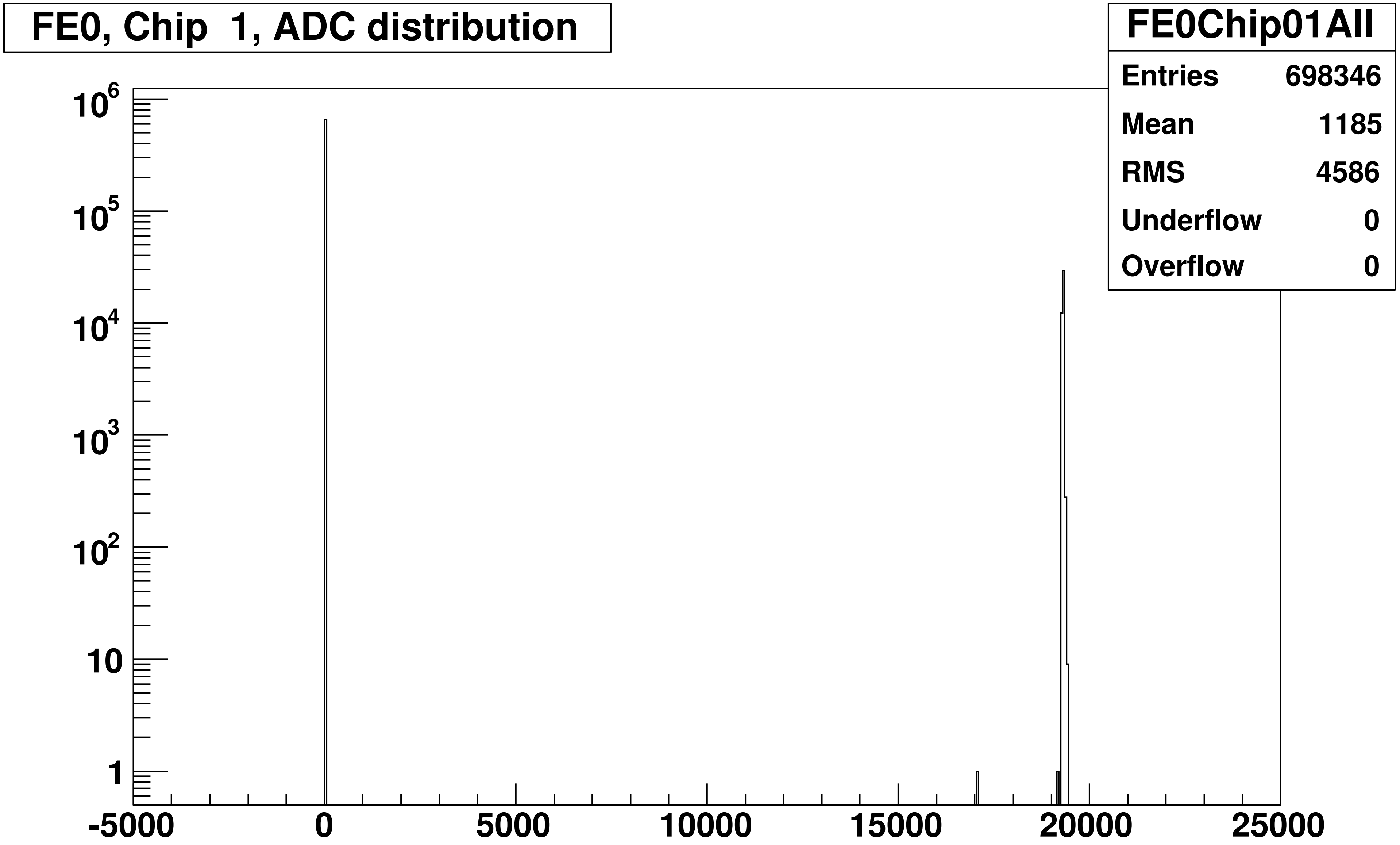
FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 698346
Mean 1185
RMS 4586
Underflow 0
Overflow 0
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10^3
10^2
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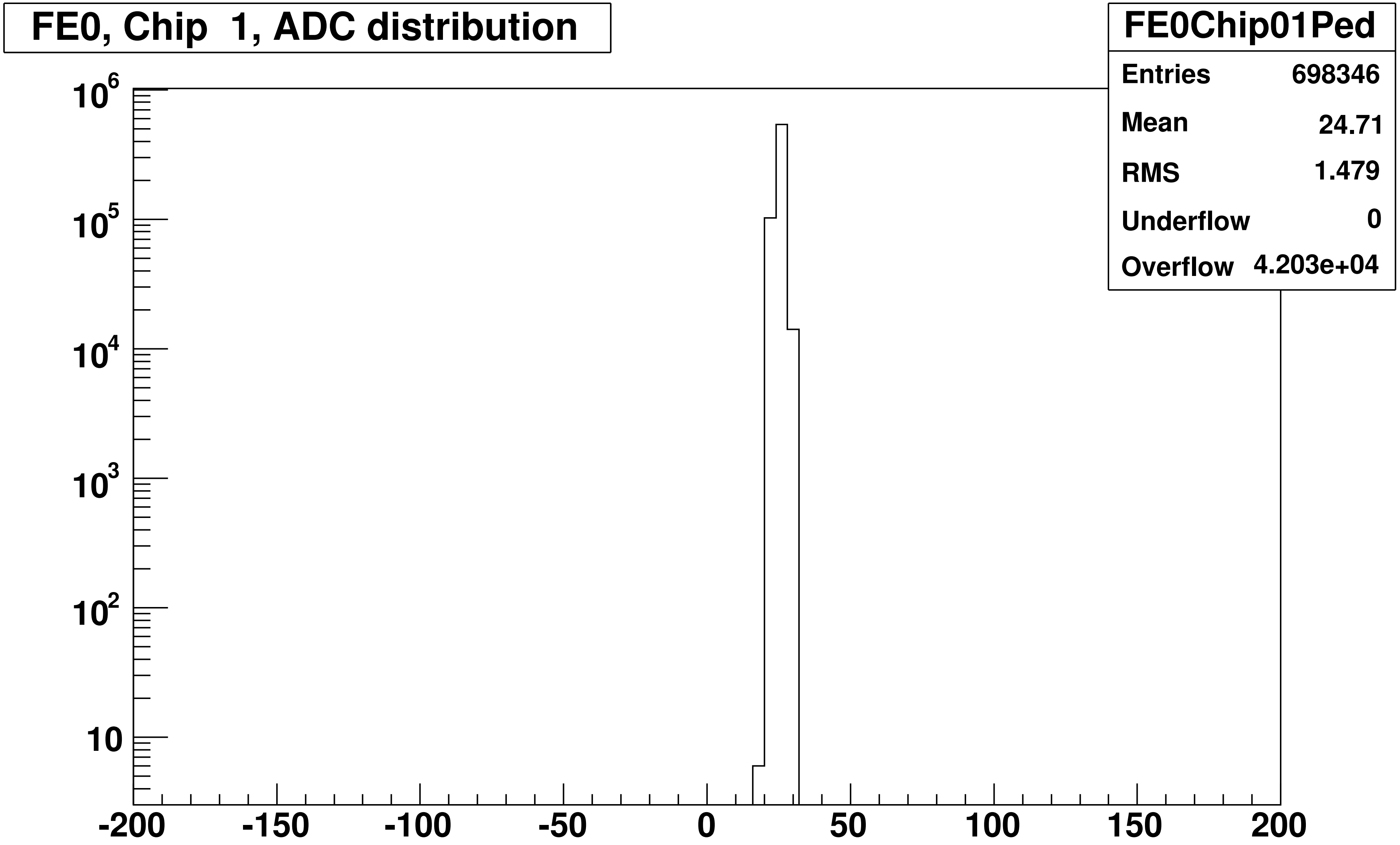
FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 698346
Mean 24.71
RMS 1.479
Underflow 0
Overflow 4.203e+04
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10^3
10^2
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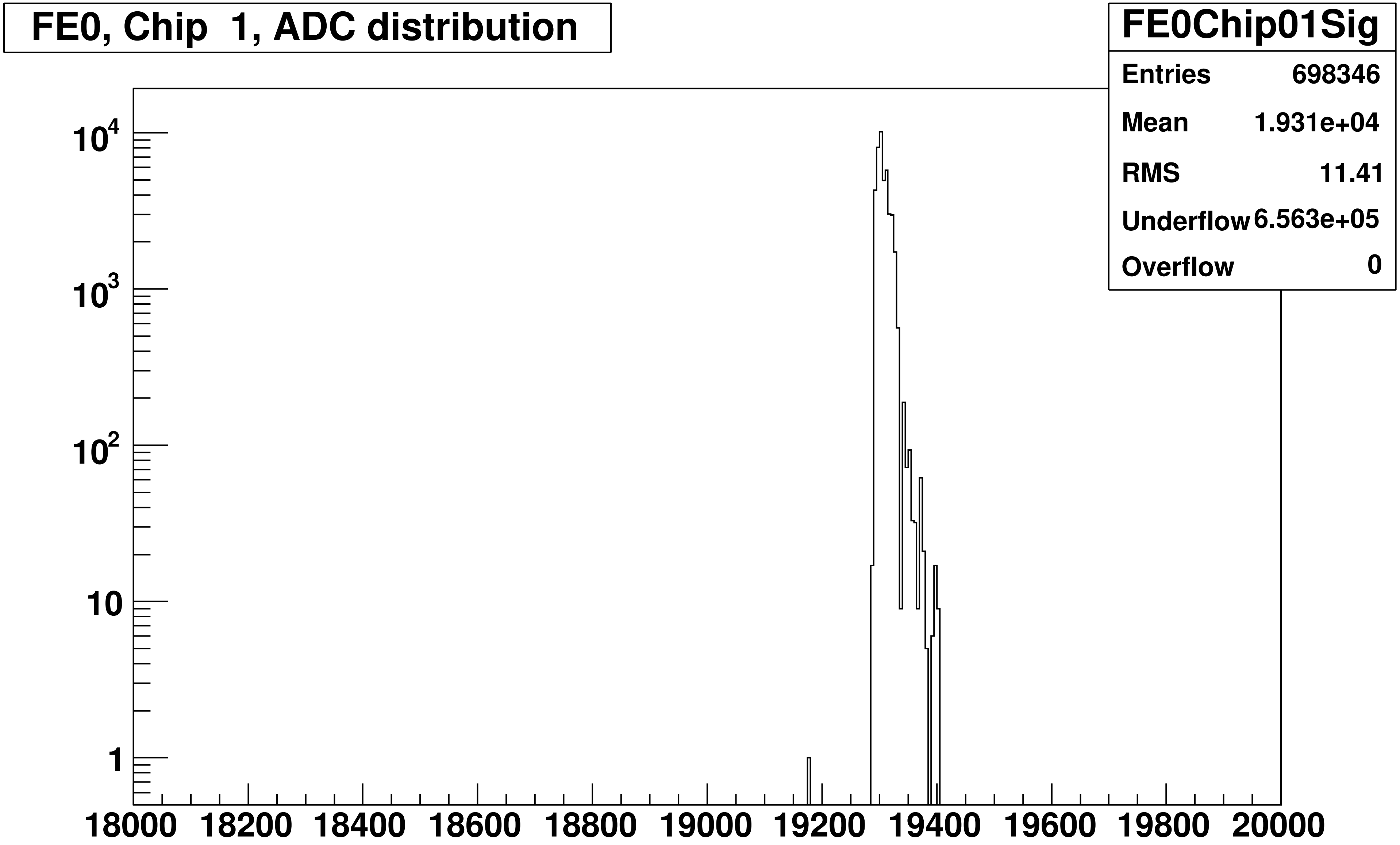
FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 698346
Mean 1.931e+04
RMS 11.41
Underflow 6.563e+05
Overflow 0
10^4
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10^2
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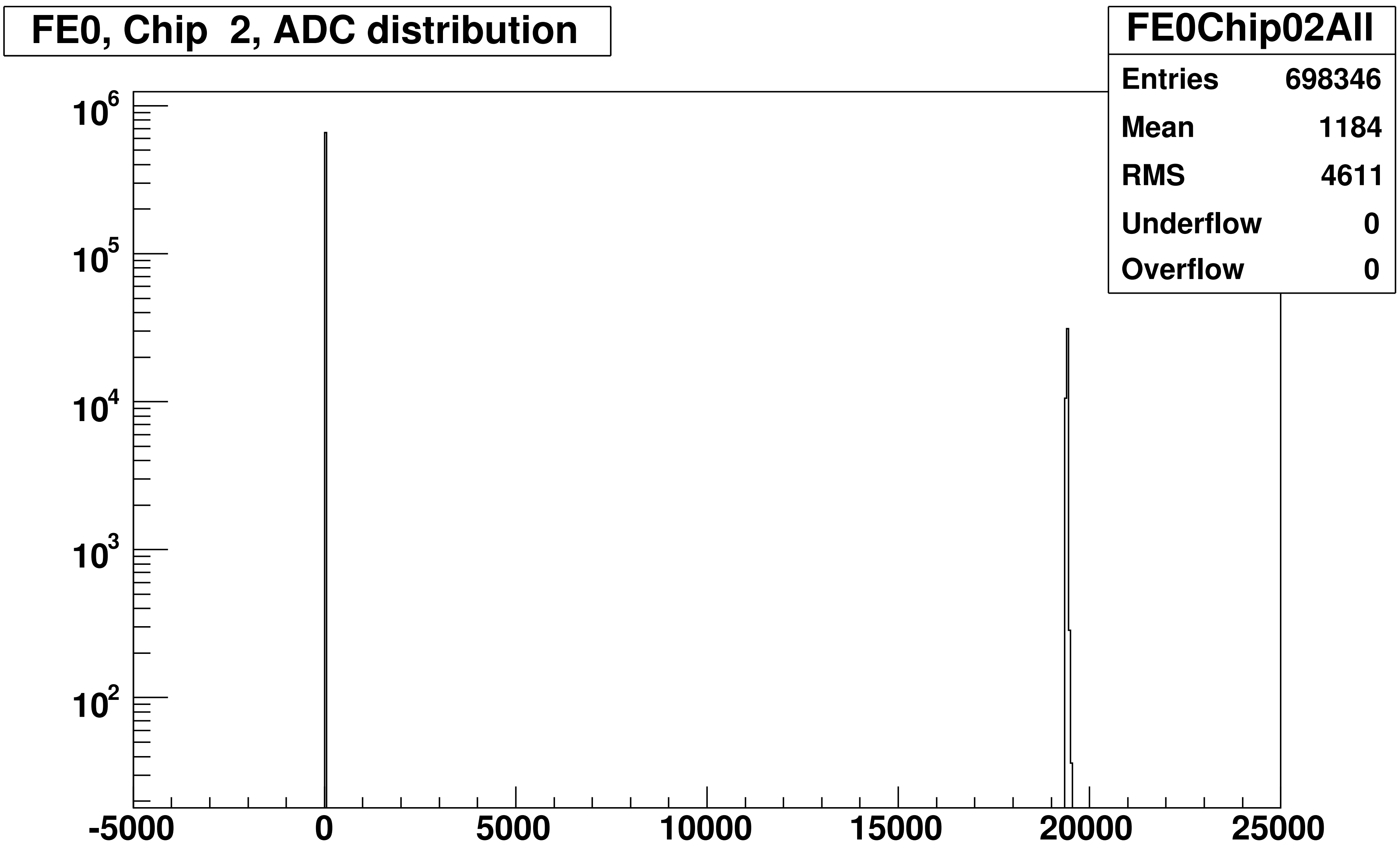
FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 698346
Mean 1184
RMS 4611
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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15000
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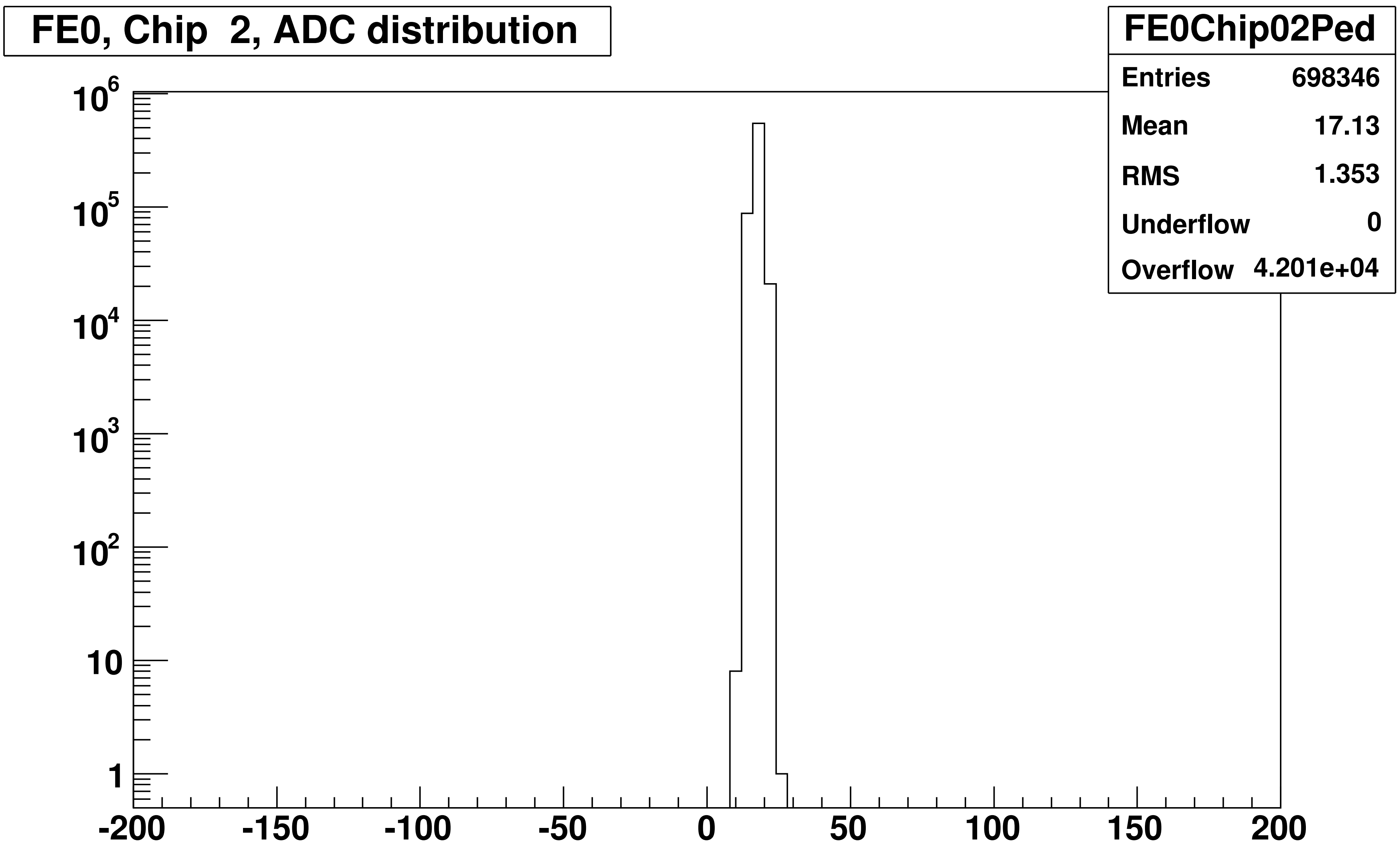

FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 698346
Mean 17.13
RMS 1.353
Underflow 0
Overflow 4.201e+04
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10^5
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10^3
10^2
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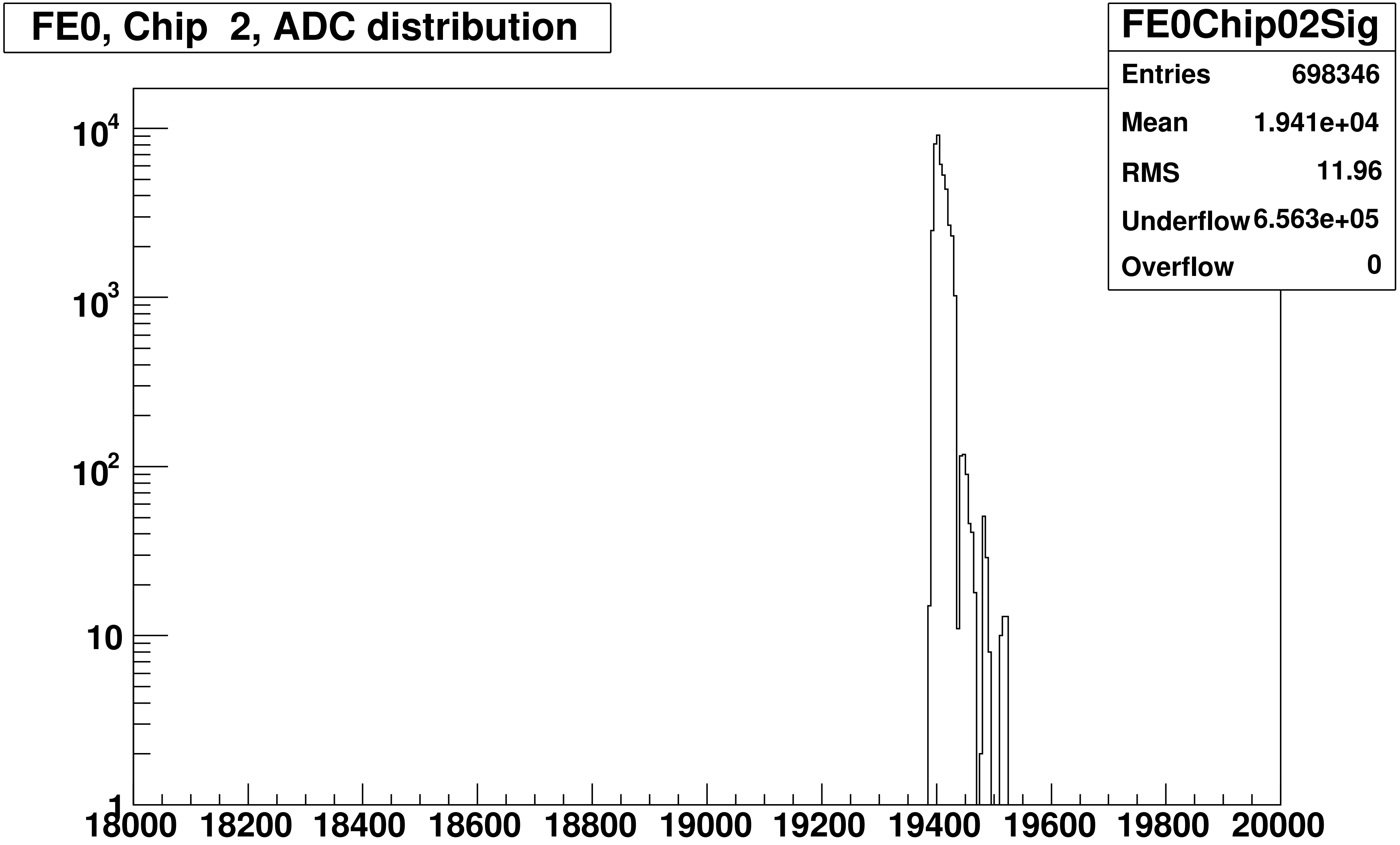
FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 698346
Mean 1.941e+04
RMS 11.96
Underflow 6.563e+05
Overflow 0
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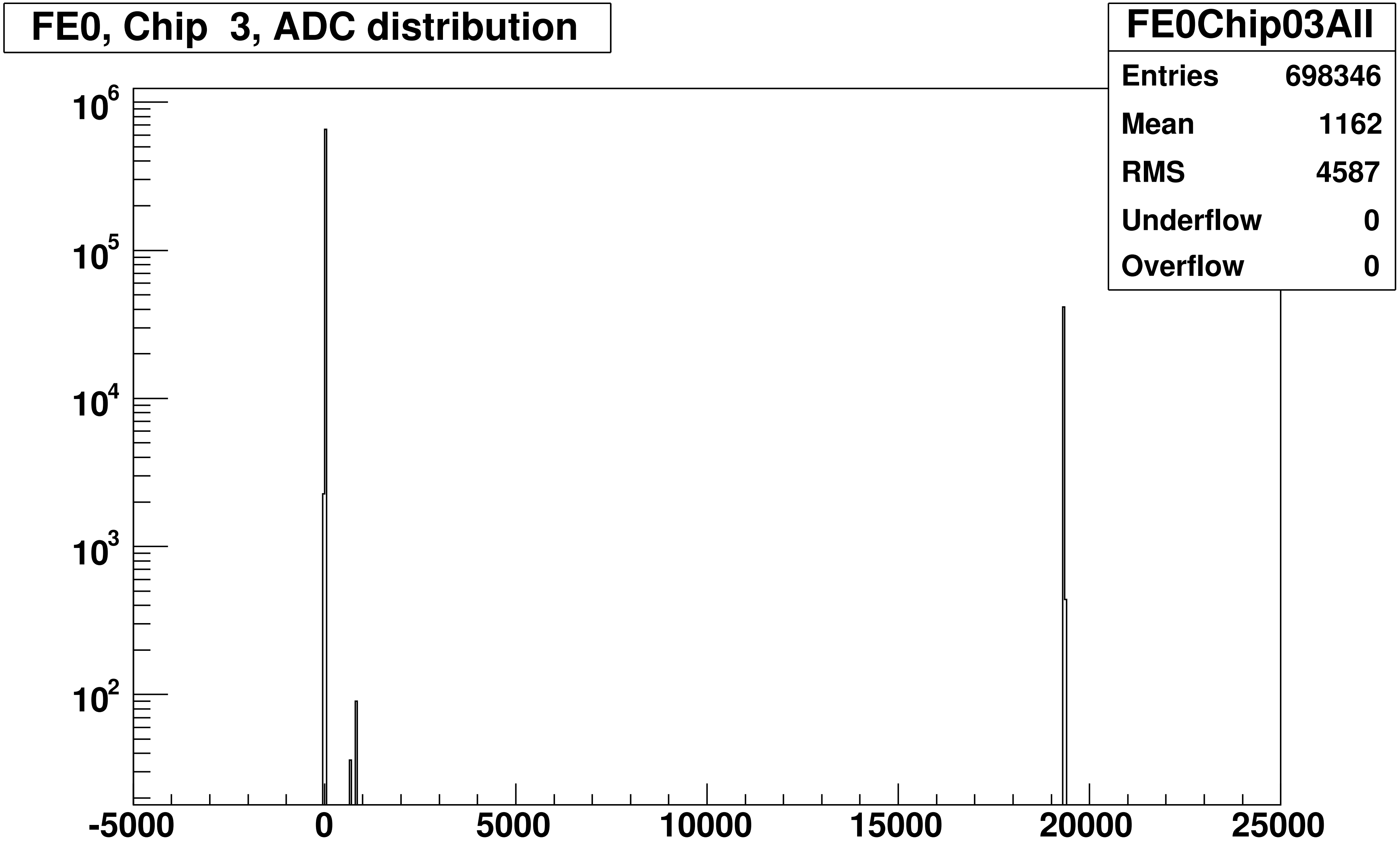
FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 698346
Mean 1162
RMS 4587
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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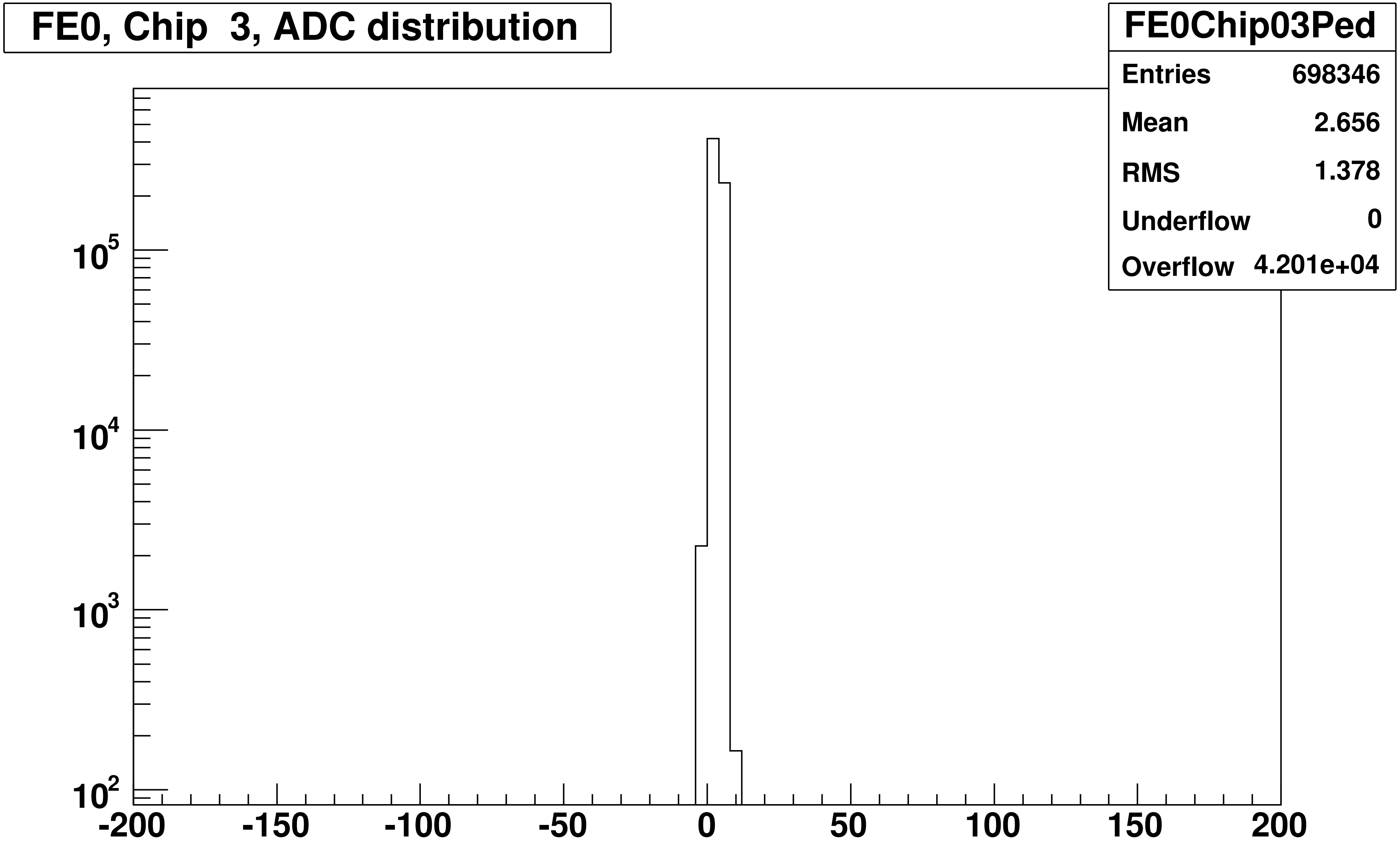

FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 698346
Mean 2.656
RMS 1.378
Underflow 0
Overflow 4.201e+04
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10^4
10^3
10^2
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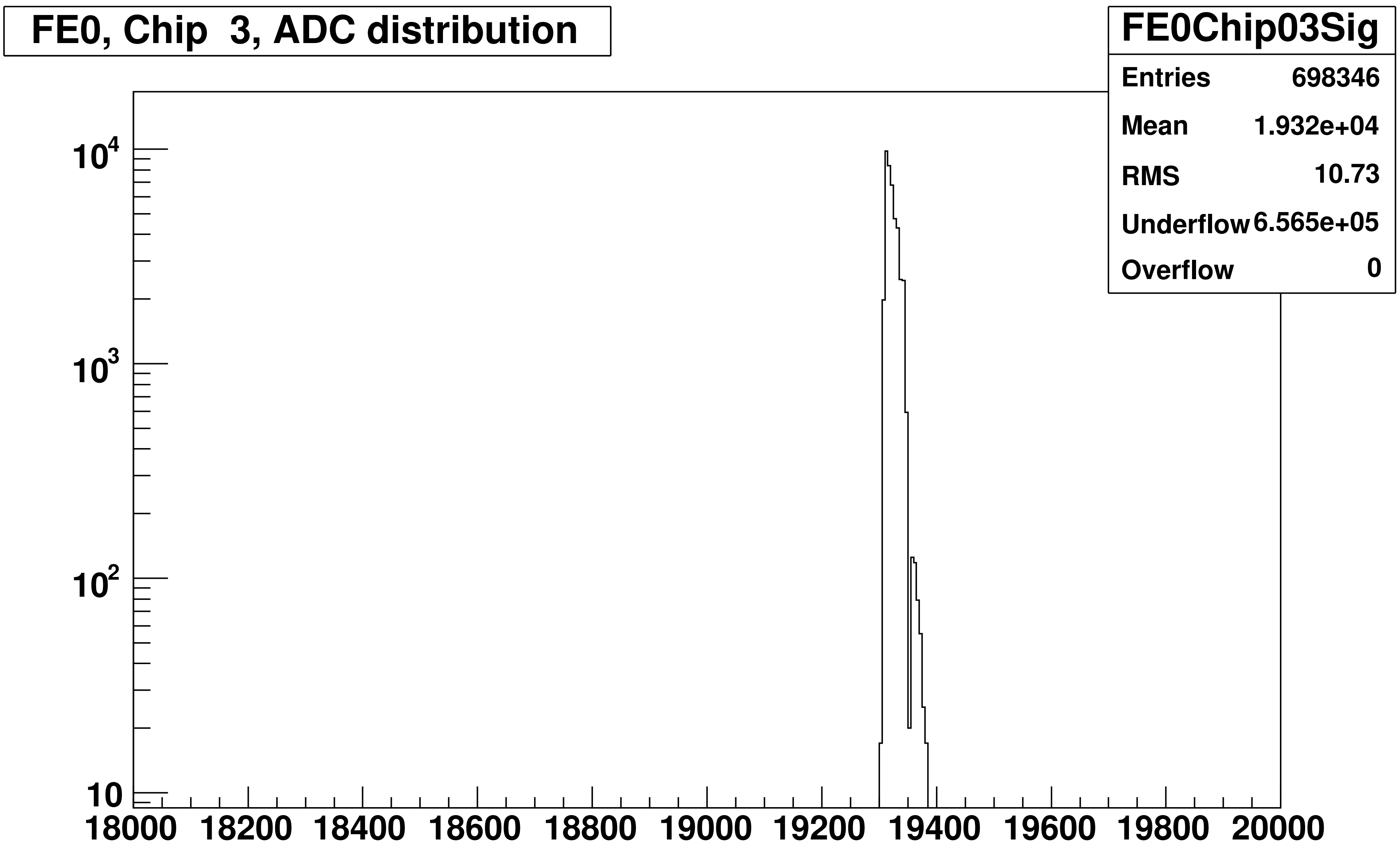
150
200


FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 698346
Mean 1.932e+04
RMS 10.73
Underflow 6.565e+05
Overflow 0
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18000
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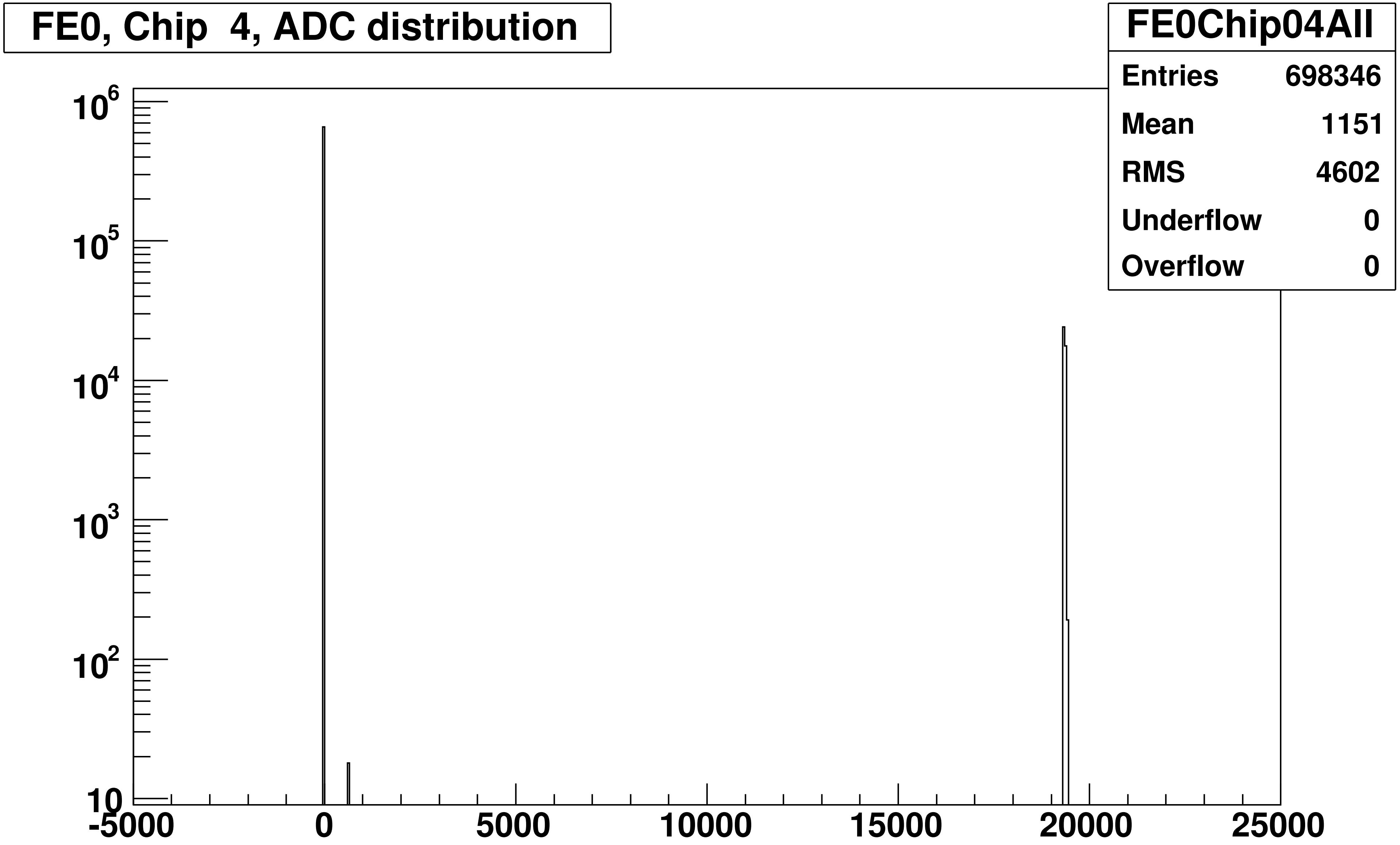
FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 698346
Mean 1151
RMS 4602
Underflow 0
Overflow 0
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10^5
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10^3
10^2
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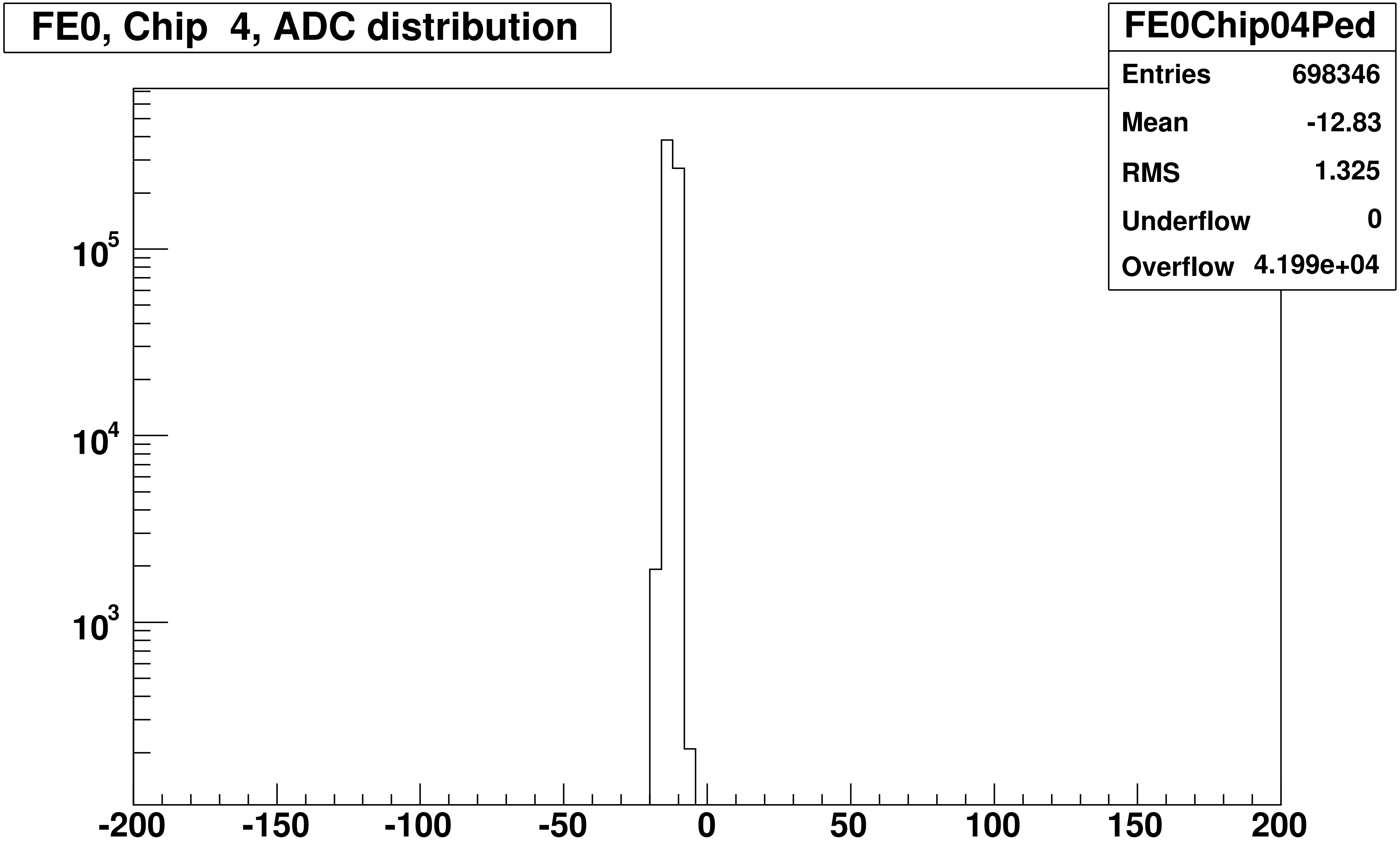
FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 698346
Mean -12.83
RMS 1.325
Underflow 0
Overflow 4.199e+04
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10^3
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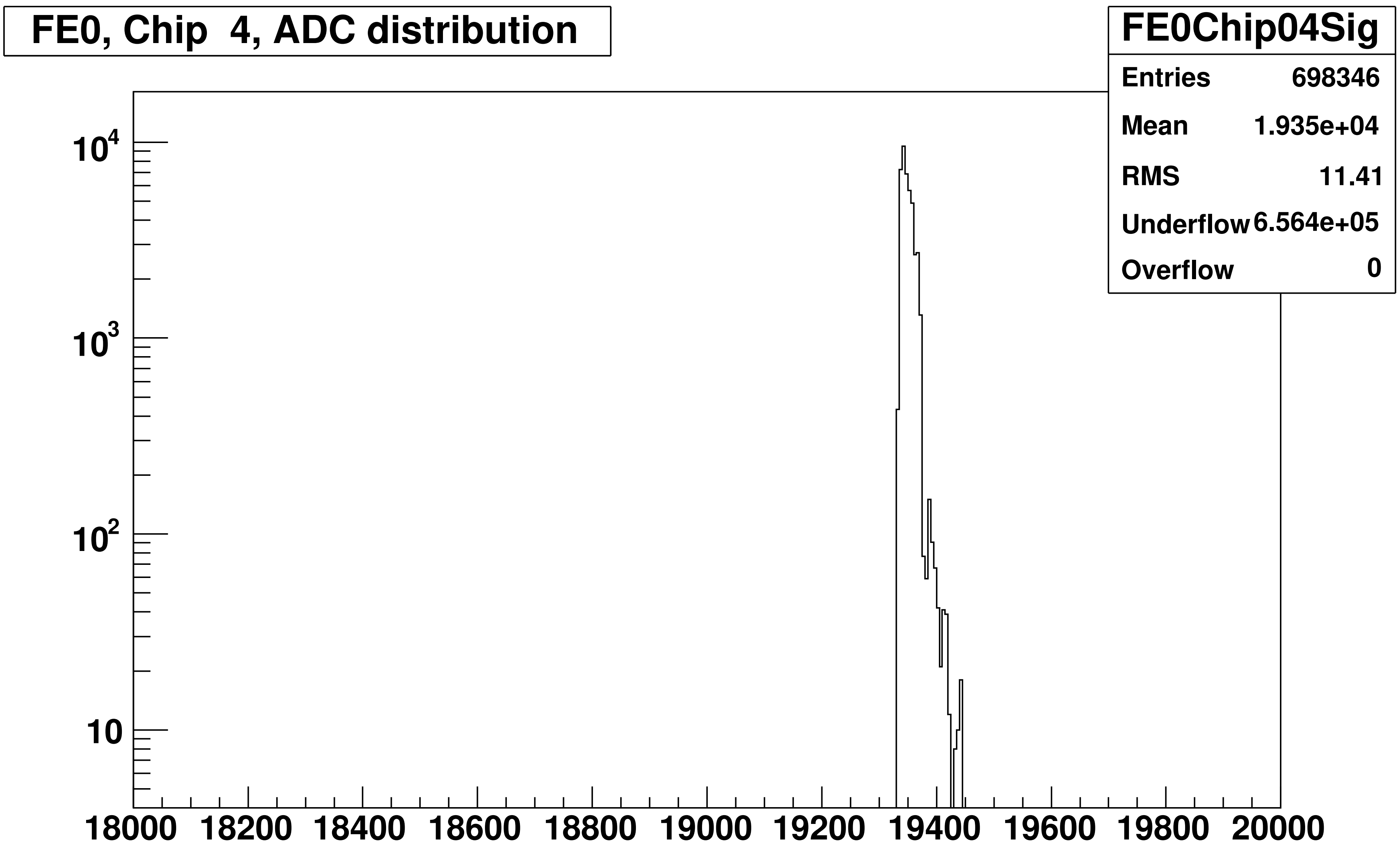
100
150
200


FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 698346
Mean 1.935e+04
RMS 11.41
Underflow 6.564e+05
Overflow 0
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10^3
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18000
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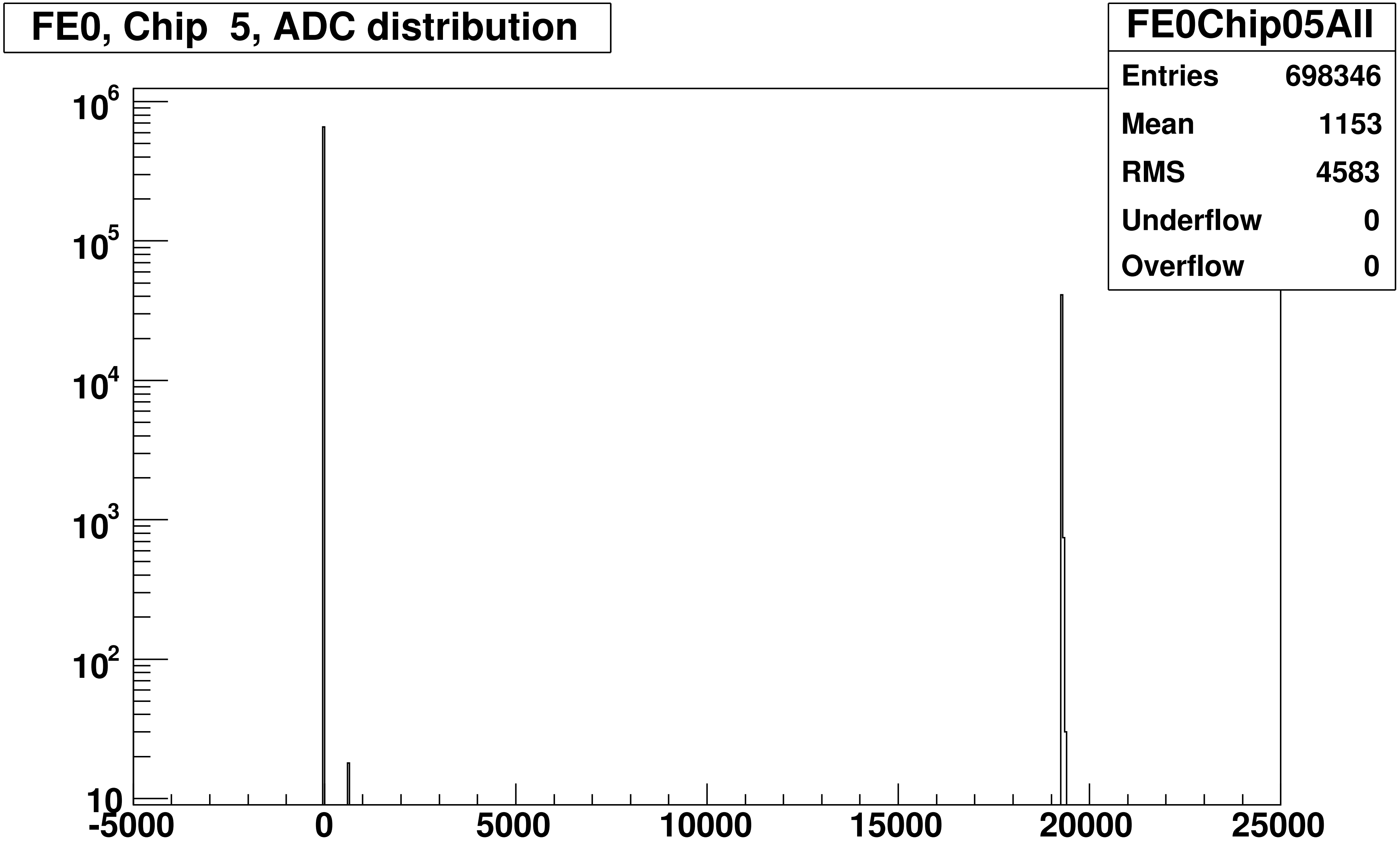
20000


FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 698346
Mean 1153
RMS 4583
Underflow 0
Overflow 0
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10^3
10^2
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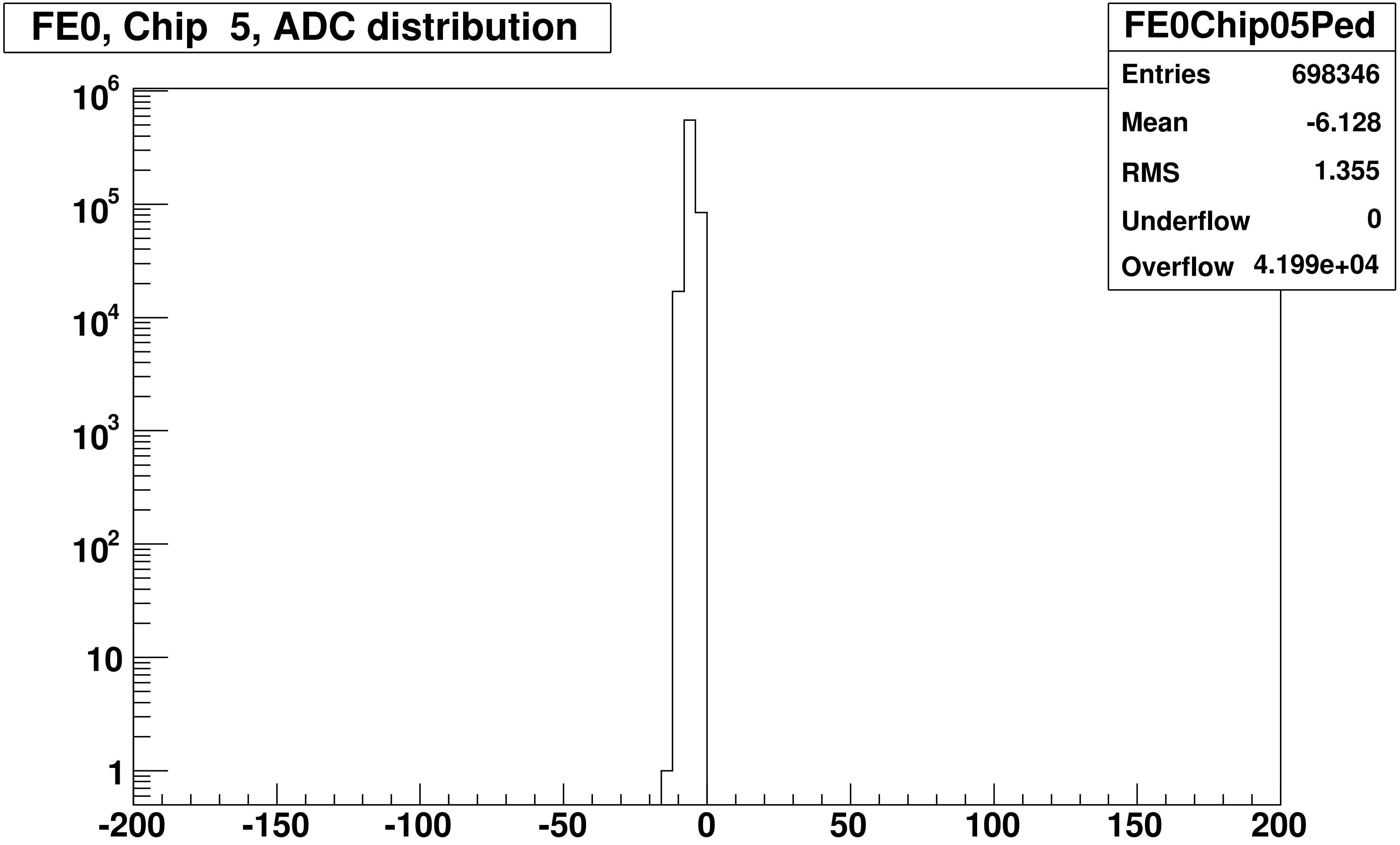
10000
15000
20000
25000


FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 698346
Mean -6.128
RMS 1.355
Underflow 0
Overflow 4.199e+04
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10^4
10^3
10^2
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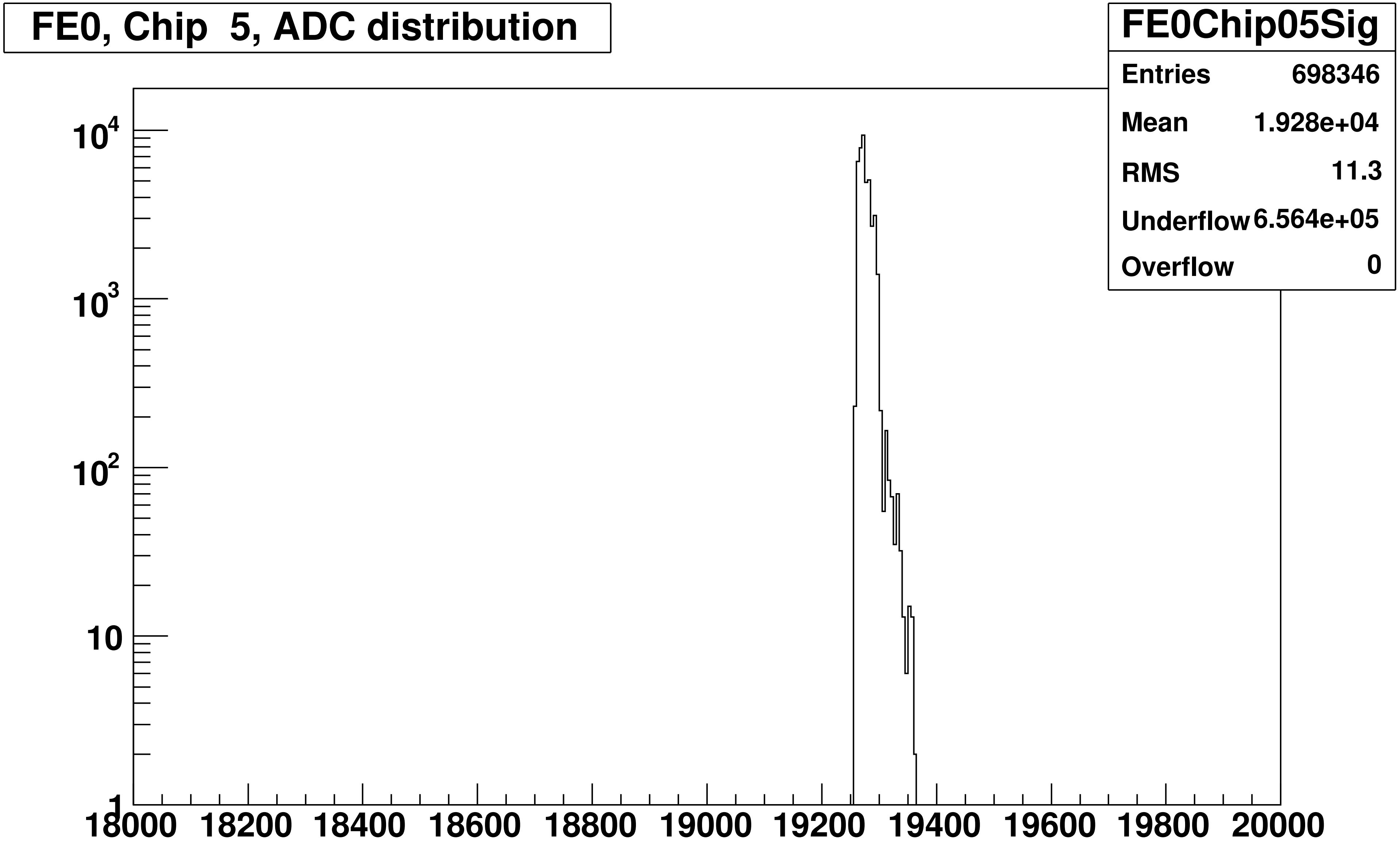
100
150
200


FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 698346
Mean 1.928e+04
RMS 11.3
Underflow 6.564e+05
Overflow 0
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10^3
10^2
10
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18000
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18400
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19000
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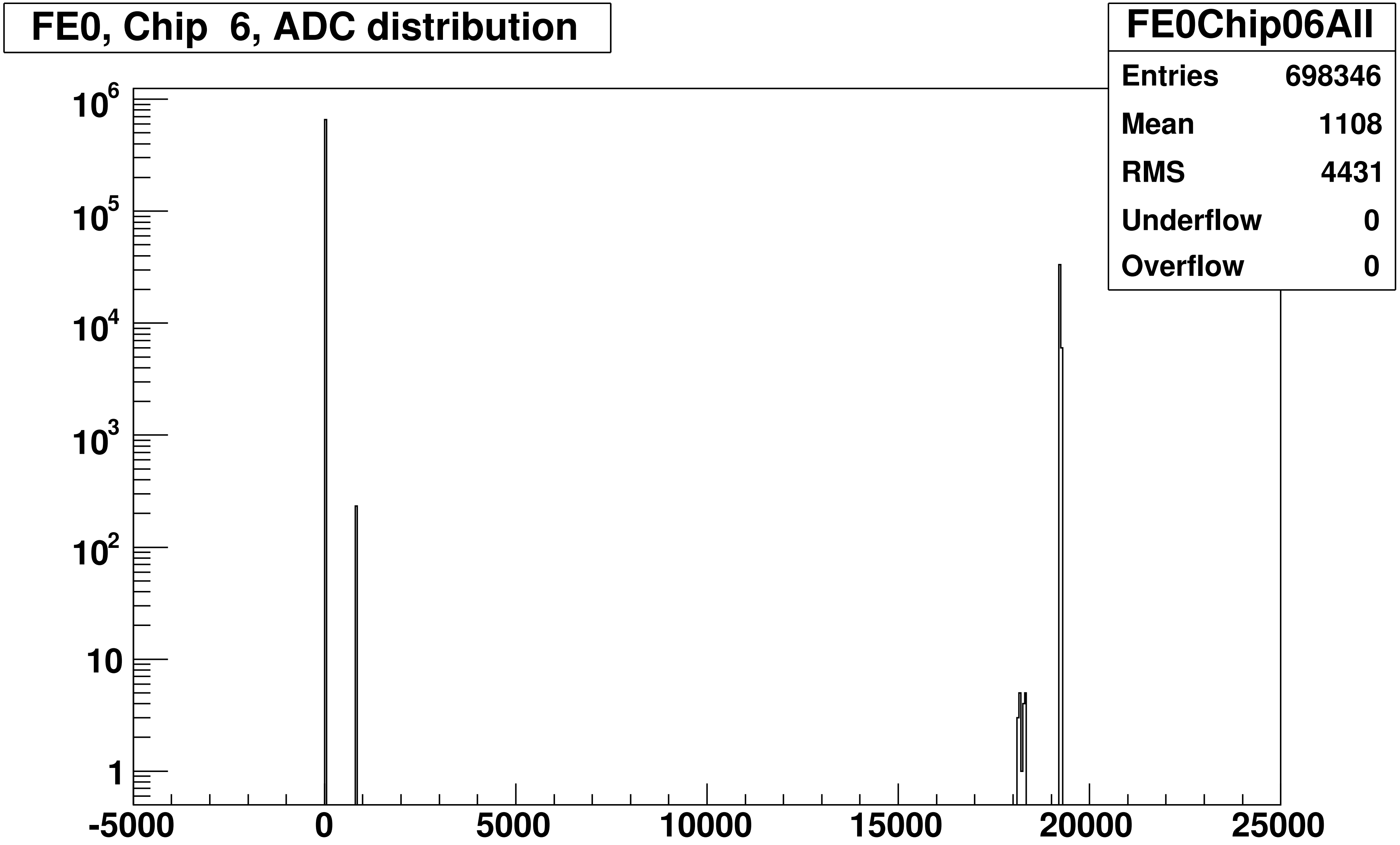

FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 698346
Mean 1108
RMS 4431
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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0
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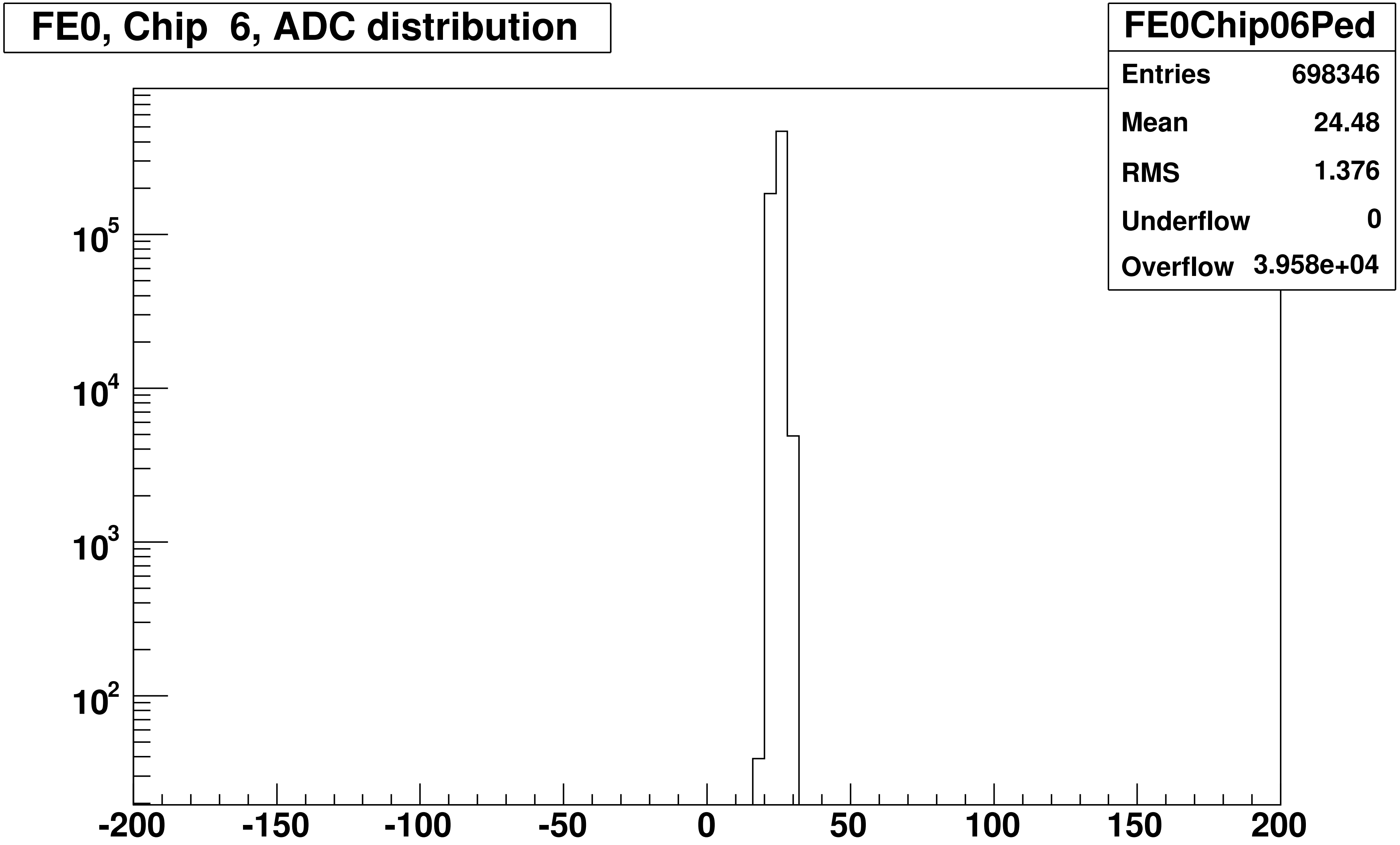
20000
25000


FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 698346
Mean 24.48
RMS 1.376
Underflow 0
Overflow 3.958e+04
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10^3
10^2
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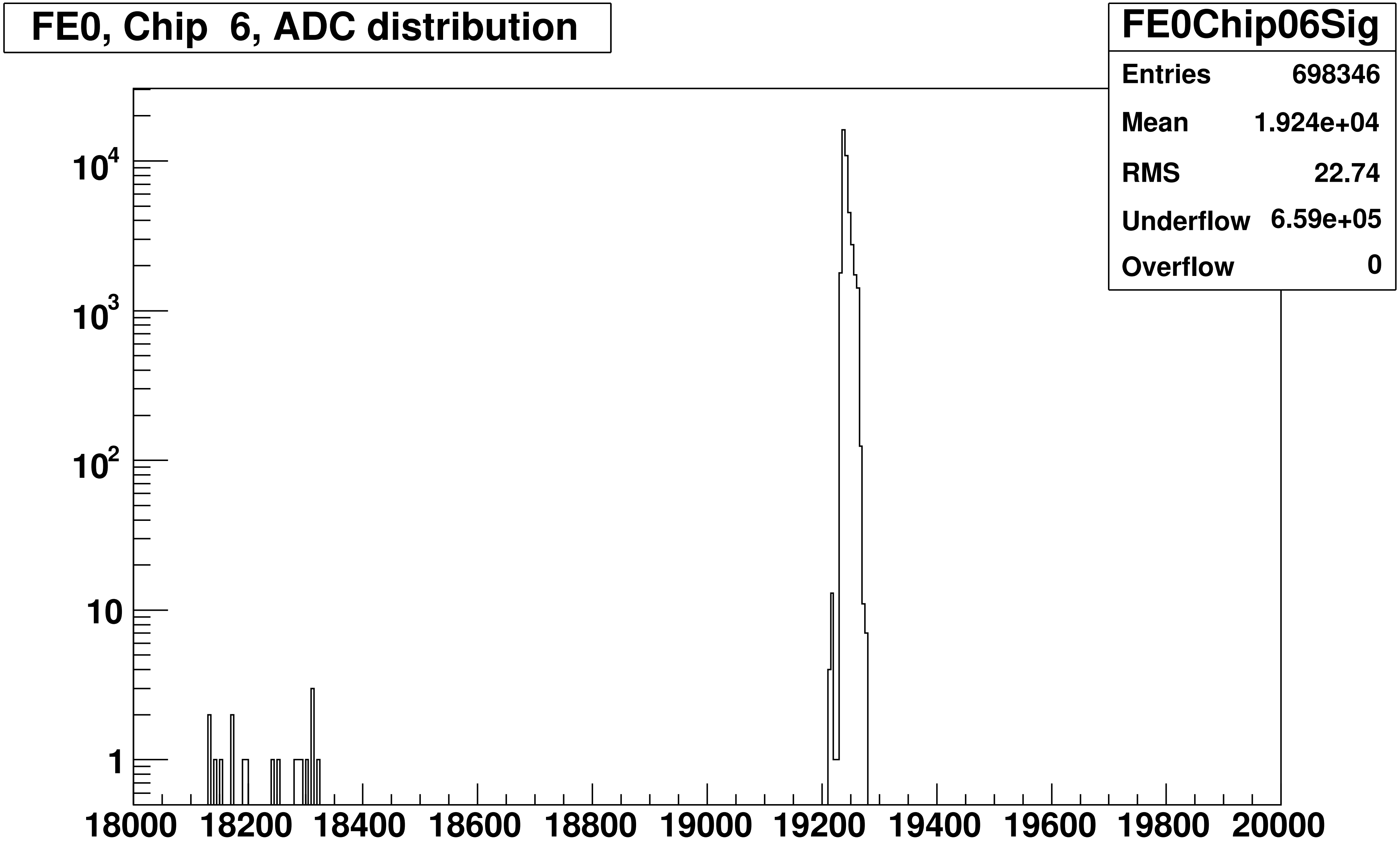
150
200


FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 698346
Mean 1.924e+04
RMS 22.74
Underflow 6.59e+05
Overflow 0
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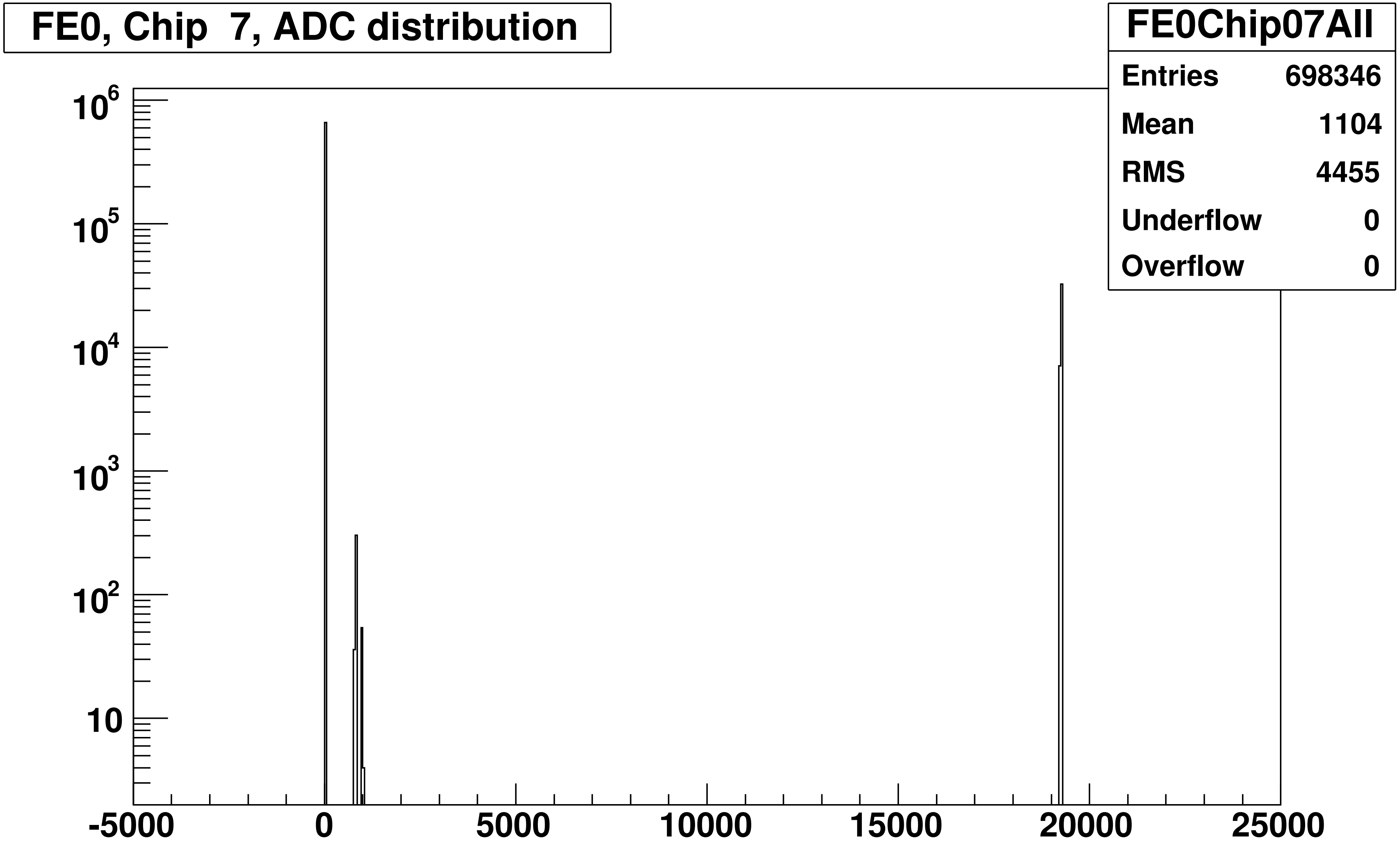

FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 698346
Mean 1104
RMS 4455
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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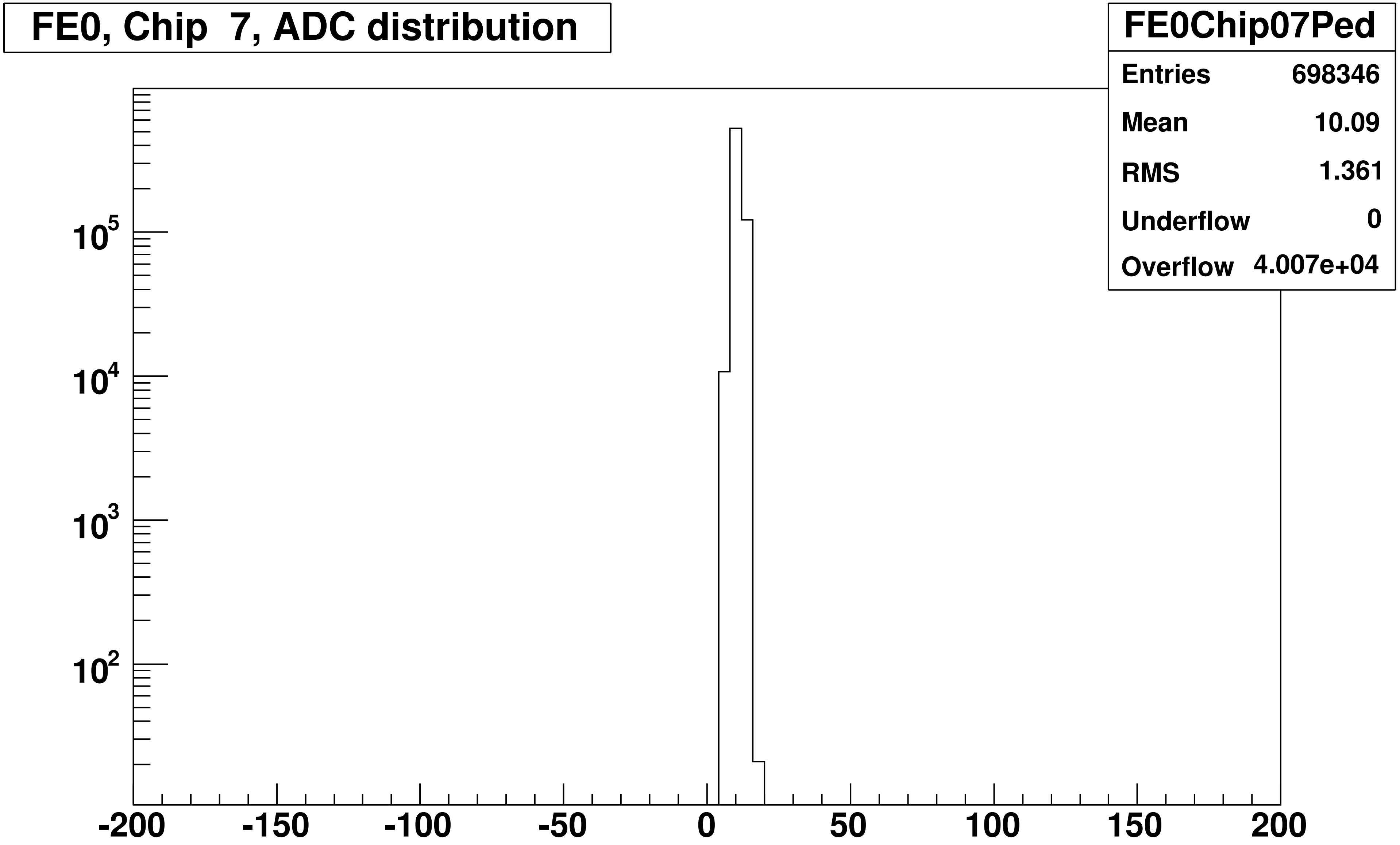
FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 698346
Mean 10.09
RMS 1.361
Underflow 0
Overflow 4.007e+04
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10^4
10^3
10^2
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-100
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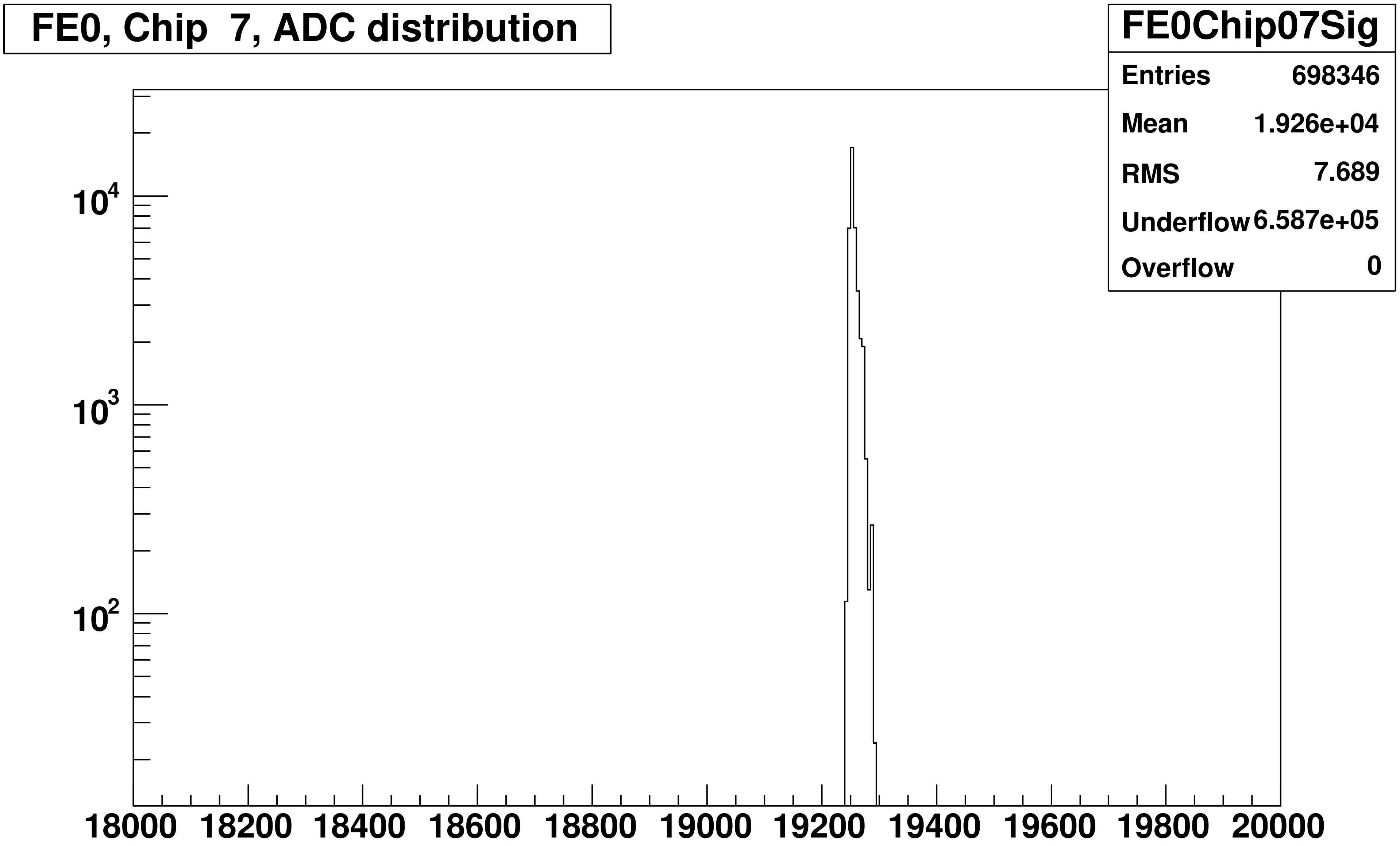
150
200


FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 698346
Mean 1.926e+04
RMS 7.689
Underflow 6.587e+05
Overflow 0
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10^3
10^2
18000
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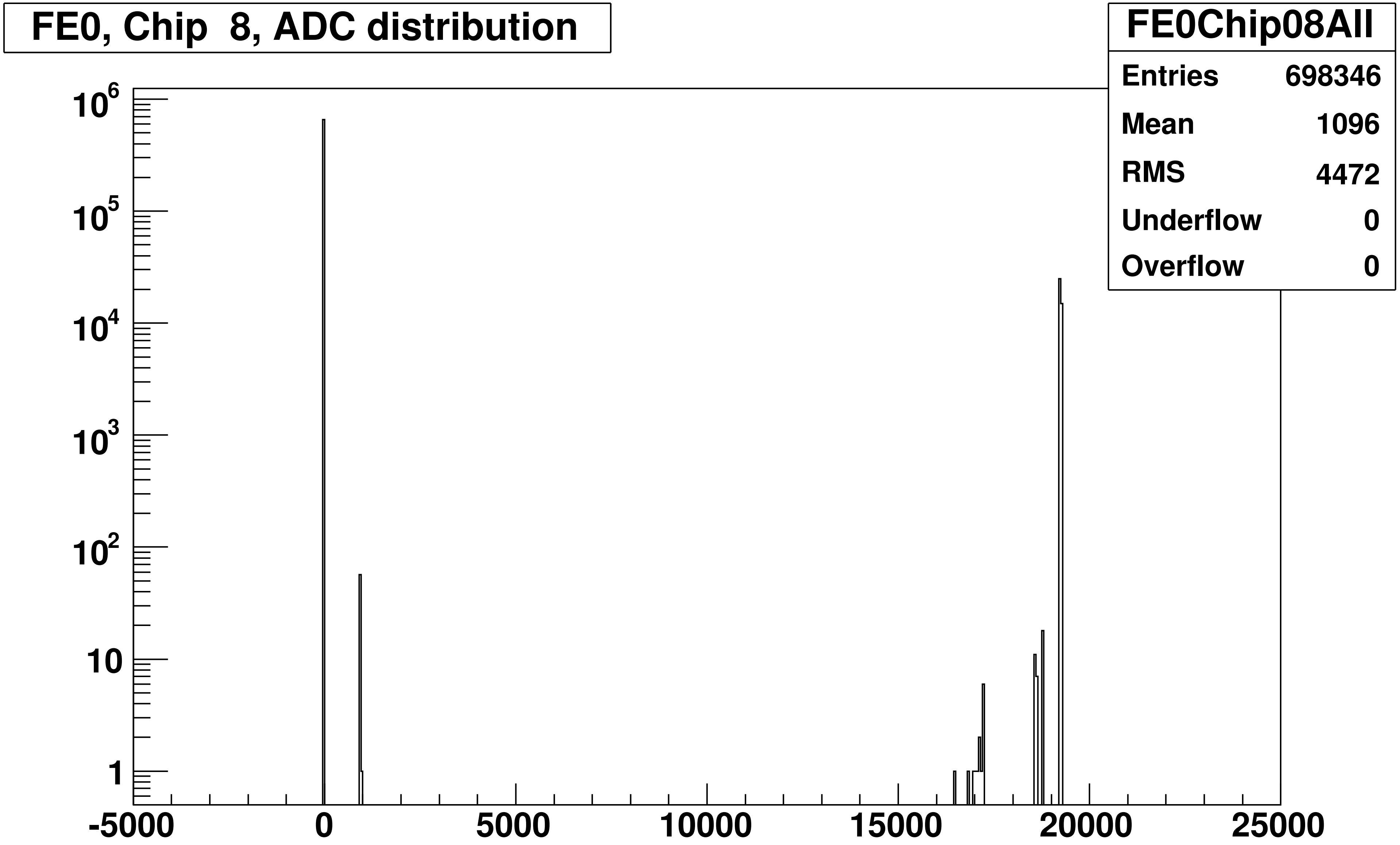

FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 698346
Mean 1096
RMS 4472
Underflow 0
Overflow 0
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10^3
10^2
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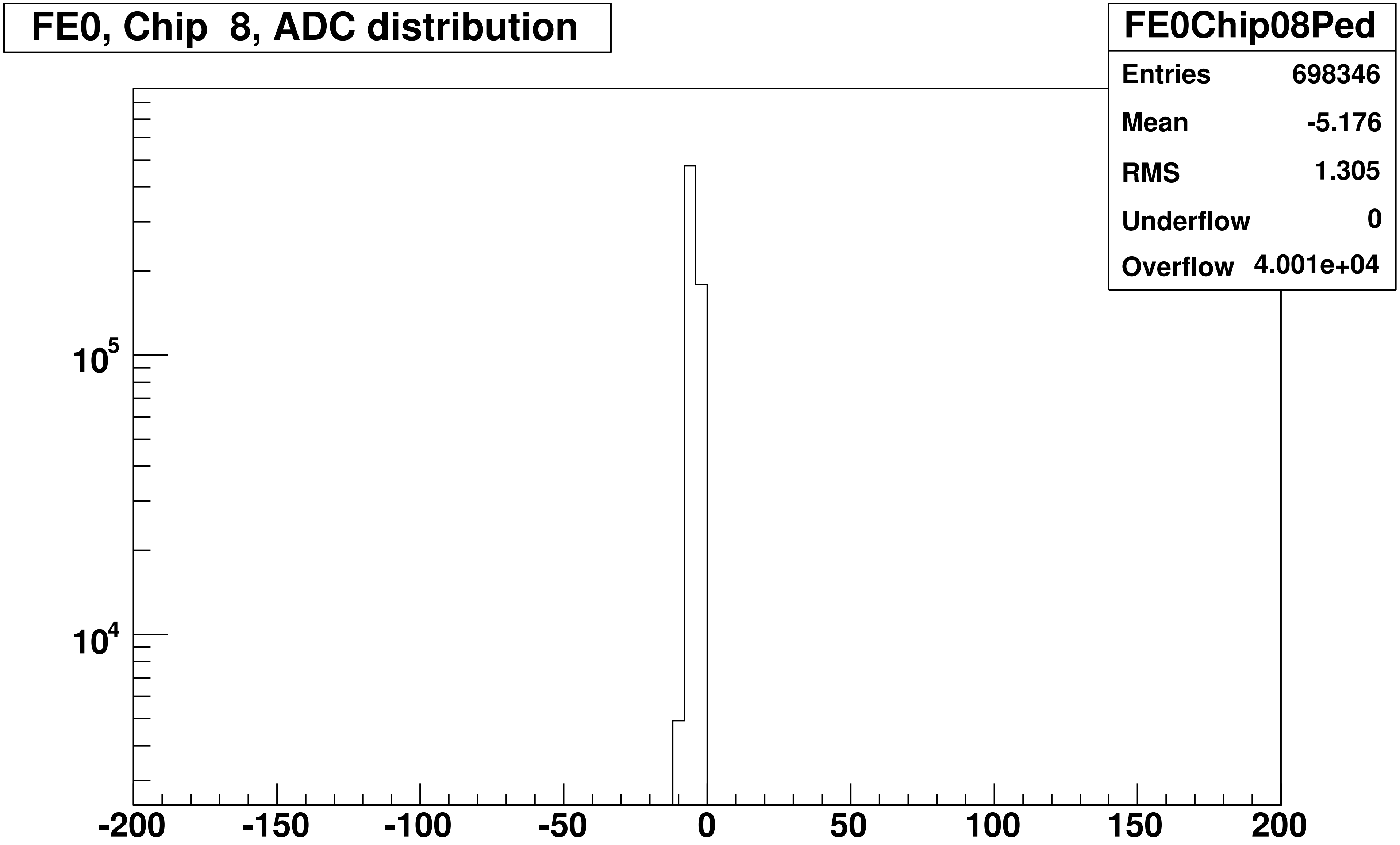
FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 698346
Mean -5.176
RMS 1.305
Underflow 0
Overflow 4.001e+04
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10^4
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-100
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150
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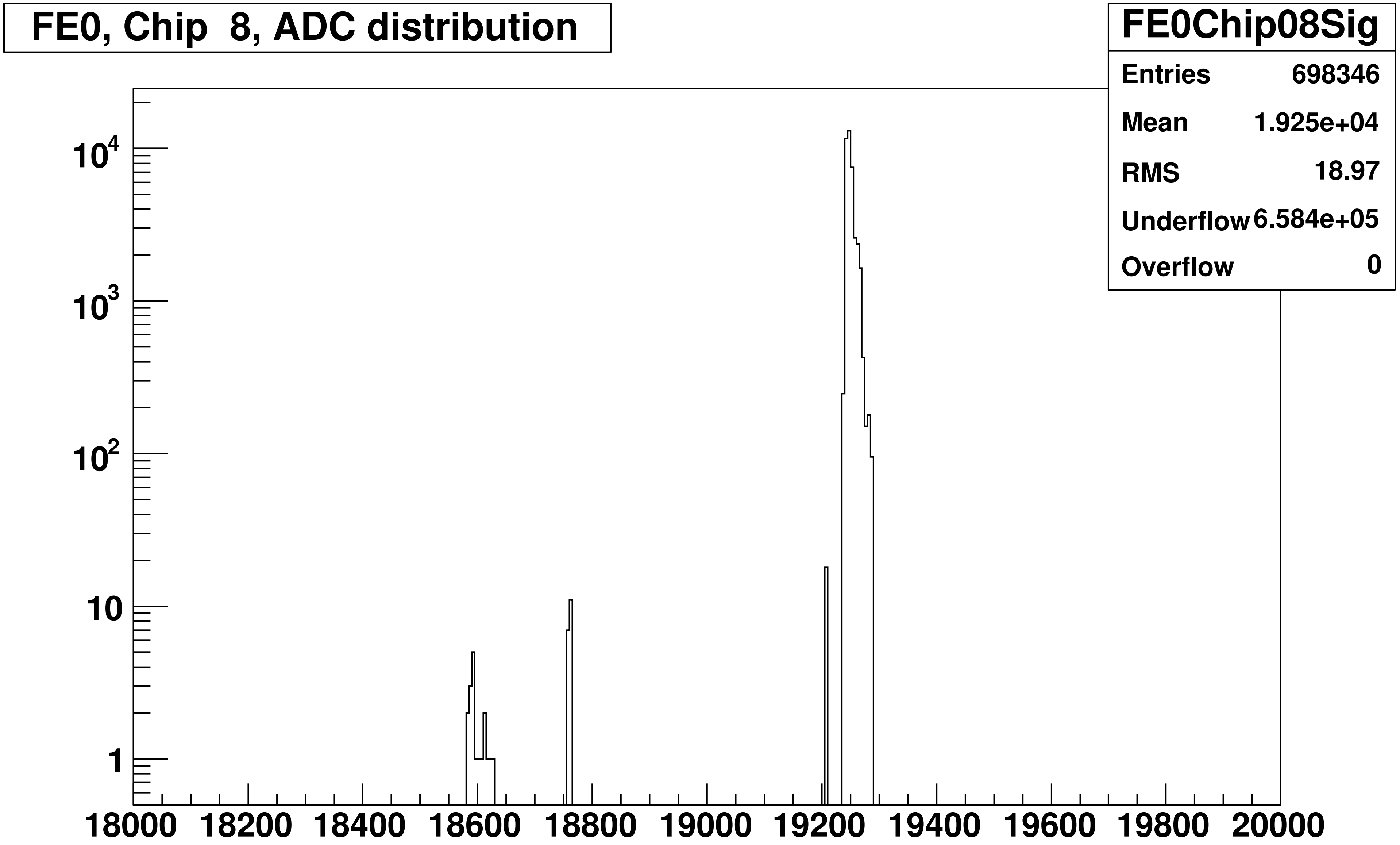
FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 698346
Mean 1.925e+04
RMS 18.97
Underflow 6.584e+05
Overflow 0
10^4
10^3
10^2
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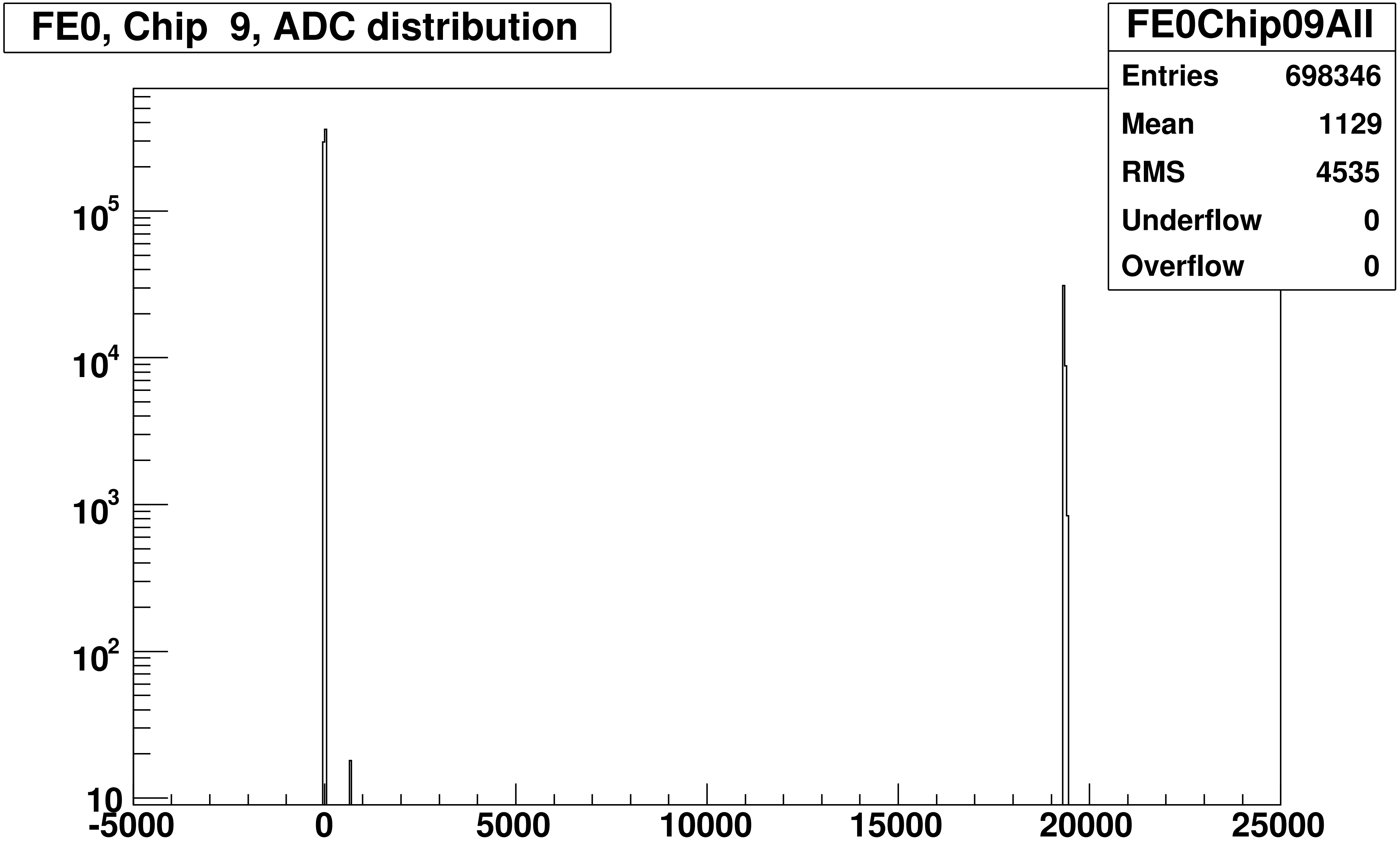
FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 698346
Mean 1129
RMS 4535
Underflow 0
Overflow 0
10
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10^5
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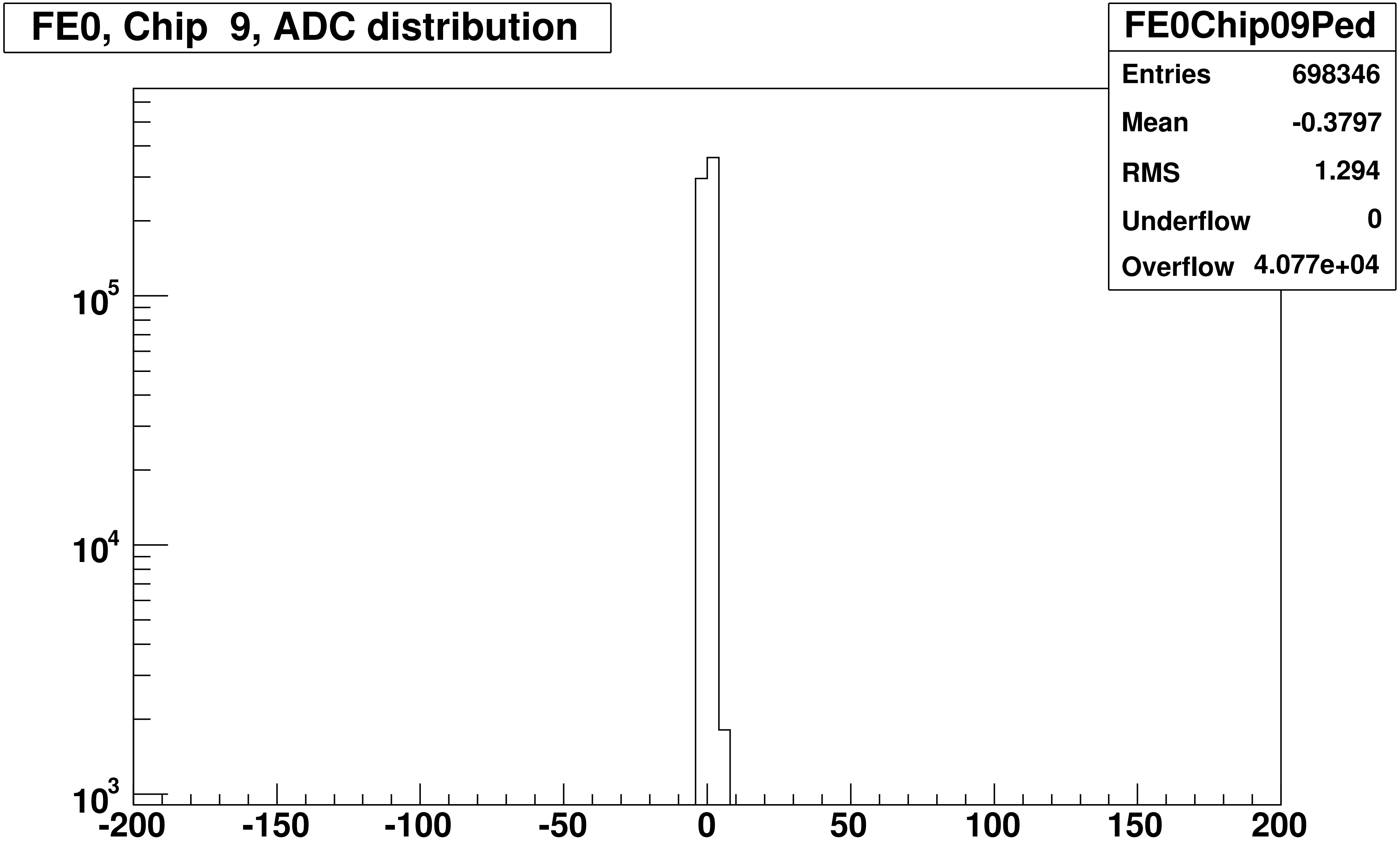
FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 698346
Mean -0.3797
RMS 1.294
Underflow 0
Overflow 4.077e+04
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10^3
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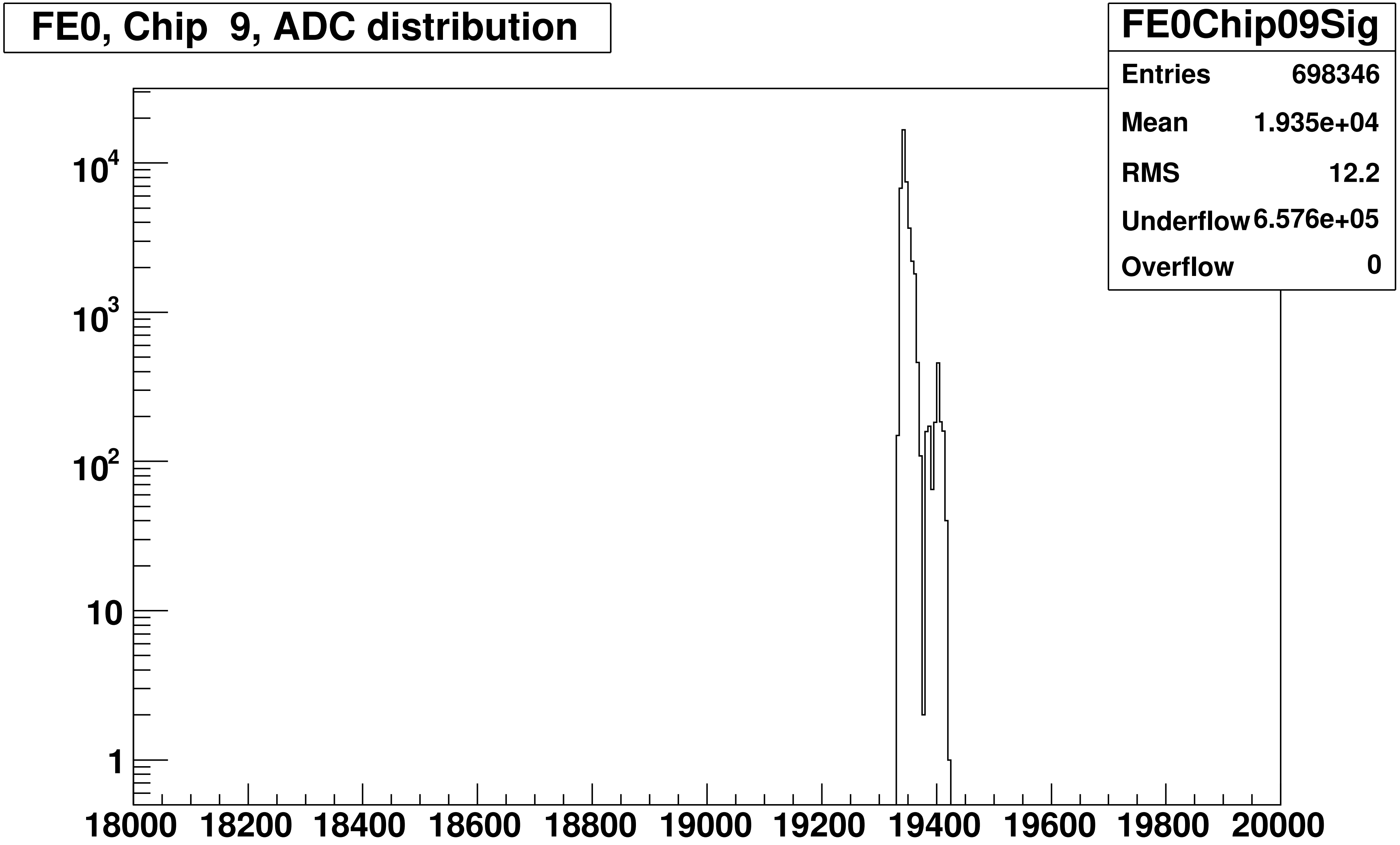
FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 698346
Mean 1.935e+04
RMS 12.2
Underflow 6.576e+05
Overflow 0
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10^3
10^2
10
1
18000
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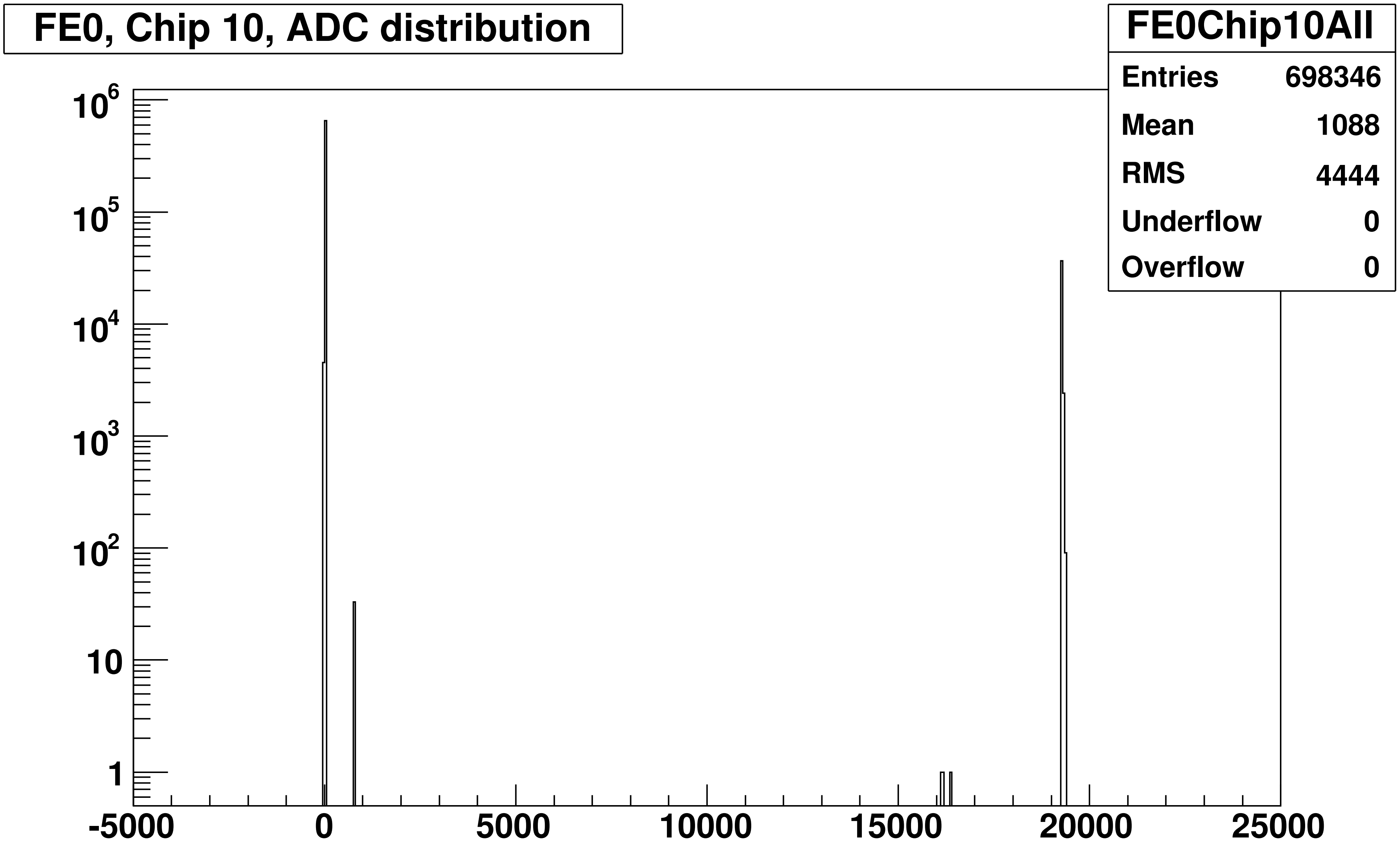
FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 698346
Mean 1088
RMS 4444
Underflow 0
Overflow 0
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10^4
10^3
10^2
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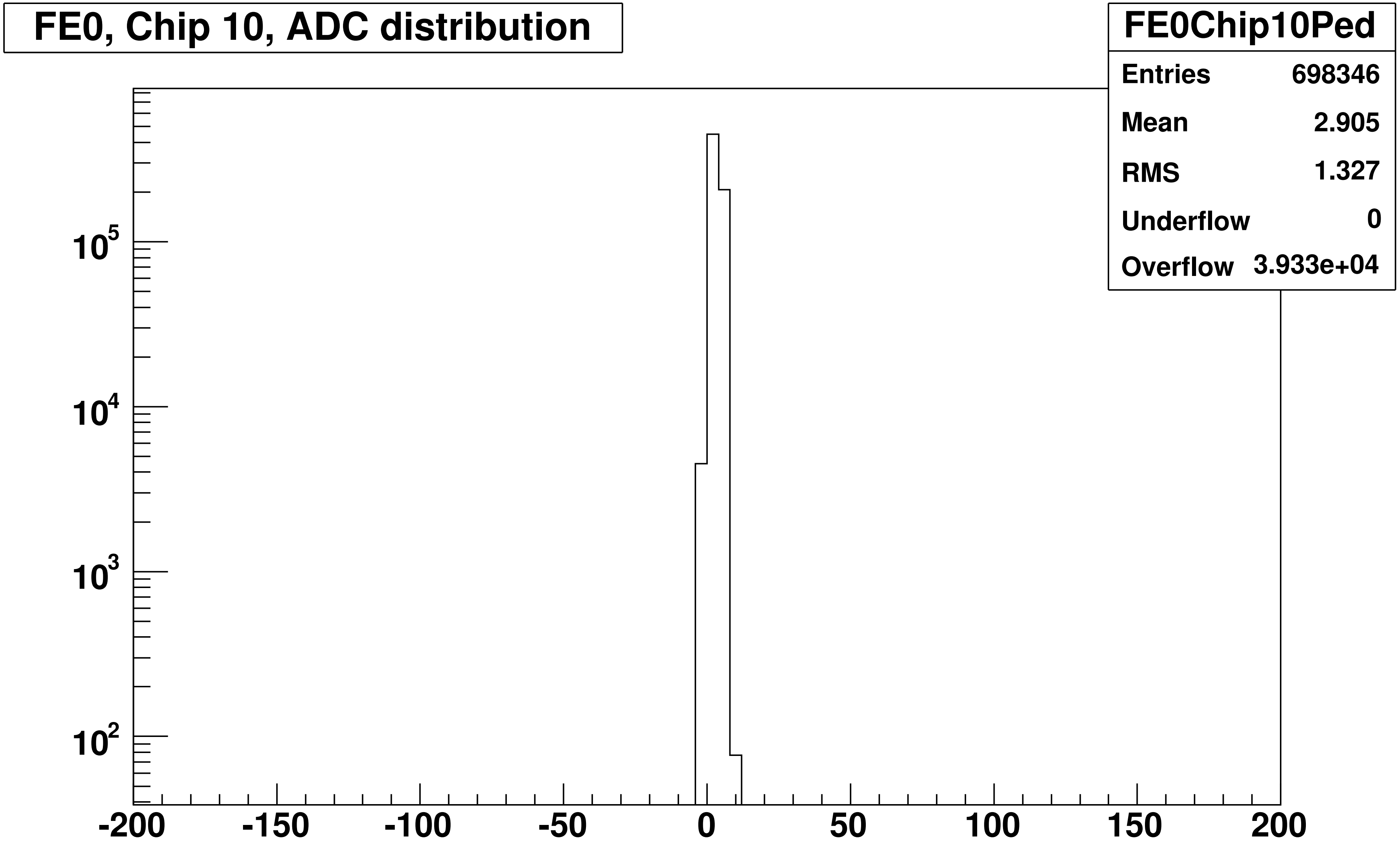

FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 698346
Mean 2.905
RMS 1.327
Underflow 0
Overflow 3.933e+04
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10^4
10^3
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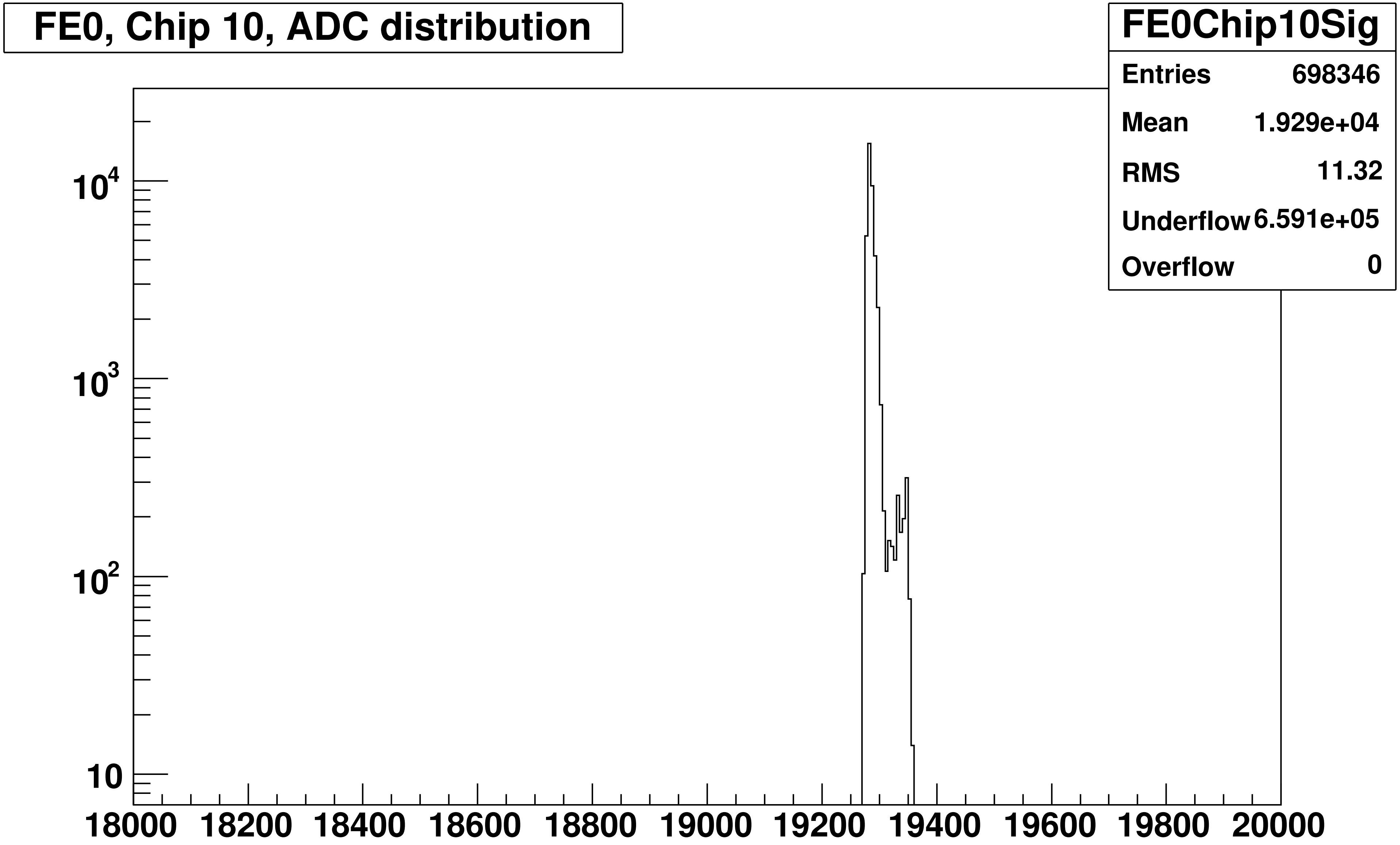
FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 698346
Mean 1.929e+04
RMS 11.32
Underflow 6.591e+05
Overflow 0
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18000
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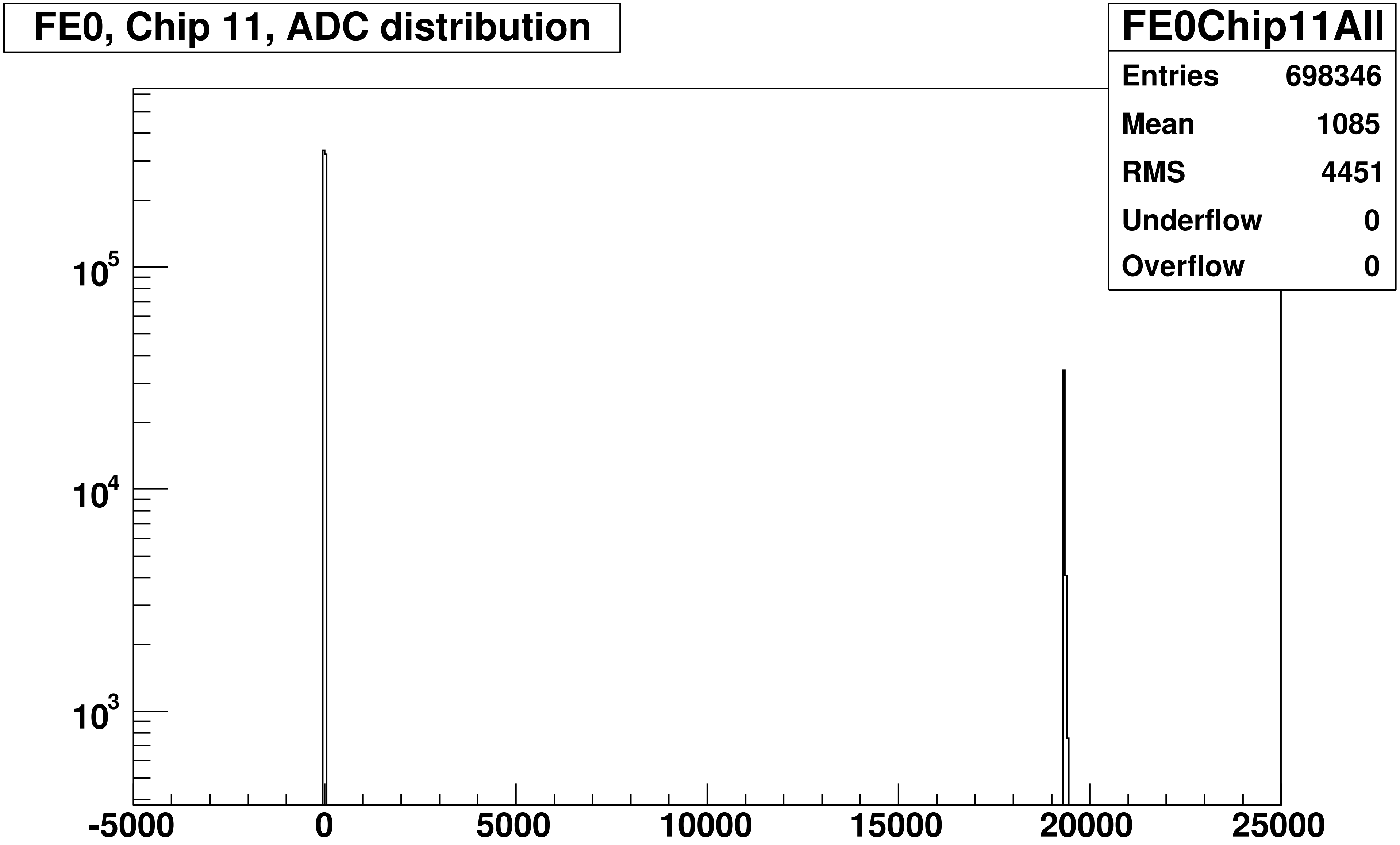
FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 698346
Mean 1085
RMS 4451
Underflow 0
Overflow 0
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10^4
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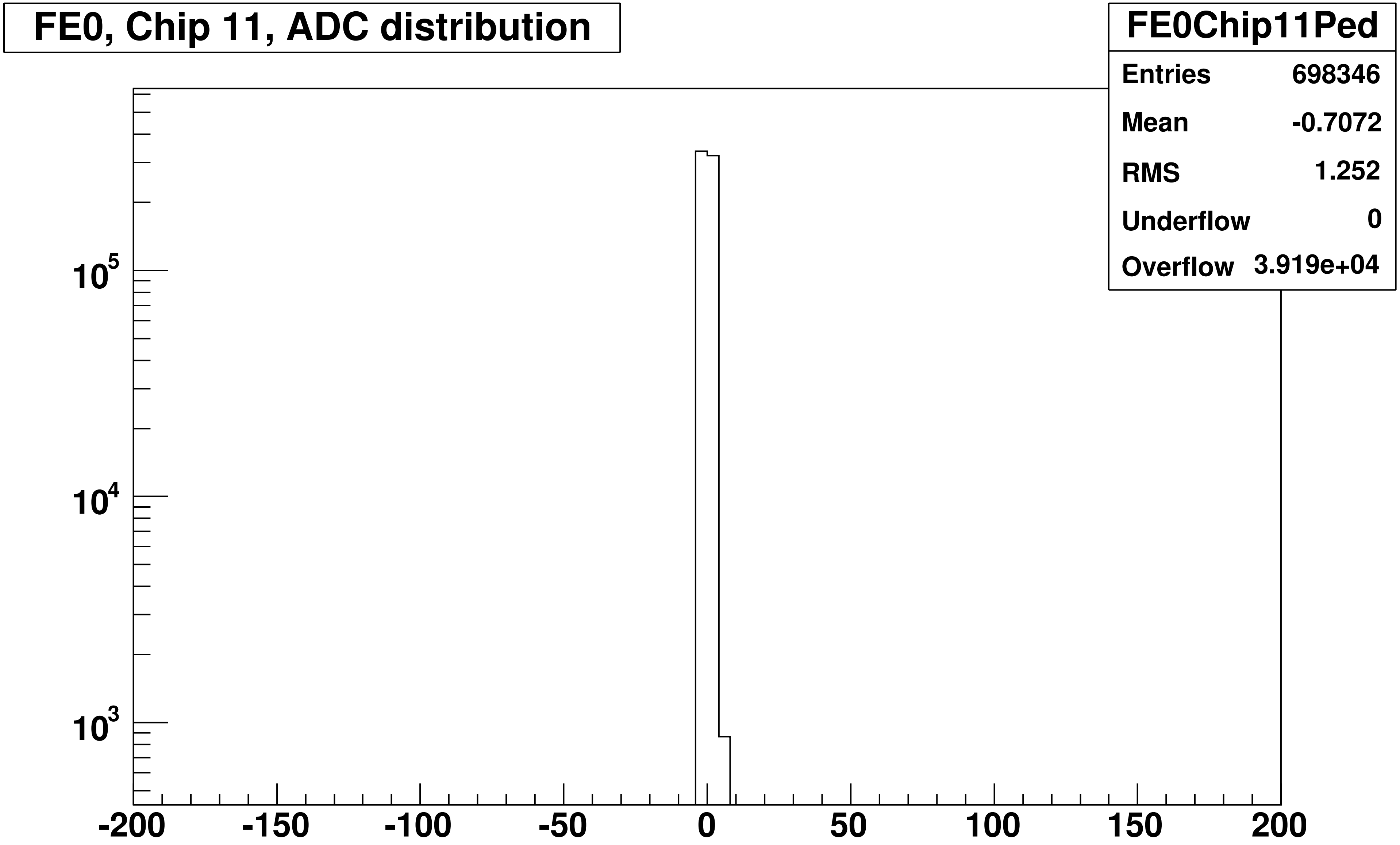
FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 698346
Mean -0.7072
RMS 1.252
Underflow 0
Overflow 3.919e+04
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10^3
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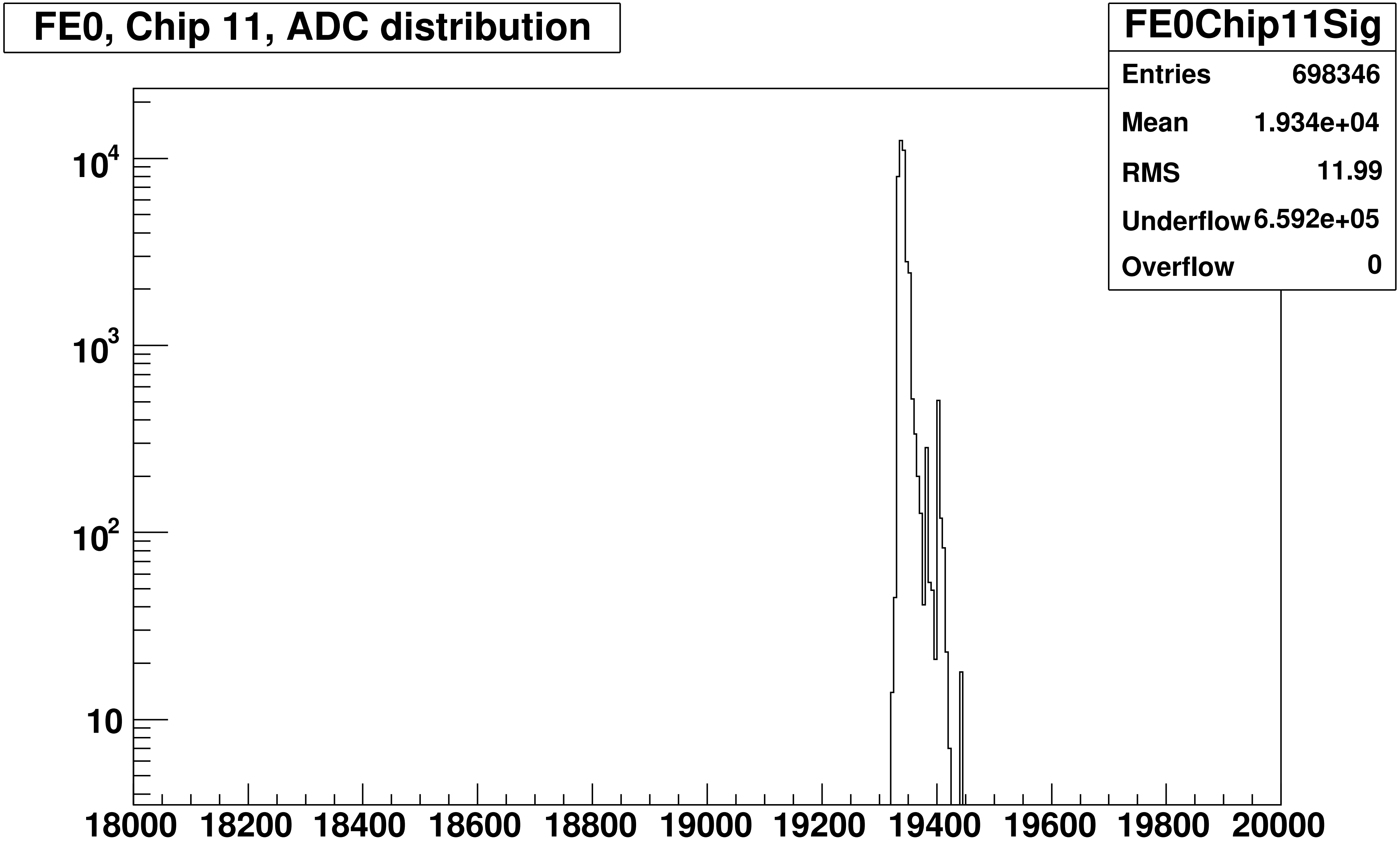
FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 698346
Mean 1.934e+04
RMS 11.99
Underflow 6.592e+05
Overflow 0
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18000
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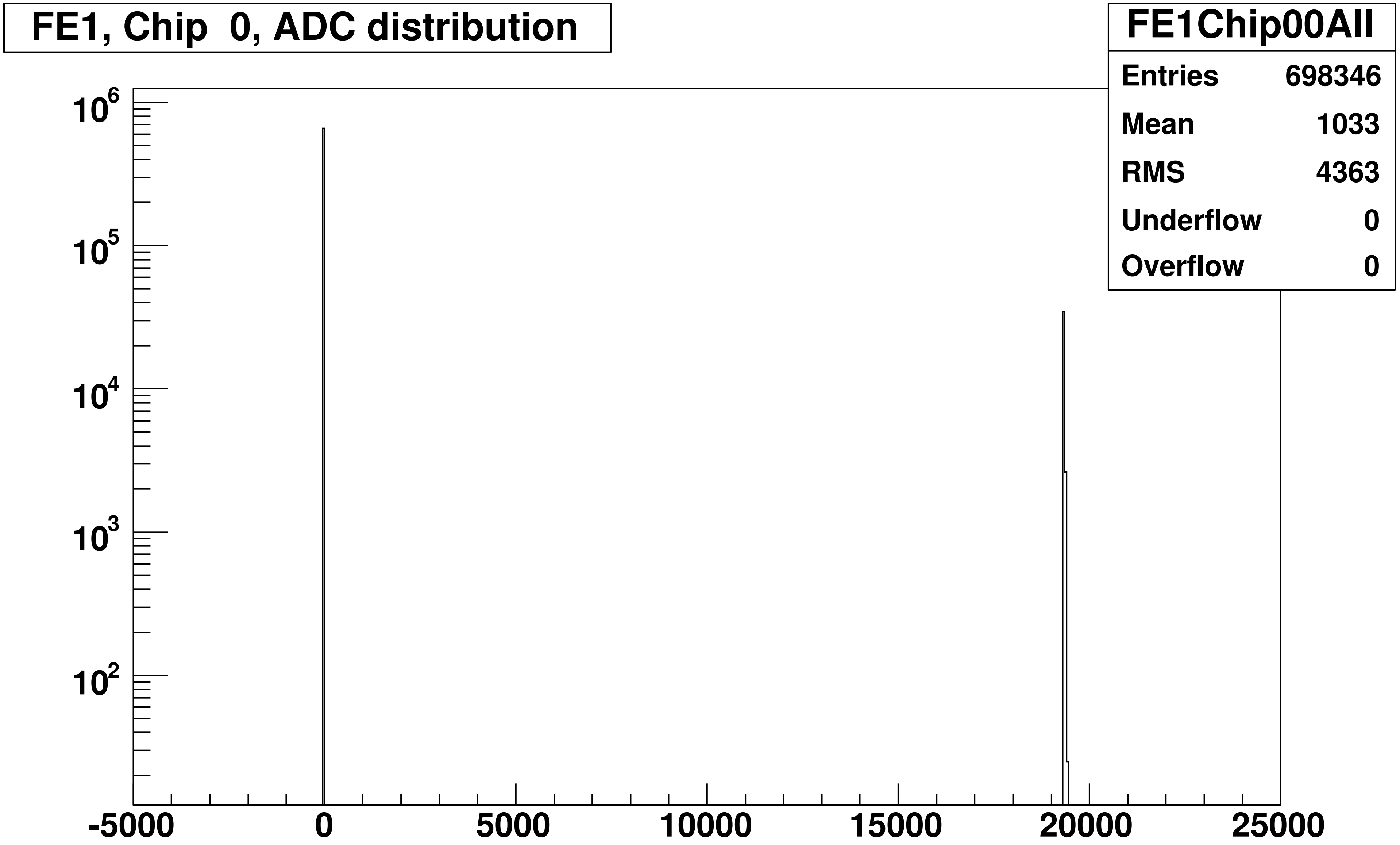
FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 698346
Mean 1033
RMS 4363
Underflow 0
Overflow 0
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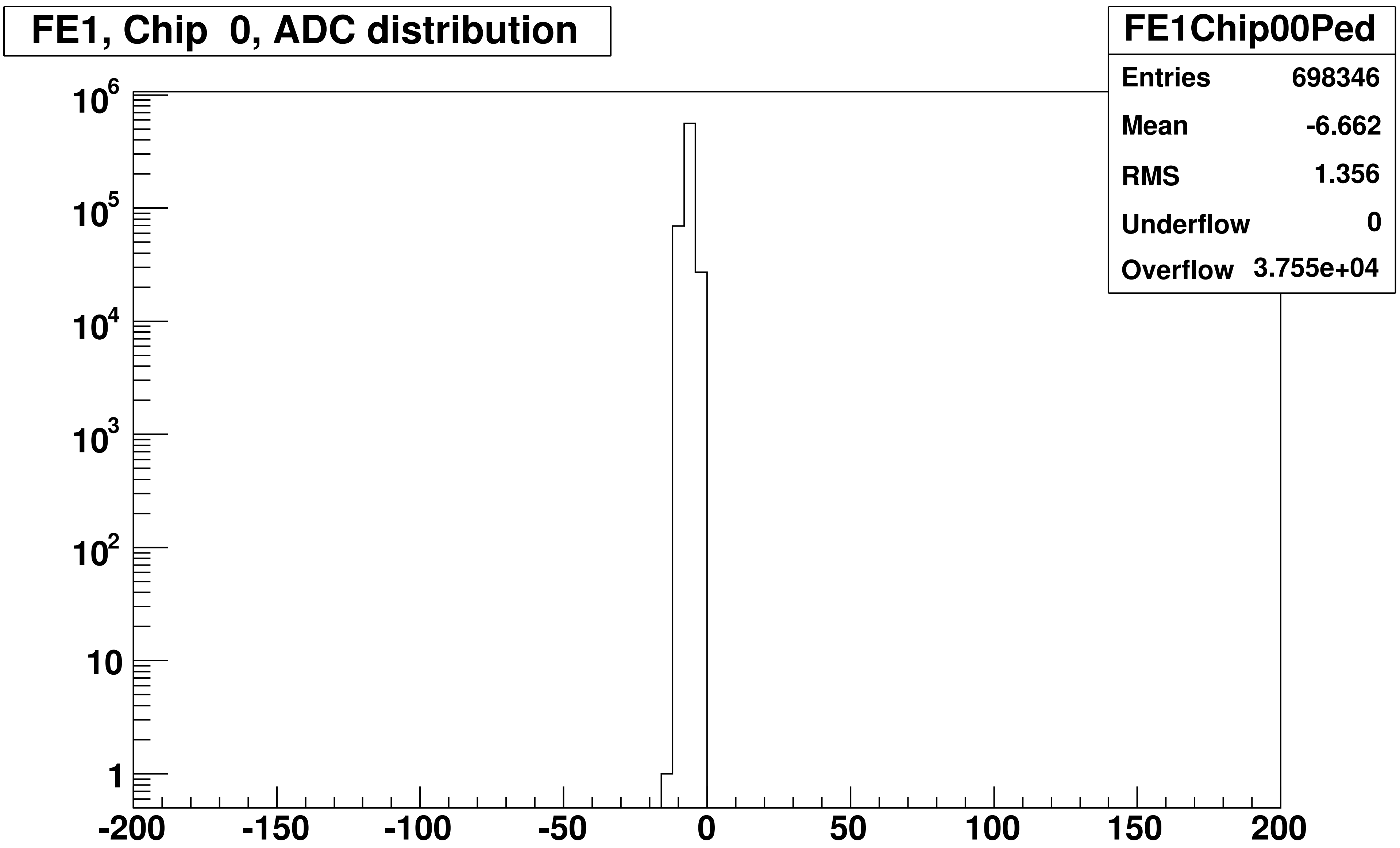

FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 698346
Mean -6.662
RMS 1.356
Underflow 0
Overflow 3.755e+04
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10^3
10^2
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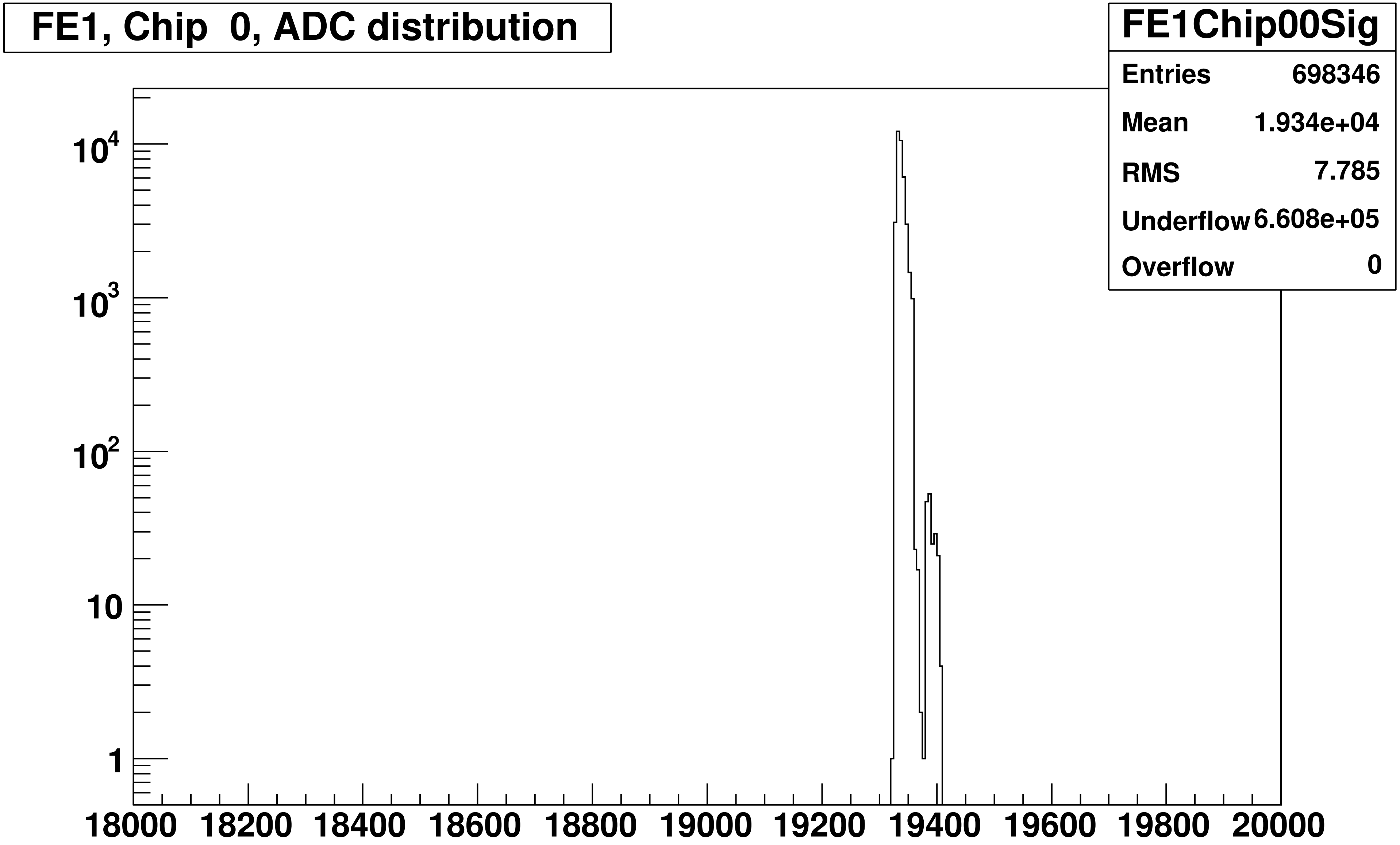
FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 698346
Mean 1.934e+04
RMS 7.785
Underflow 6.608e+05
Overflow 0
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10^3
10^2
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18000
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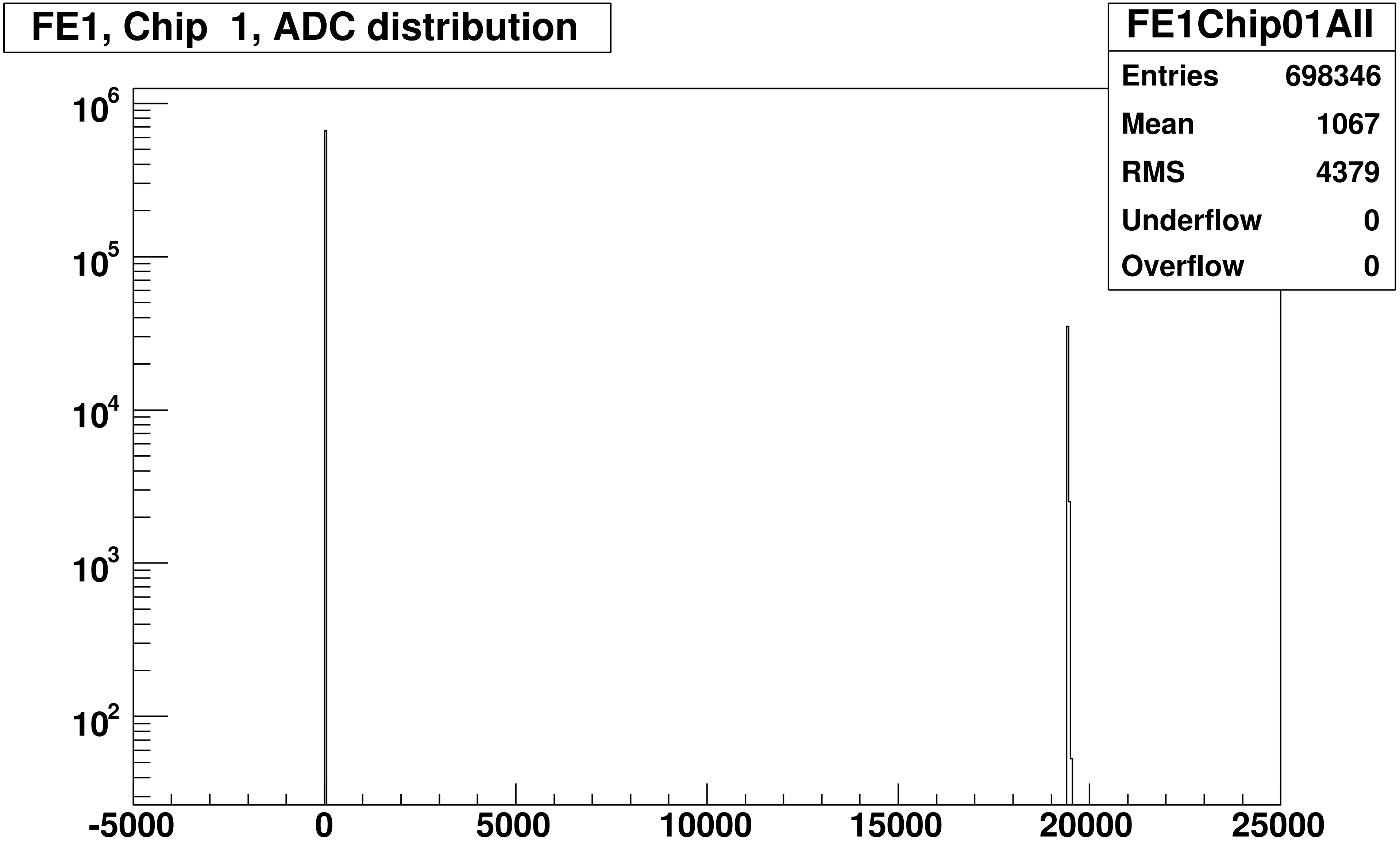
20000


FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 698346
Mean 1067
RMS 4379
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
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10000
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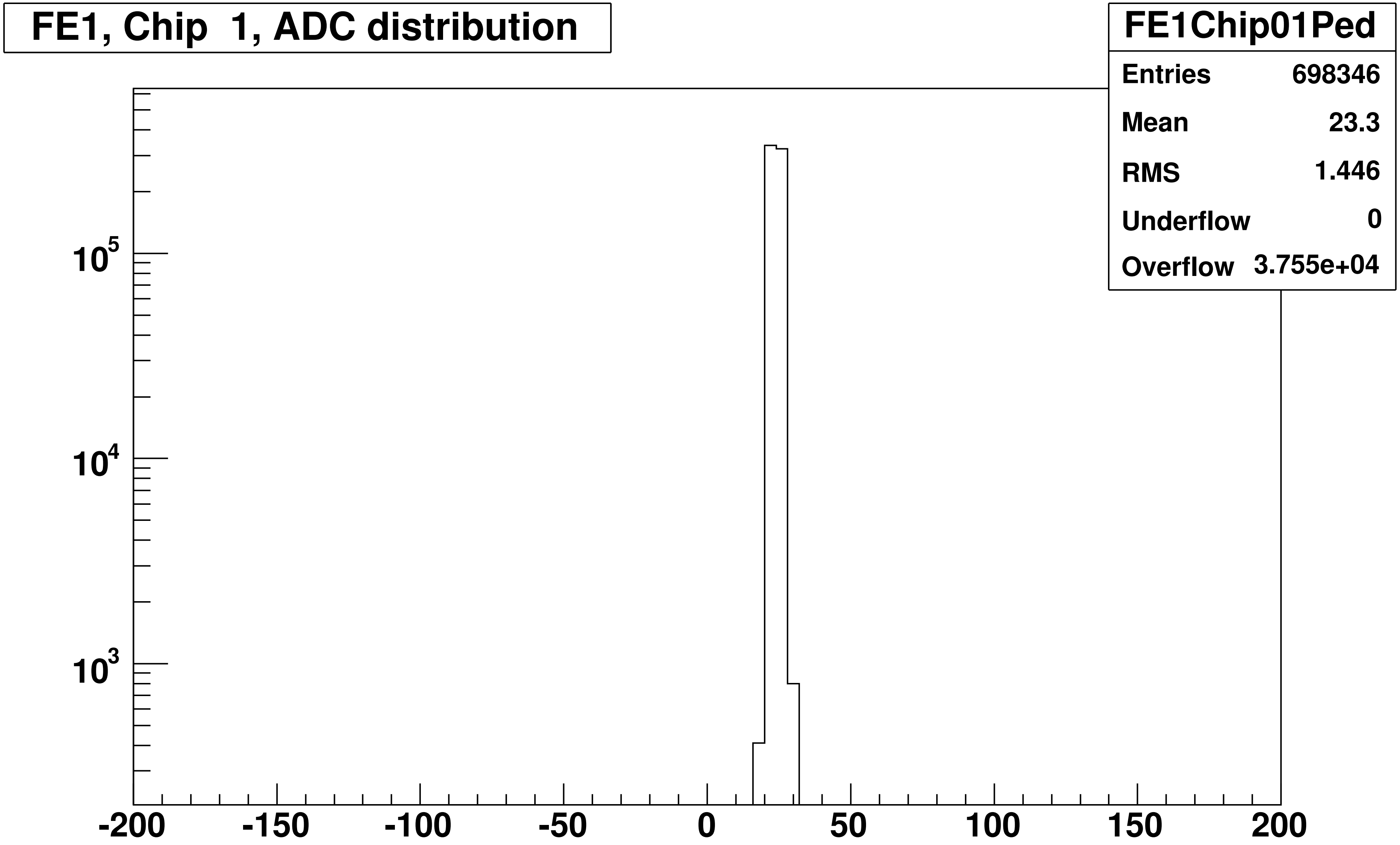
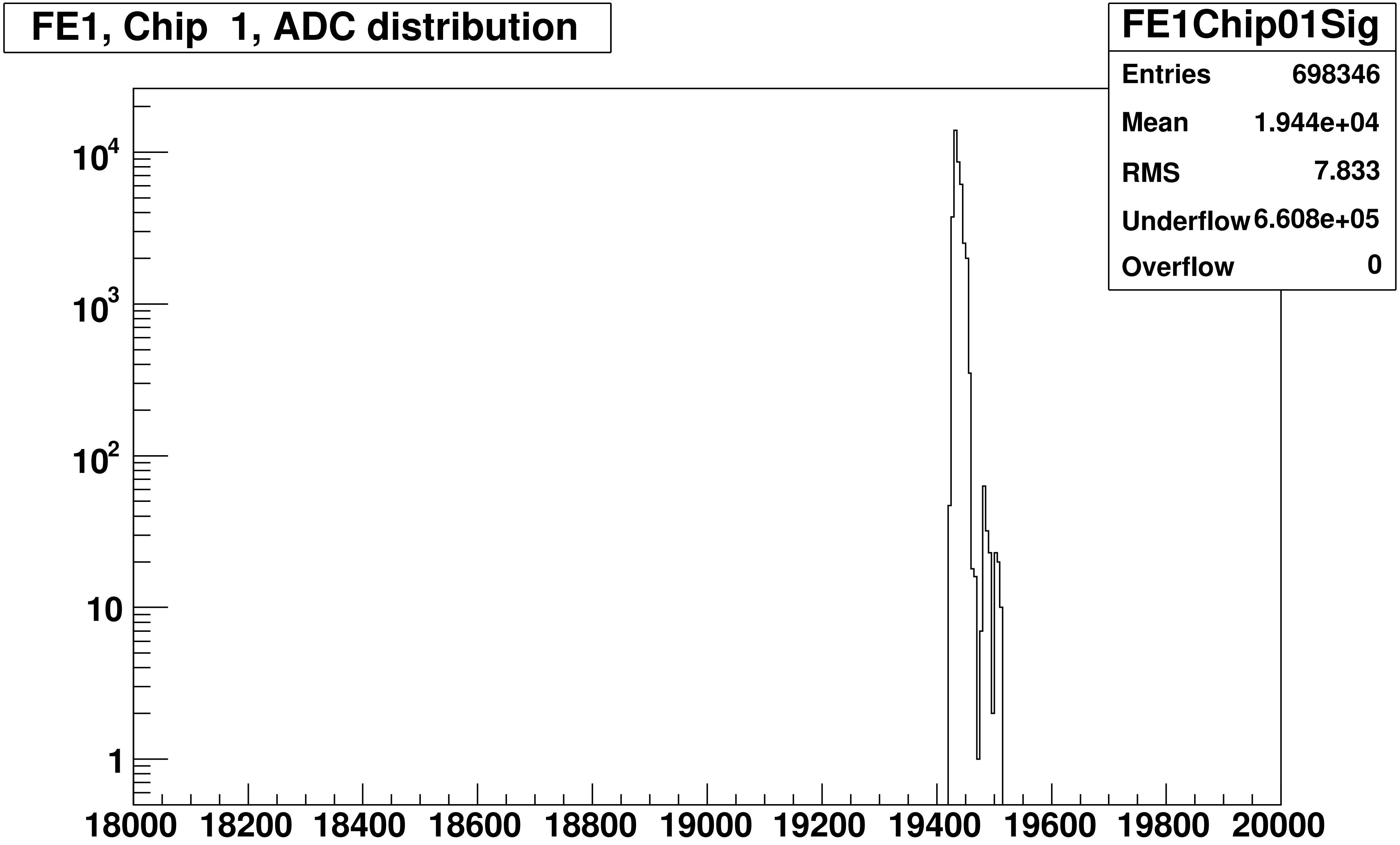
20000
25000



FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 698346
Mean 23.3
RMS 1.446
Underflow 0
Overflow 3.755e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 698346
Mean 1.944e+04
RMS 7.833
Underflow 6.608e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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18800
19000
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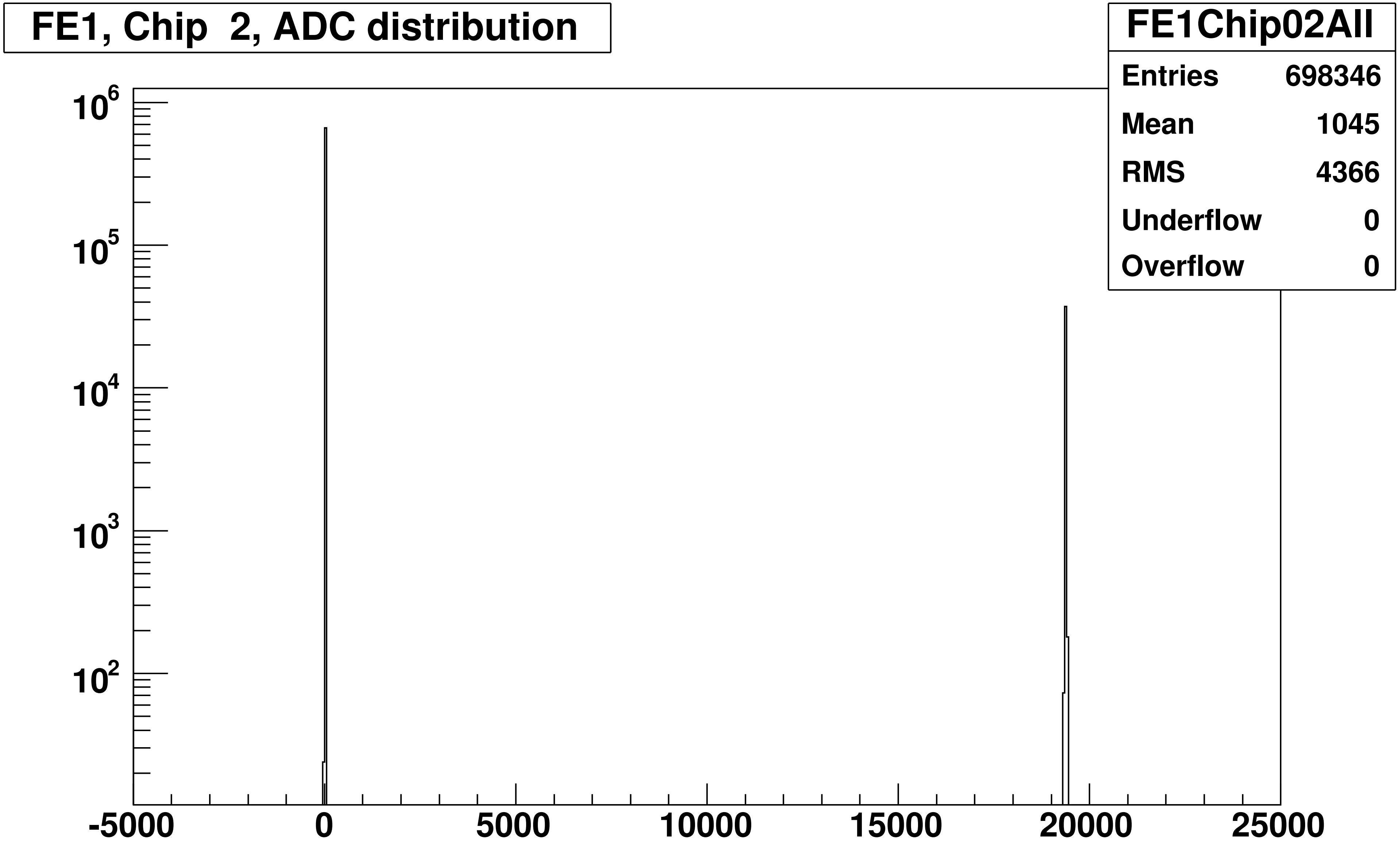
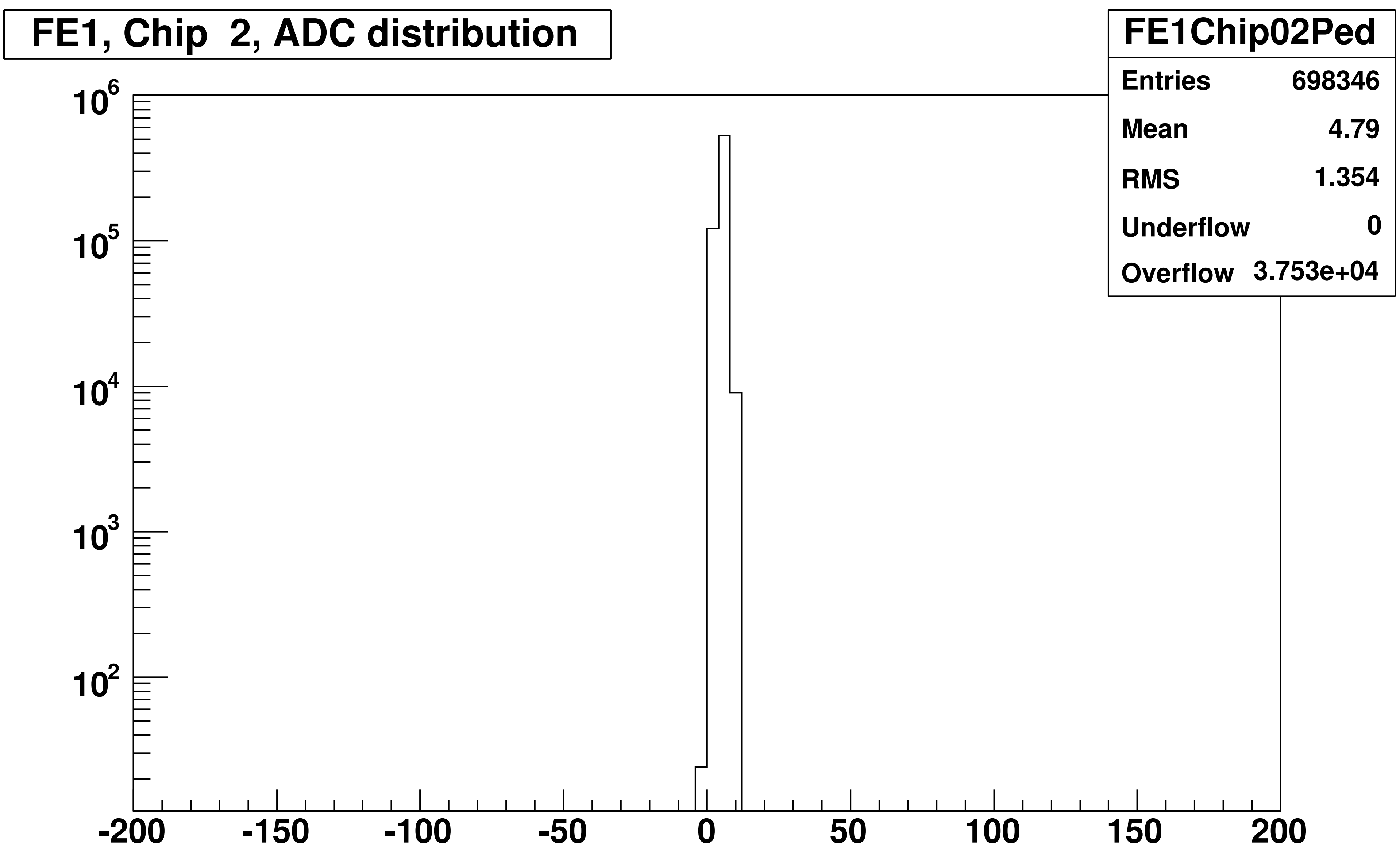
20000


FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 698346
Mean 1045
RMS 4366
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 698346
Mean 4.79
RMS 1.354
Underflow 0
Overflow 3.753e+04
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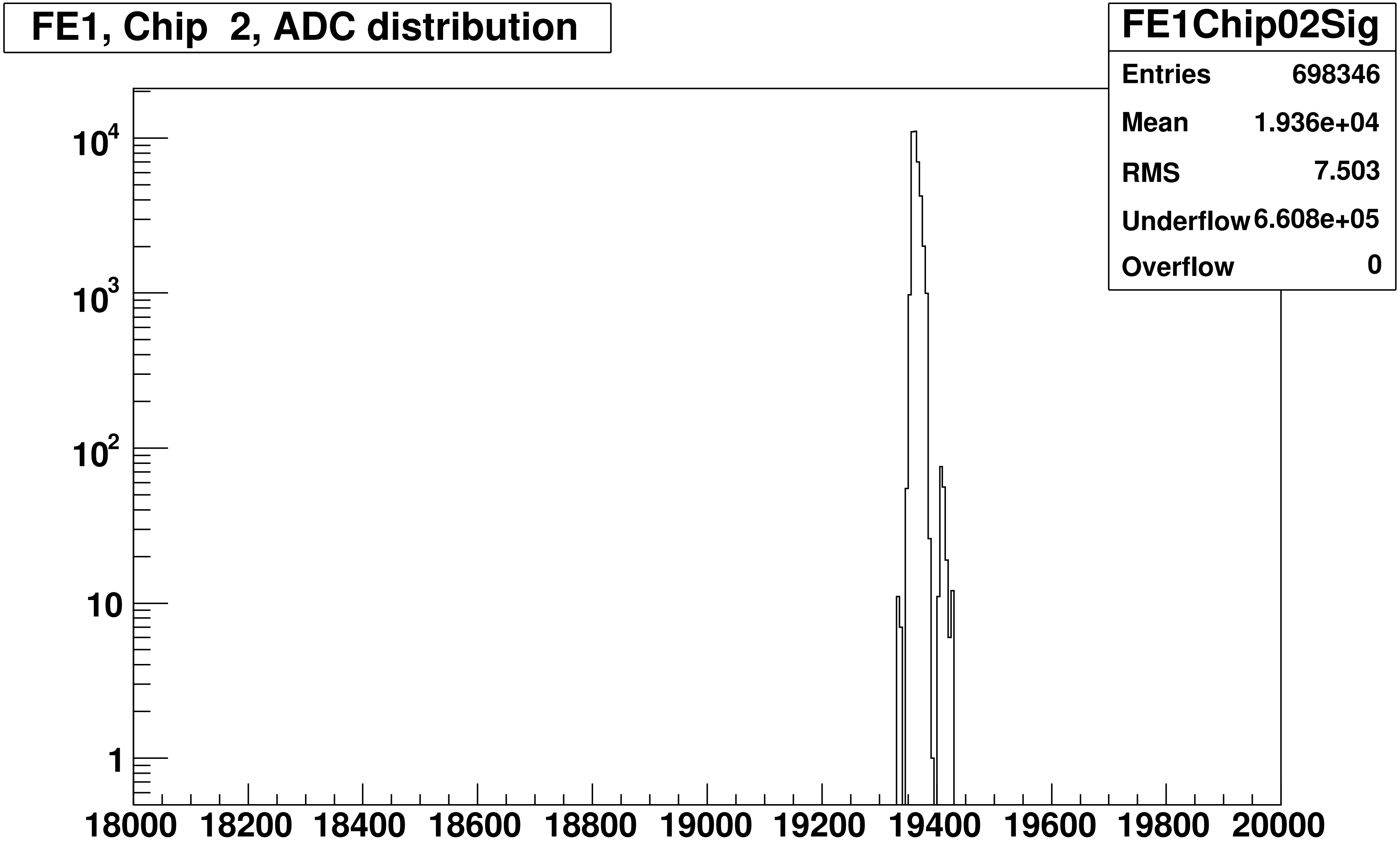
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 698346
Mean 1.936e+04
RMS 7.503
Underflow 6.608e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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19000
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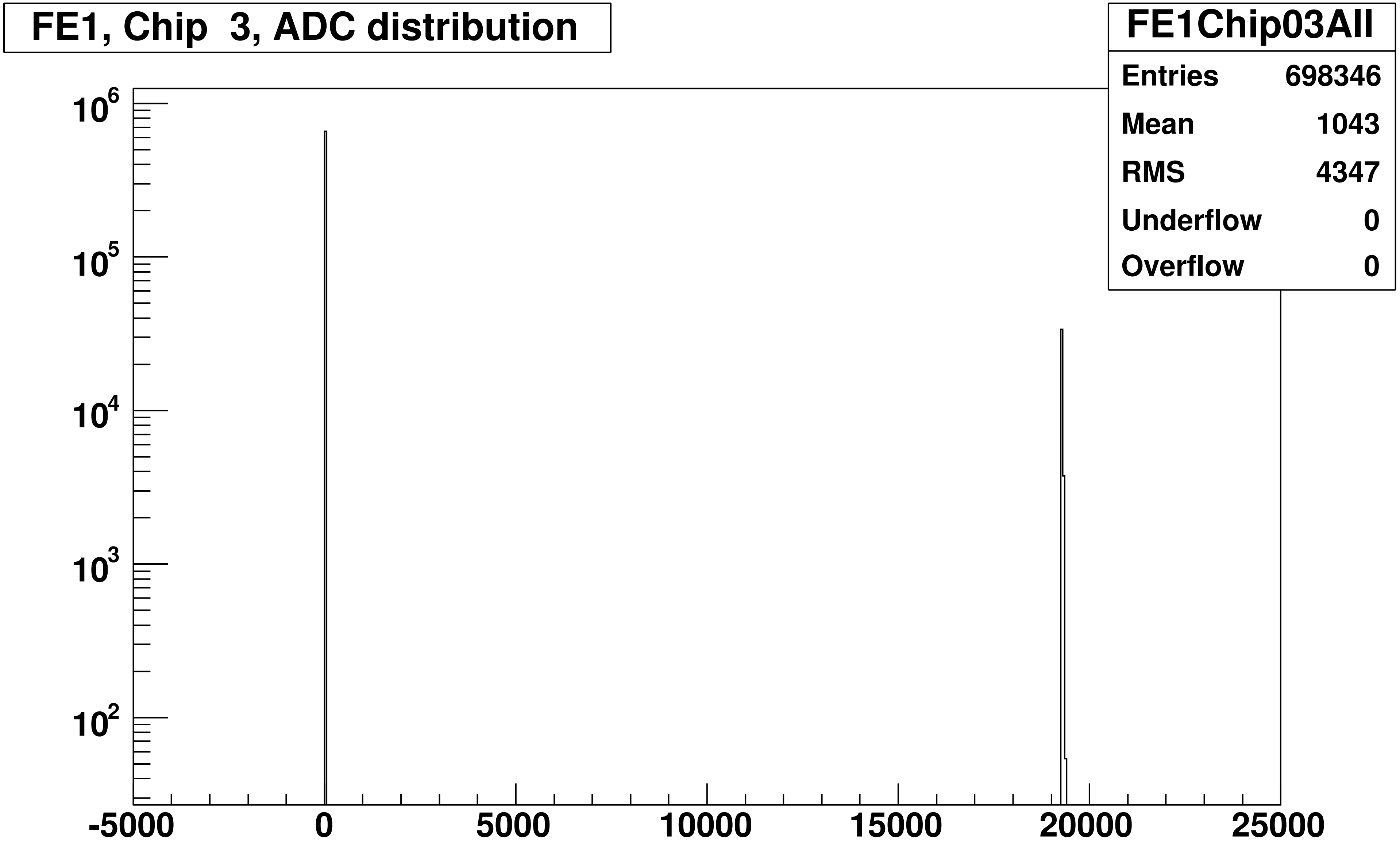
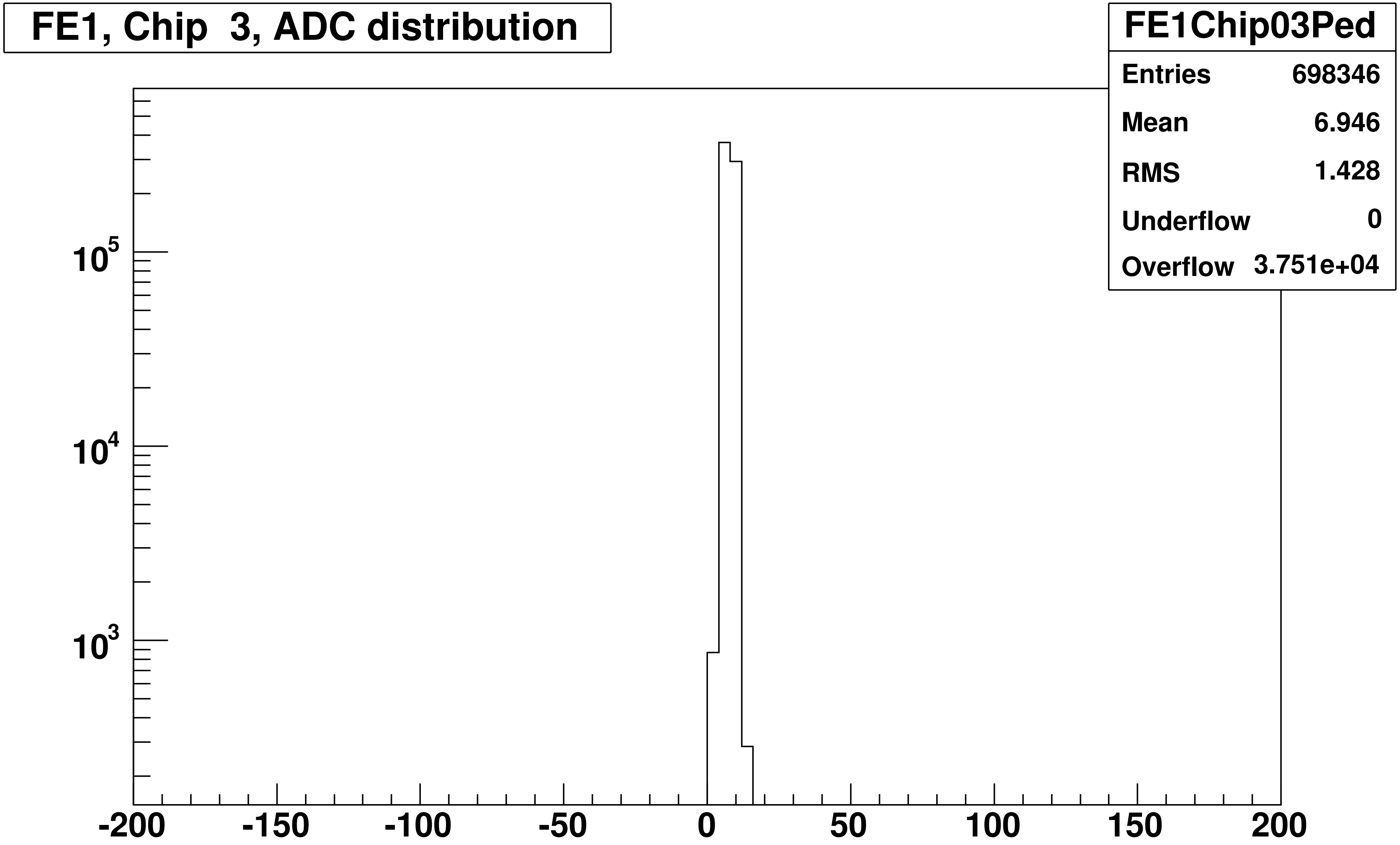
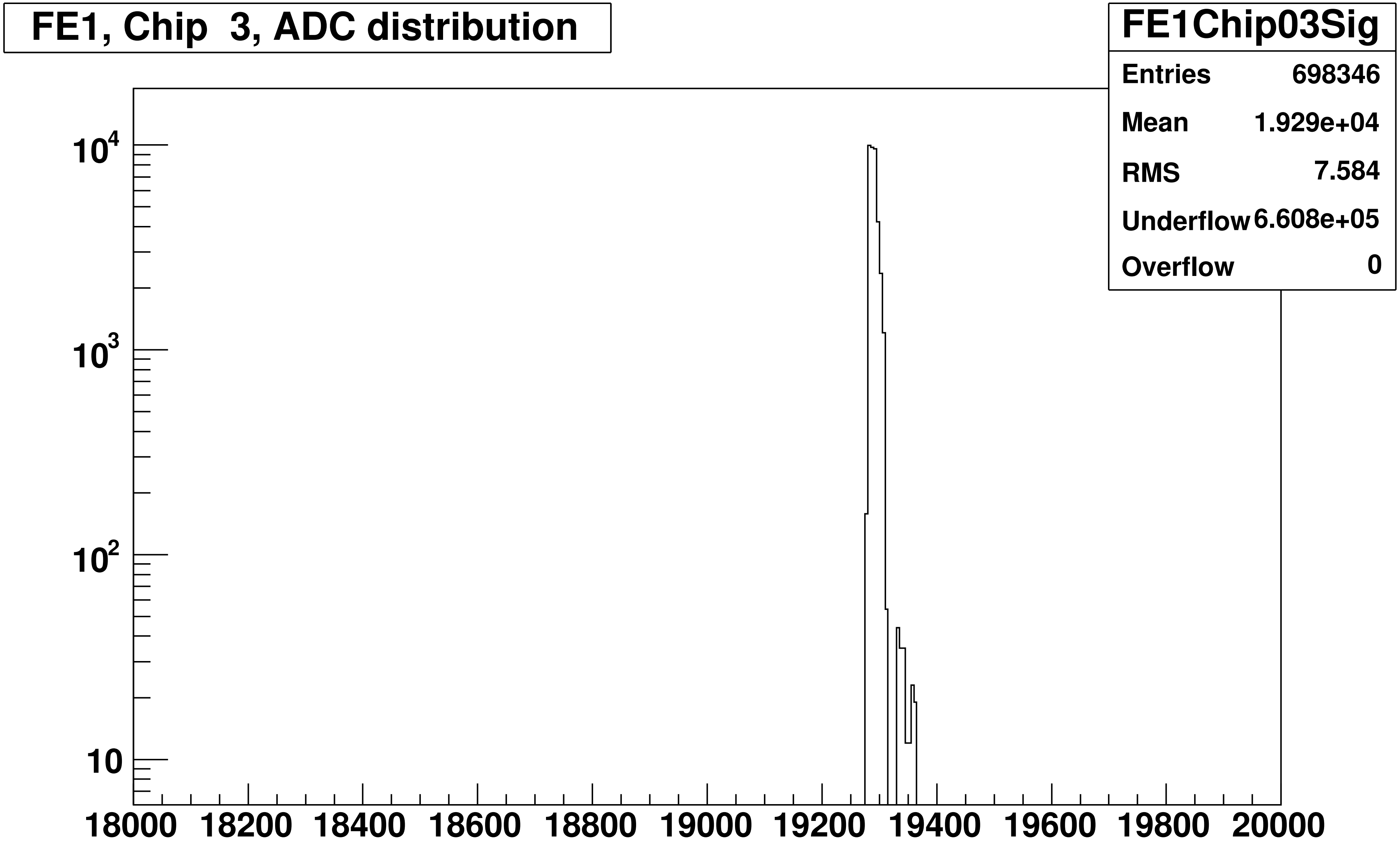
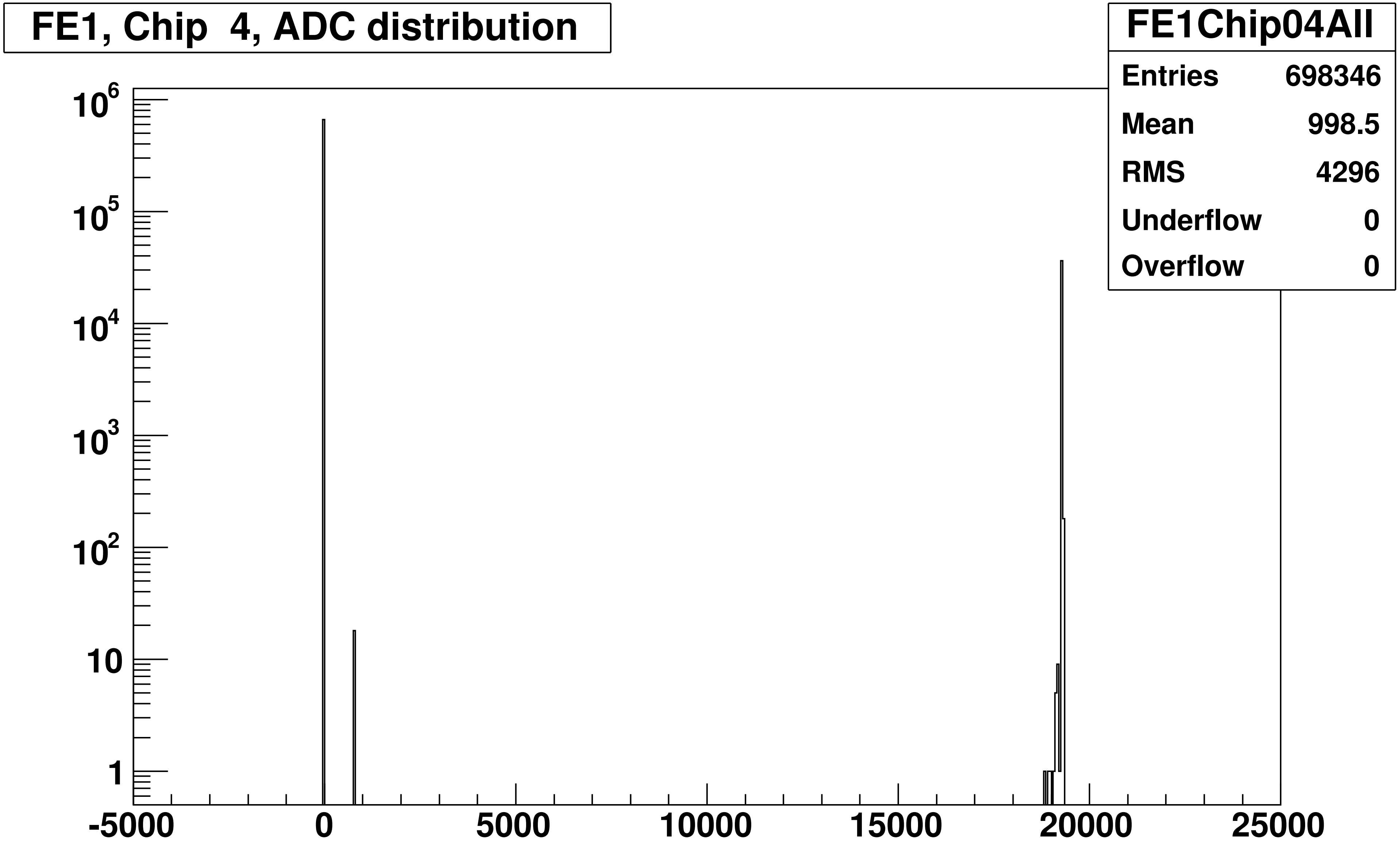
19400
19600
19800
20000



FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 698346
Mean 1043
RMS 4347
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 698346
Mean 6.946
RMS 1.428
Underflow 0
Overflow 3.751e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 698346
Mean 1.929e+04
RMS 7.584
Underflow 6.608e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 698346
Mean 998.5
RMS 4296
Underflow 0
Overflow 0
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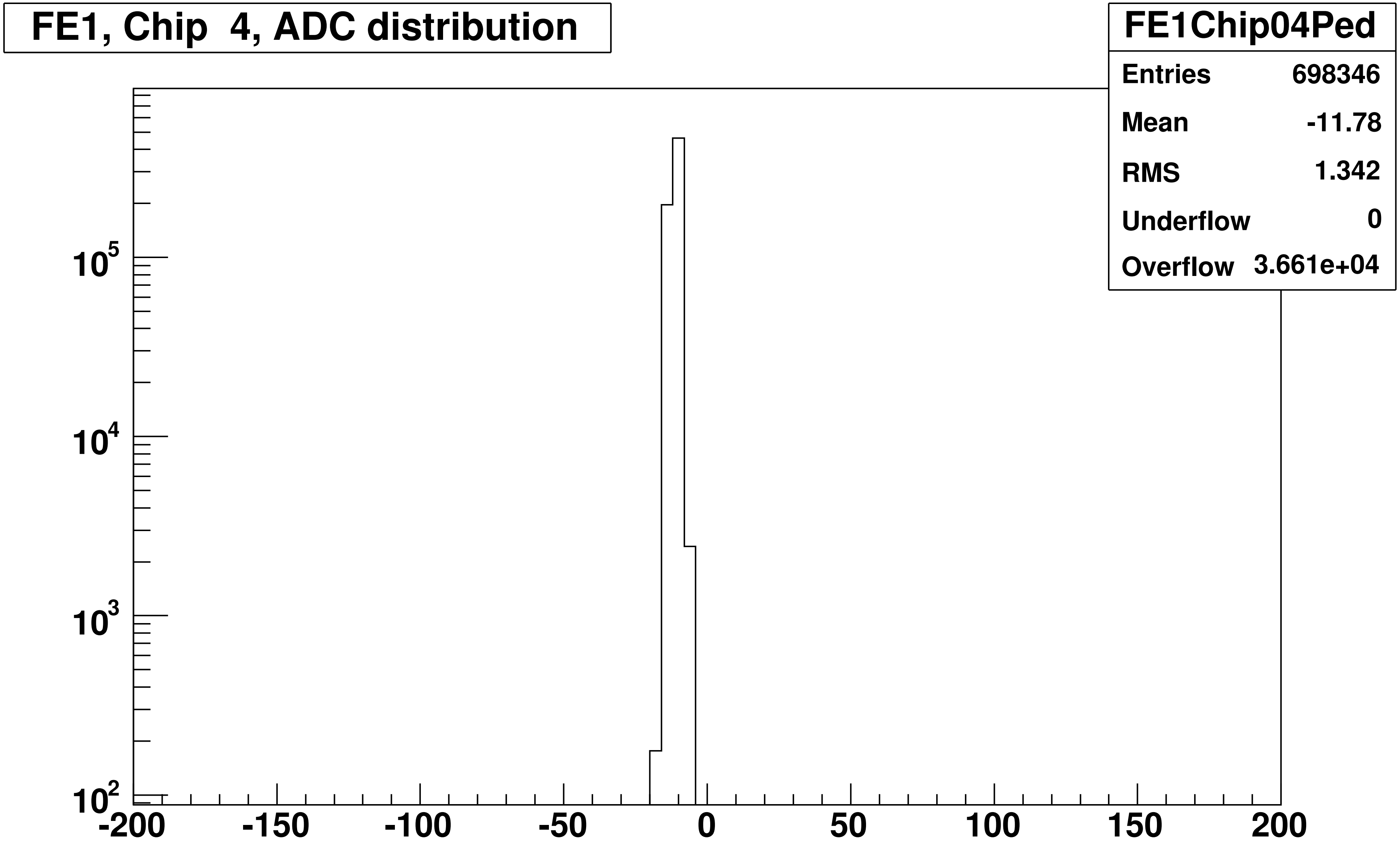
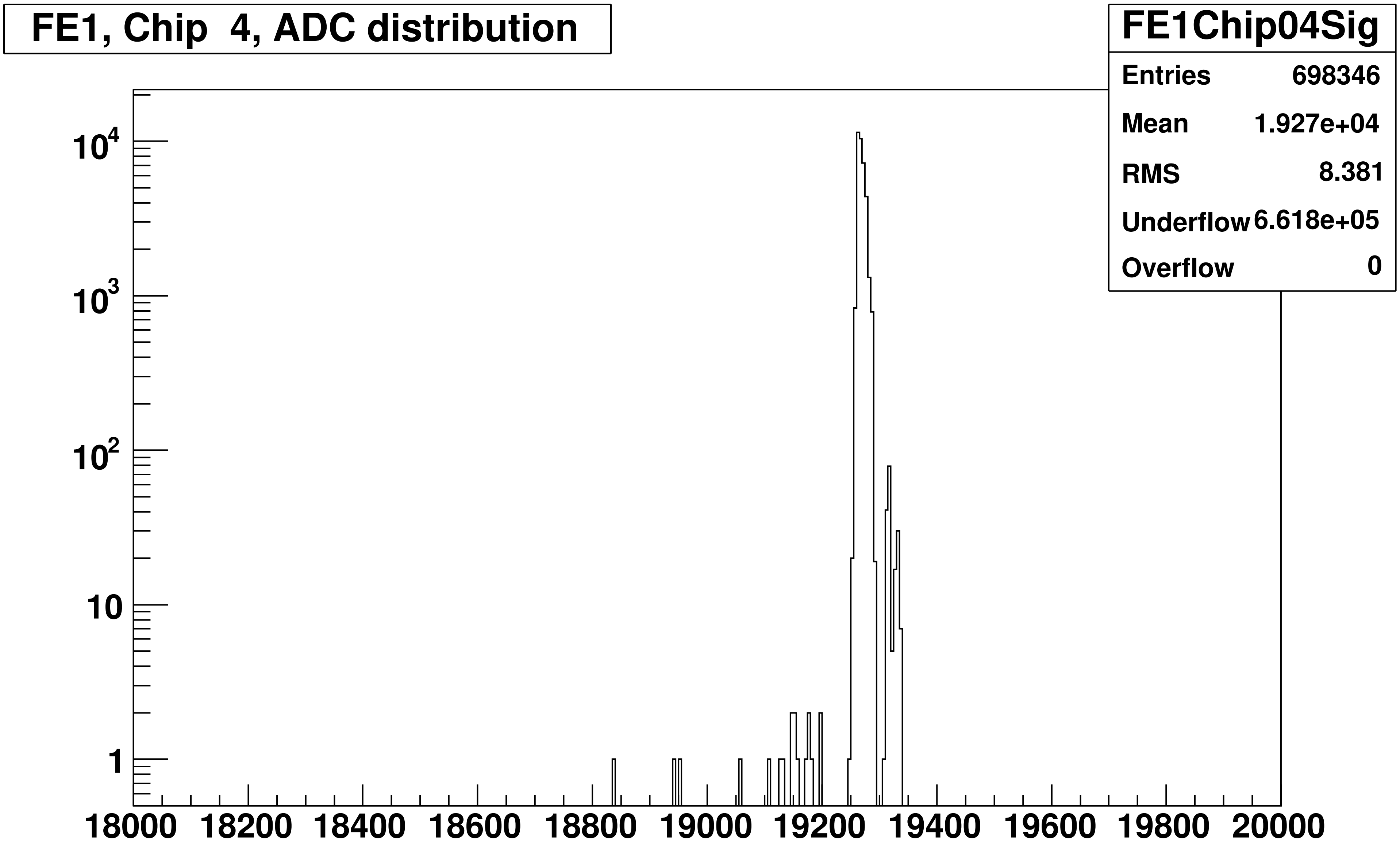
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 698346
Mean -11.78
RMS 1.342
Underflow 0
Overflow 3.661e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 698346
Mean 1.927e+04
RMS 8.381
Underflow 6.618e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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19000
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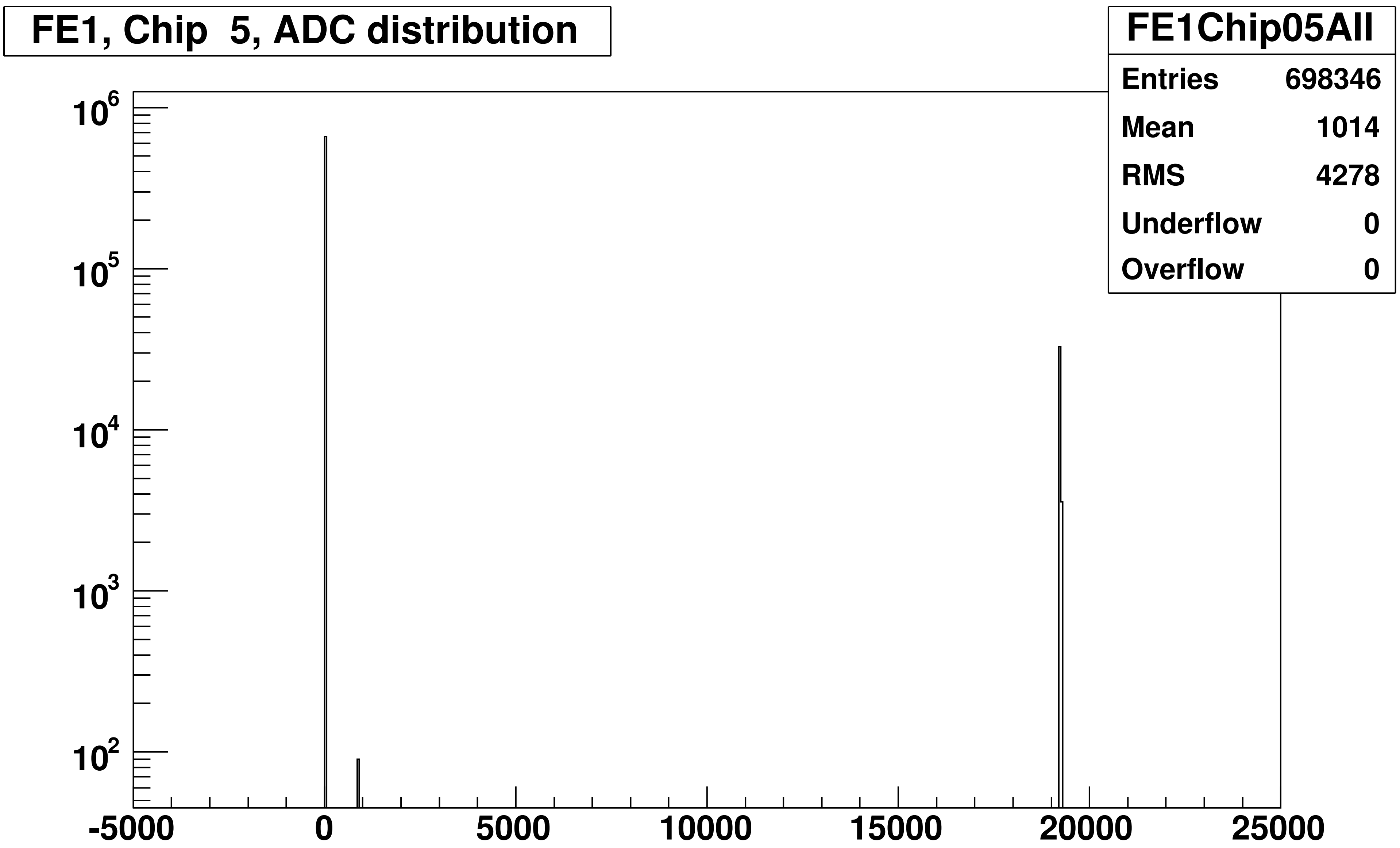
19400
19600
19800
20000


FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 698346
Mean 1014
RMS 4278
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
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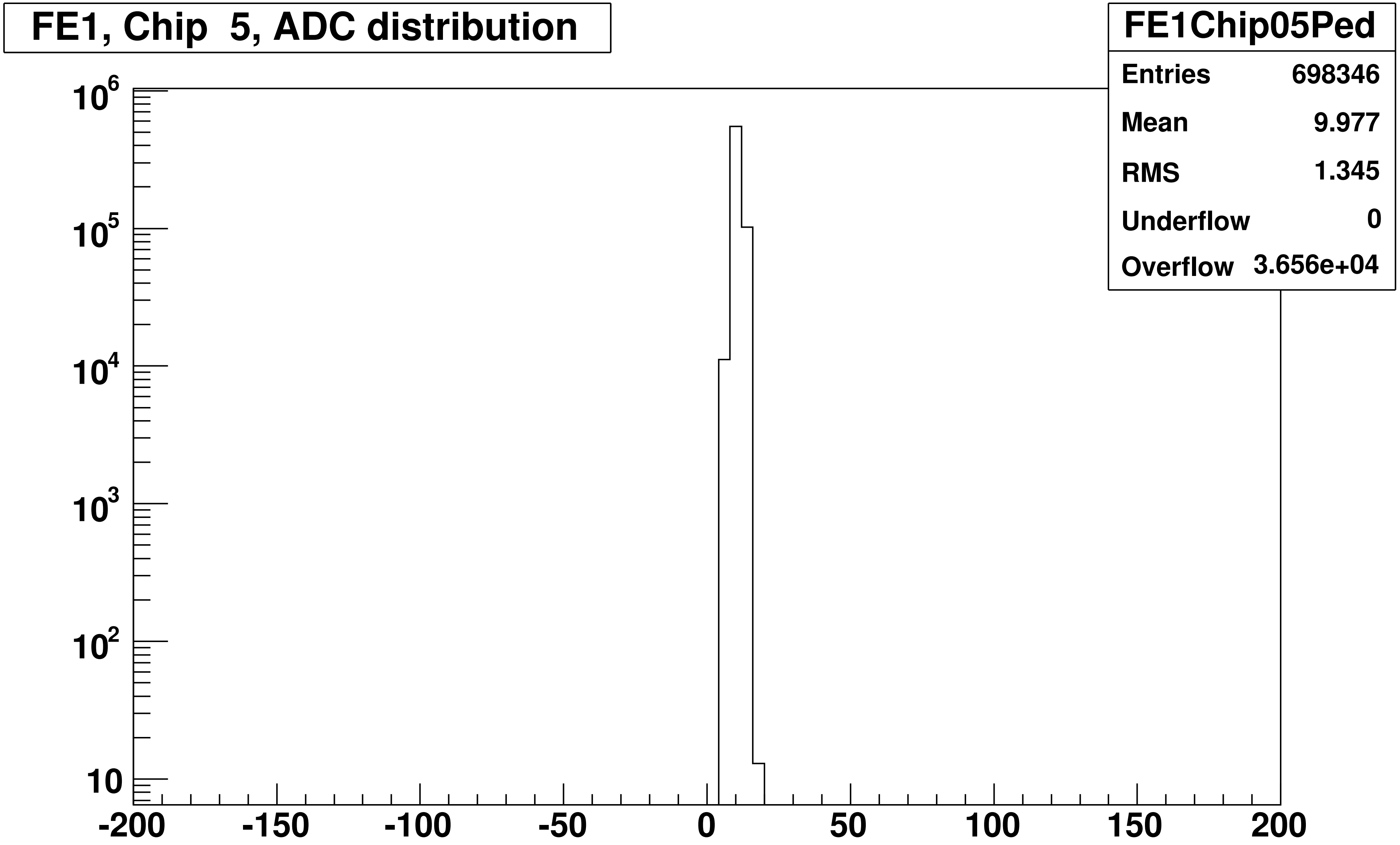
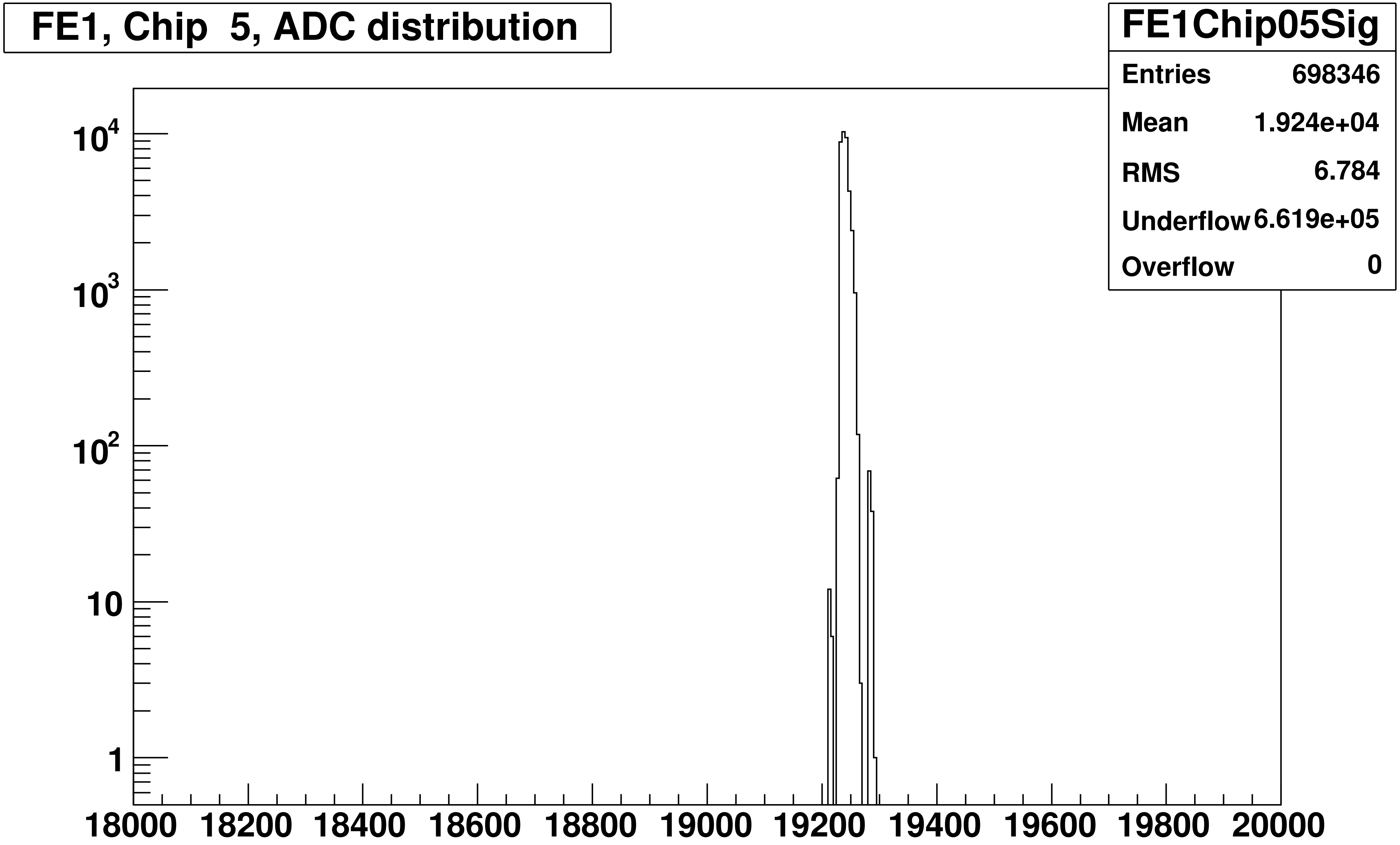
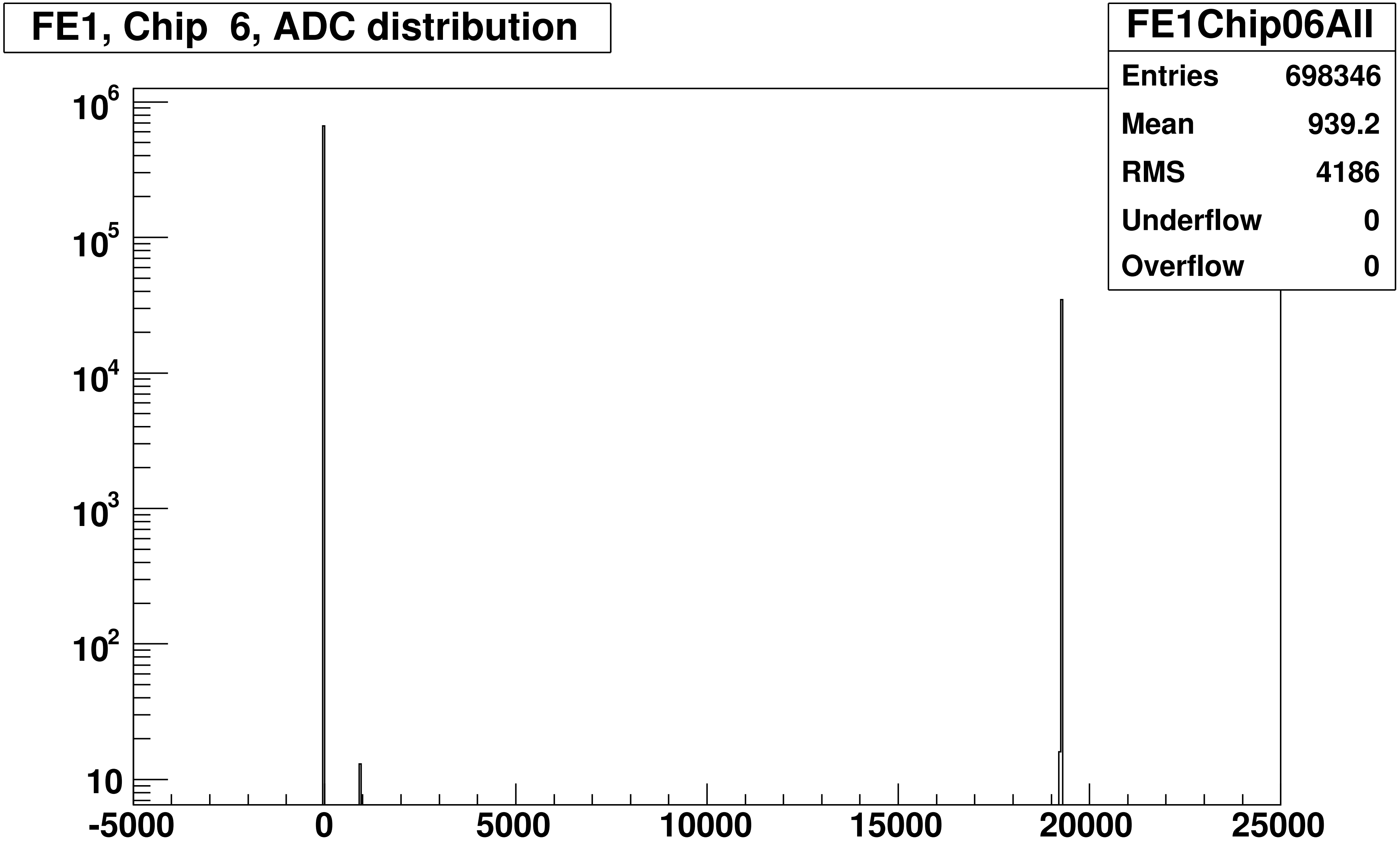
10000
15000
20000
25000


FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 698346
Mean 9.977
RMS 1.345
Underflow 0
Overflow 3.656e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 698346
Mean 1.924e+04
RMS 6.784
Underflow 6.619e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 698346
Mean 939.2
RMS 4186
Underflow 0
Overflow 0
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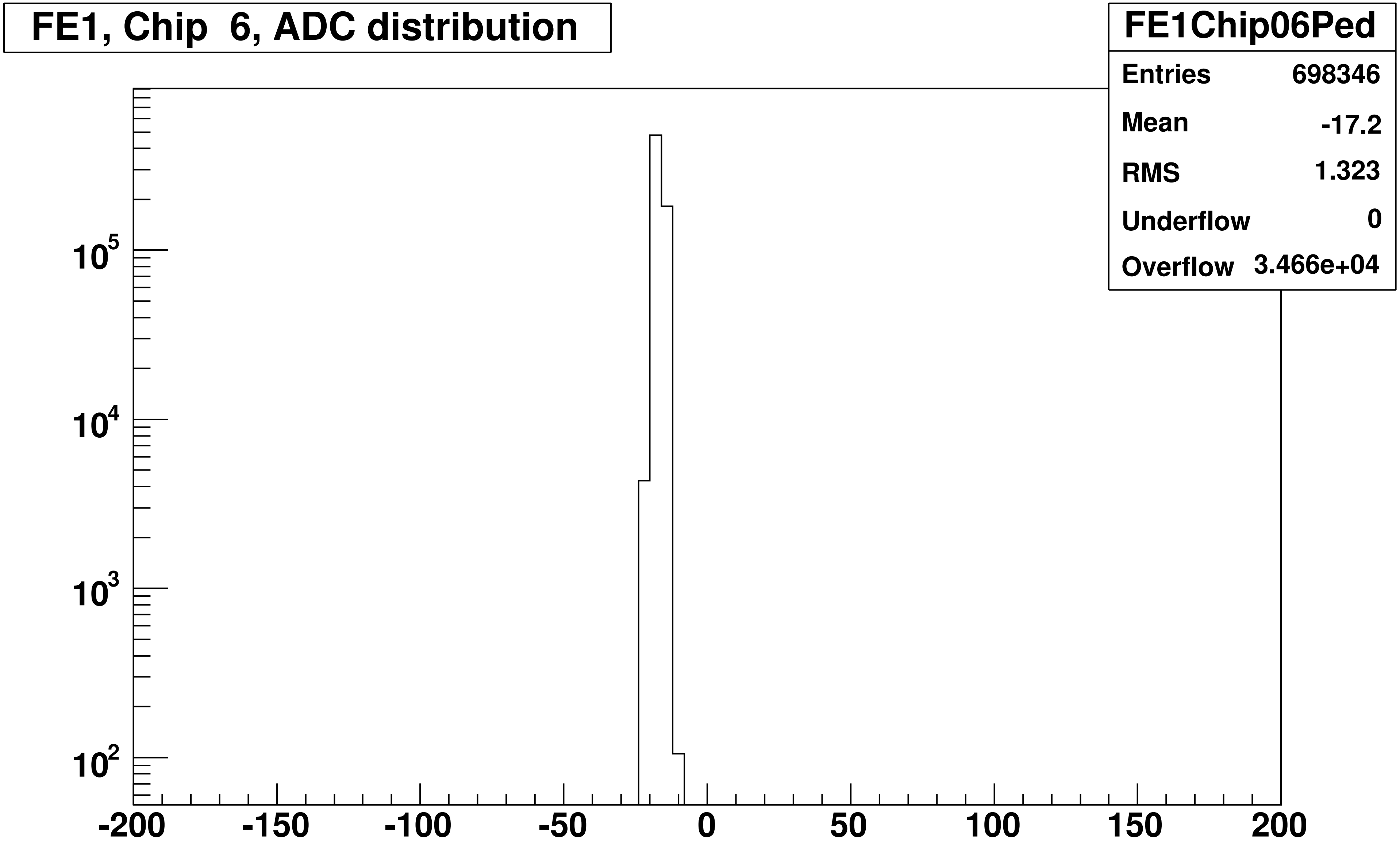
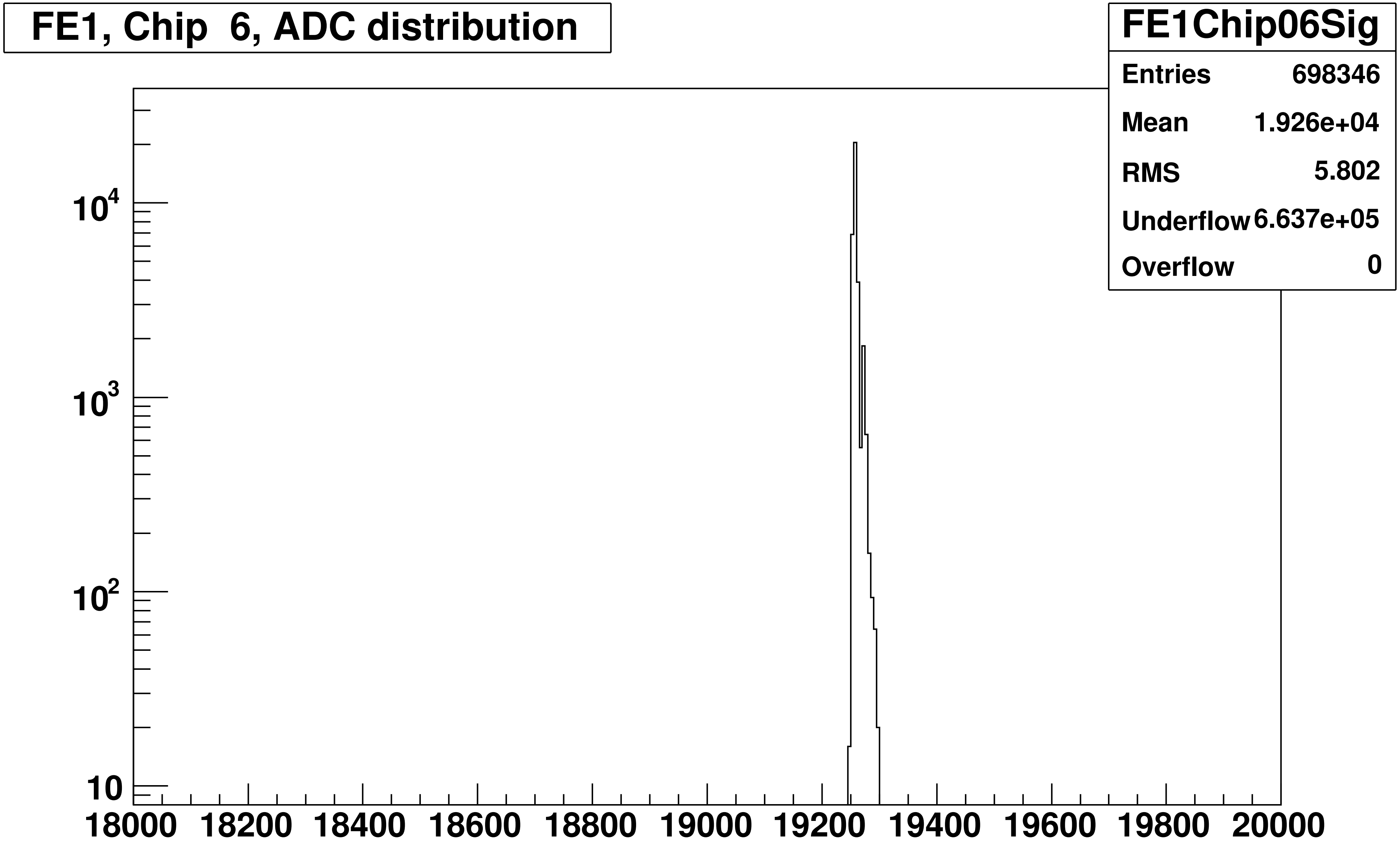
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 698346
Mean -17.2
RMS 1.323
Underflow 0
Overflow 3.466e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 698346
Mean 1.926e+04
RMS 5.802
Underflow 6.637e+05
Overflow 0
10
10^2
10^3
10^4
18000
18200
18400
18600
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19000
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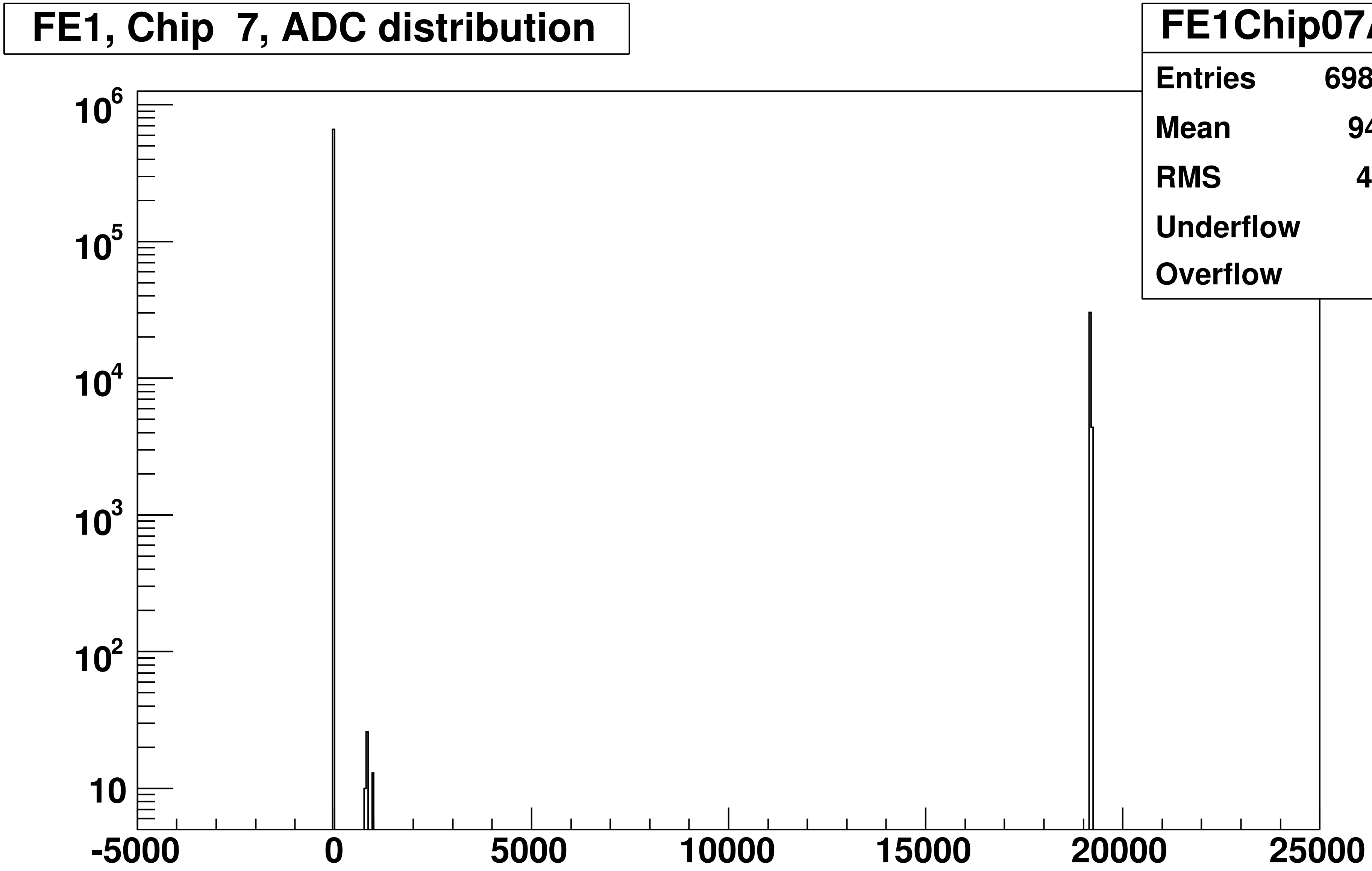
19400
19600
19800
20000


FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 698346
Mean 941.4
RMS 4170
Underflow 0
Overflow 0
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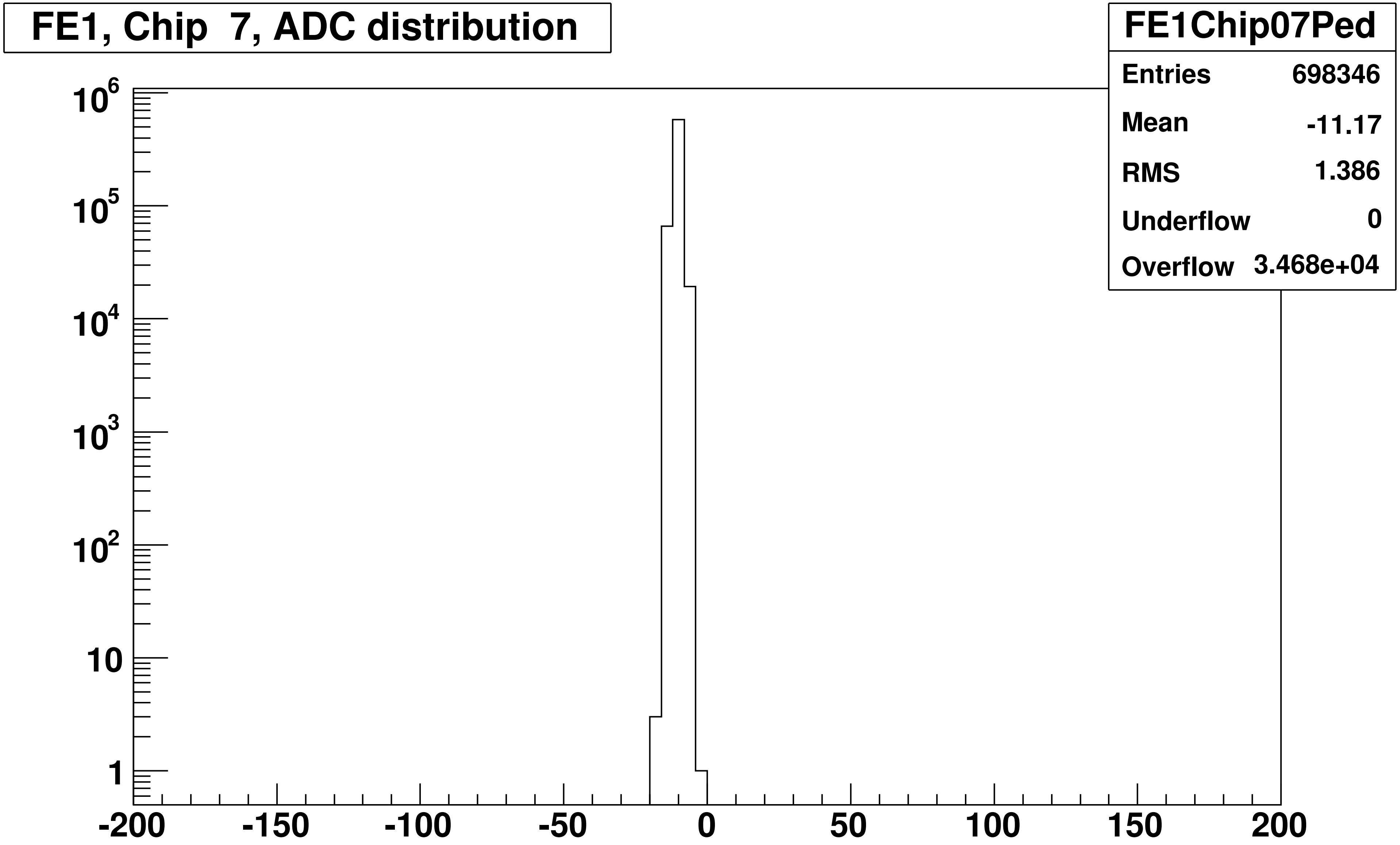
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 698346
Mean -11.17
RMS 1.386
Underflow 0
Overflow 3.468e+04
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10^5
10^4
10^3
10^2
10
1
-200
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-100
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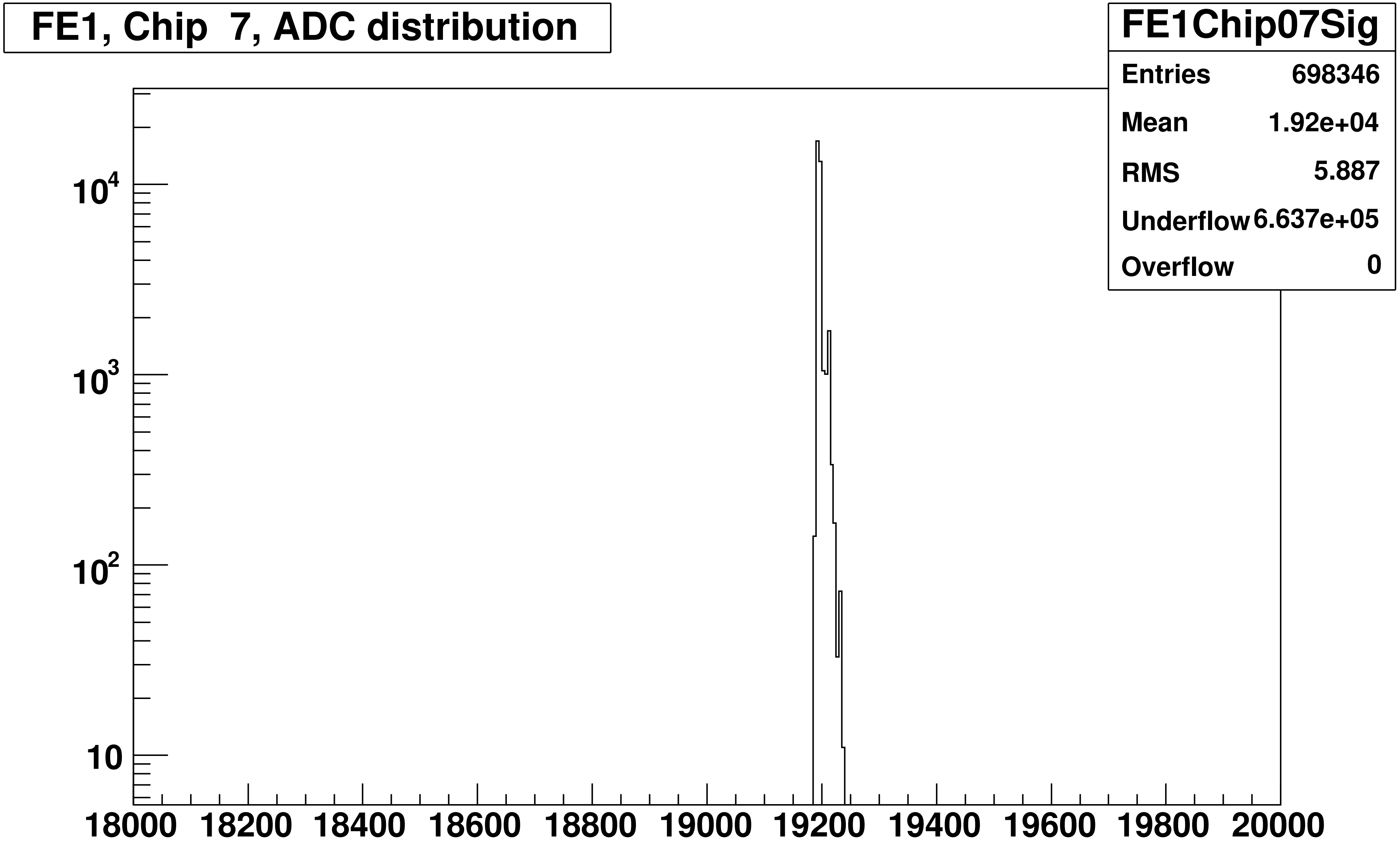
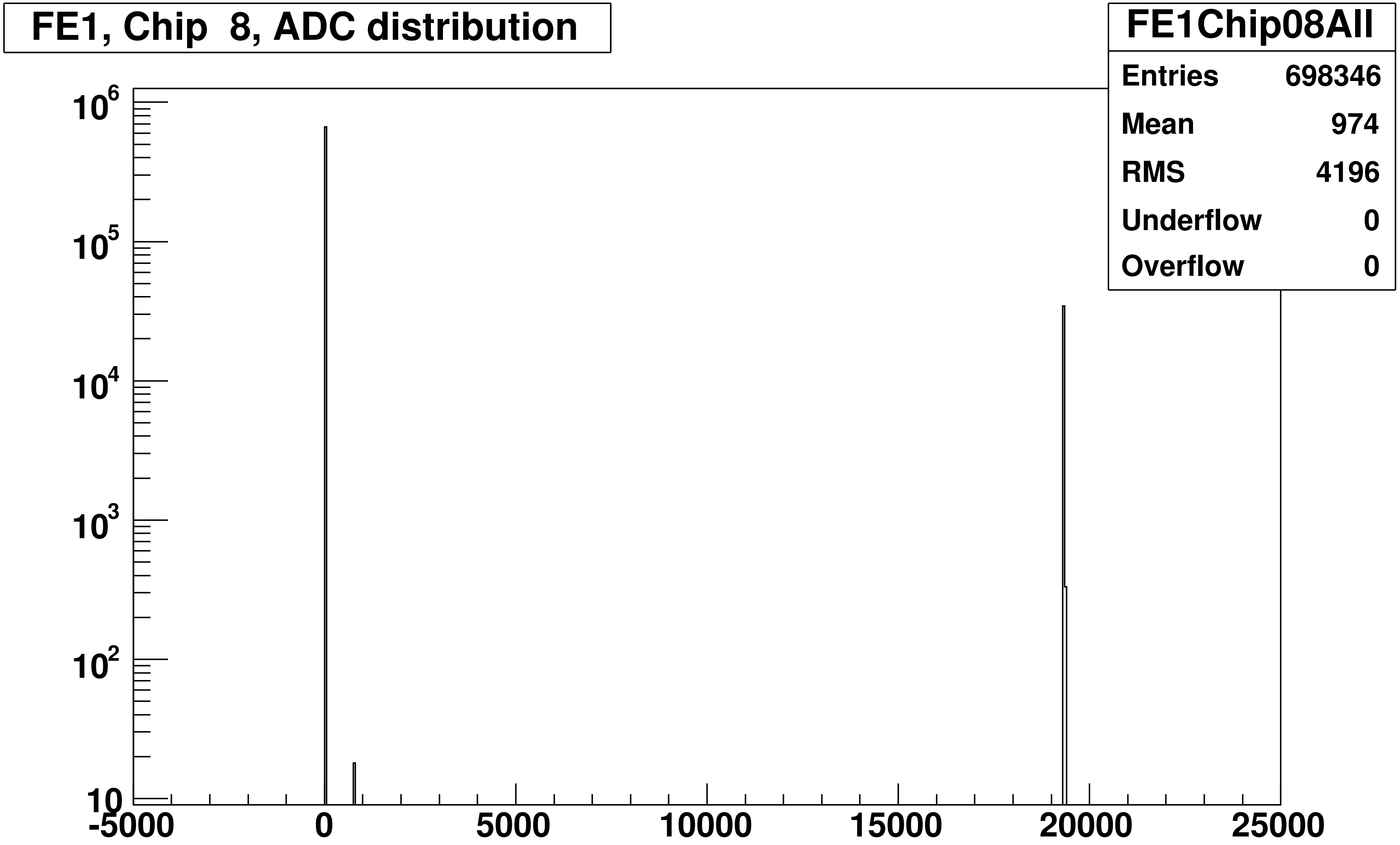
50
100
150
200


FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 698346
Mean 1.92e+04
RMS 5.887
Underflow 6.637e+05
Overflow 0
10
10^2
10^3
10^4
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 698346
Mean 974
RMS 4196
Underflow 0
Overflow 0
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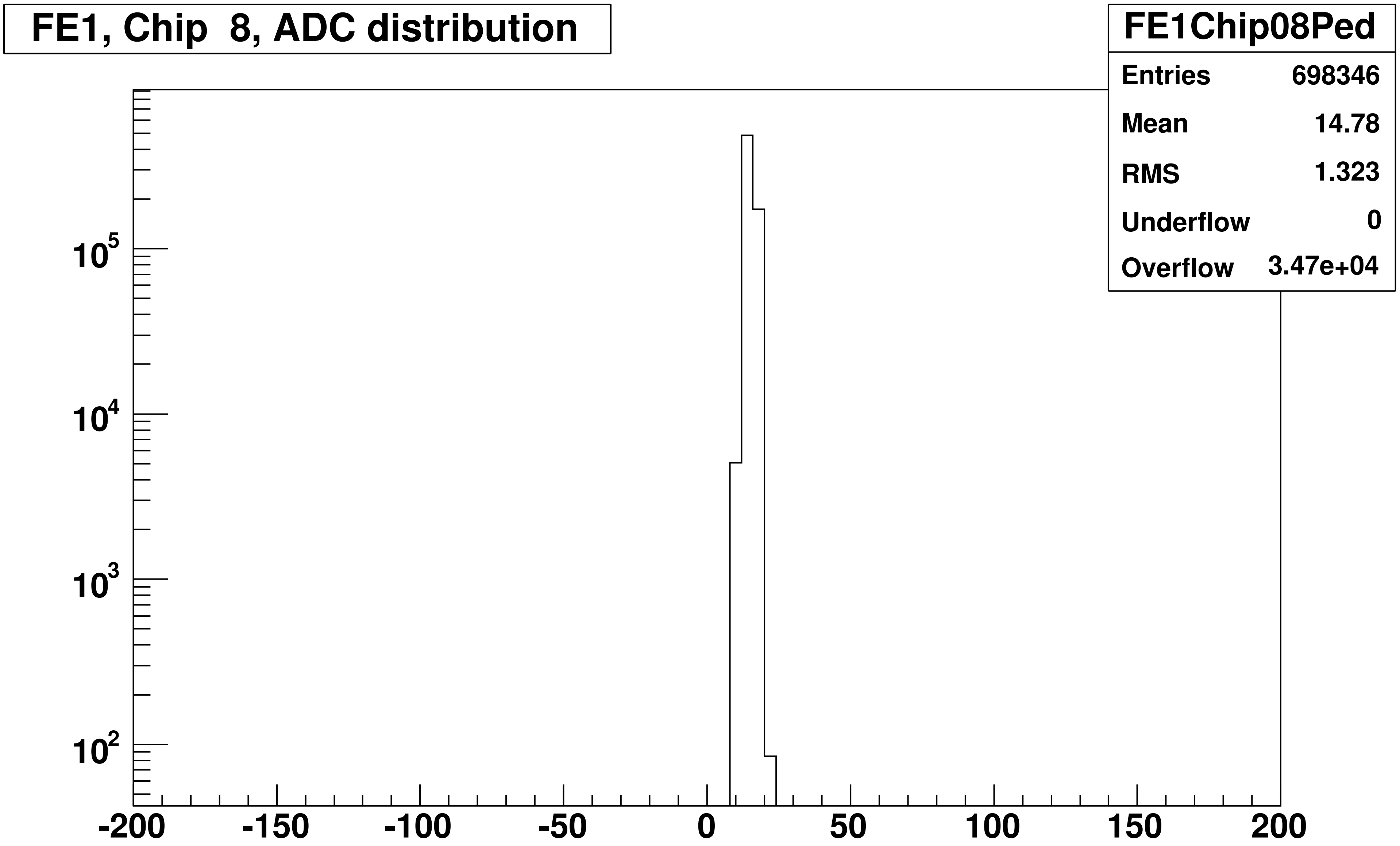
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 698346
Mean 14.78
RMS 1.323
Underflow 0
Overflow 3.47e+04
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10^4
10^3
10^2
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0
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100
150
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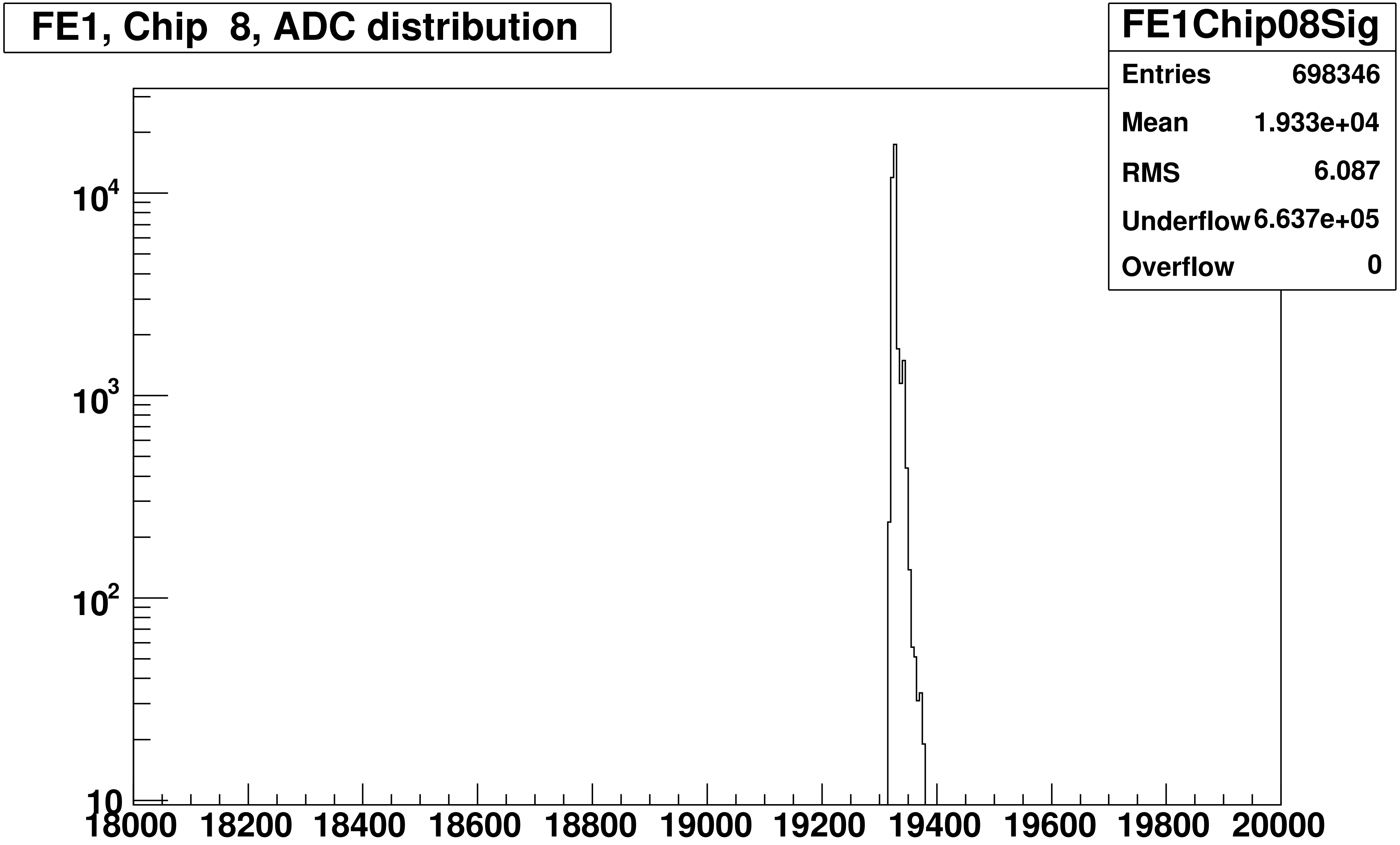

FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 698346
Mean 1.933e+04
RMS 6.087
Underflow 6.637e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
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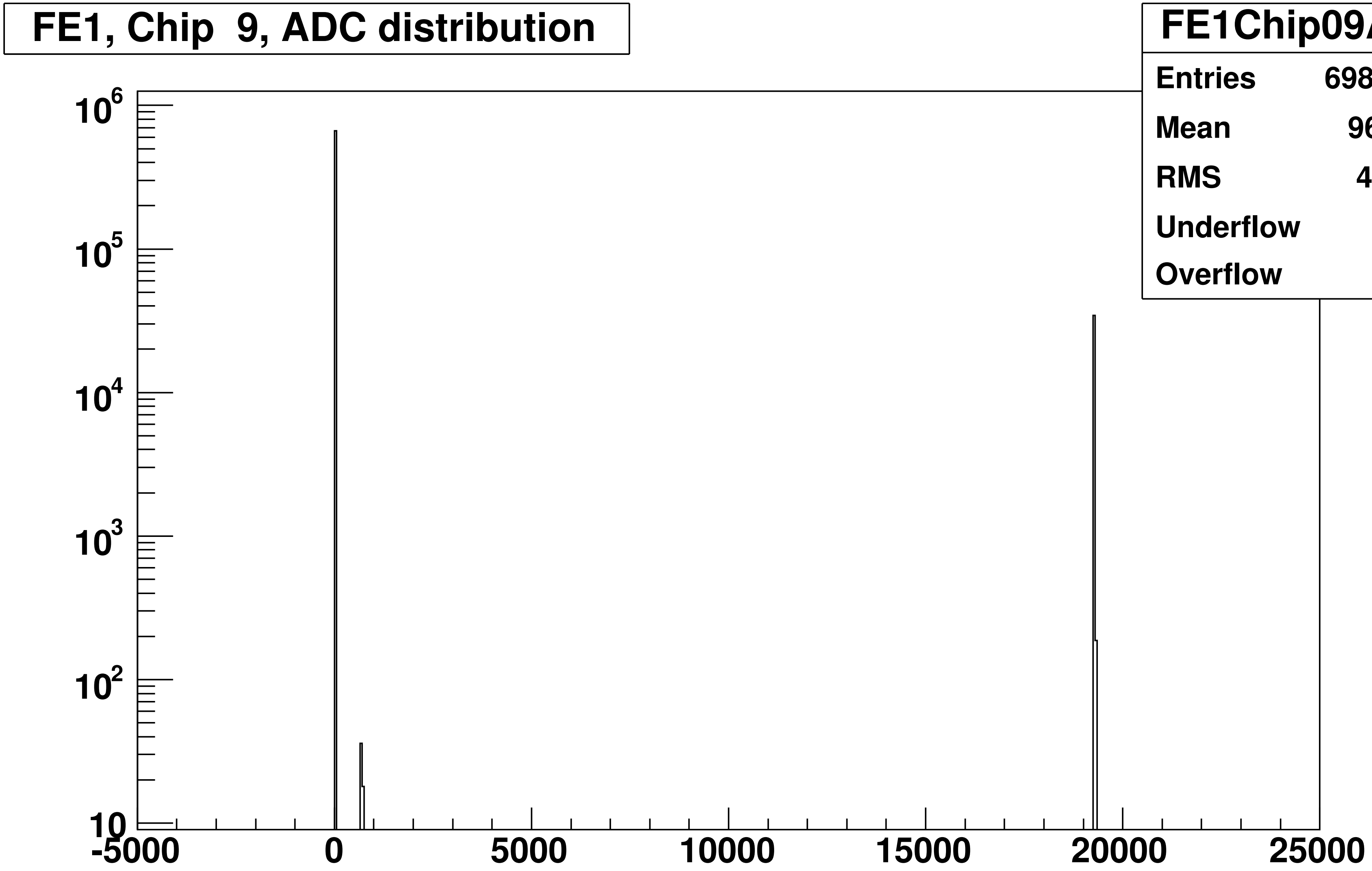
19000
19200
19400
19600
19800
20000


FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 698346
Mean 965.3
RMS 4187
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
0
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10000
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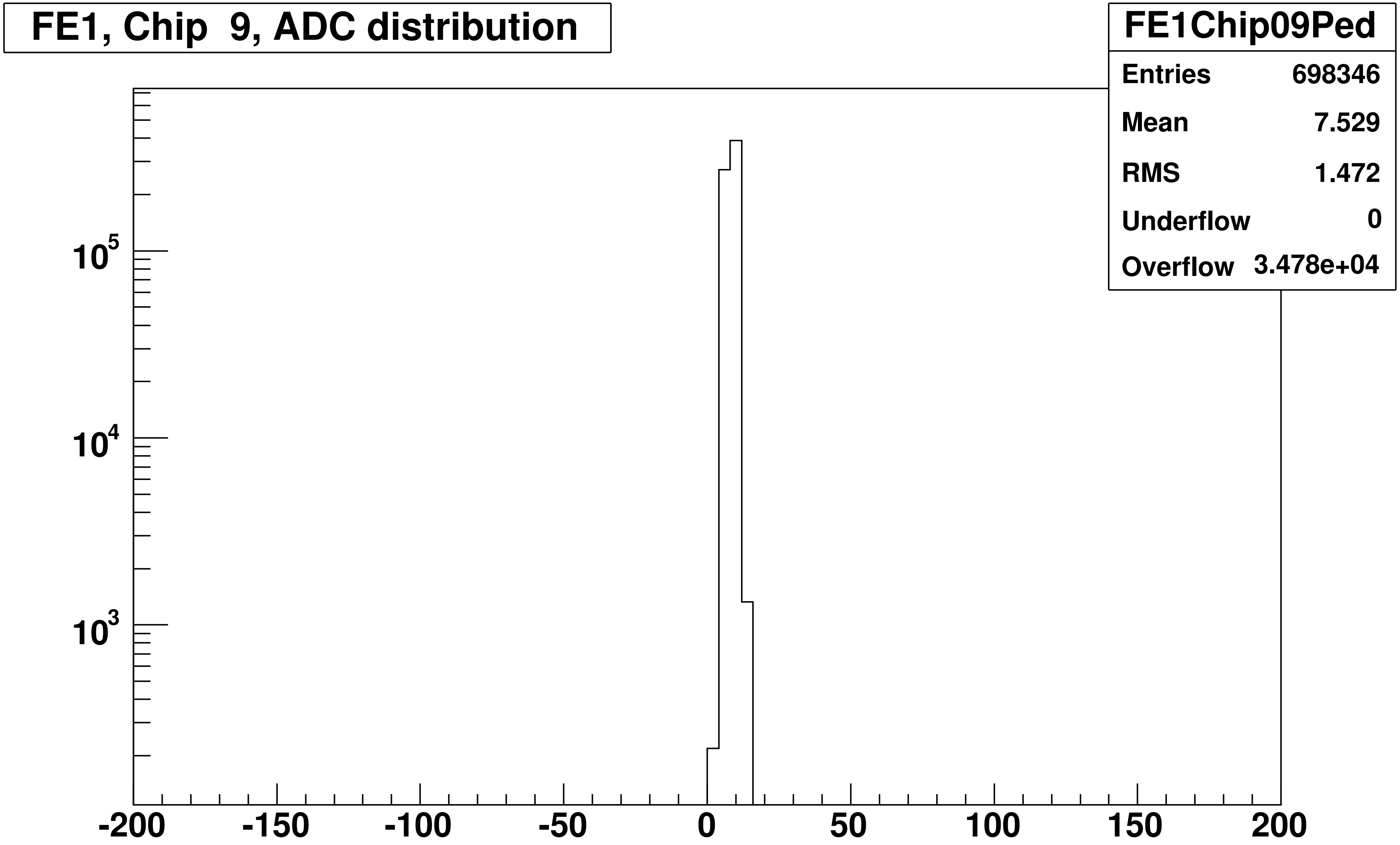
20000
25000


FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 698346
Mean 7.529
RMS 1.472
Underflow 0
Overflow 3.478e+04
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10^4
10^3
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-100
-50
0
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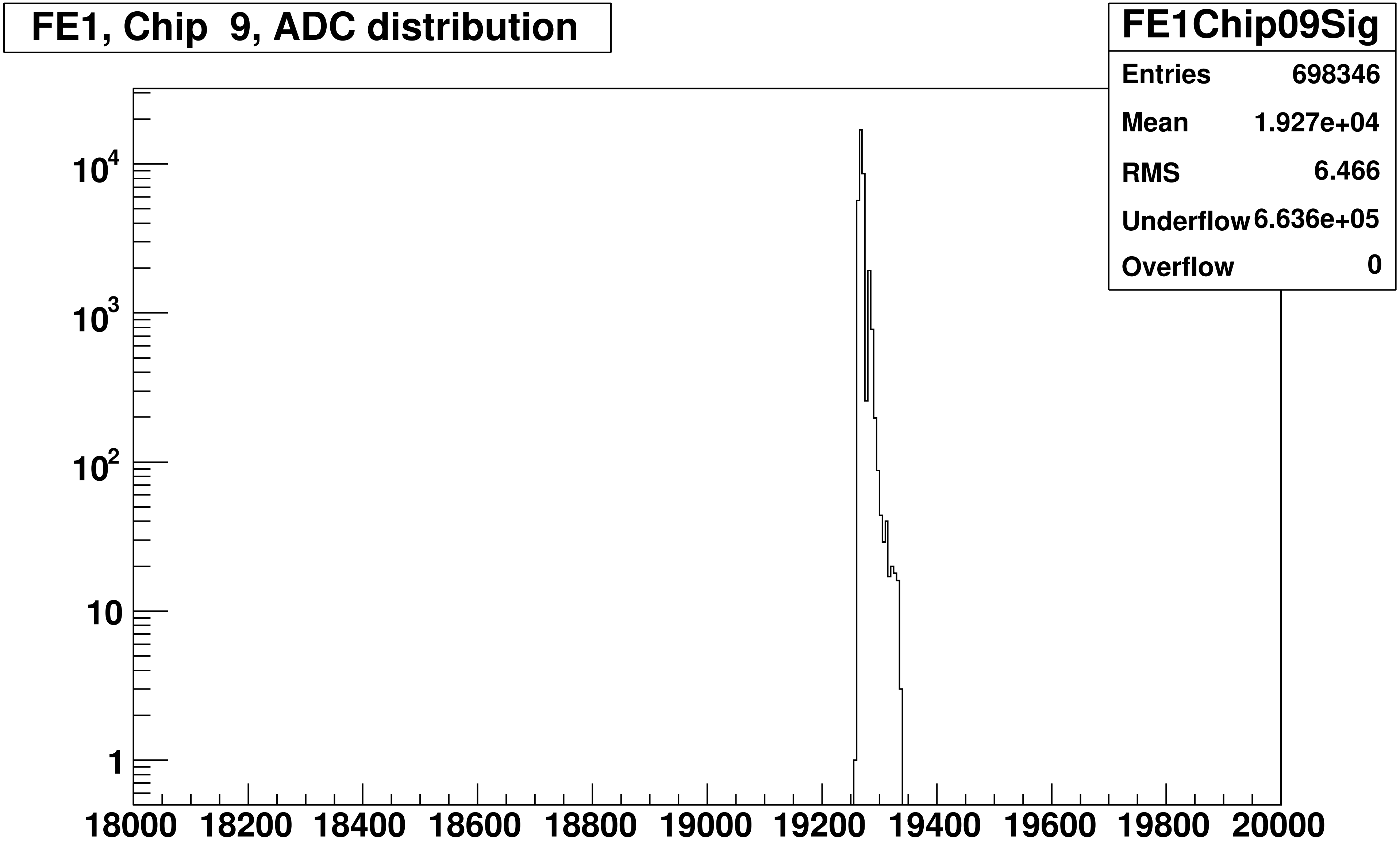
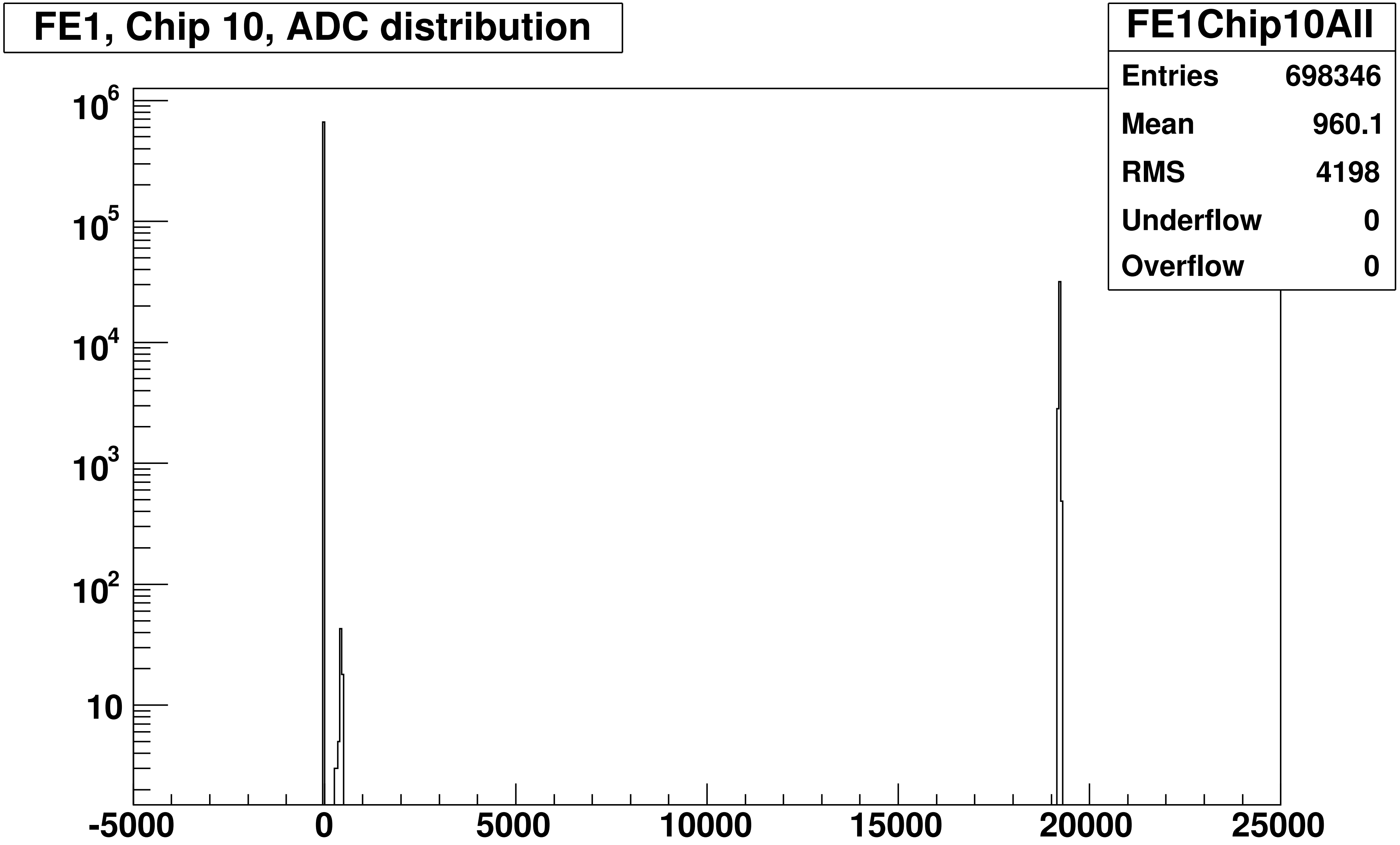
150
200


FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 698346
Mean 1.927e+04
RMS 6.466
Underflow 6.636e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 698346
Mean 960.1
RMS 4198
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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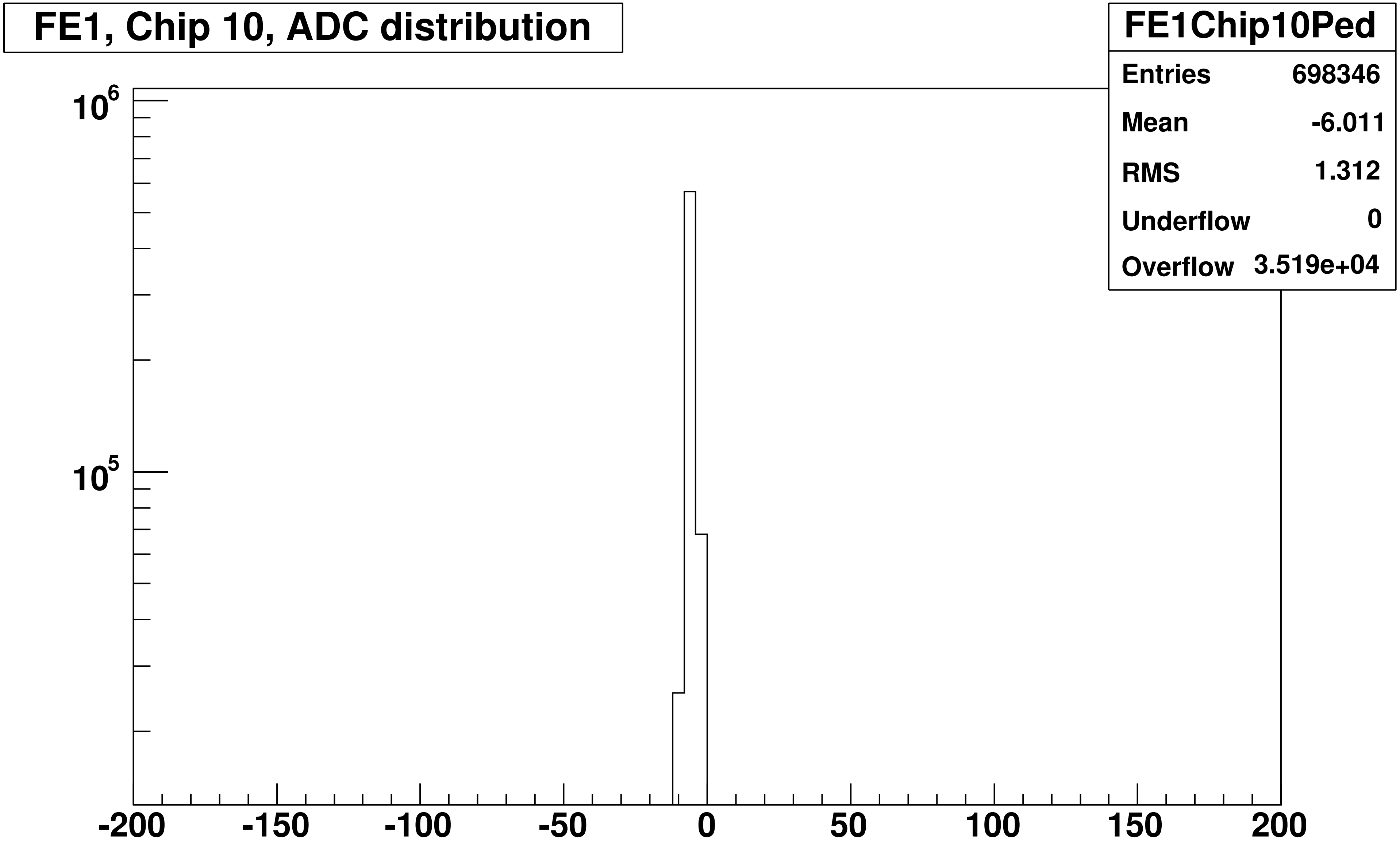
25000



FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 698346
Mean -6.011
RMS 1.312
Underflow 0
Overflow 3.519e+04
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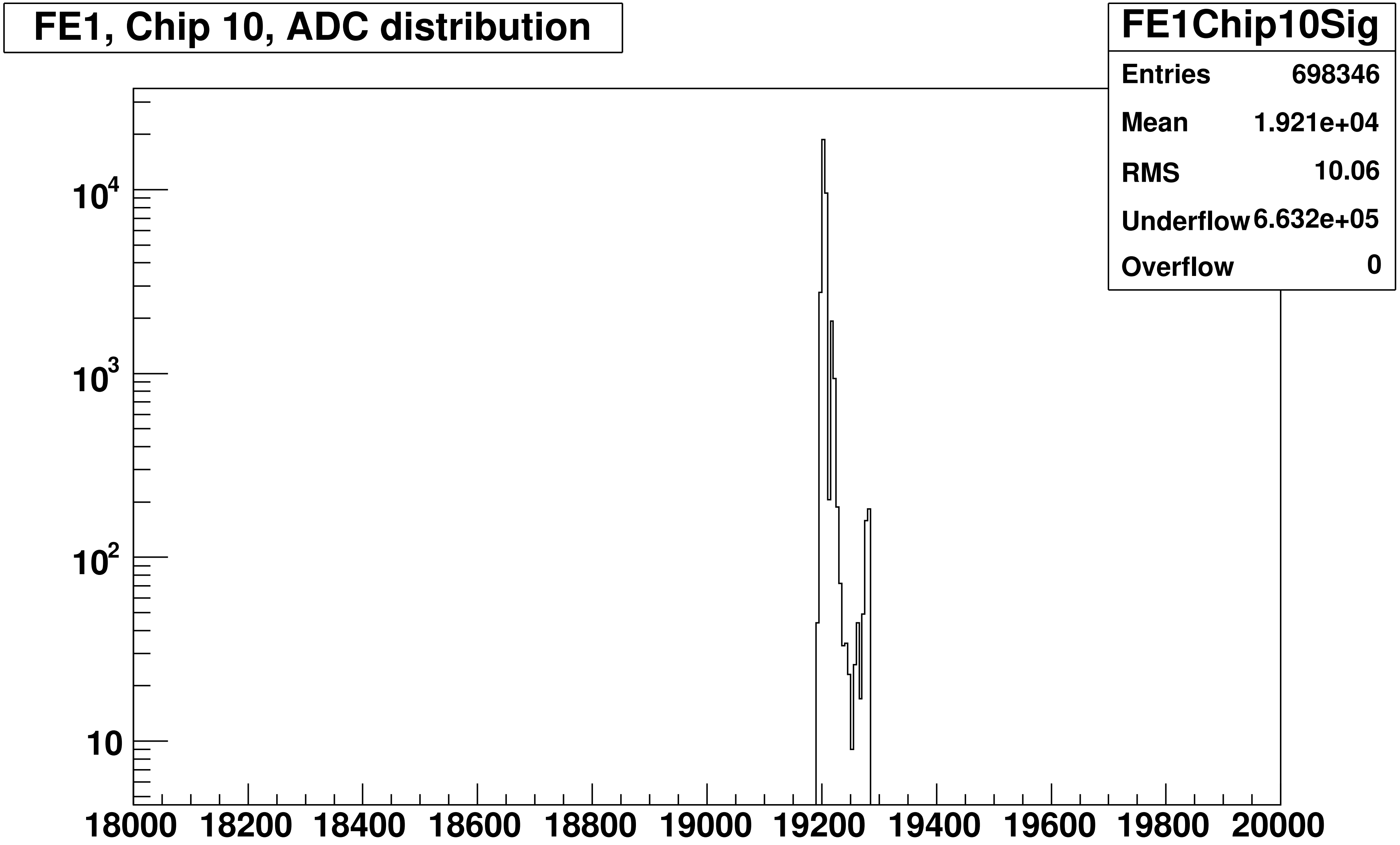
10^5
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 698346
Mean 1.921e+04
RMS 10.06
Underflow 6.632e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
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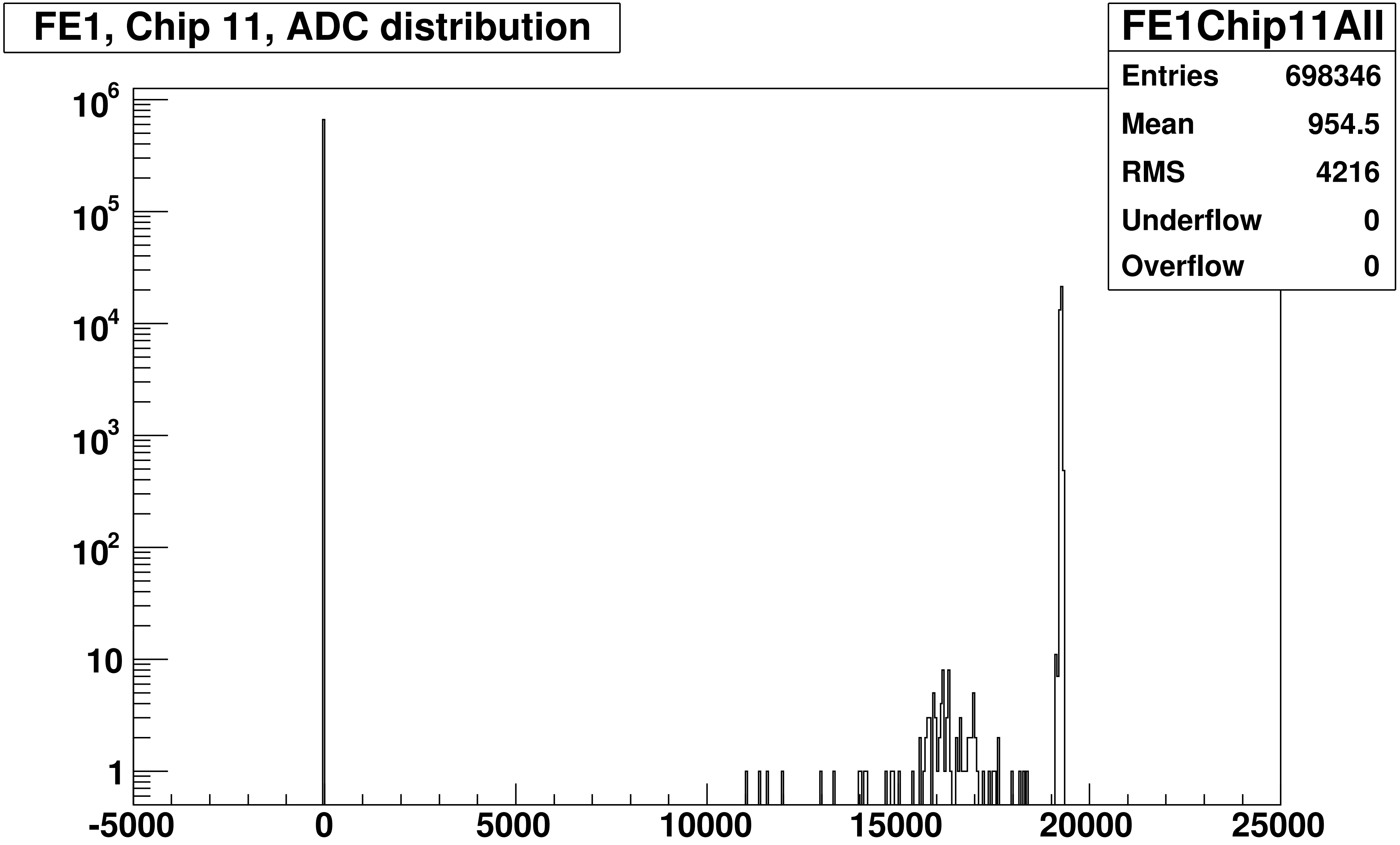
19400
19600
19800
20000


FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 698346
Mean 954.5
RMS 4216
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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10000
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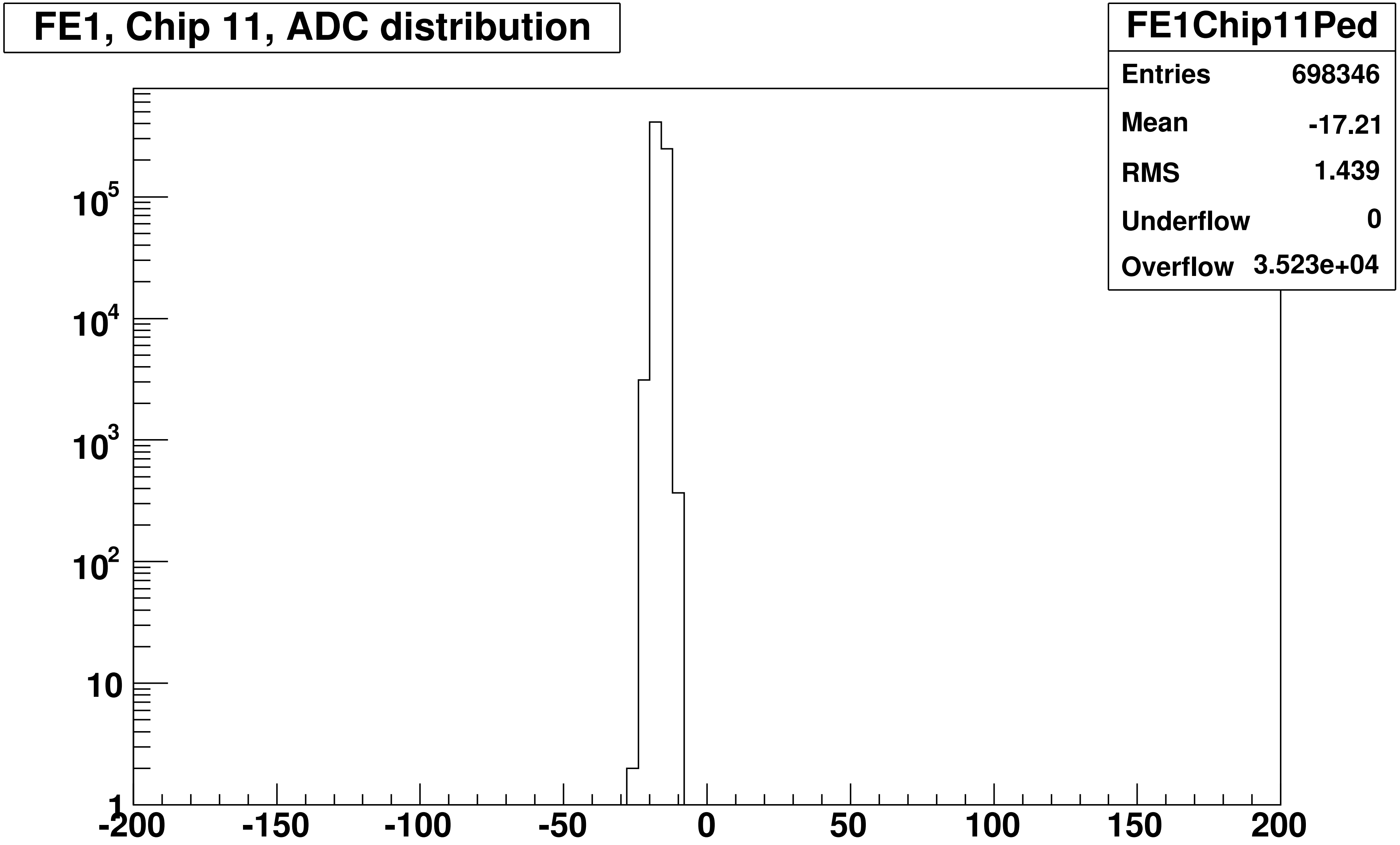
20000
25000


FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 698346
Mean -17.21
RMS 1.439
Underflow 0
Overflow 3.523e+04
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10^3
10^2
10
1
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-100
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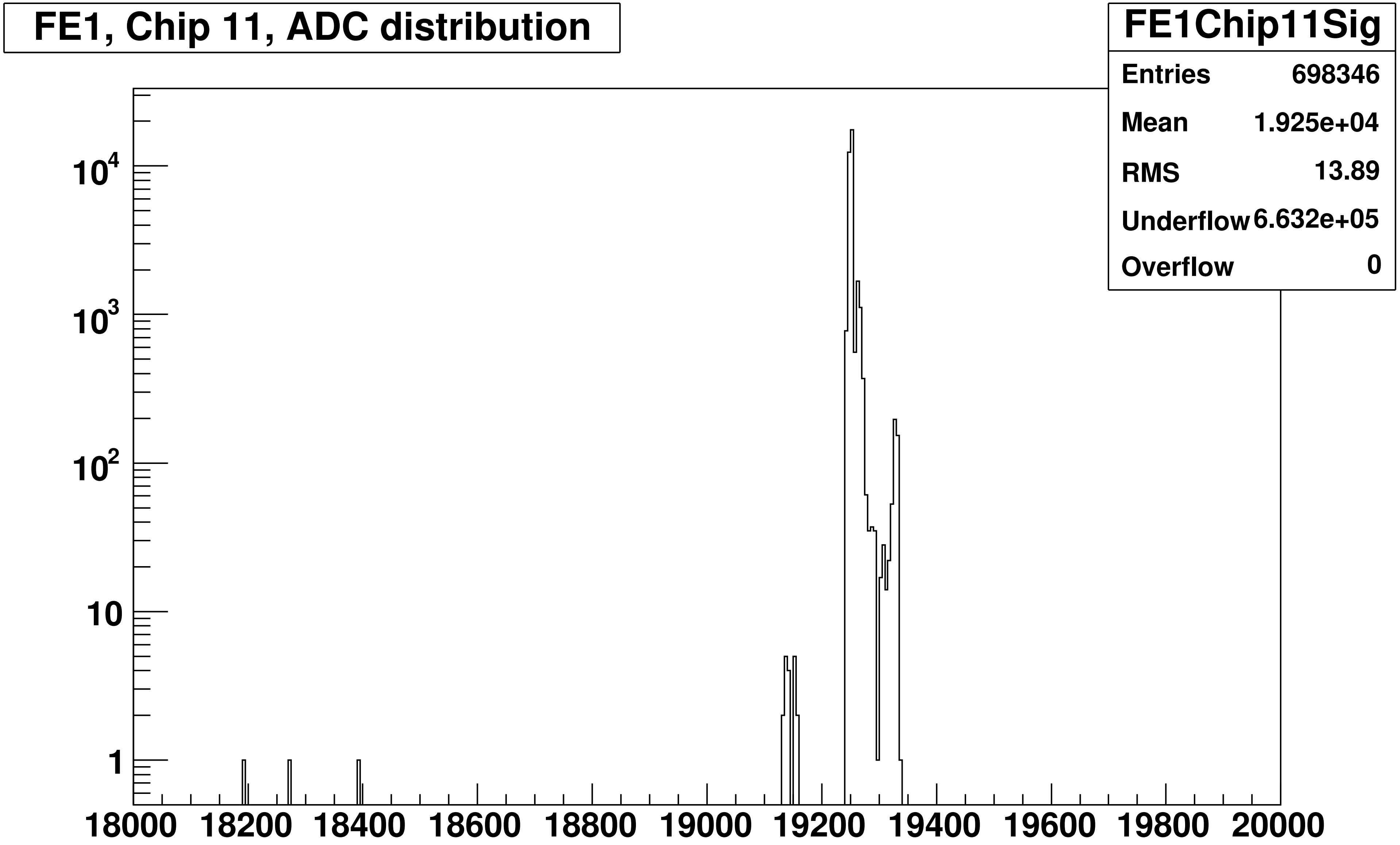
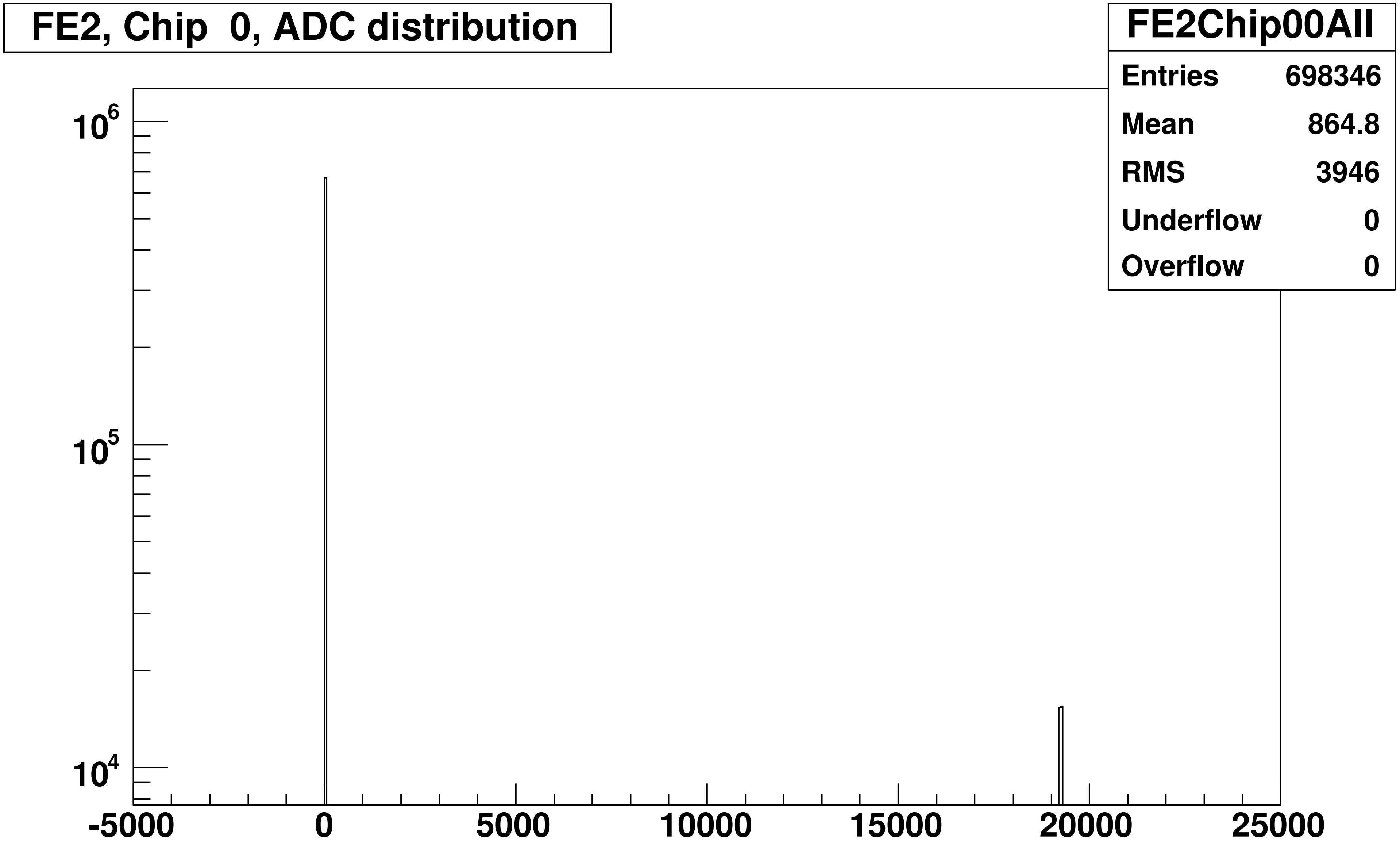
0
50
100
150
200


FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 698346
Mean 1.925e+04
RMS 13.89
Underflow 6.632e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 698346
Mean 864.8
RMS 3946
Underflow 0
Overflow 0
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10^5
10^4
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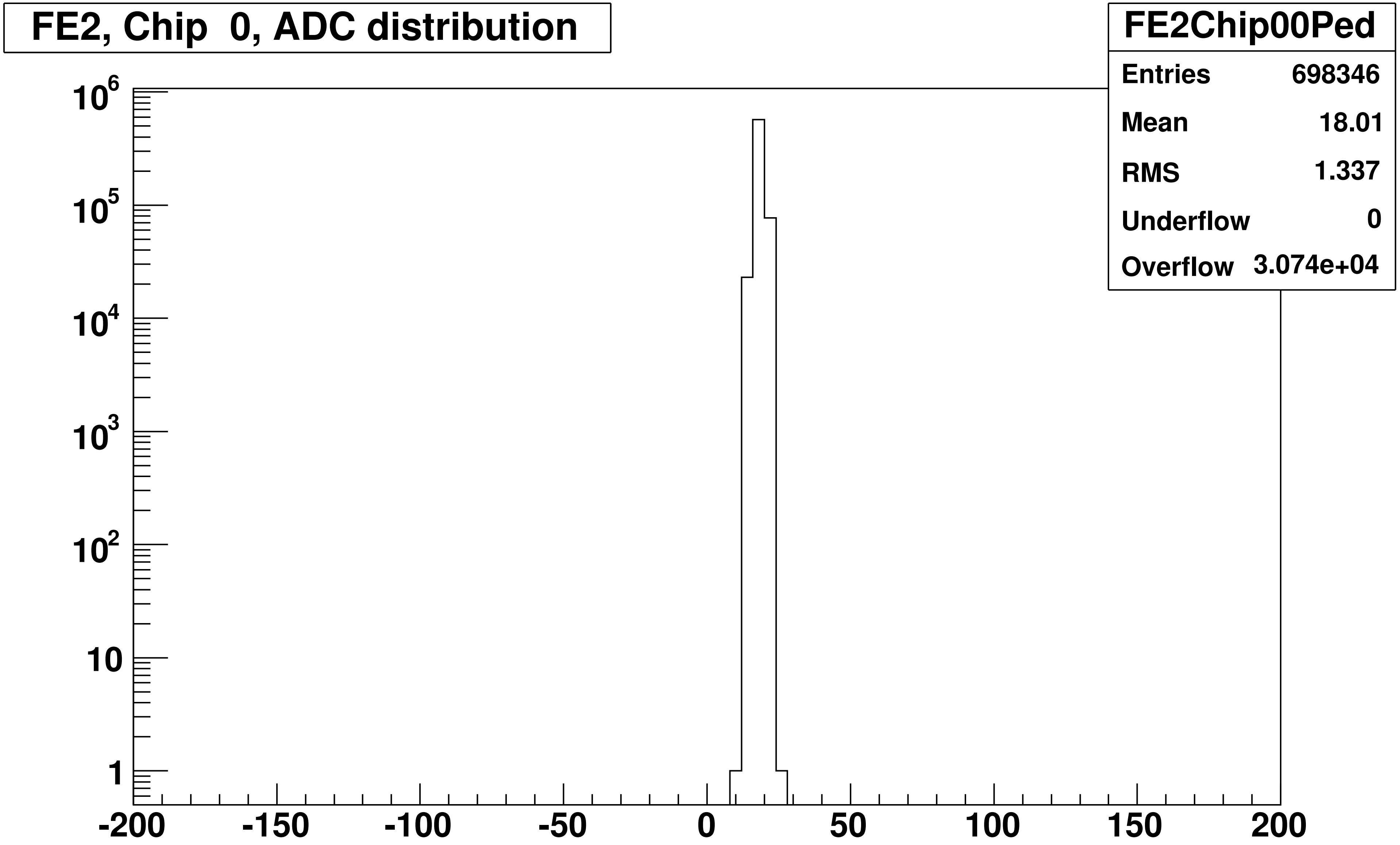
0
5000
10000
15000
20000
25000


FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 698346
Mean 18.01
RMS 1.337
Underflow 0
Overflow 3.074e+04
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10^5
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10^3
10^2
10
1
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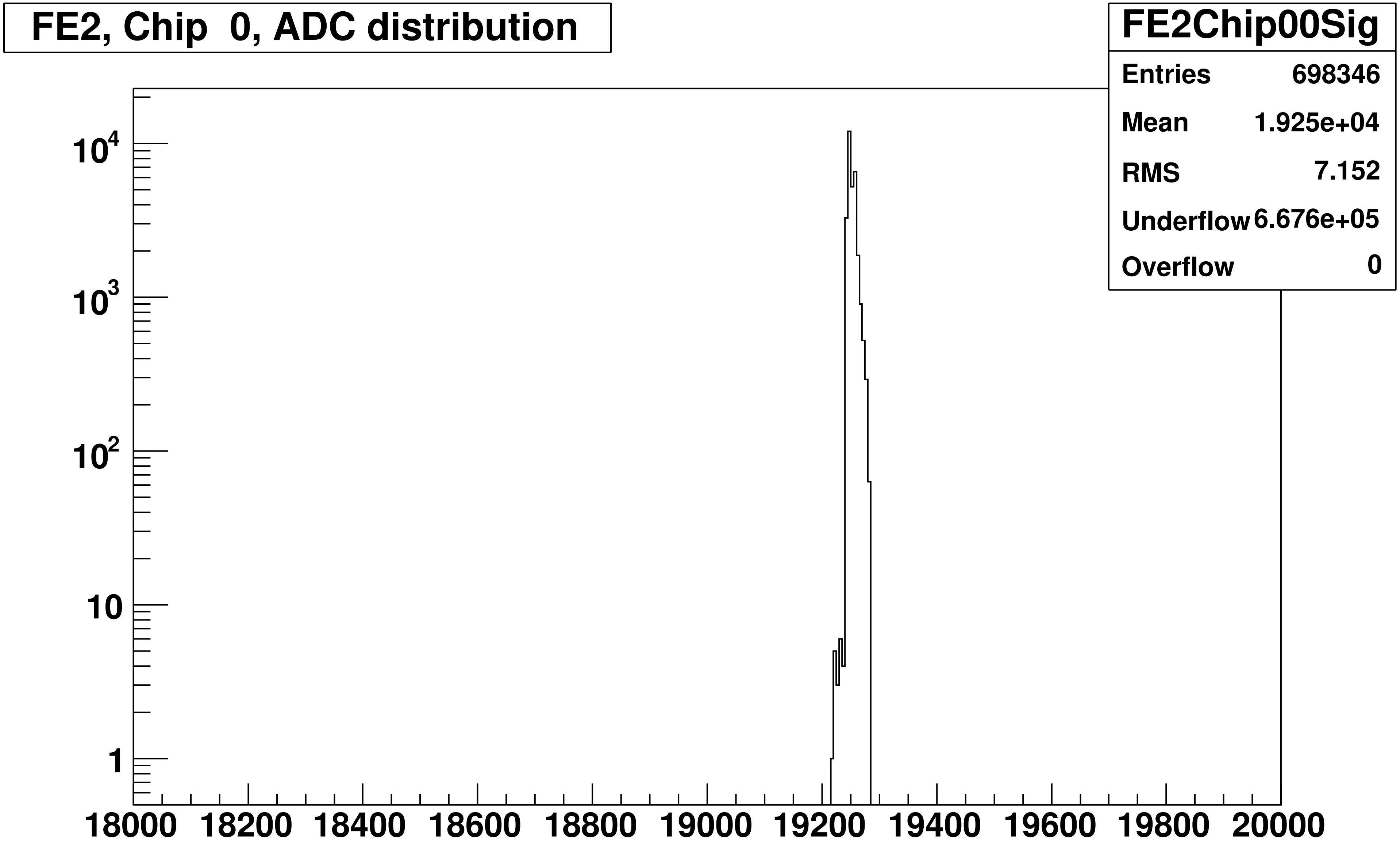
100
150
200


FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 698346
Mean 1.925e+04
RMS 7.152
Underflow 6.676e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
18400
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19000
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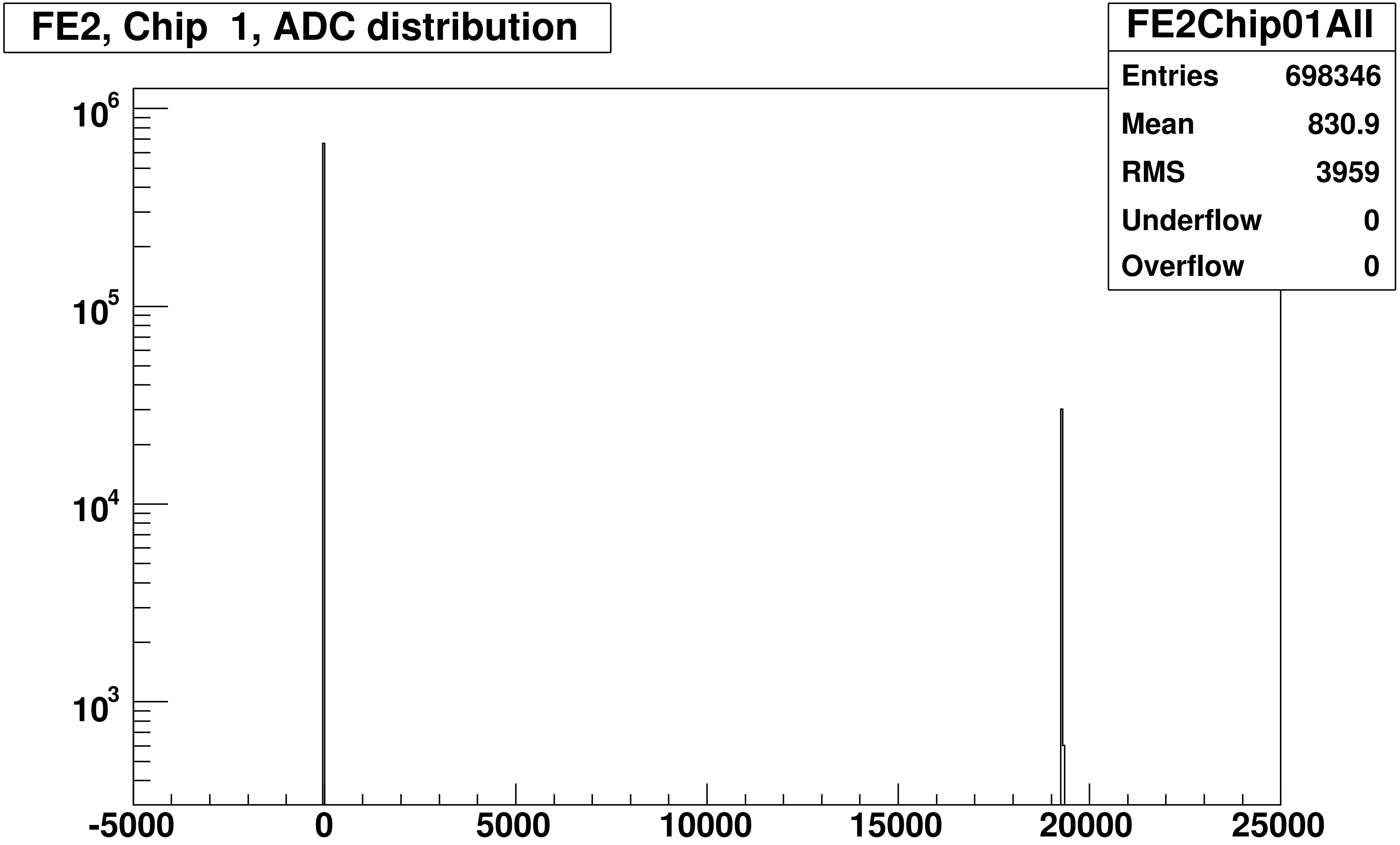
20000


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 698346
Mean 830.9
RMS 3959
Underflow 0
Overflow 0
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10^4
10^3
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10000
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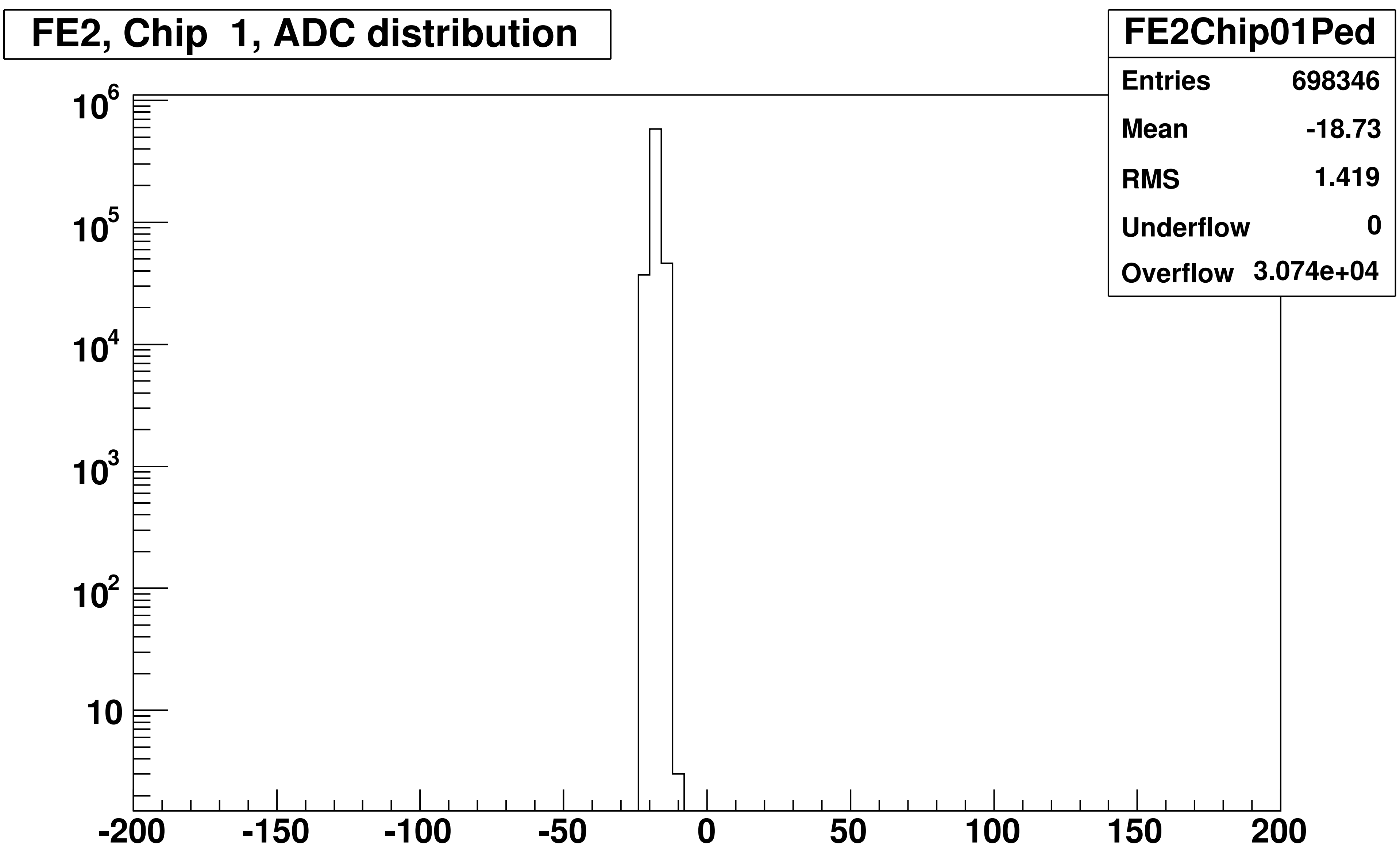
FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 698346
Mean -18.73
RMS 1.419
Underflow 0
Overflow 3.074e+04
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10^5
10^4
10^3
10^2
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-100
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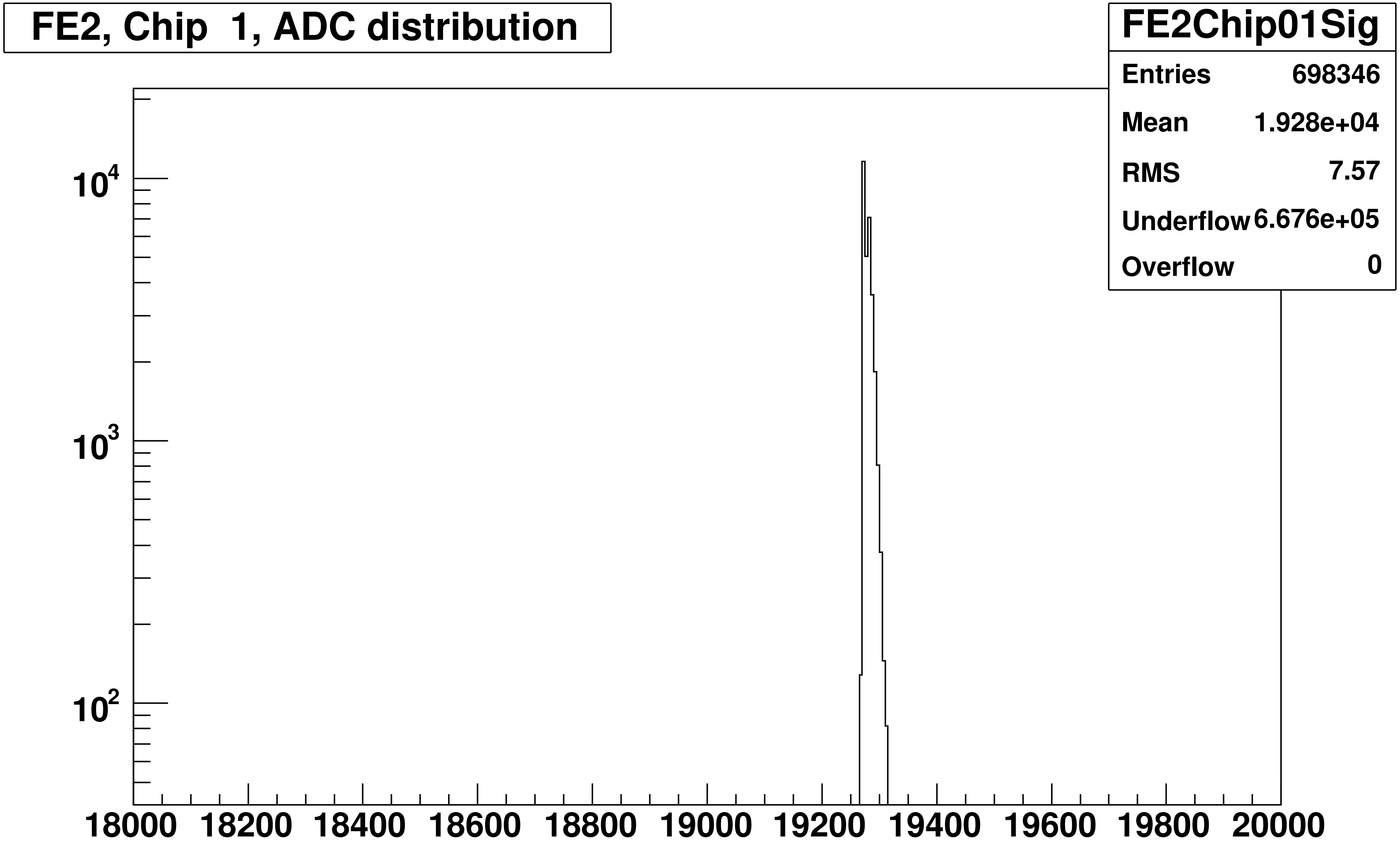
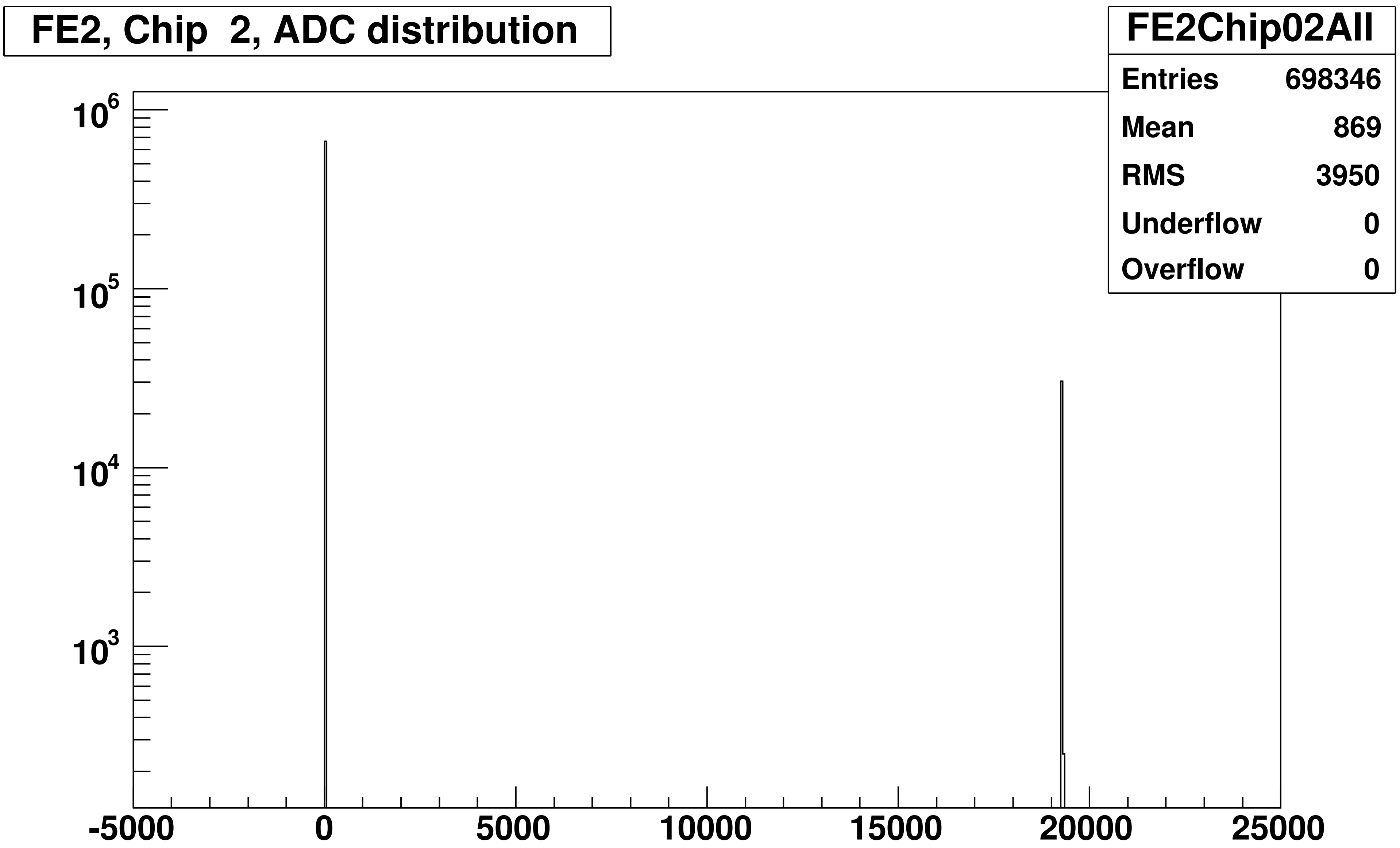
100
150
200


FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 698346
Mean 1.928e+04
RMS 7.57
Underflow 6.676e+05
Overflow 0
10^4
10^3
10^2
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 698346
Mean 869
RMS 3950
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
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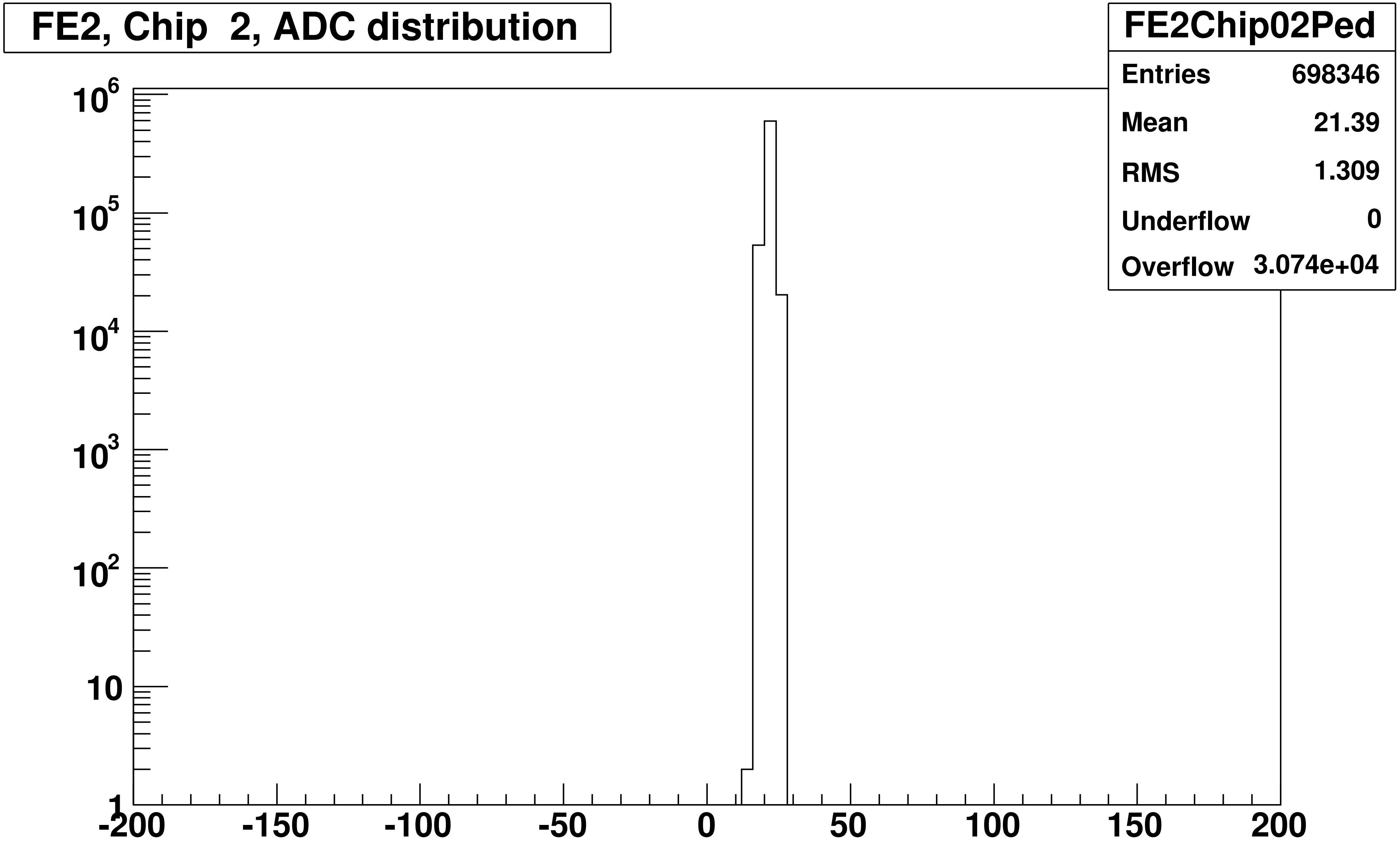
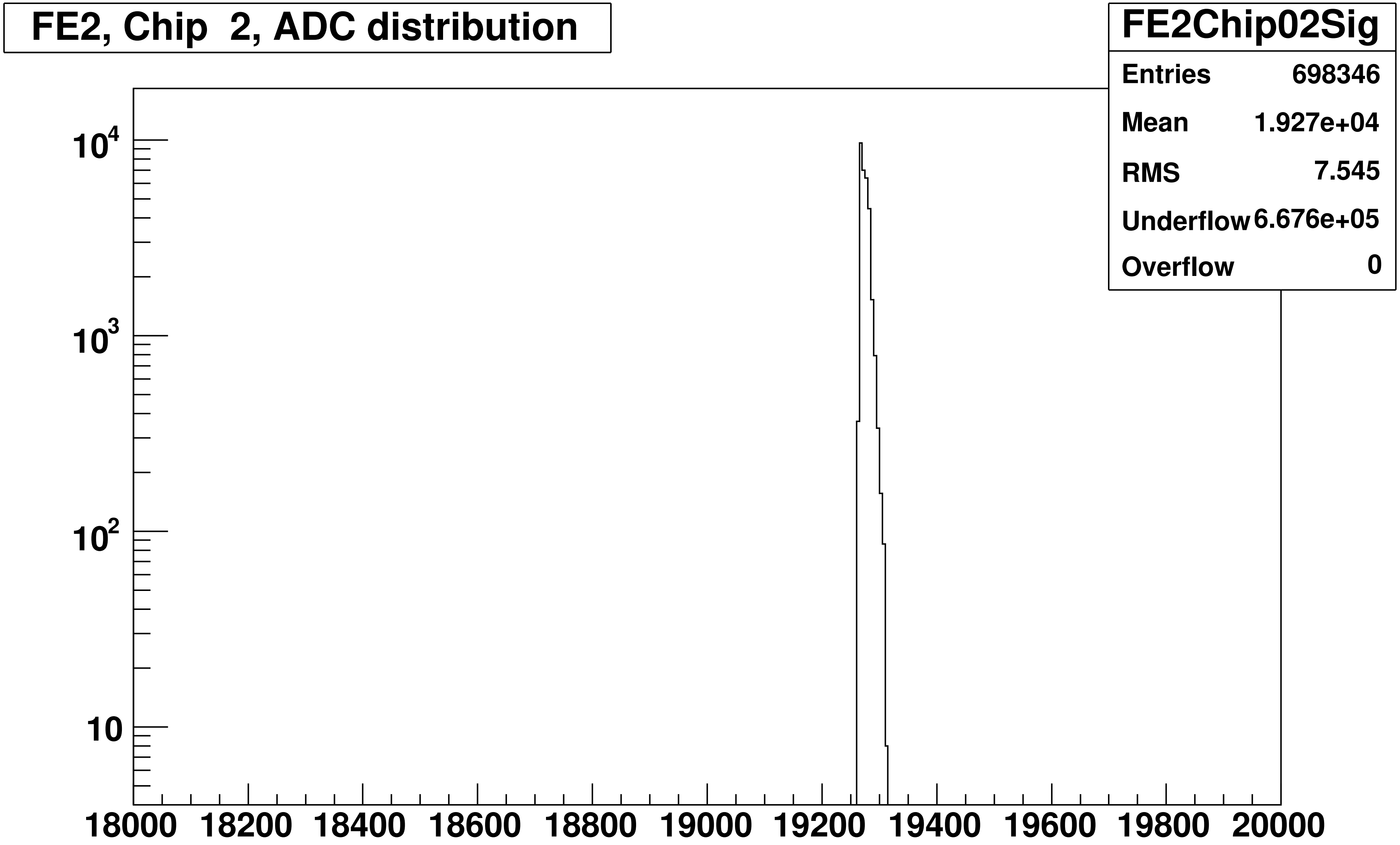
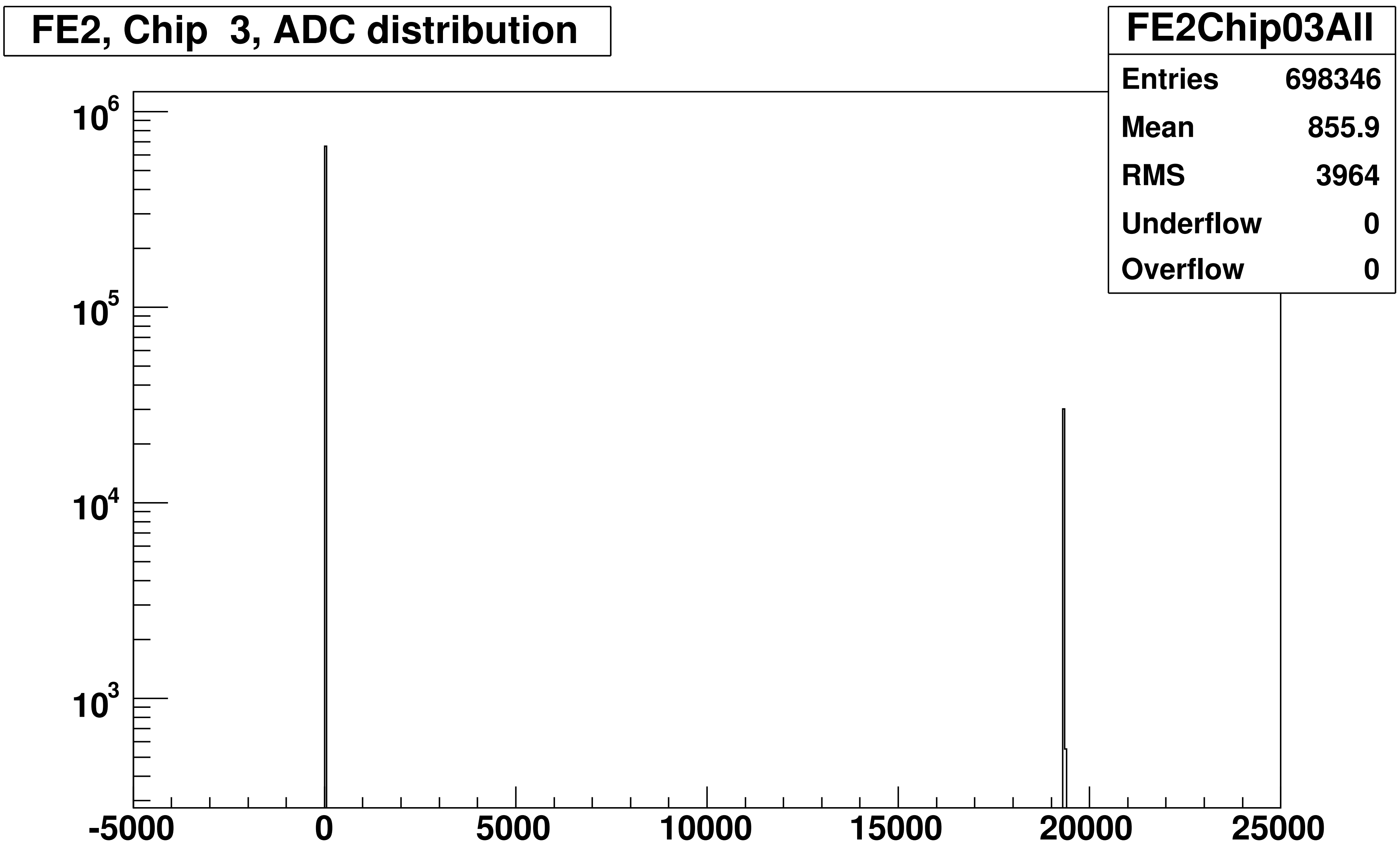
10000
15000
20000
25000



FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 698346
Mean 21.39
RMS 1.309
Underflow 0
Overflow 3.074e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 698346
Mean 1.927e+04
RMS 7.545
Underflow 6.676e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 698346
Mean 855.9
RMS 3964
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
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20000
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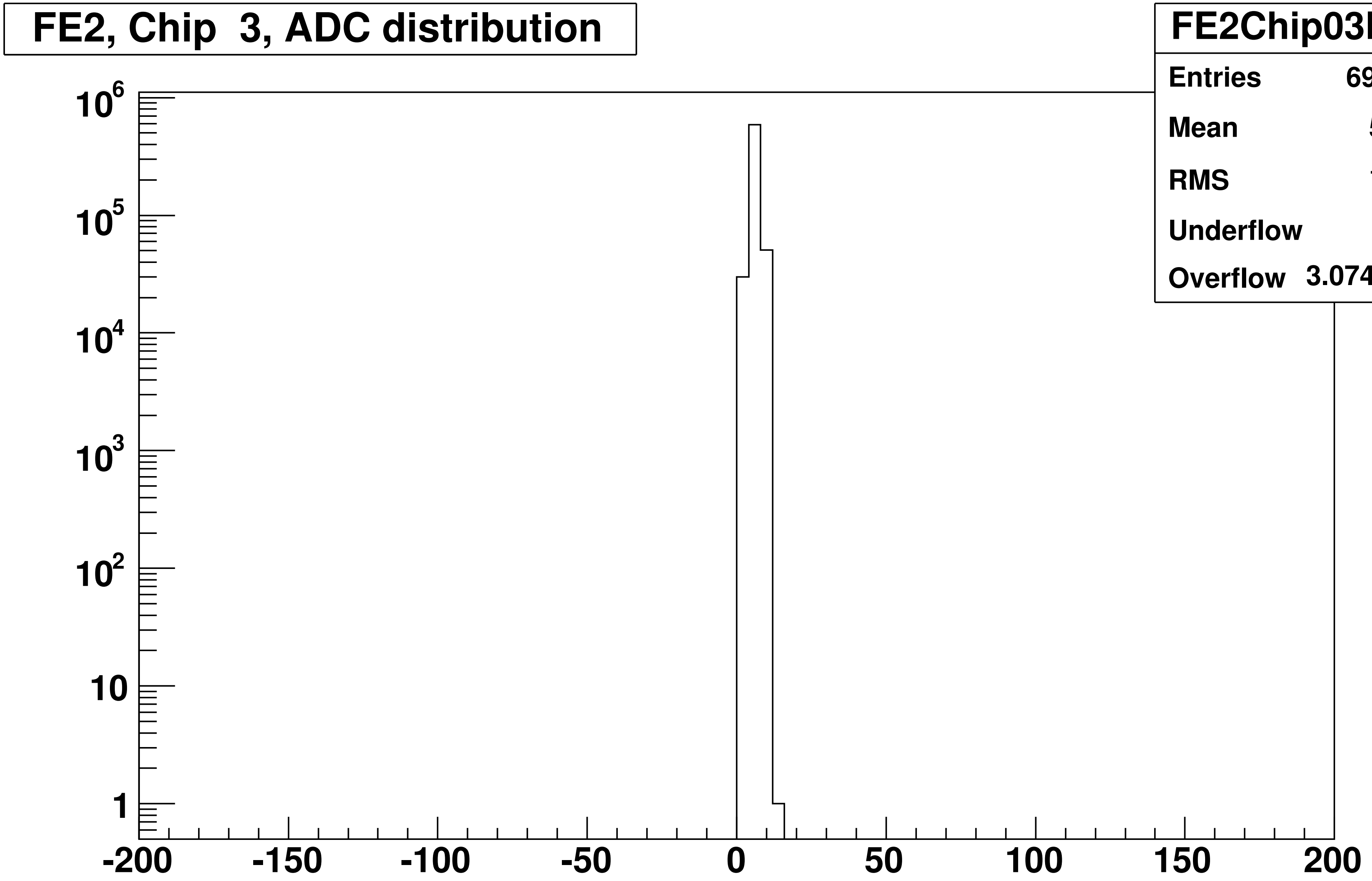
FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 698346
Mean 5.249
RMS 1.405
Underflow 0
Overflow 3.074e+04
10^6
10^5
10^4
10^3
10^2
10
1
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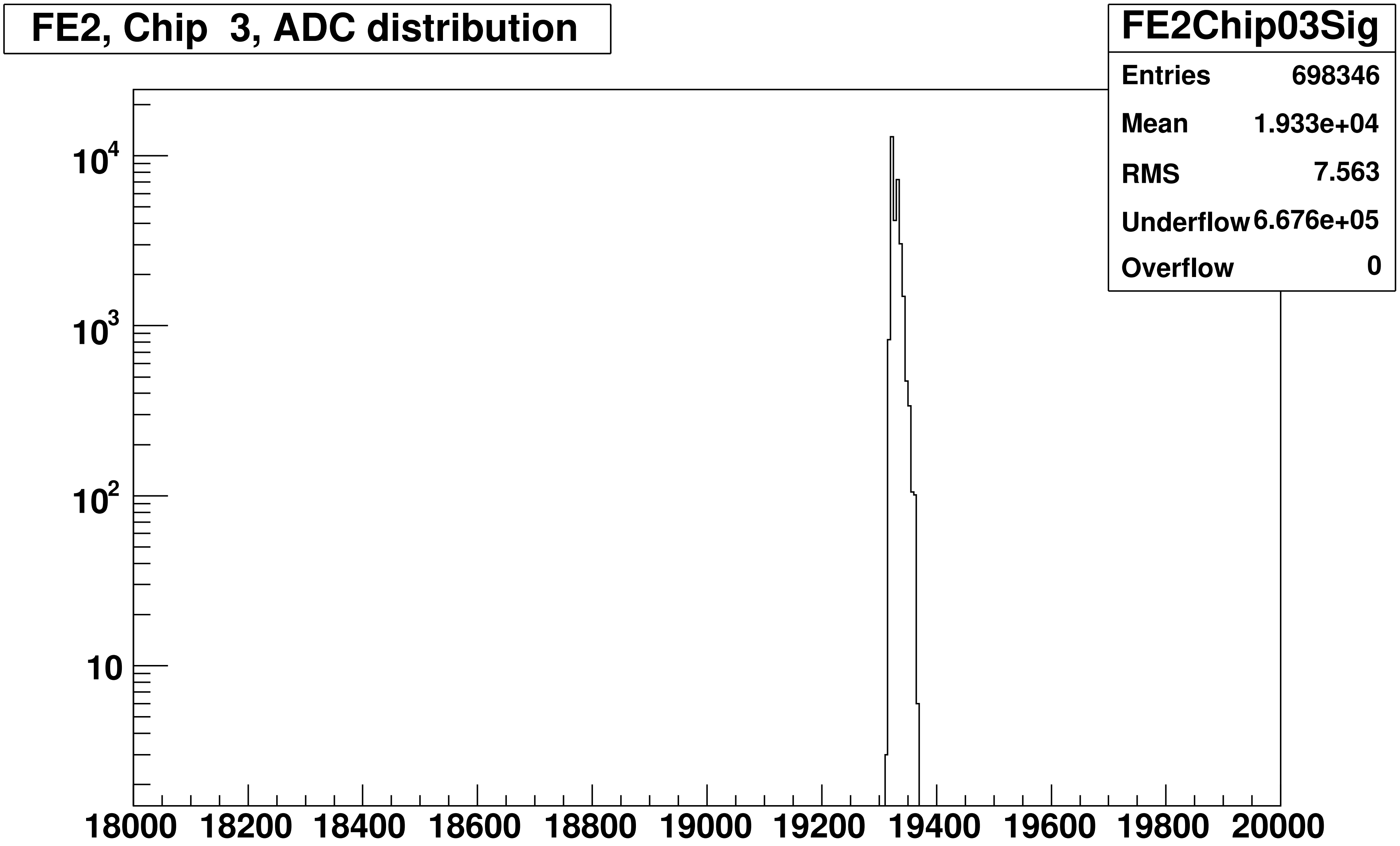
0
50
100
150
200


FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 698346
Mean 1.933e+04
RMS 7.563
Underflow 6.676e+05
Overflow 0
10
10^2
10^3
10^4
18000
18200
18400
18600
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19000
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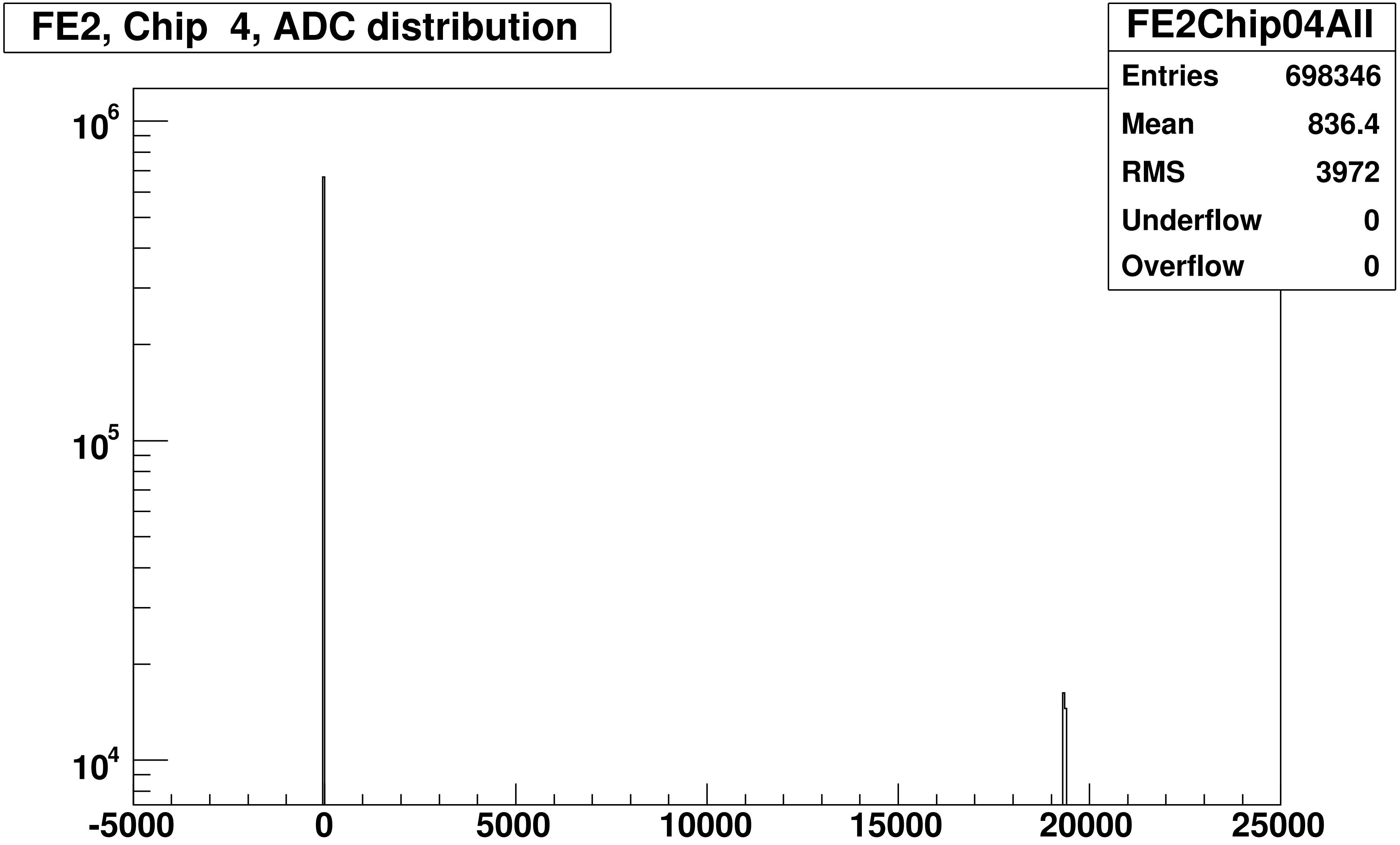
19400
19600
19800
20000


FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 698346
Mean 836.4
RMS 3972
Underflow 0
Overflow 0
10^6
10^5
10^4
-5000
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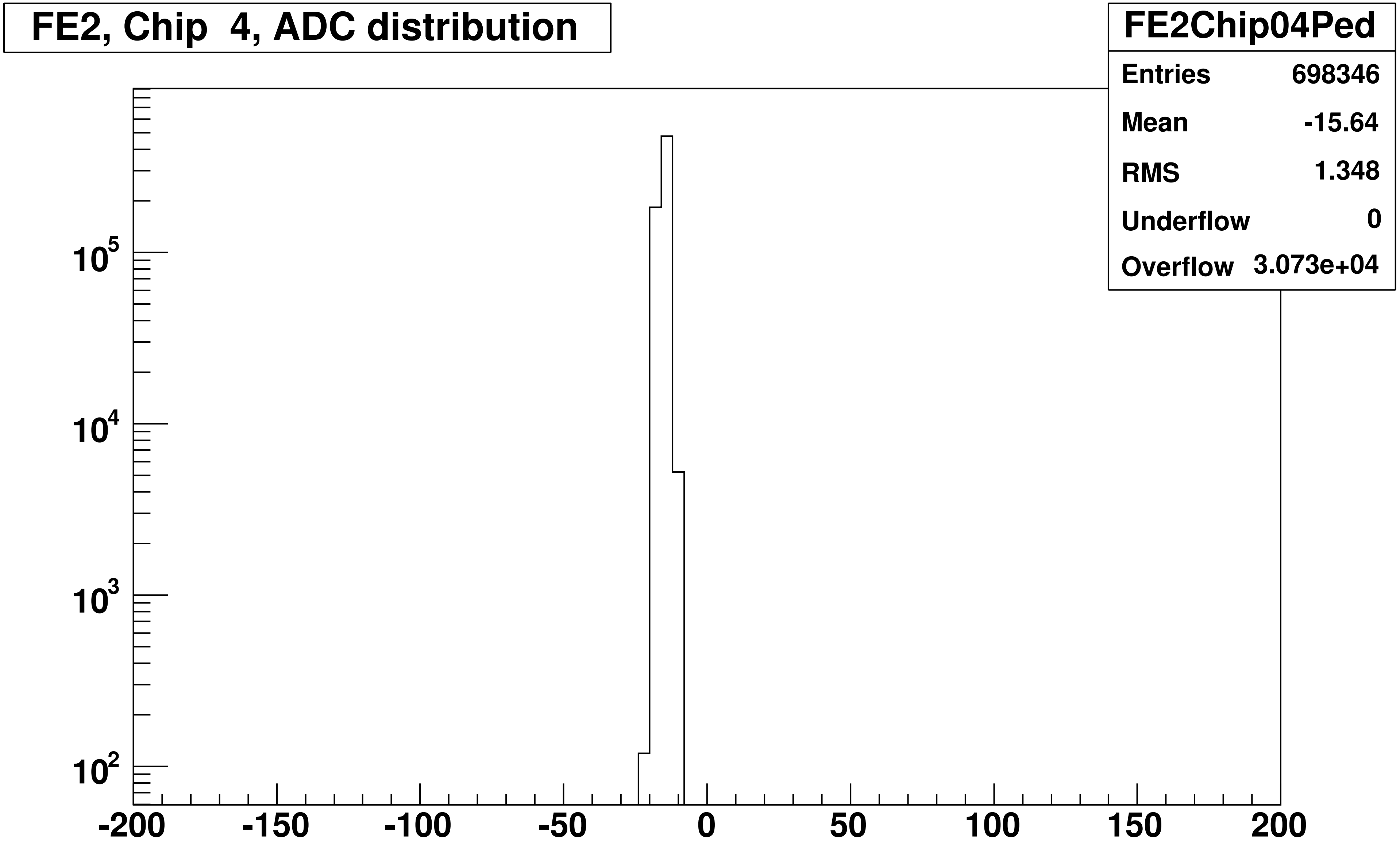
5000
10000
15000
20000
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 698346
Mean -15.64
RMS 1.348
Underflow 0
Overflow 3.073e+04
10^5
10^4
10^3
10^2
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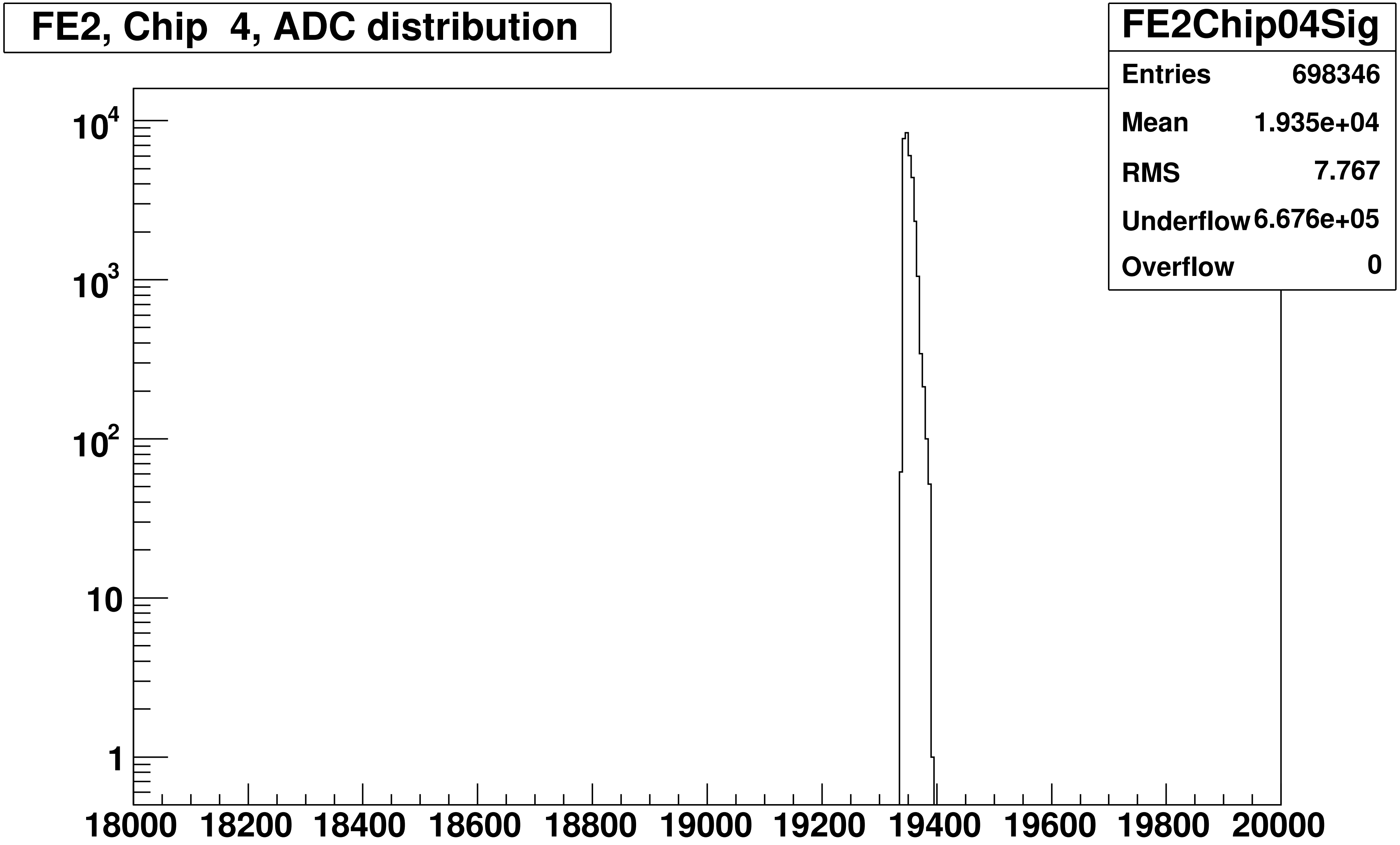
100
150
200


FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 698346
Mean 1.935e+04
RMS 7.767
Underflow 6.676e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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19000
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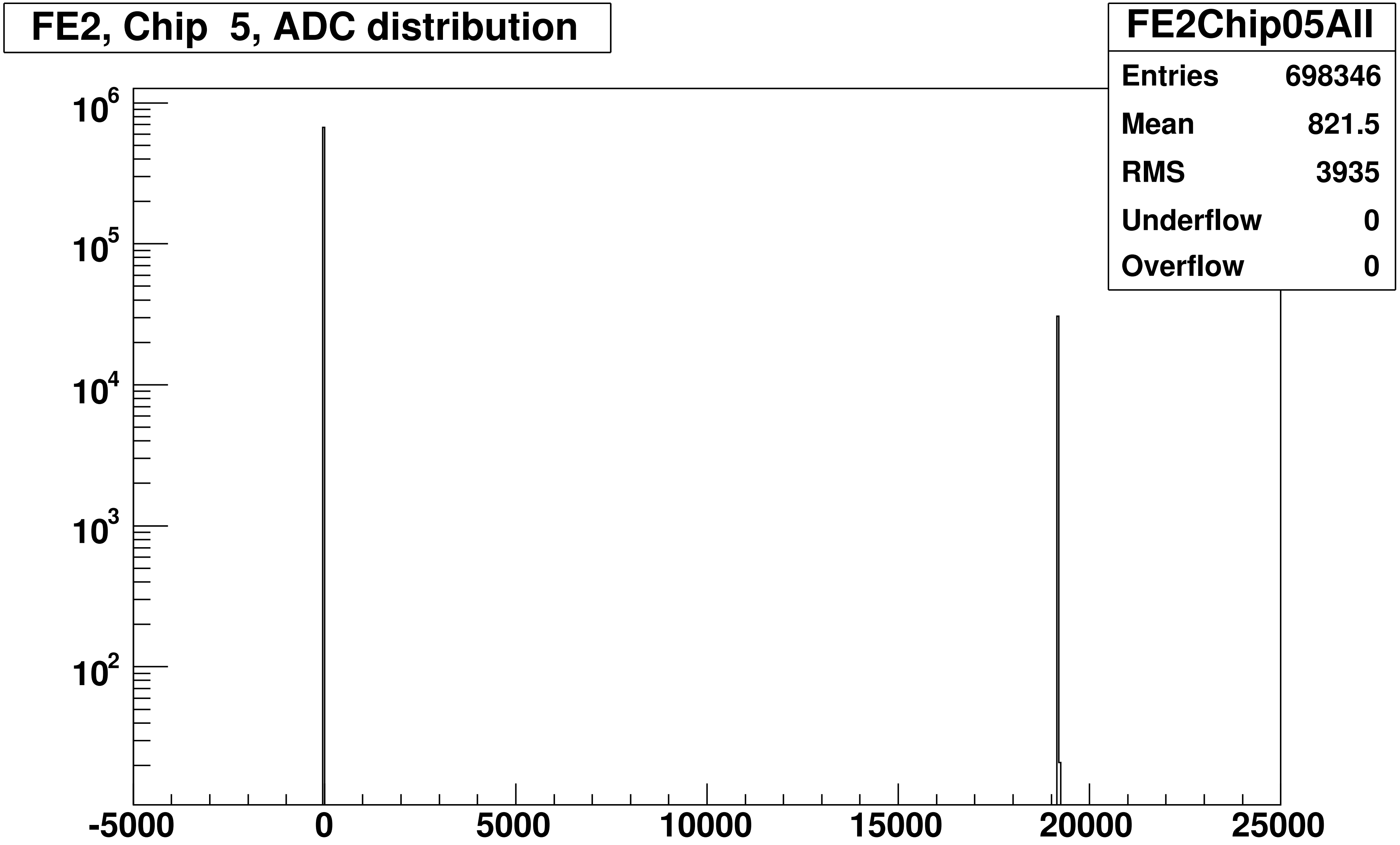
19400
19600
19800
20000


FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 698346
Mean 821.5
RMS 3935
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
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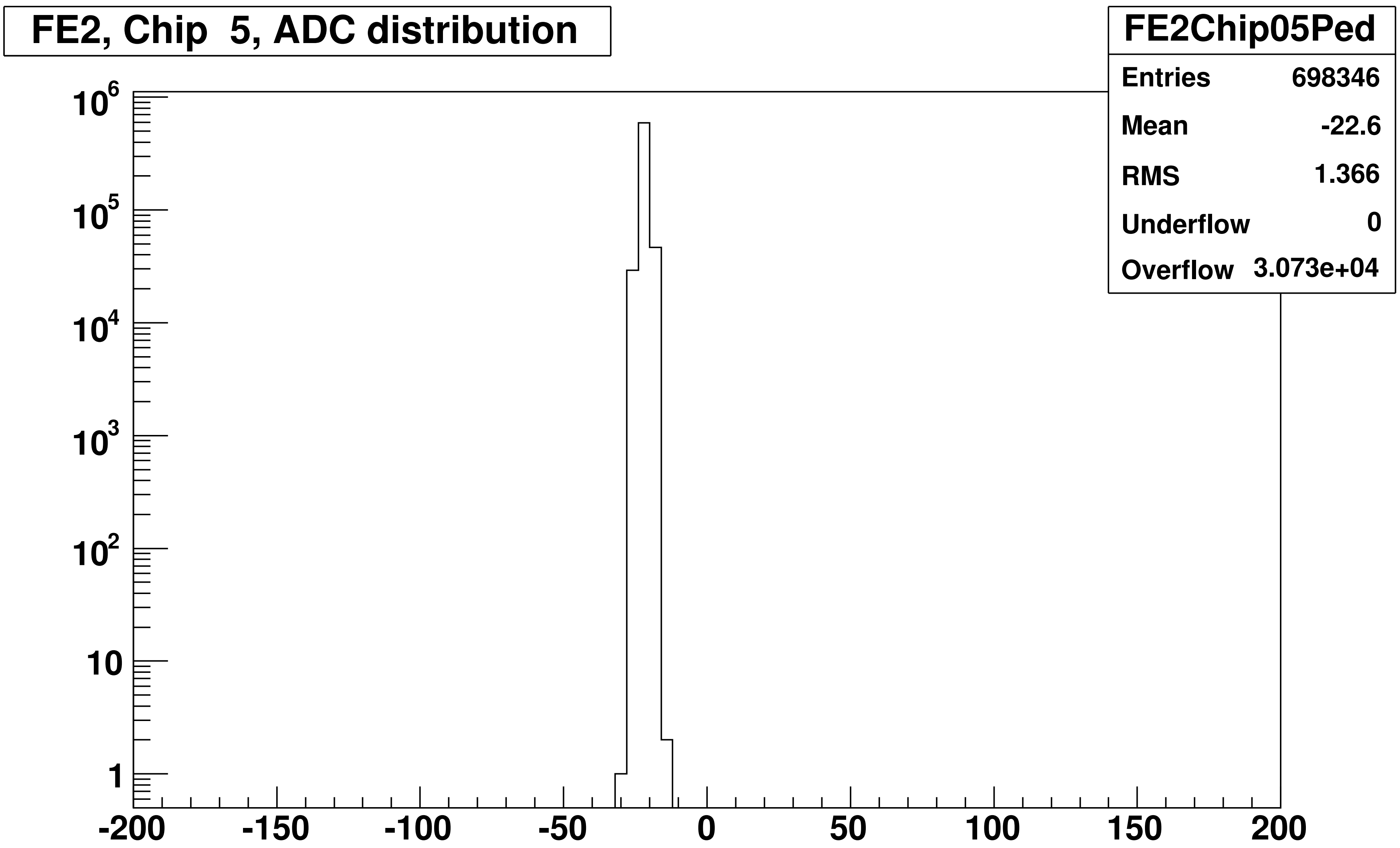
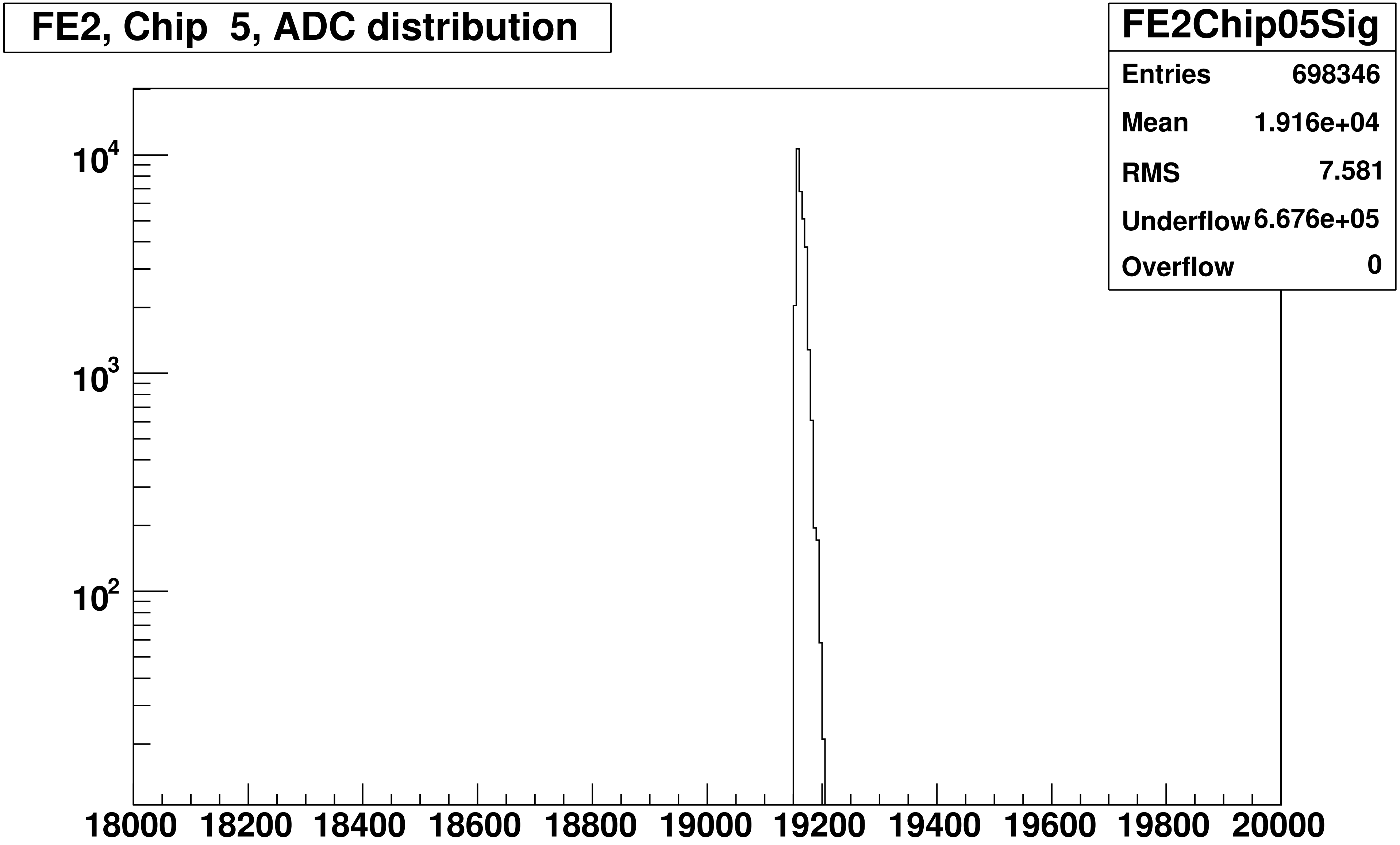
10000
15000
20000
25000


FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 698346
Mean -22.6
RMS 1.366
Underflow 0
Overflow 3.073e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 698346
Mean 1.916e+04
RMS 7.581
Underflow 6.676e+05
Overflow 0
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10^3
10^2
18000
18200
18400
18600
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19000
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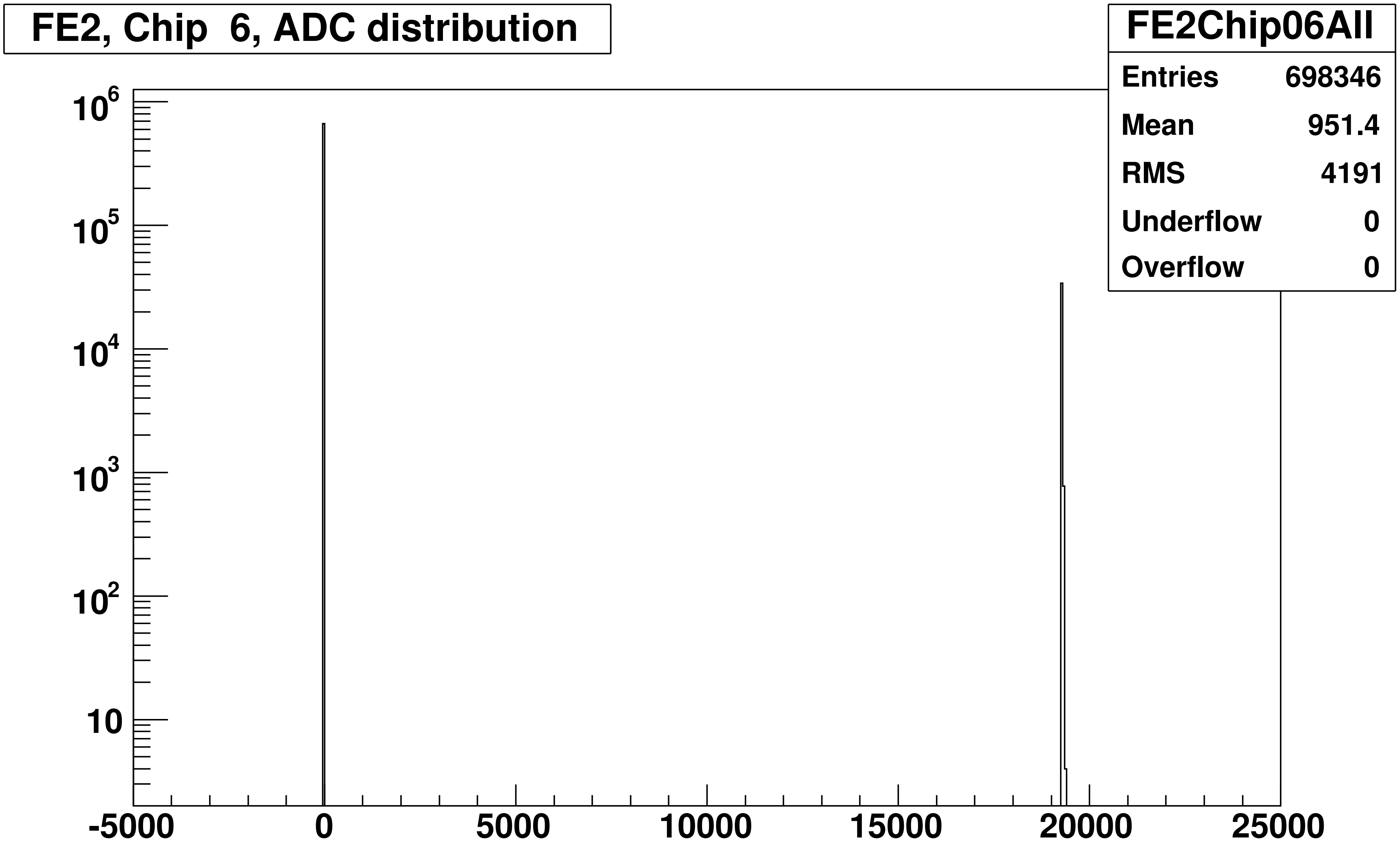
19600
19800
20000


FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 698346
Mean 951.4
RMS 4191
Underflow 0
Overflow 0
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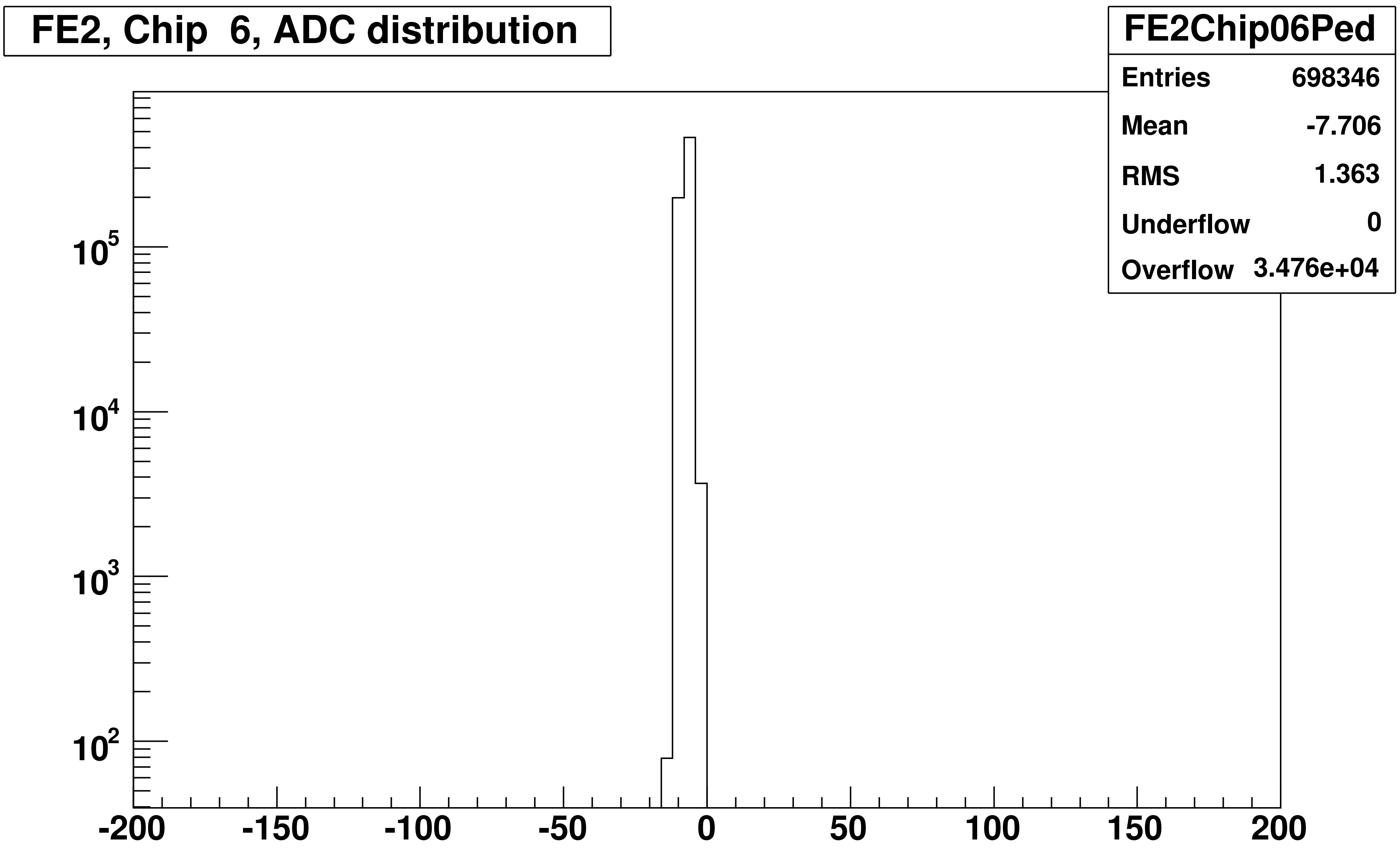
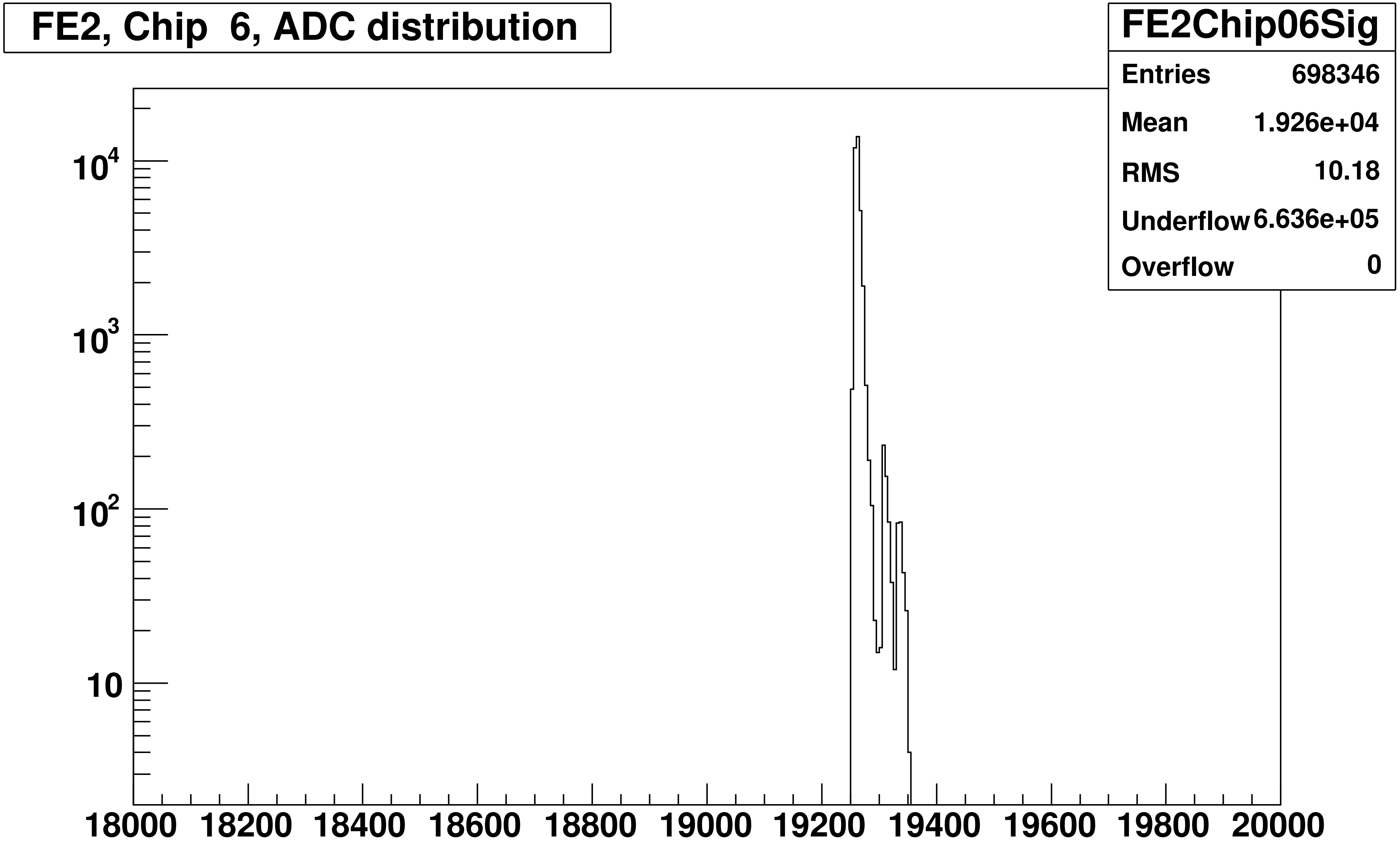
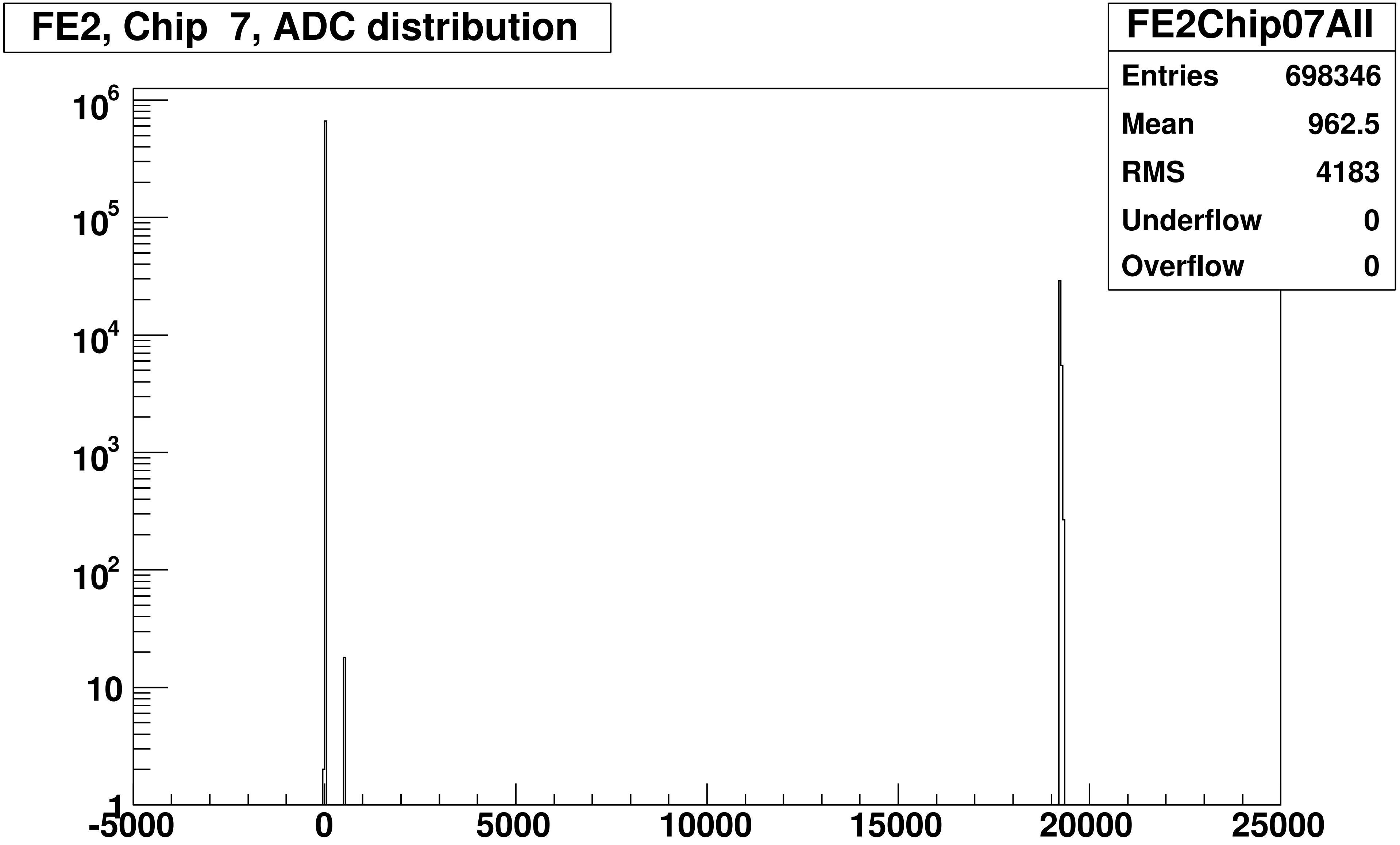
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 698346
Mean -7.706
RMS 1.363
Underflow 0
Overflow 3.476e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 698346
Mean 1.926e+04
RMS 10.18
Underflow 6.636e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 698346
Mean 962.5
RMS 4183
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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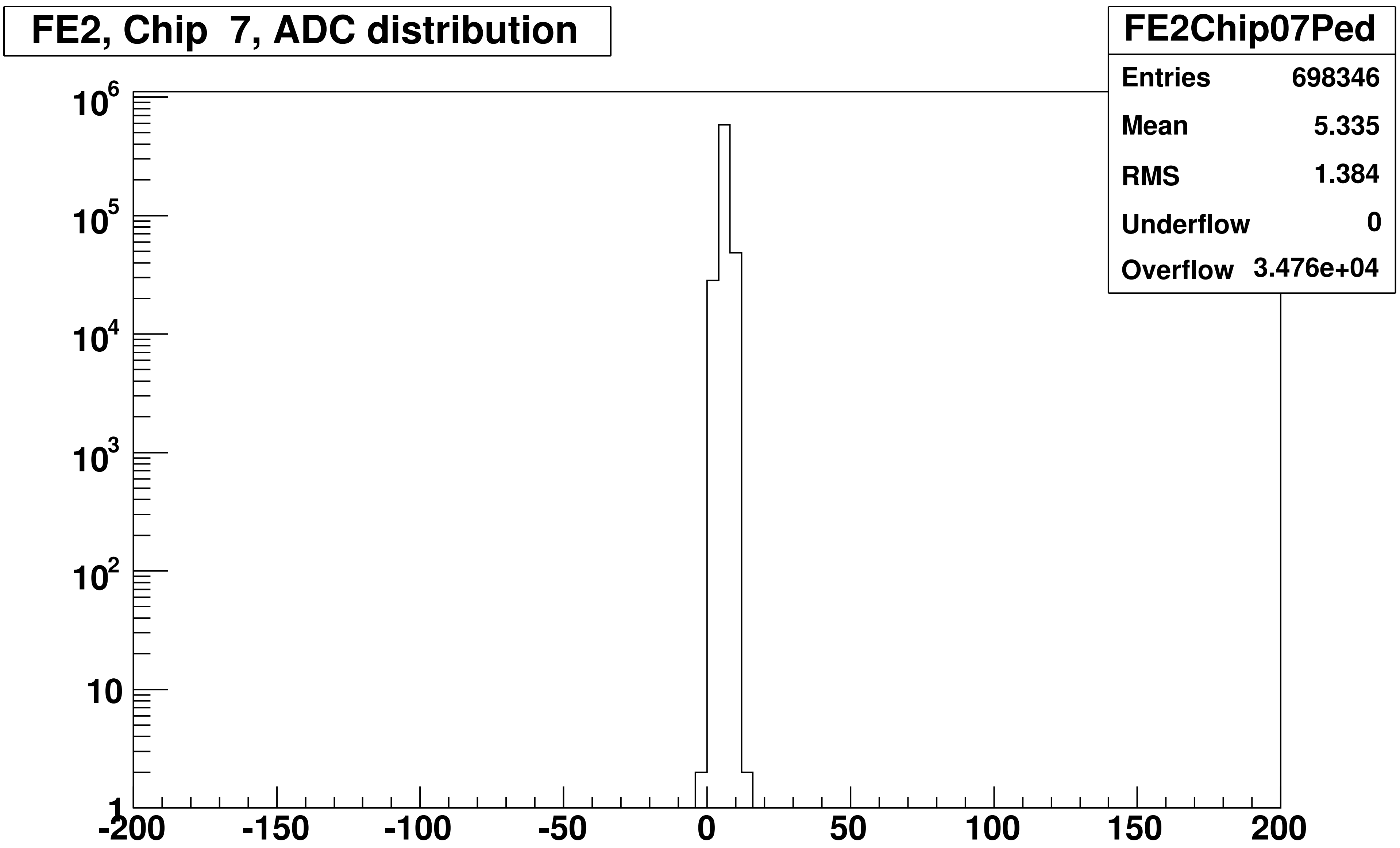
10000
15000
20000
25000


FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 698346
Mean 5.335
RMS 1.384
Underflow 0
Overflow 3.476e+04
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10^5
10^4
10^3
10^2
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1
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-100
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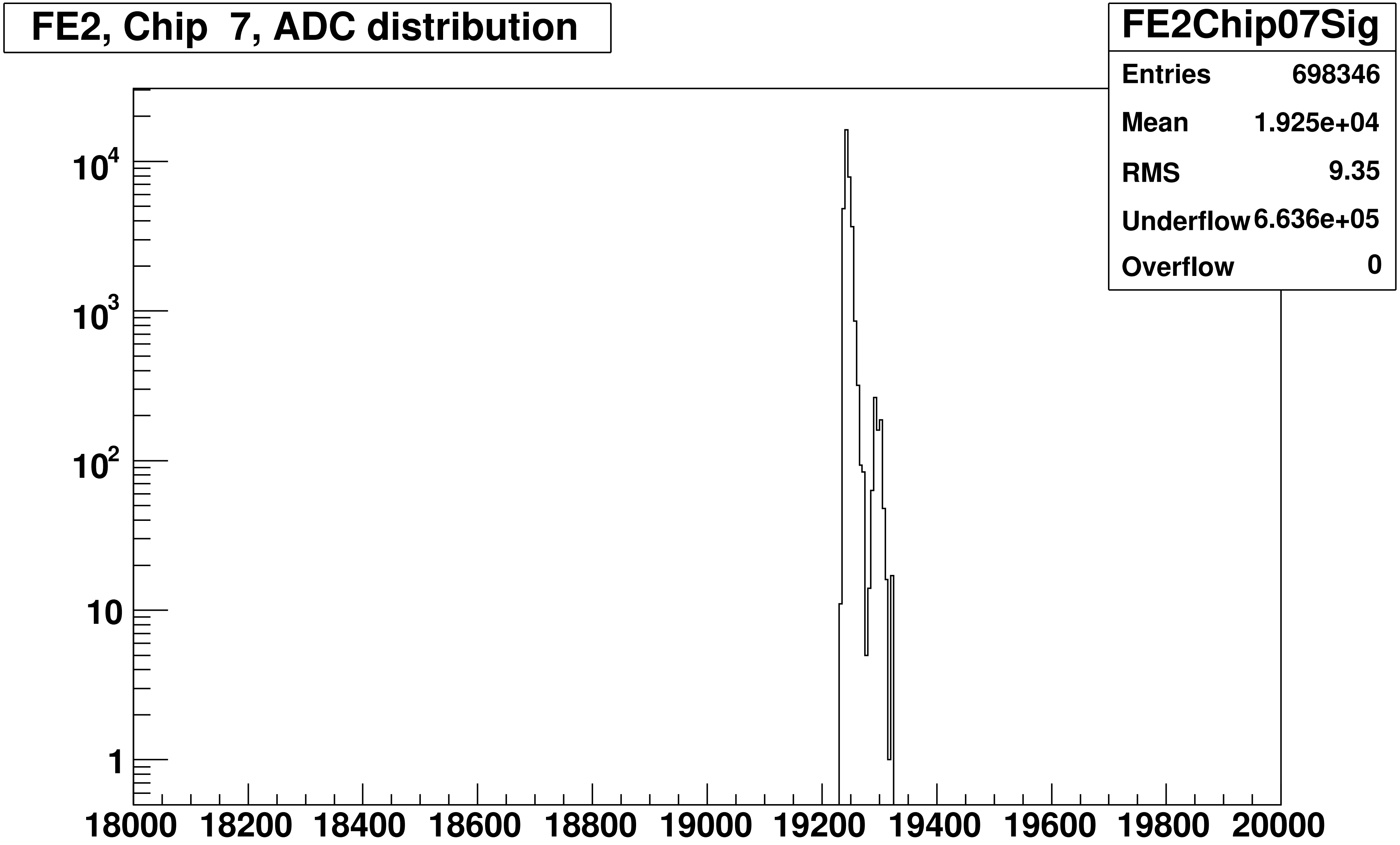
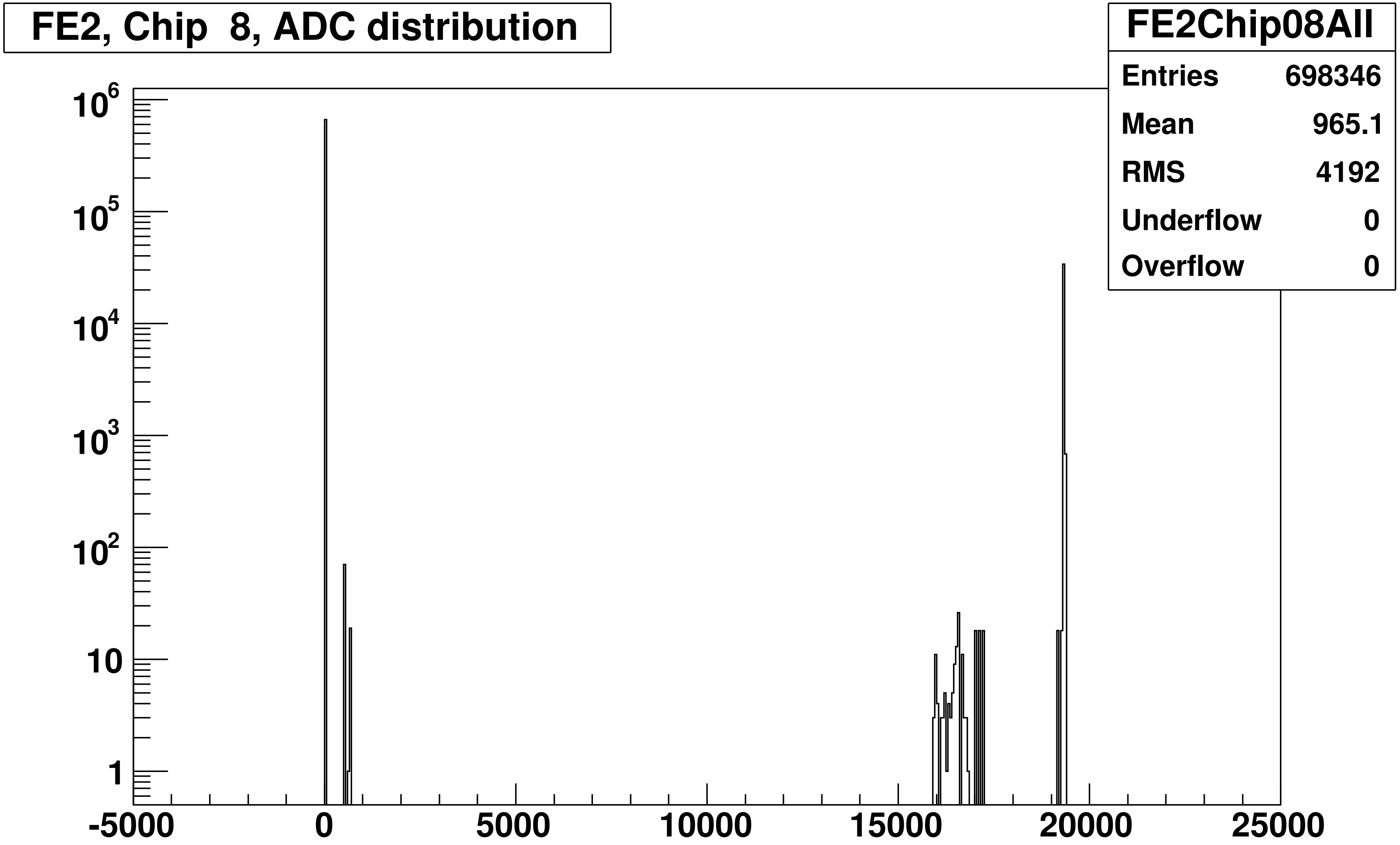
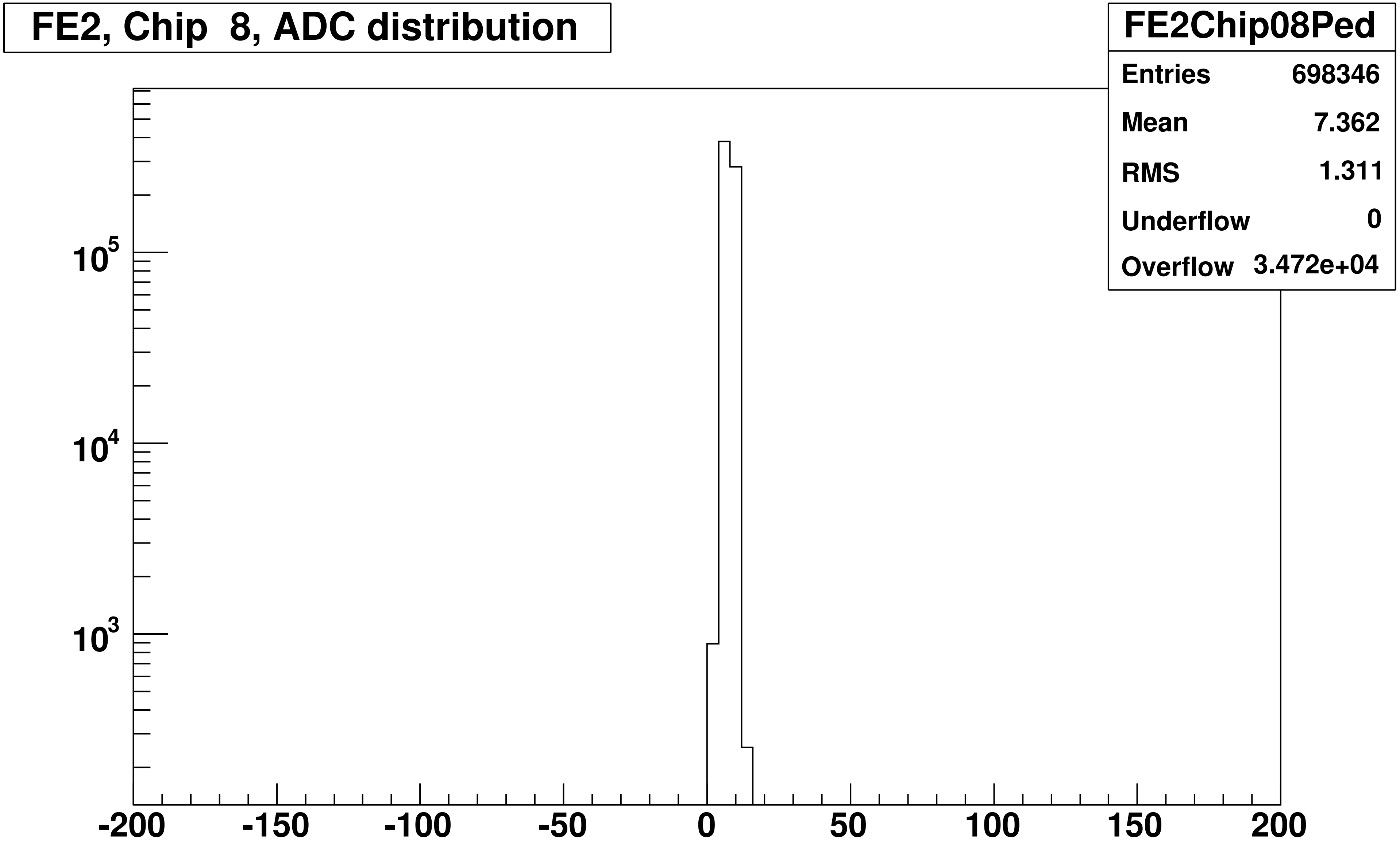
0
50
100
150
200


FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 698346
Mean 1.925e+04
RMS 9.35
Underflow 6.636e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 698346
Mean 965.1
RMS 4192
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 698346
Mean 7.362
RMS 1.311
Underflow 0
Overflow 3.472e+04
10^5
10^4
10^3
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-100
-50
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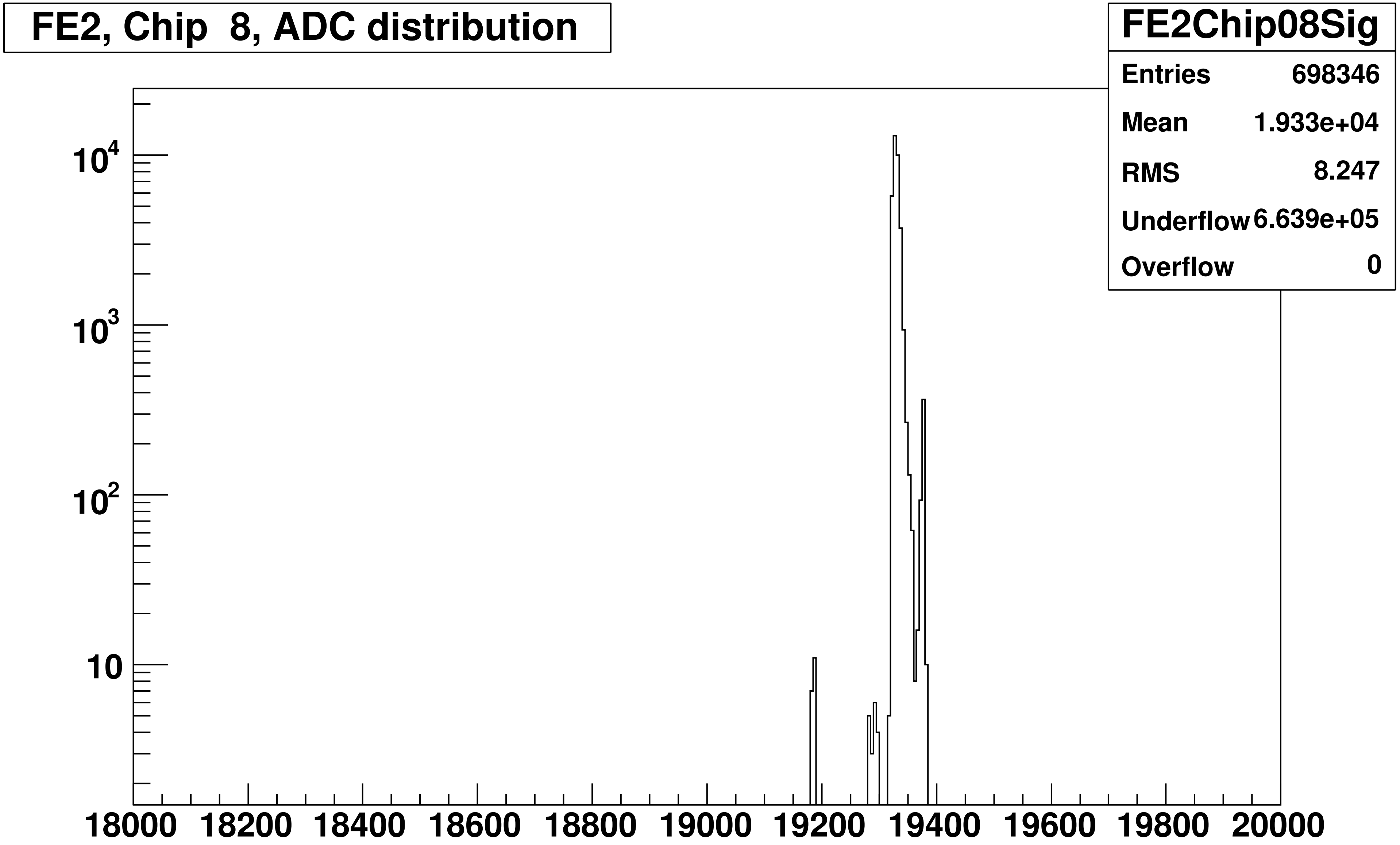
100
150
200



FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 698346
Mean 1.933e+04
RMS 8.247
Underflow 6.639e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
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19000
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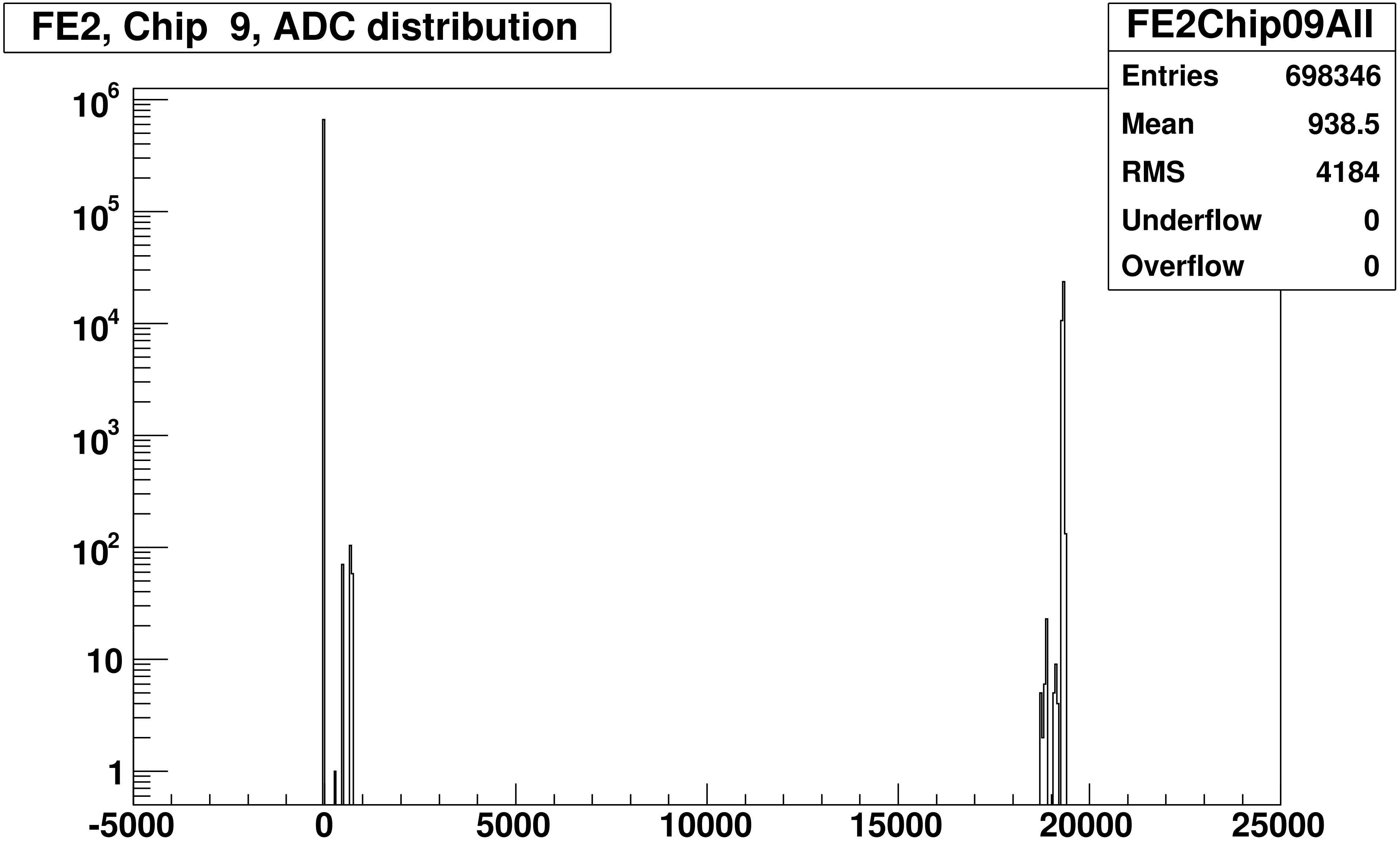
19600
19800
20000



FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 698346
Mean 938.5
RMS 4184
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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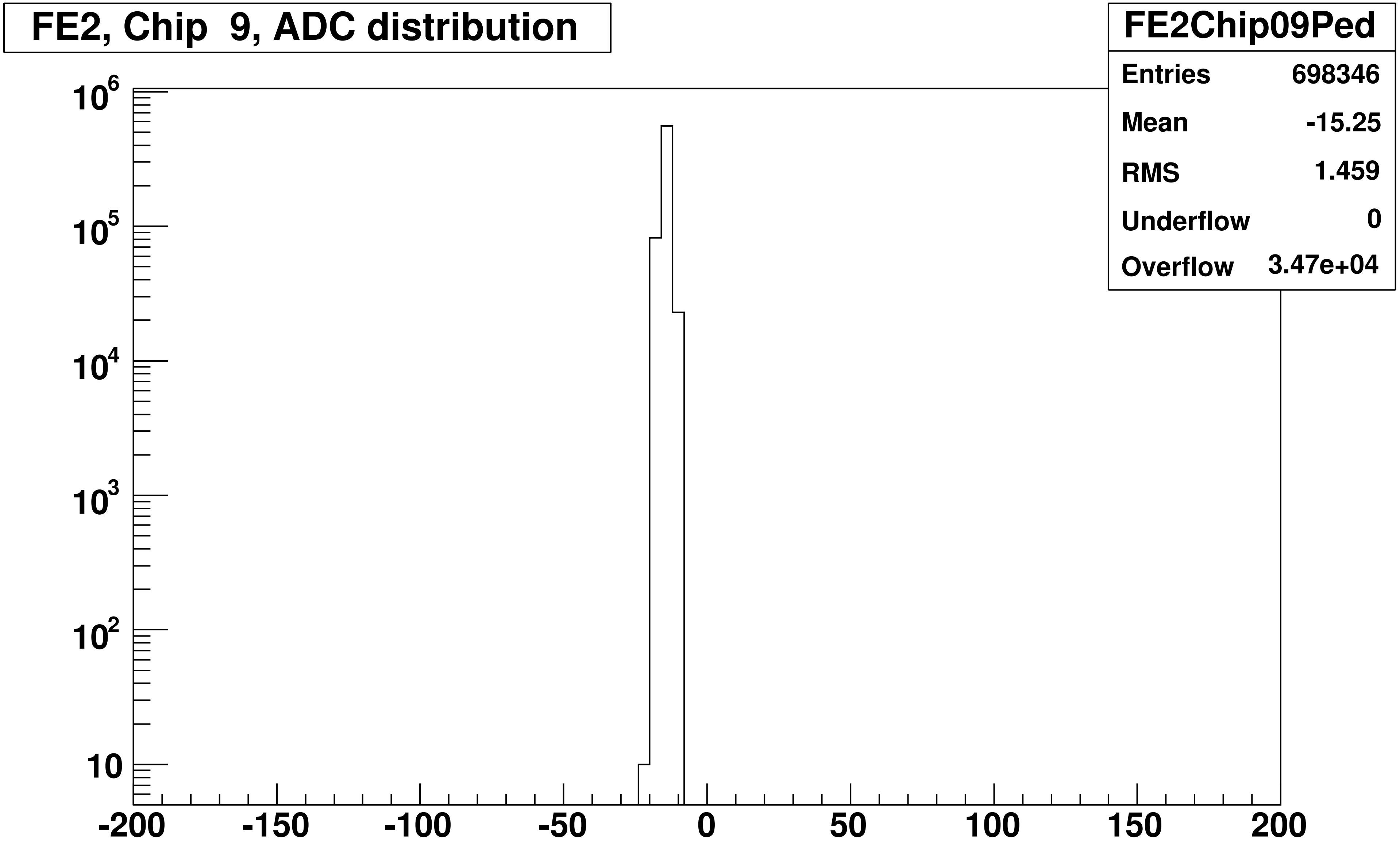
10000
15000
20000
25000


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 698346
Mean -15.25
RMS 1.459
Underflow 0
Overflow 3.47e+04
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10^5
10^4
10^3
10^2
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-100
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0
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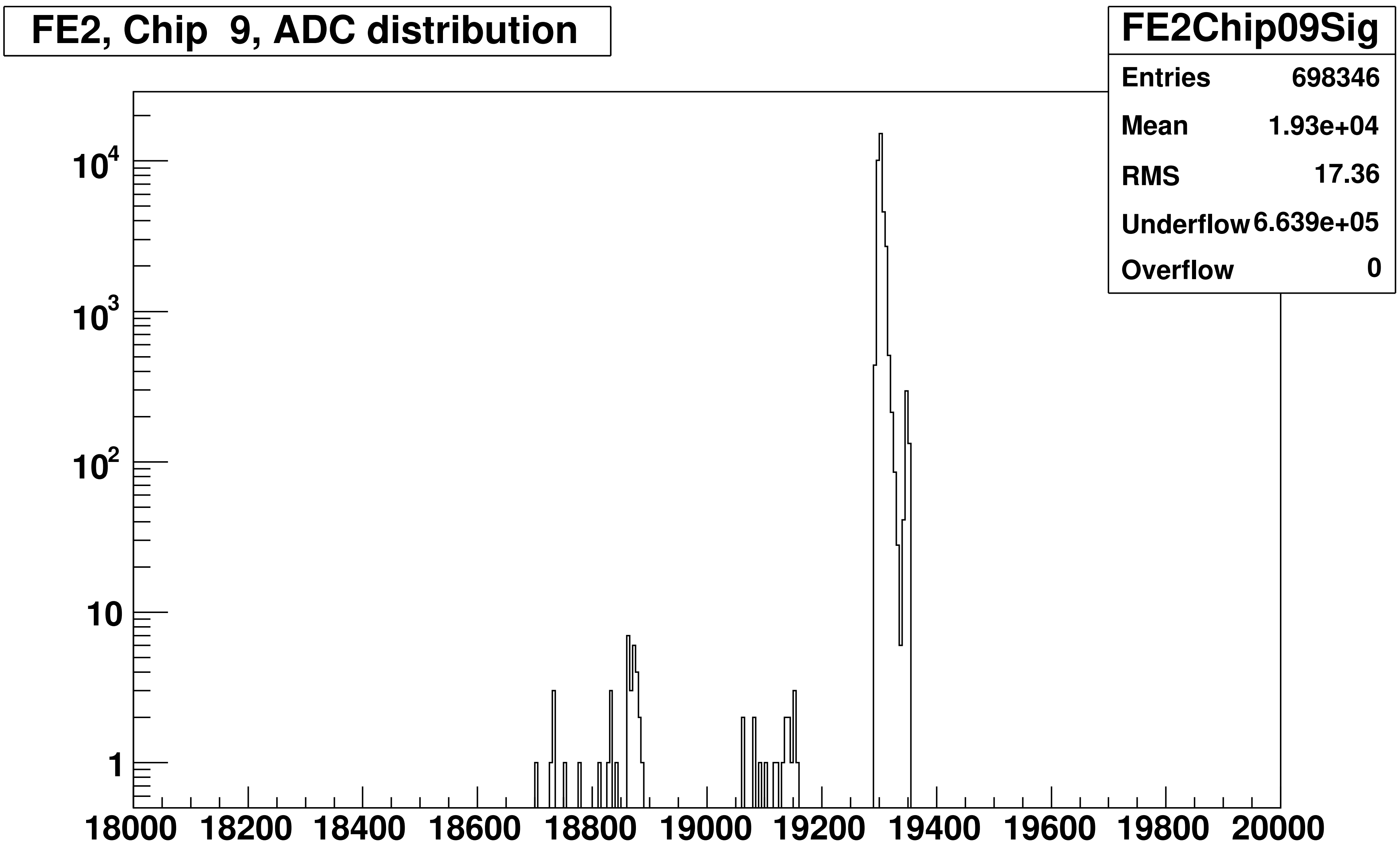
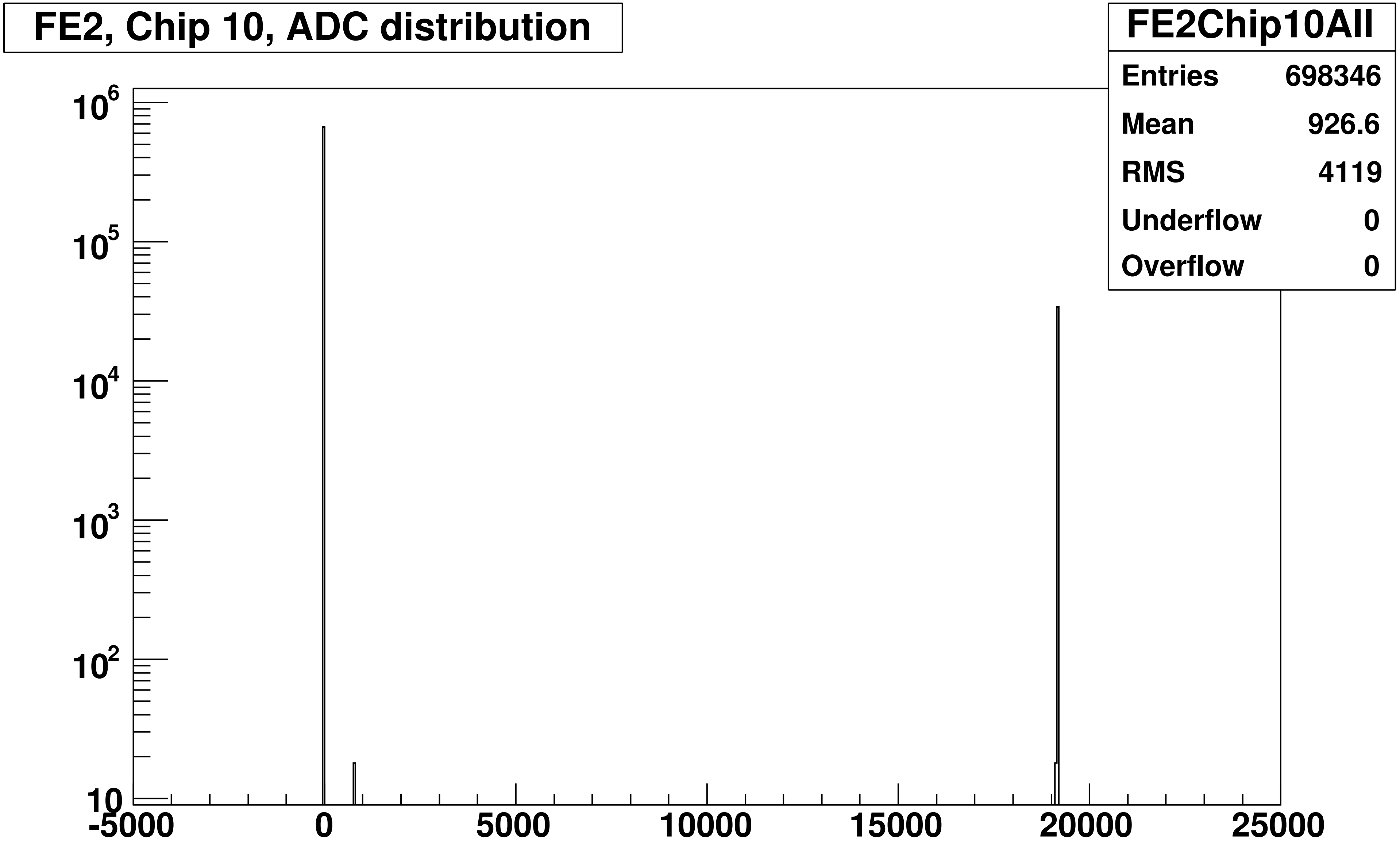
100
150
200


FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 698346
Mean 1.93e+04
RMS 17.36
Underflow 6.639e+05
Overflow 0
1
10
10^2
10^3
10^4
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 698346
Mean 926.6
RMS 4119
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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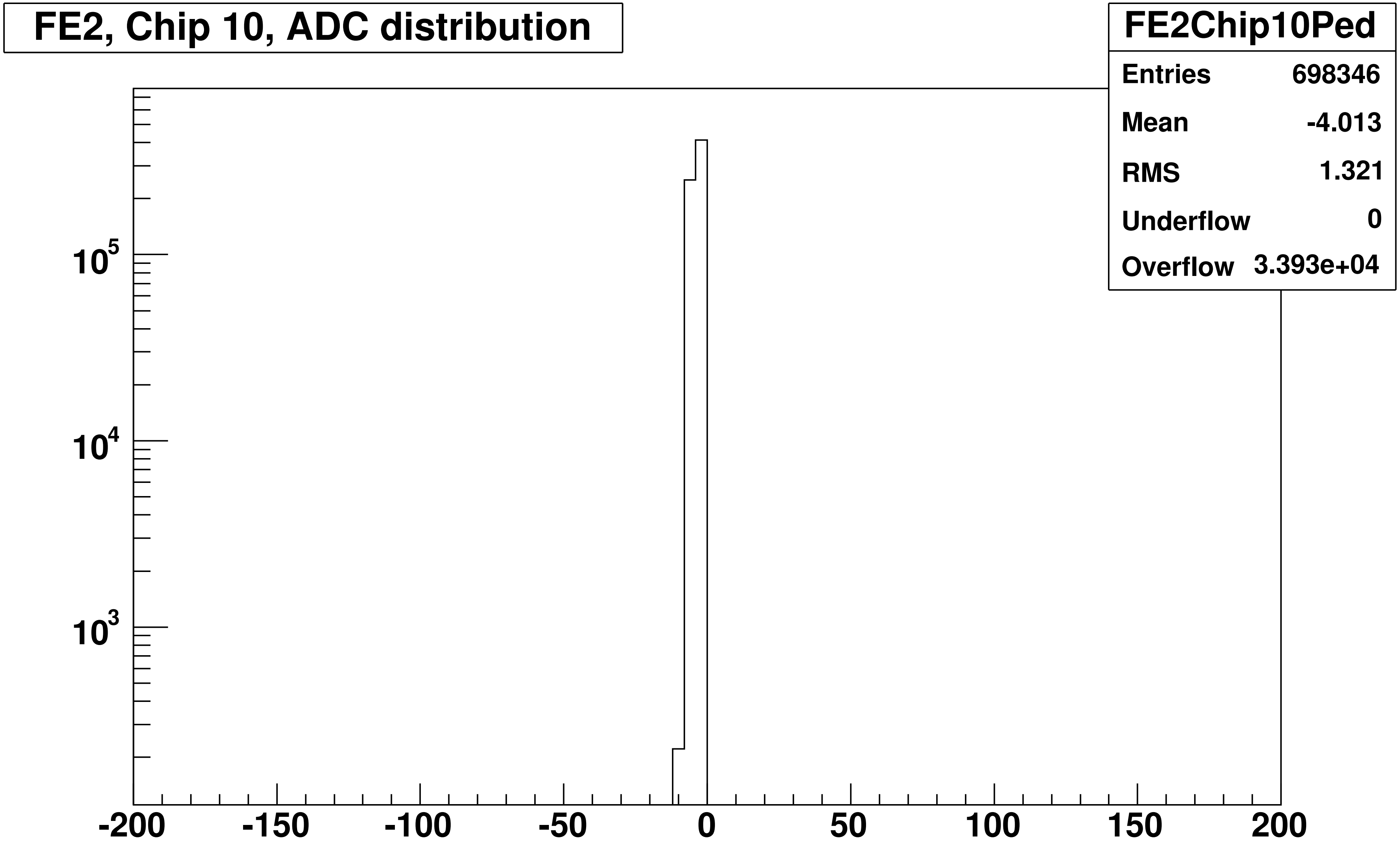
15000
20000
25000


FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 698346
Mean -4.013
RMS 1.321
Underflow 0
Overflow 3.393e+04
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10^4
10^3
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-100
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150
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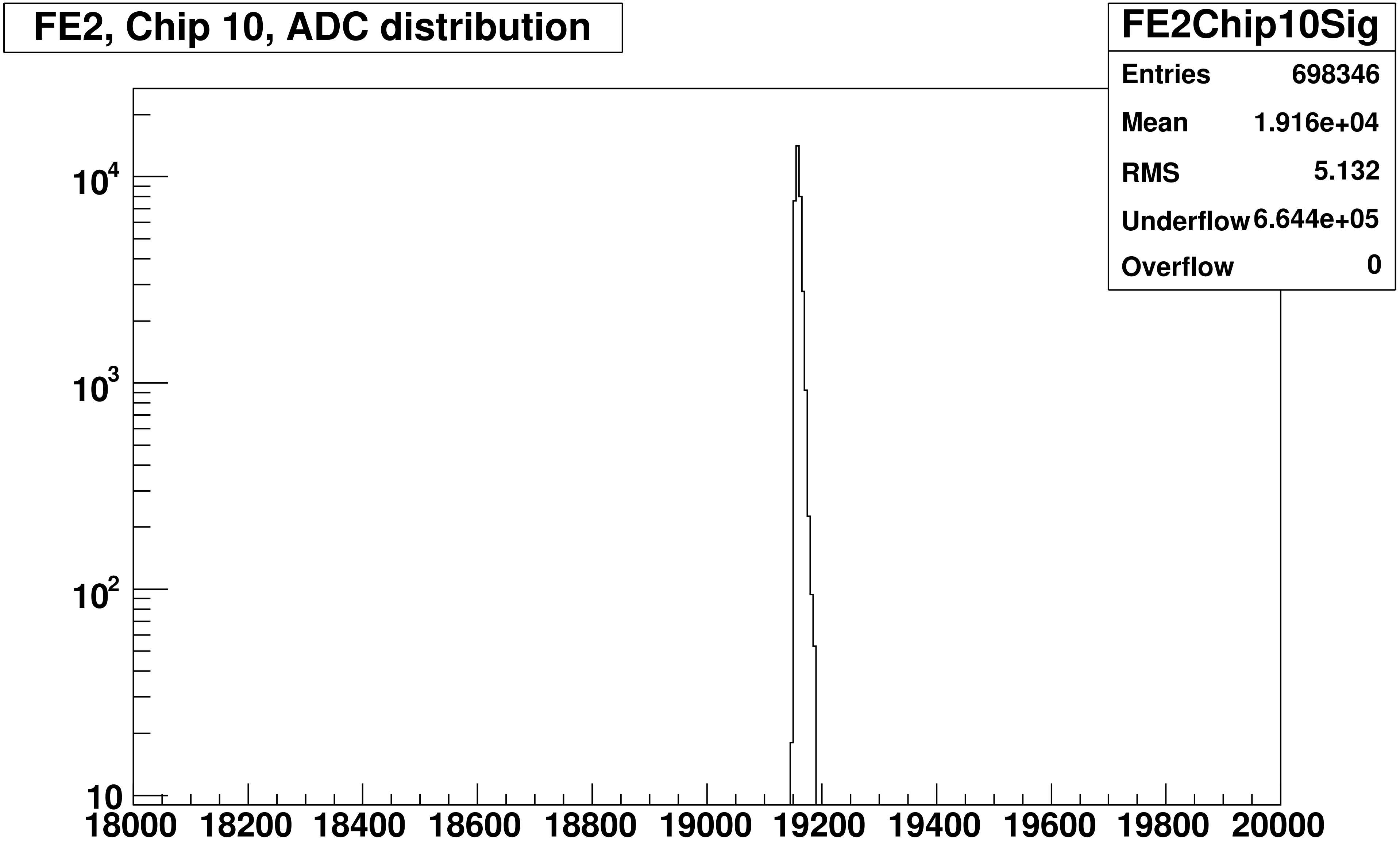
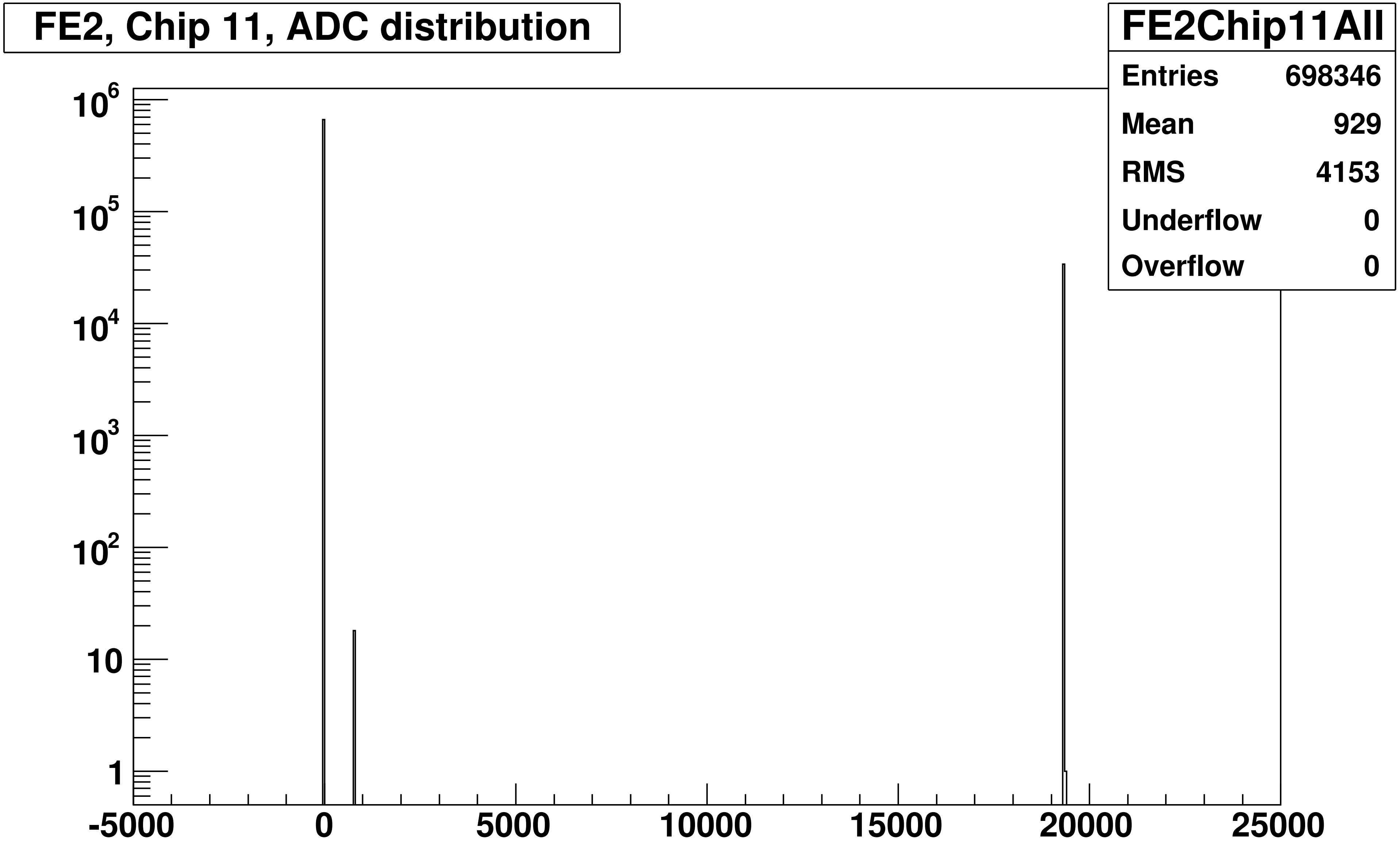

FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 698346
Mean 1.916e+04
RMS 5.132
Underflow 6.644e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 698346
Mean 929
RMS 4153
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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1
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0
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10000
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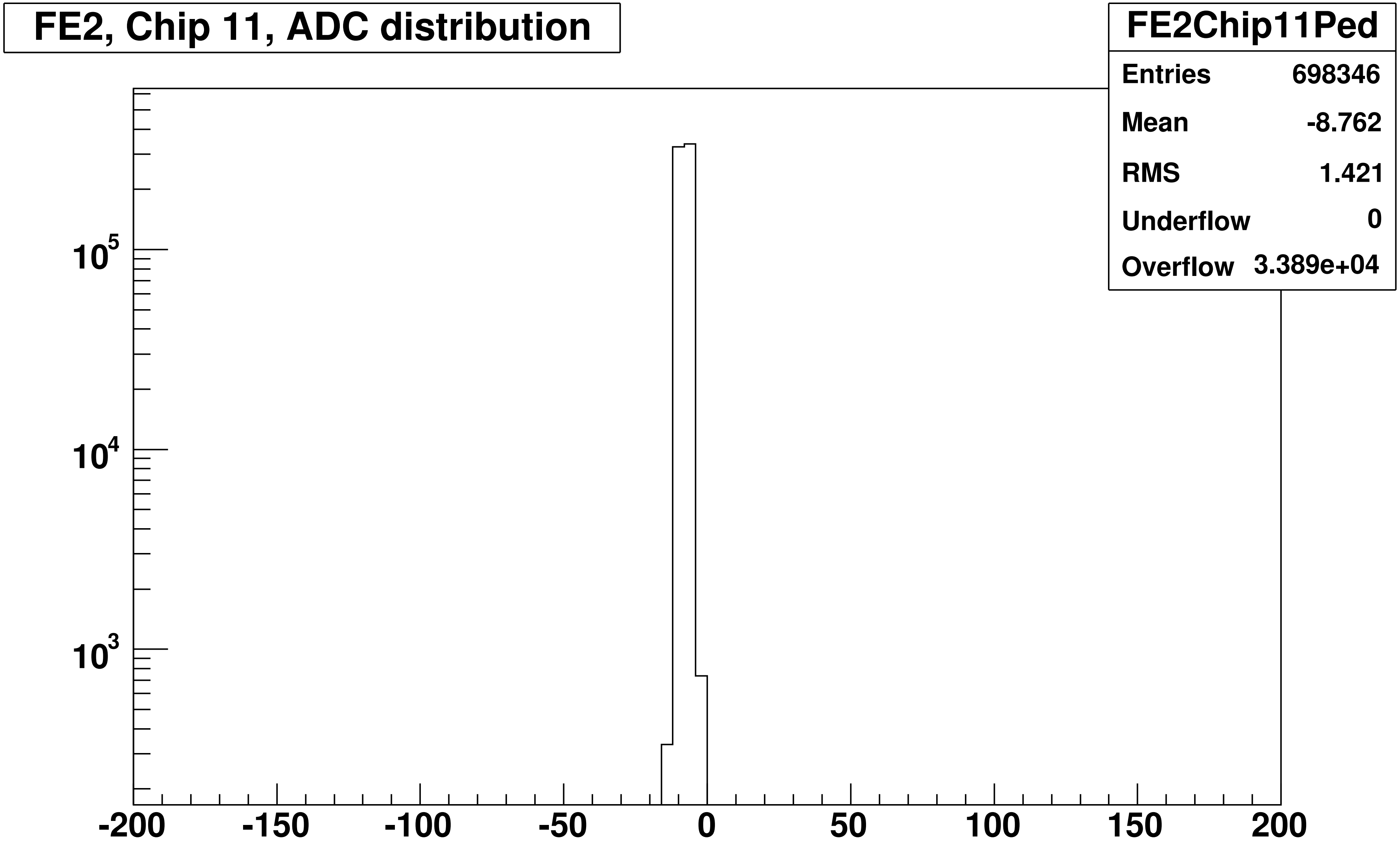
20000
25000


FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 698346
Mean -8.762
RMS 1.421
Underflow 0
Overflow 3.389e+04
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10^4
10^3
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150
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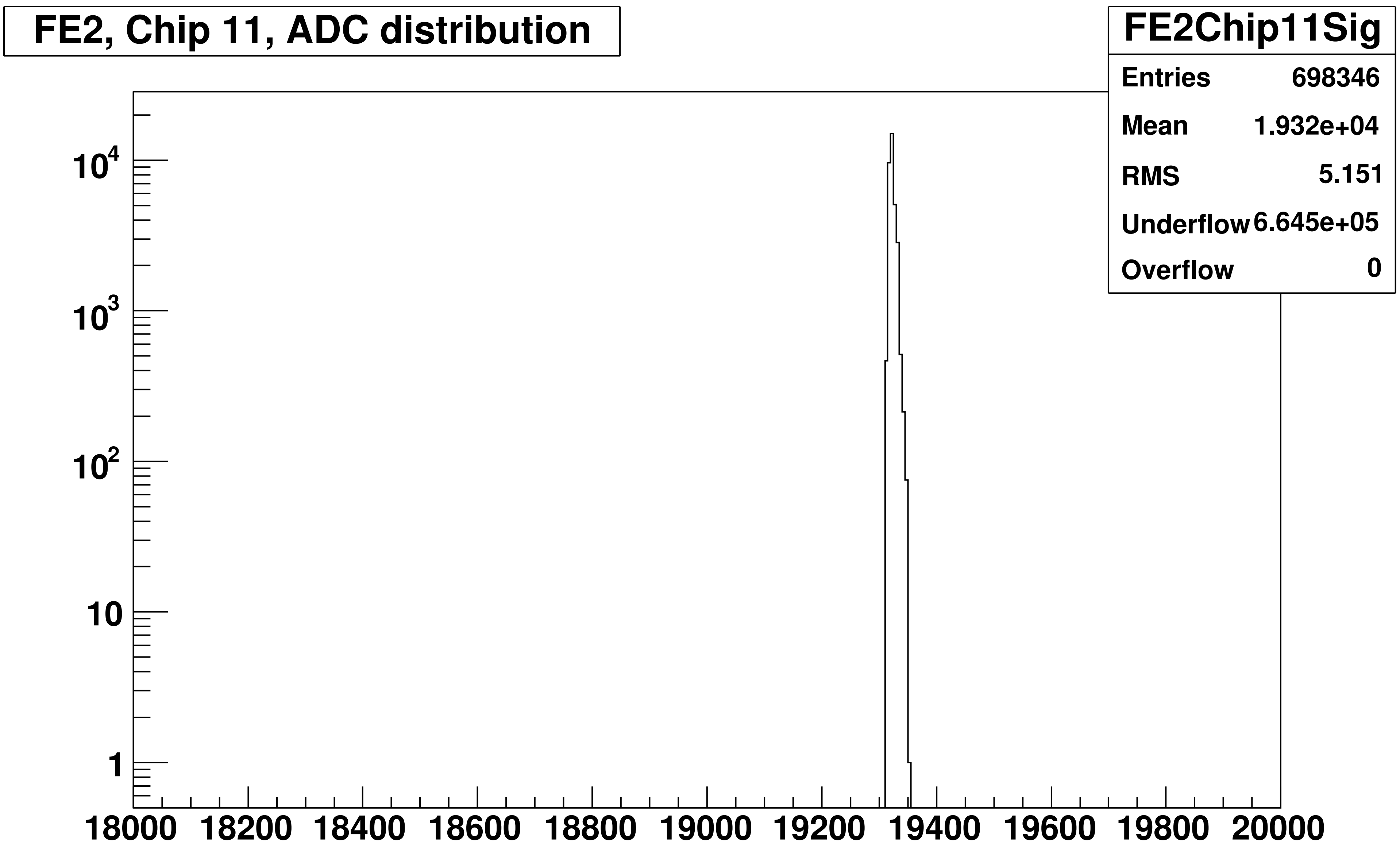
FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 698346
Mean 1.932e+04
RMS 5.151
Underflow 6.645e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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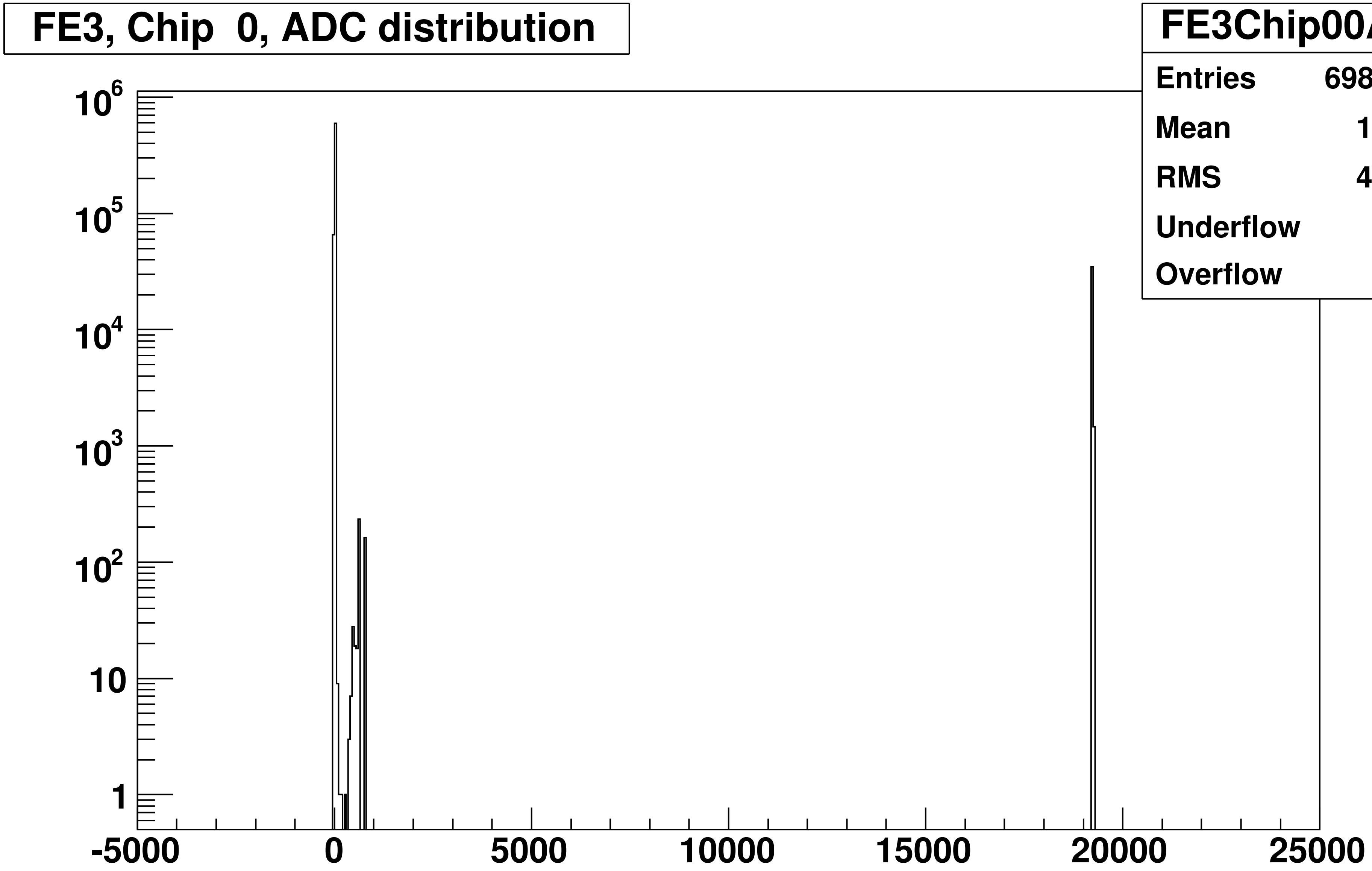
19600
19800
20000



FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 698346
Mean 1005
RMS 4276
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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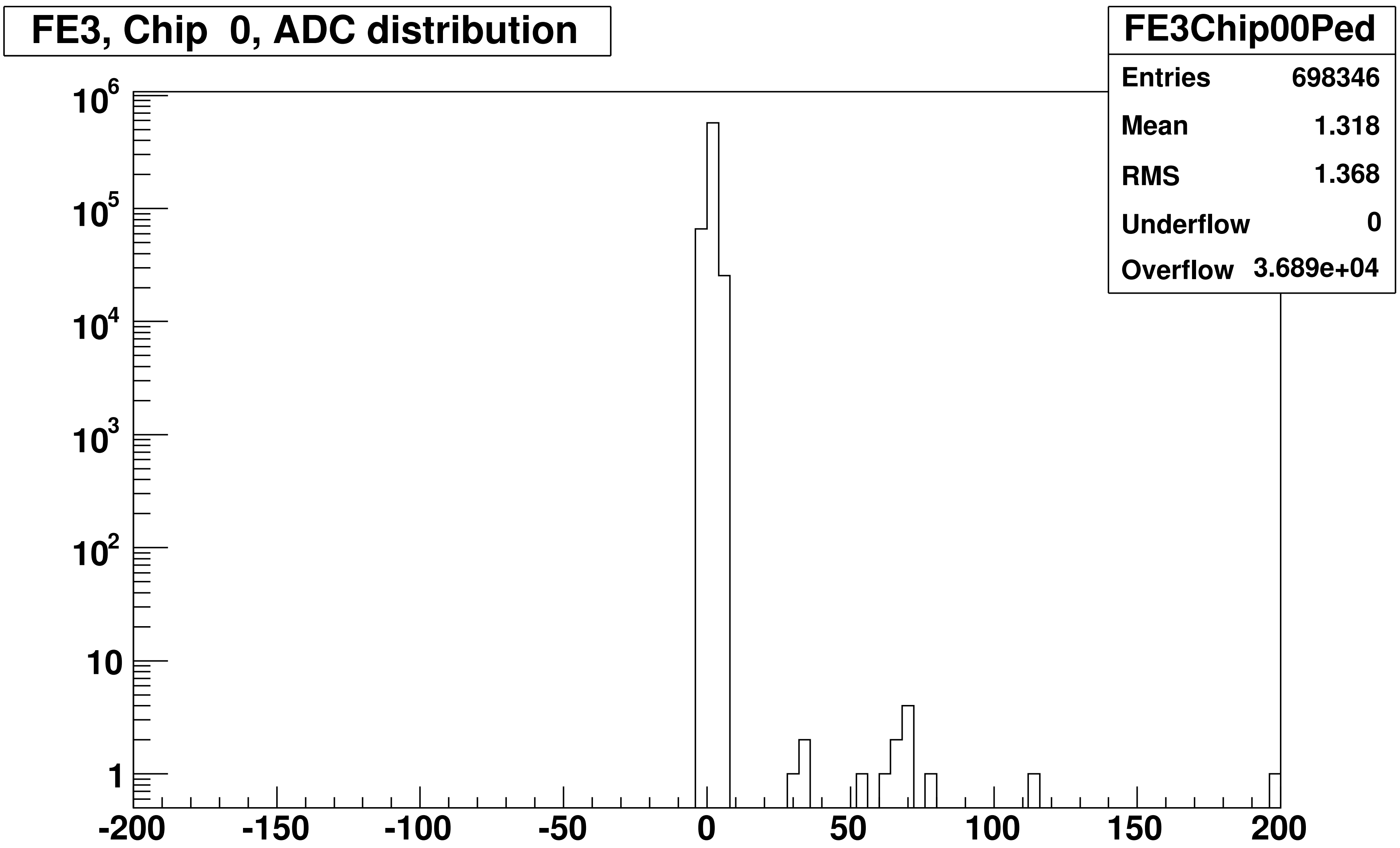
FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 698346
Mean 1.318
RMS 1.368
Underflow 0
Overflow 3.689e+04
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10^5
10^4
10^3
10^2
10
1
-200
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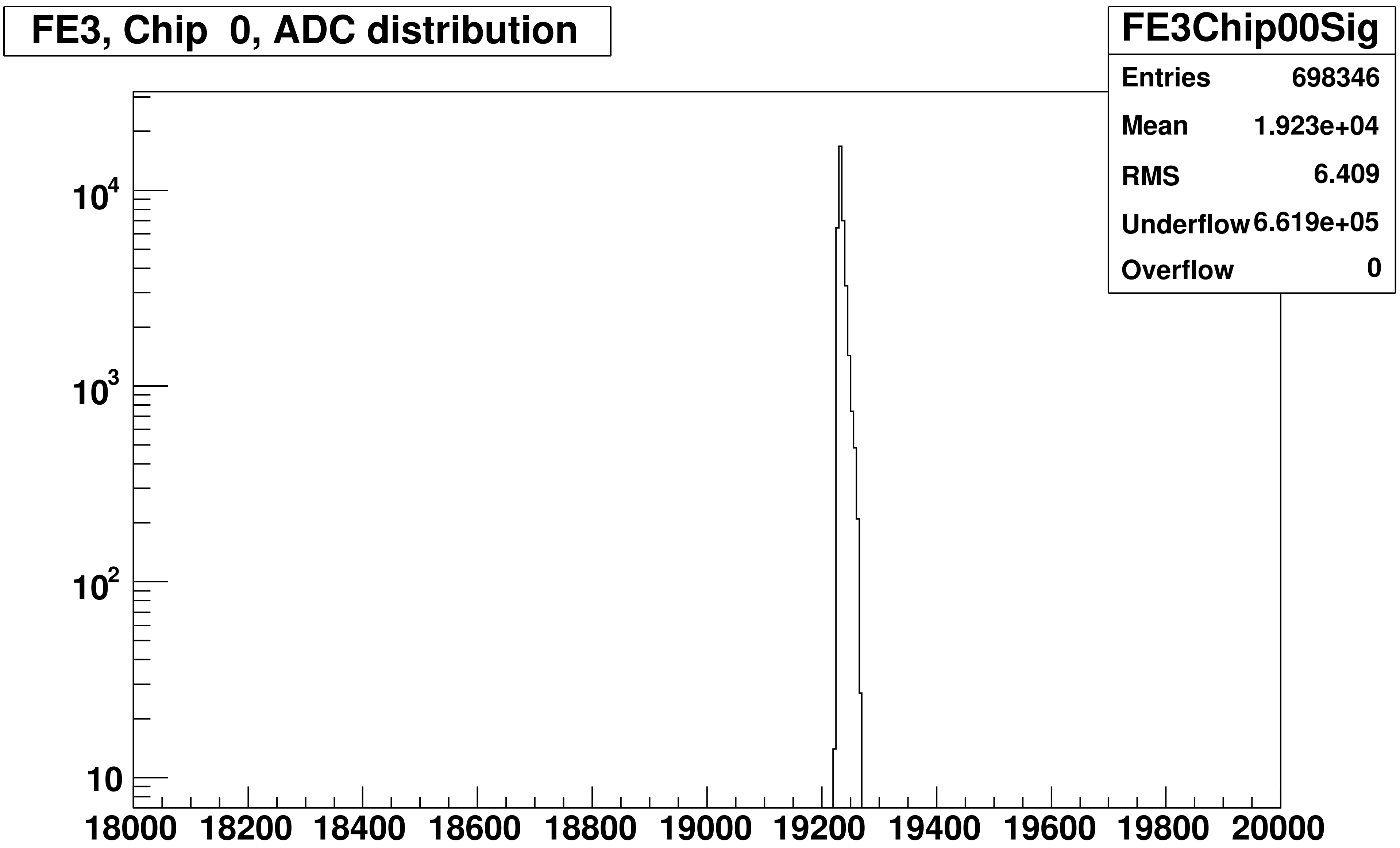
150
200



FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 698346
Mean 1.923e+04
RMS 6.409
Underflow 6.619e+05
Overflow 0
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10^3
10^2
10
18000
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18400
18600
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19000
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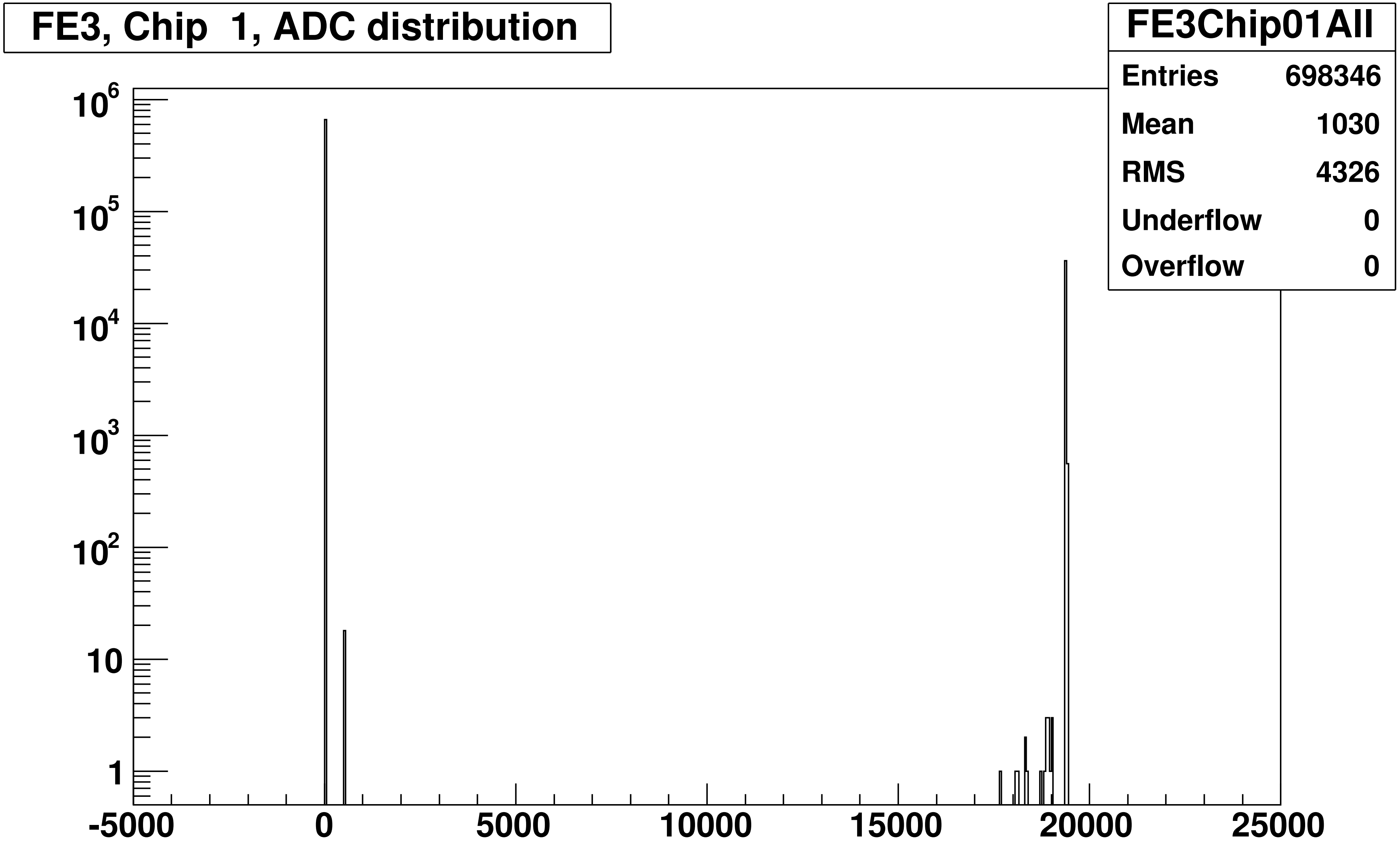
20000


FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 698346
Mean 1030
RMS 4326
Underflow 0
Overflow 0
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10^3
10^2
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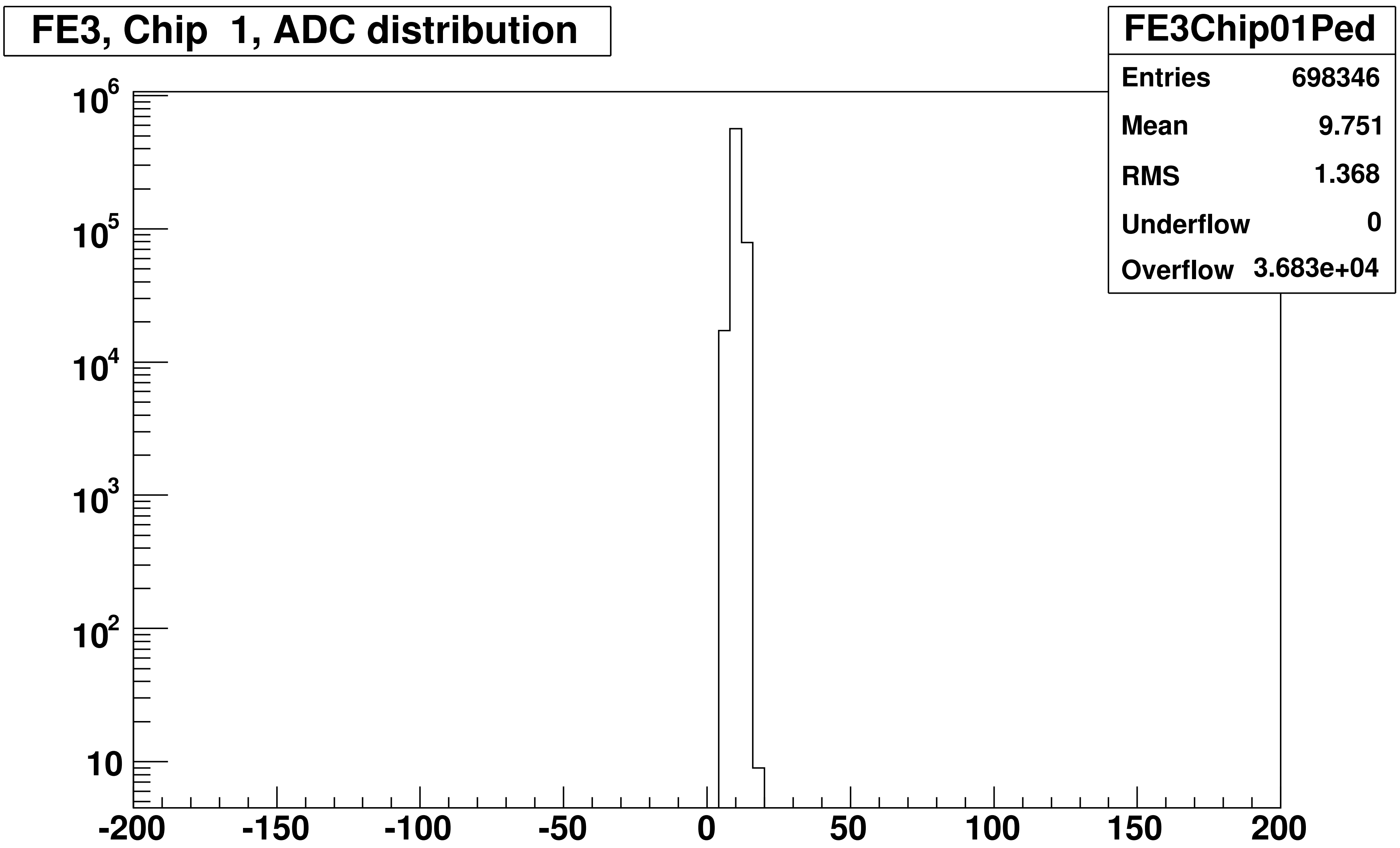
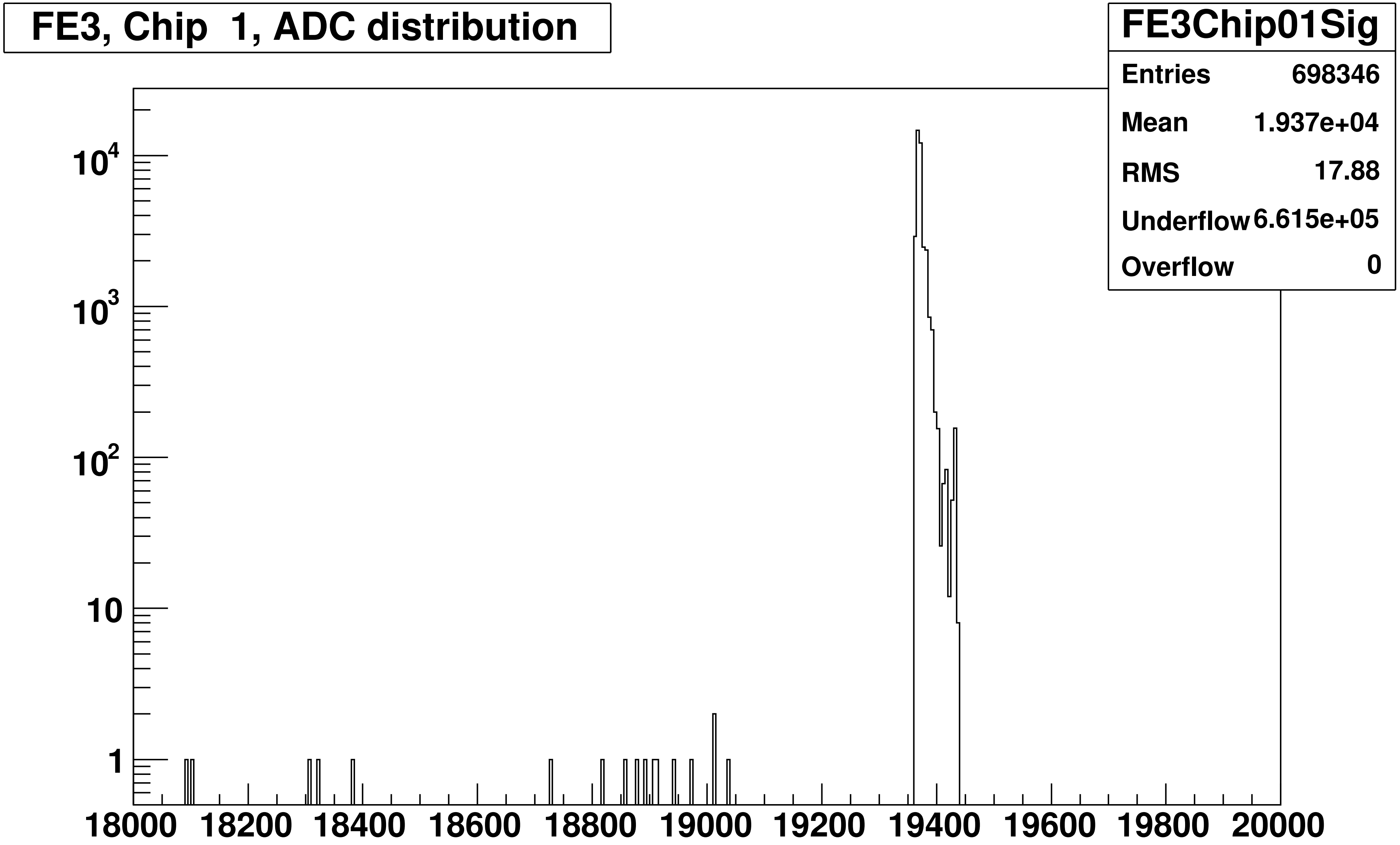
25000



FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 698346
Mean 9.751
RMS 1.368
Underflow 0
Overflow 3.683e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 698346
Mean 1.937e+04
RMS 17.88
Underflow 6.615e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
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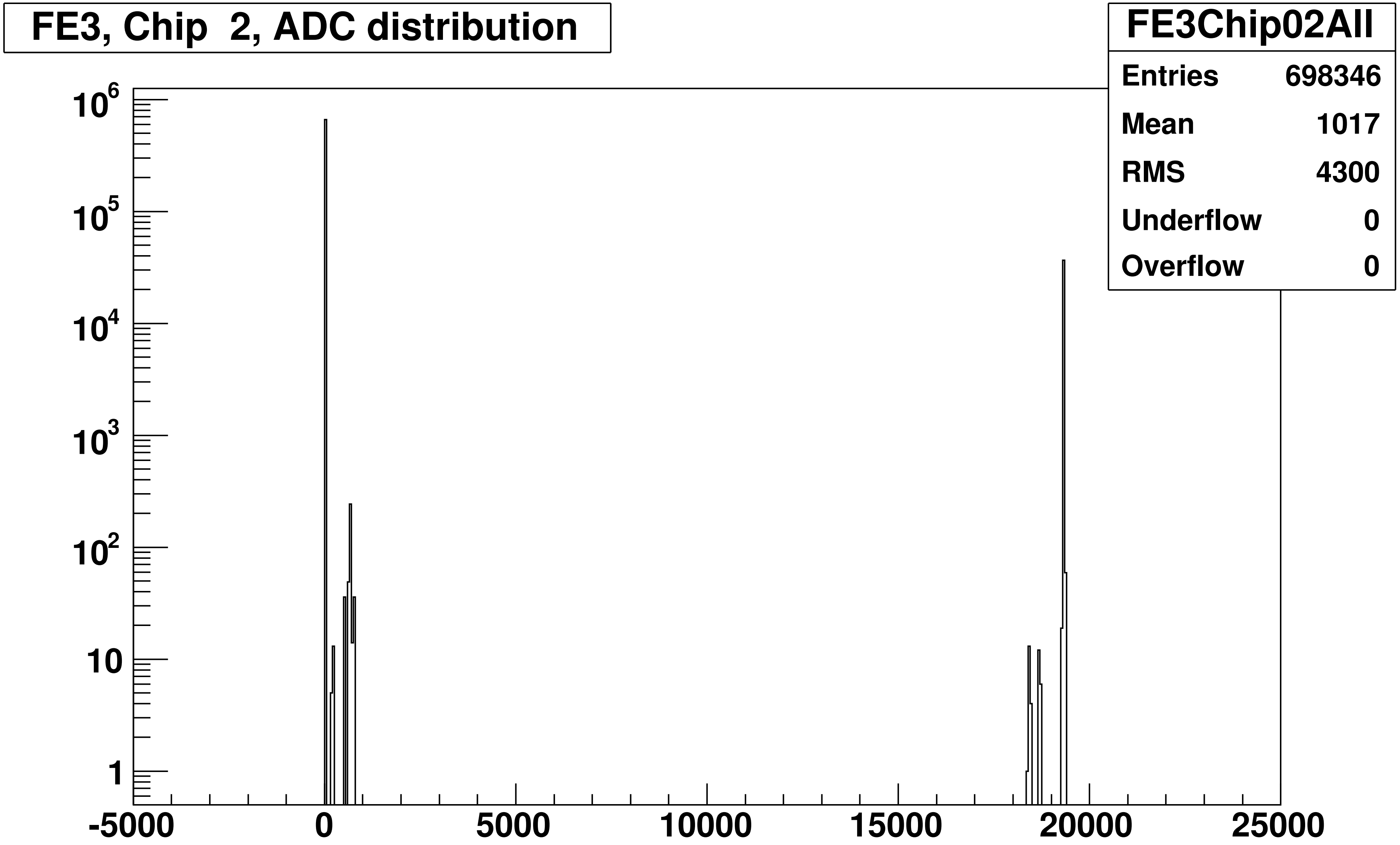
19800
20000



FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 698346
Mean 1017
RMS 4300
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
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0
5000
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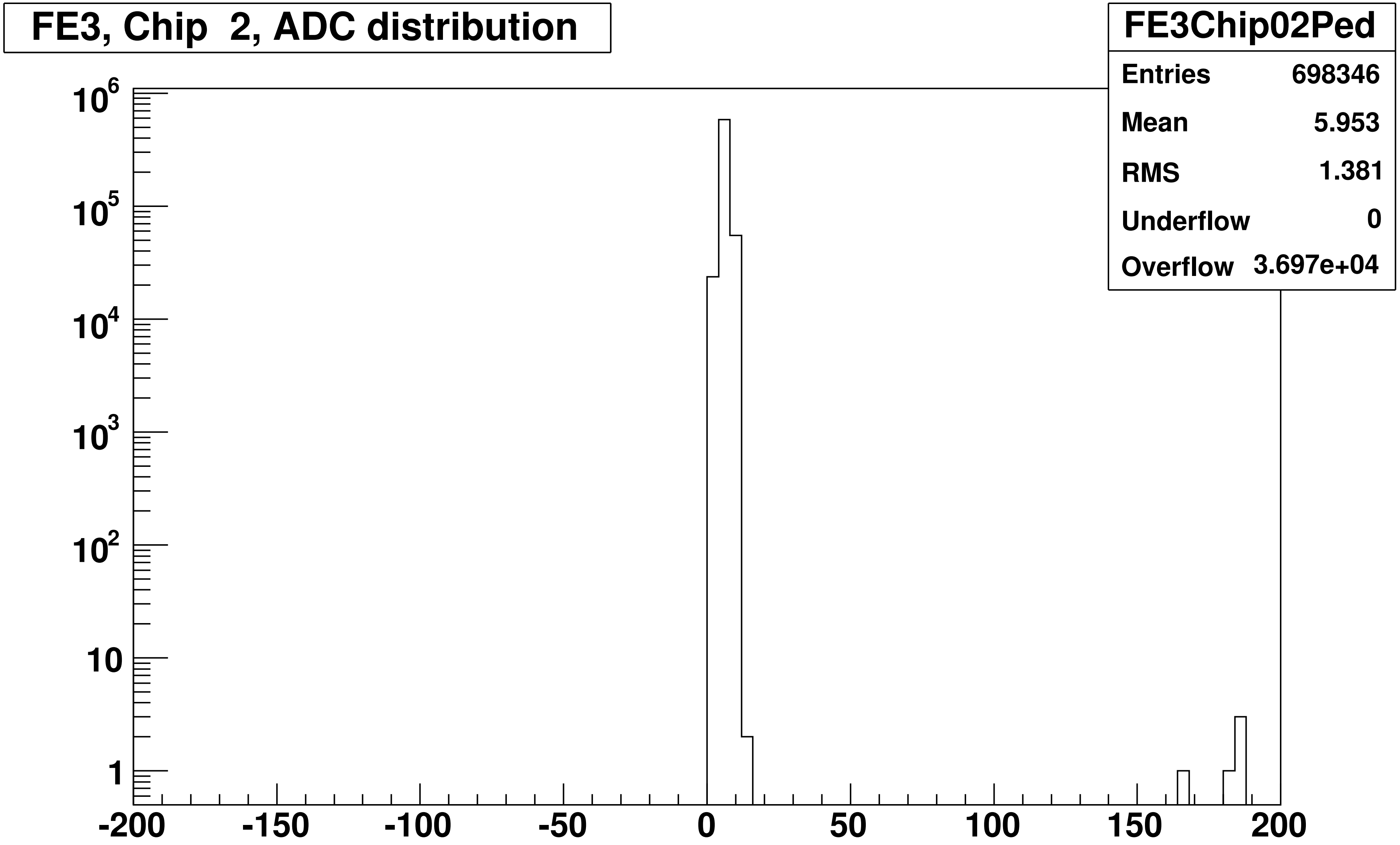
20000
25000


FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 698346
Mean 5.953
RMS 1.381
Underflow 0
Overflow 3.697e+04
10^6
10^5
10^4
10^3
10^2
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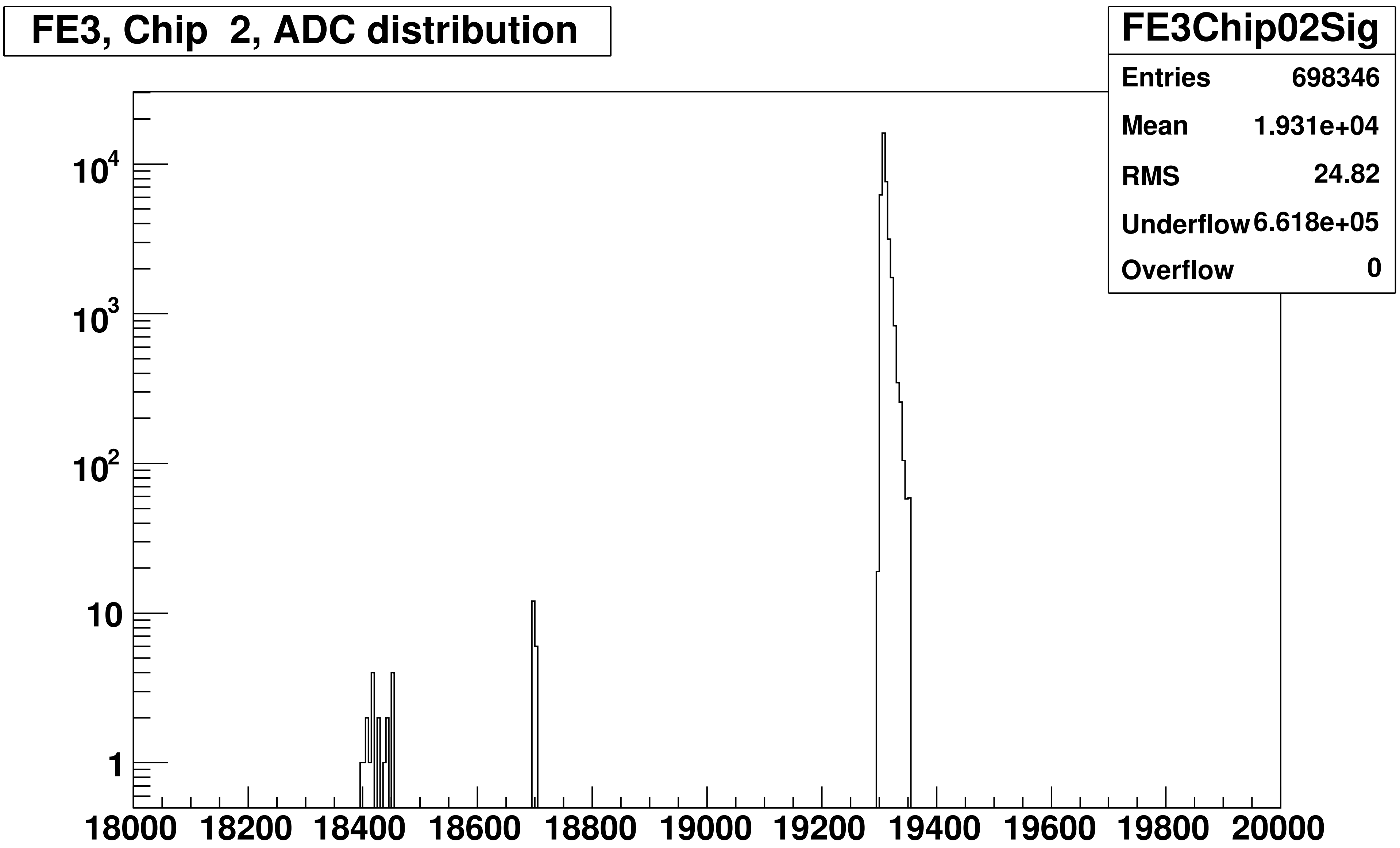
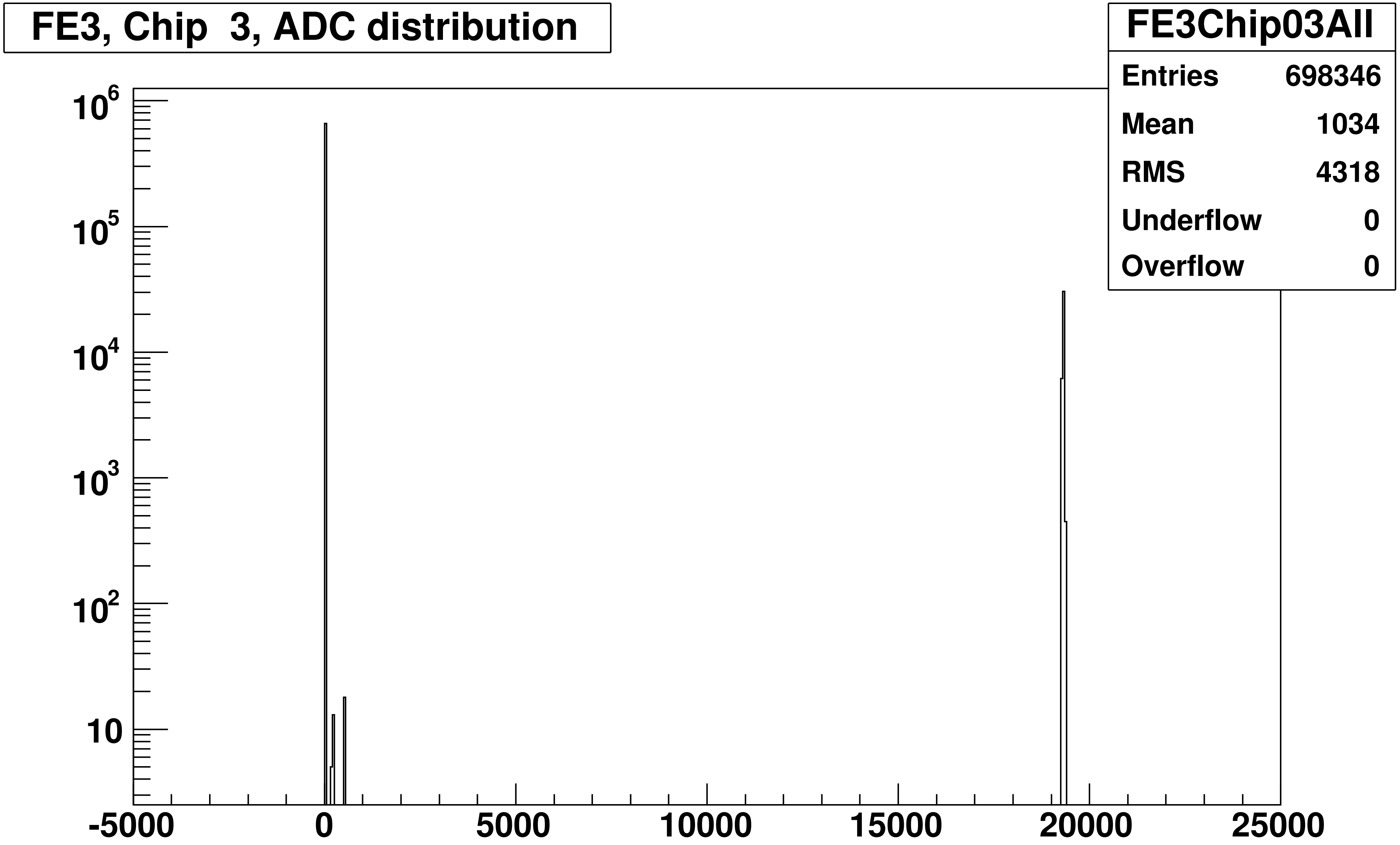
100
150
200


FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 698346
Mean 1.931e+04
RMS 24.82
Underflow 6.618e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 698346
Mean 1034
RMS 4318
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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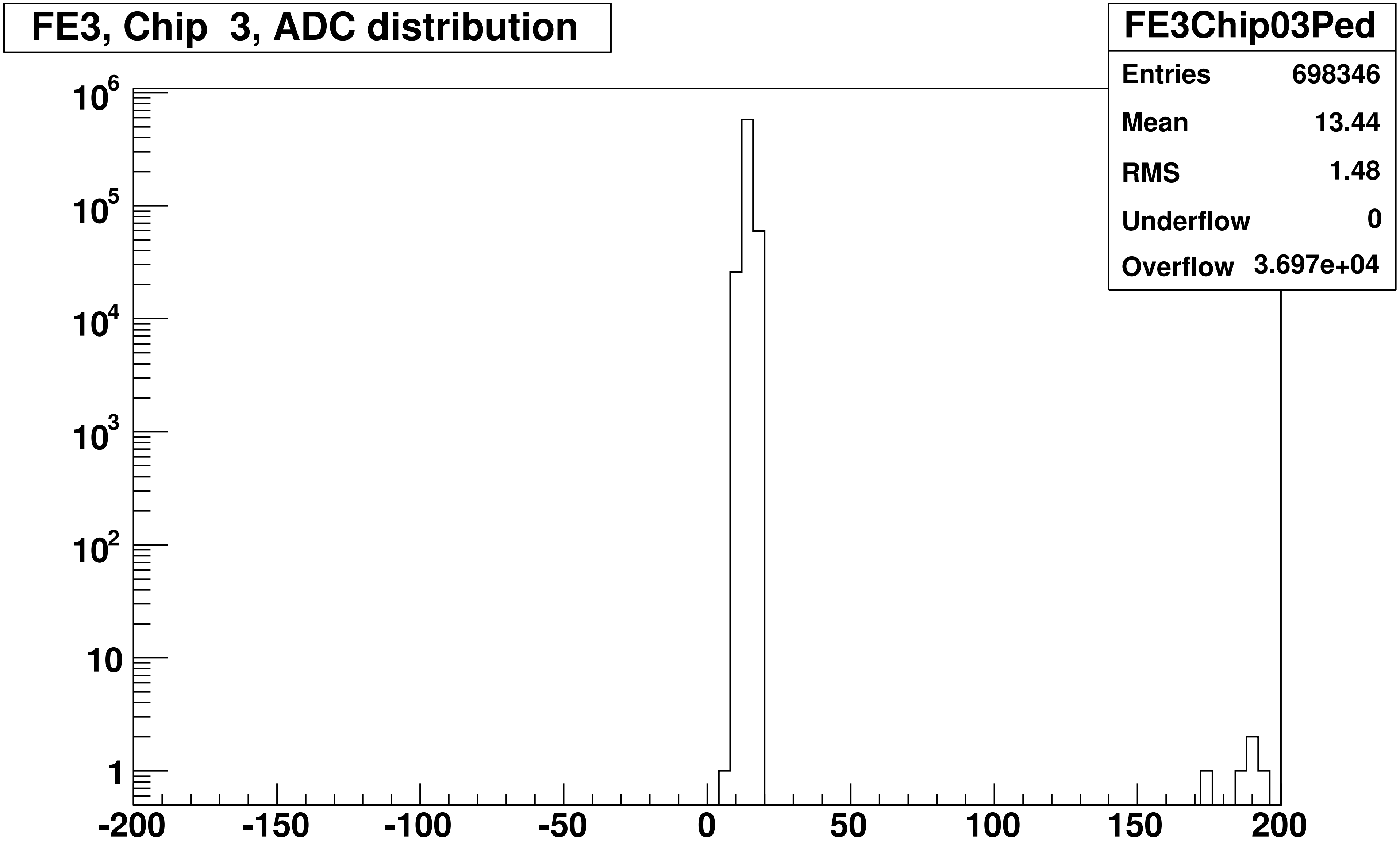
25000



FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 698346
Mean 13.44
RMS 1.48
Underflow 0
Overflow 3.697e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
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-100
-50
0
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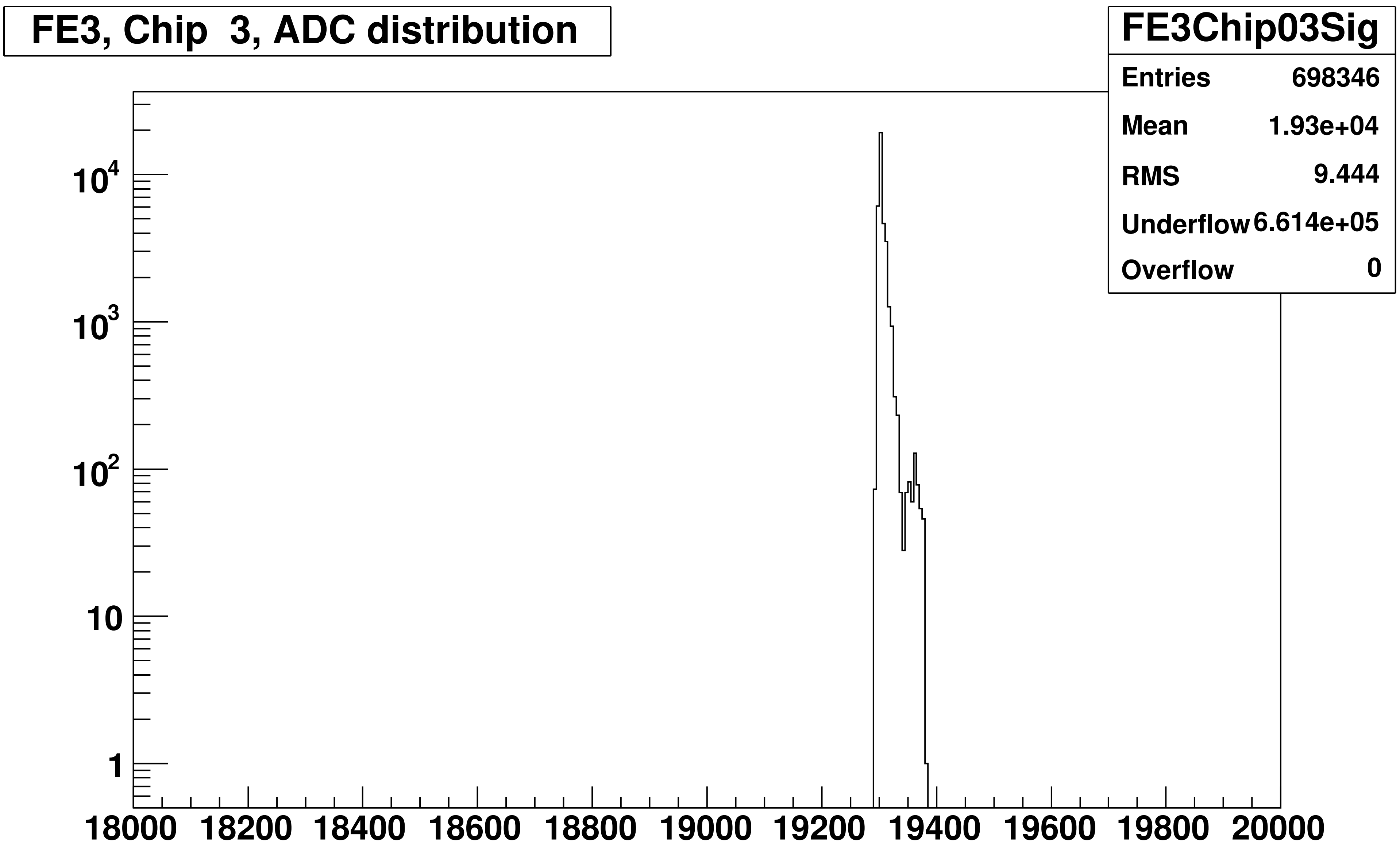
100
150
200



FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 698346
Mean 1.93e+04
RMS 9.444
Underflow 6.614e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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19000
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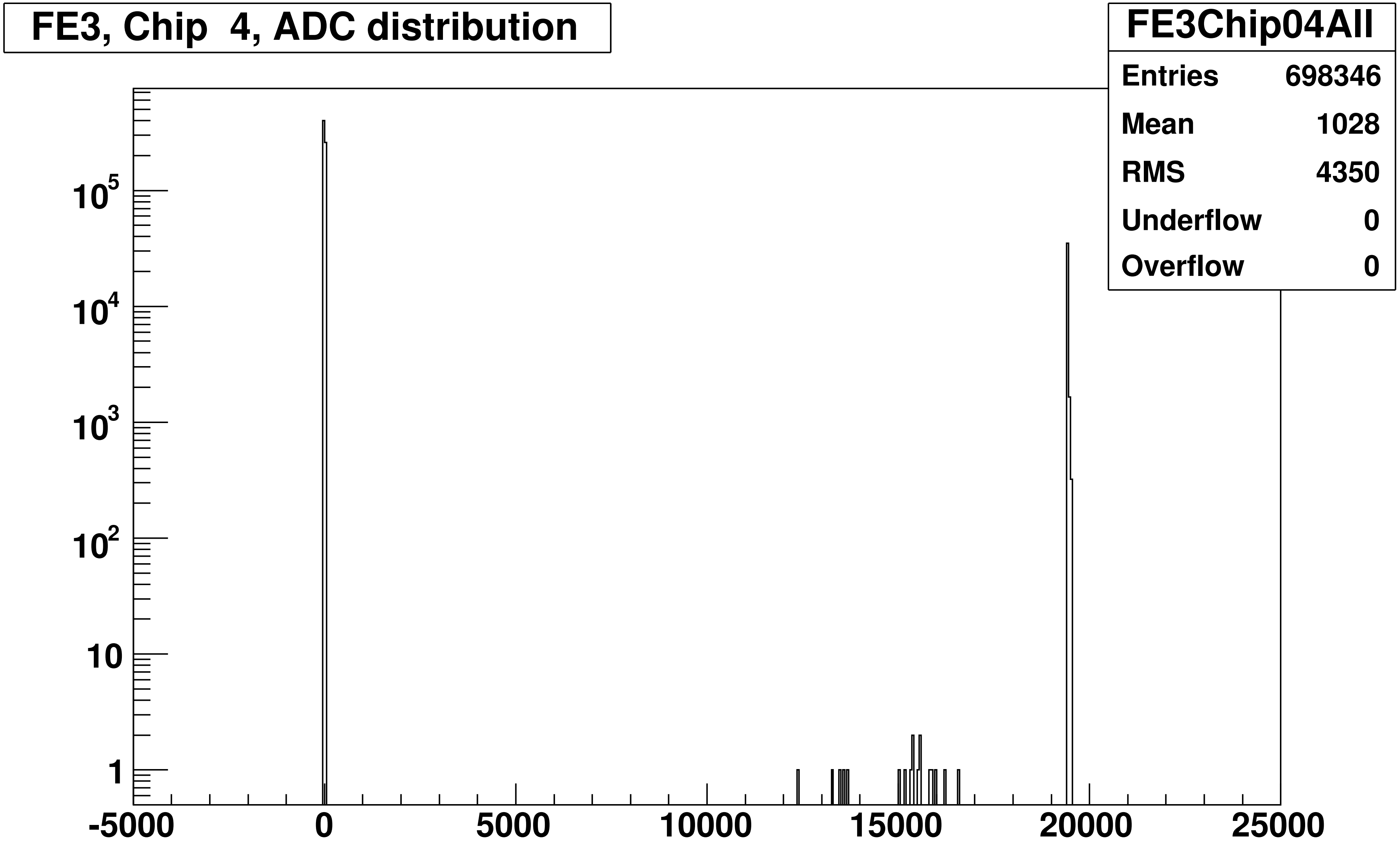
19800
20000


FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 698346
Mean 1028
RMS 4350
Underflow 0
Overflow 0
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10
10^2
10^3
10^4
10^5
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15000
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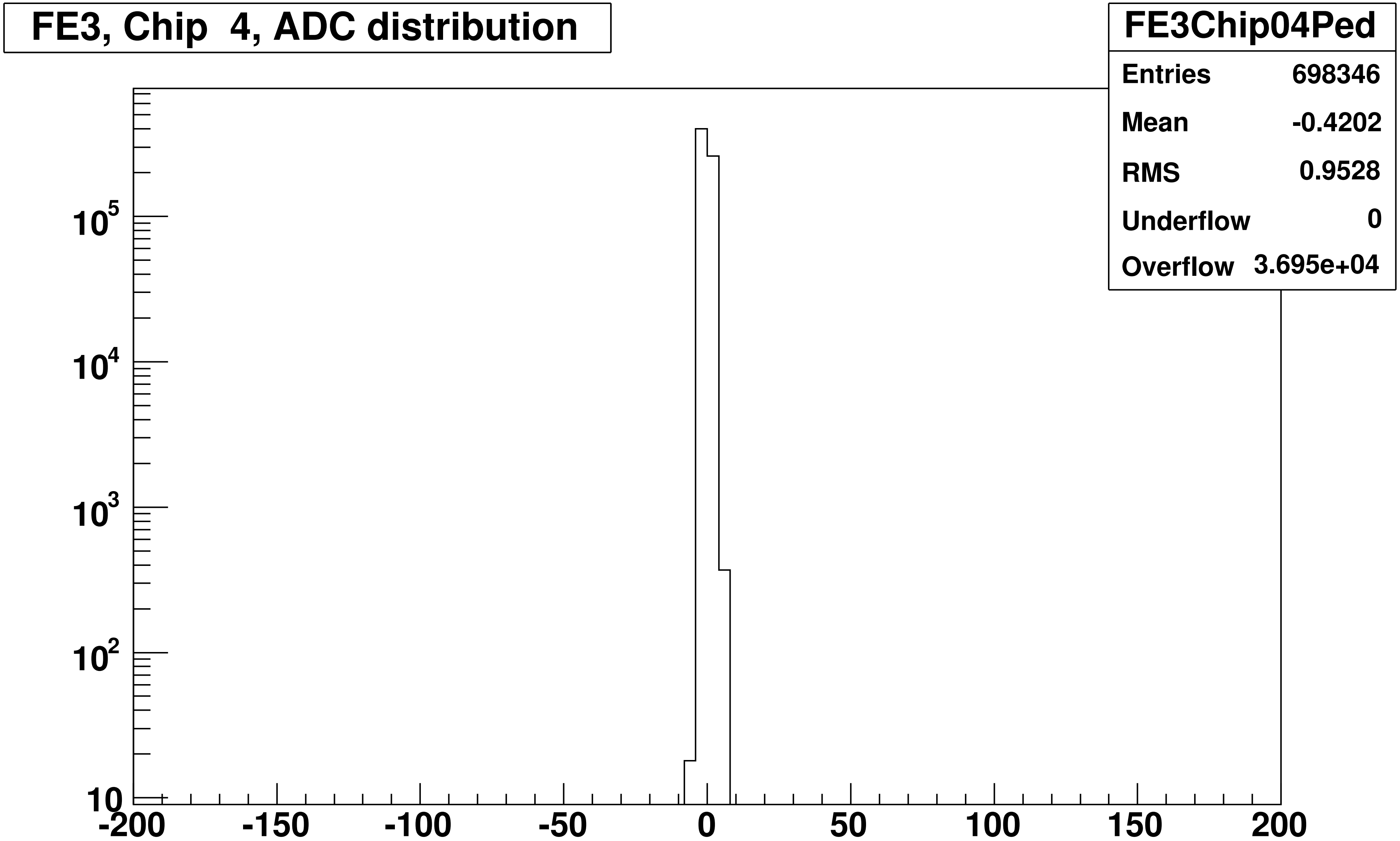
FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 698346
Mean -0.4202
RMS 0.9528
Underflow 0
Overflow 3.695e+04
10
10^2
10^3
10^4
10^5
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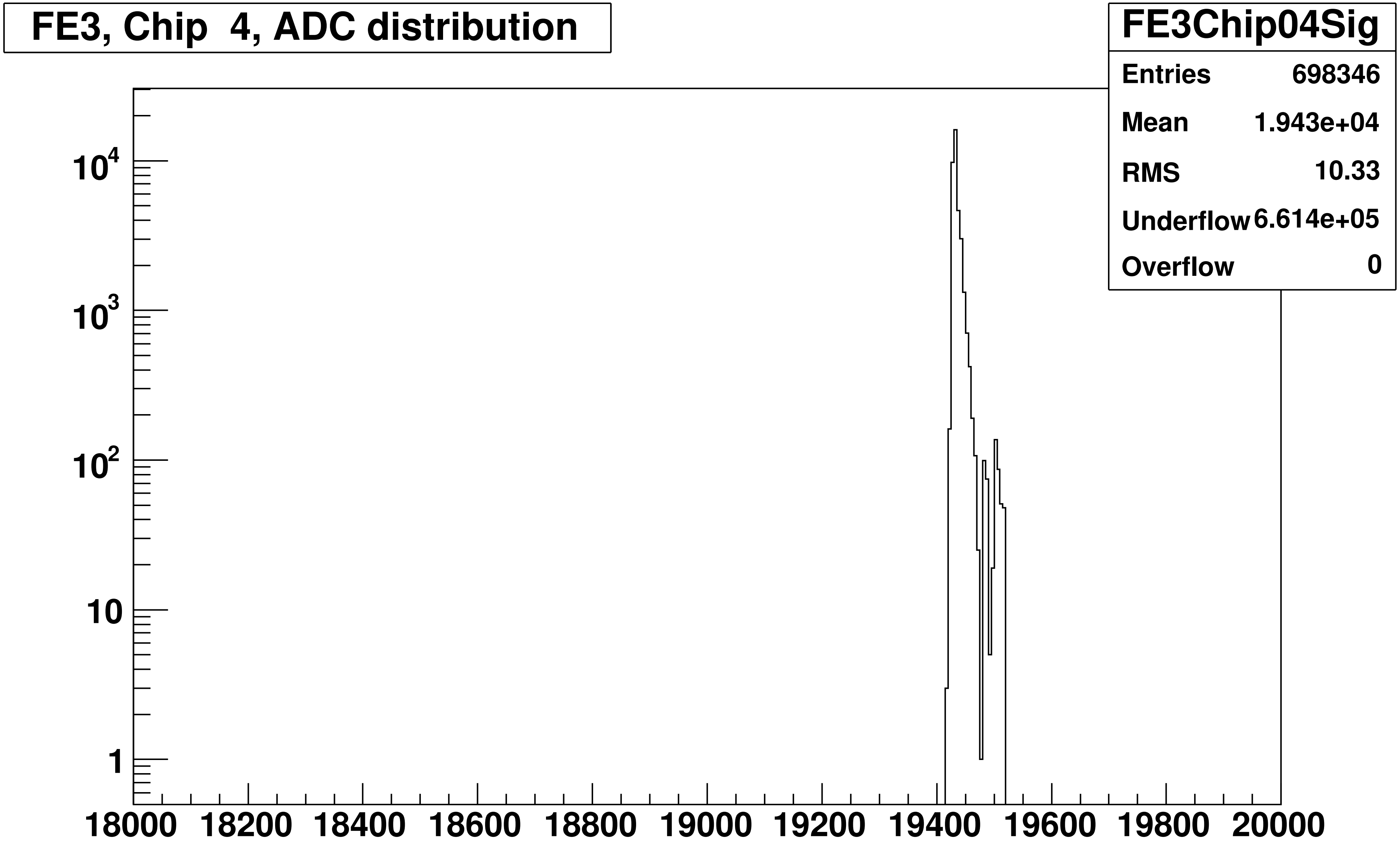
-150
-100
-50
0
50
100
150
200



FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 698346
Mean 1.943e+04
RMS 10.33
Underflow 6.614e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
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18600
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19000
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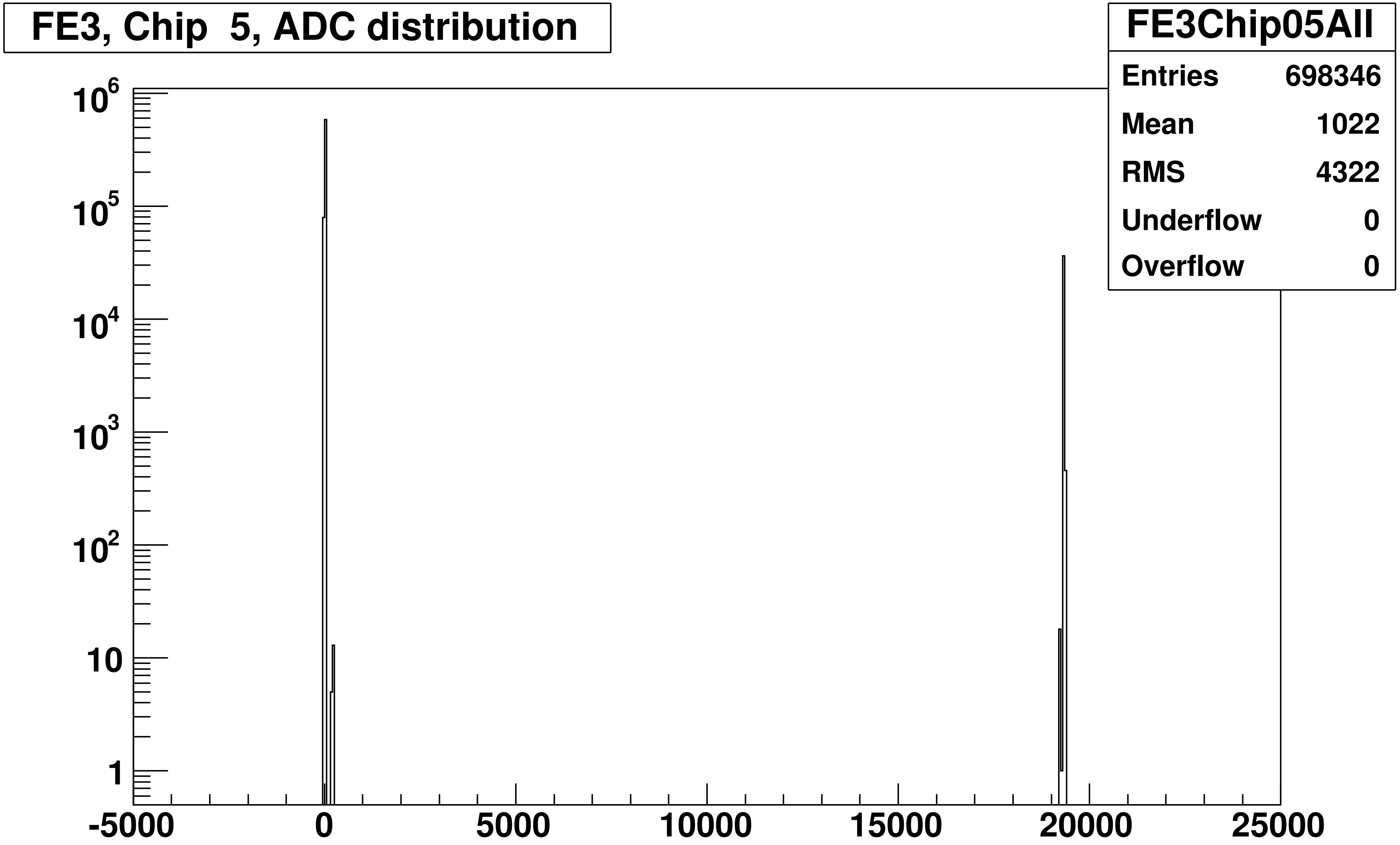
FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 698346
Mean 1022
RMS 4322
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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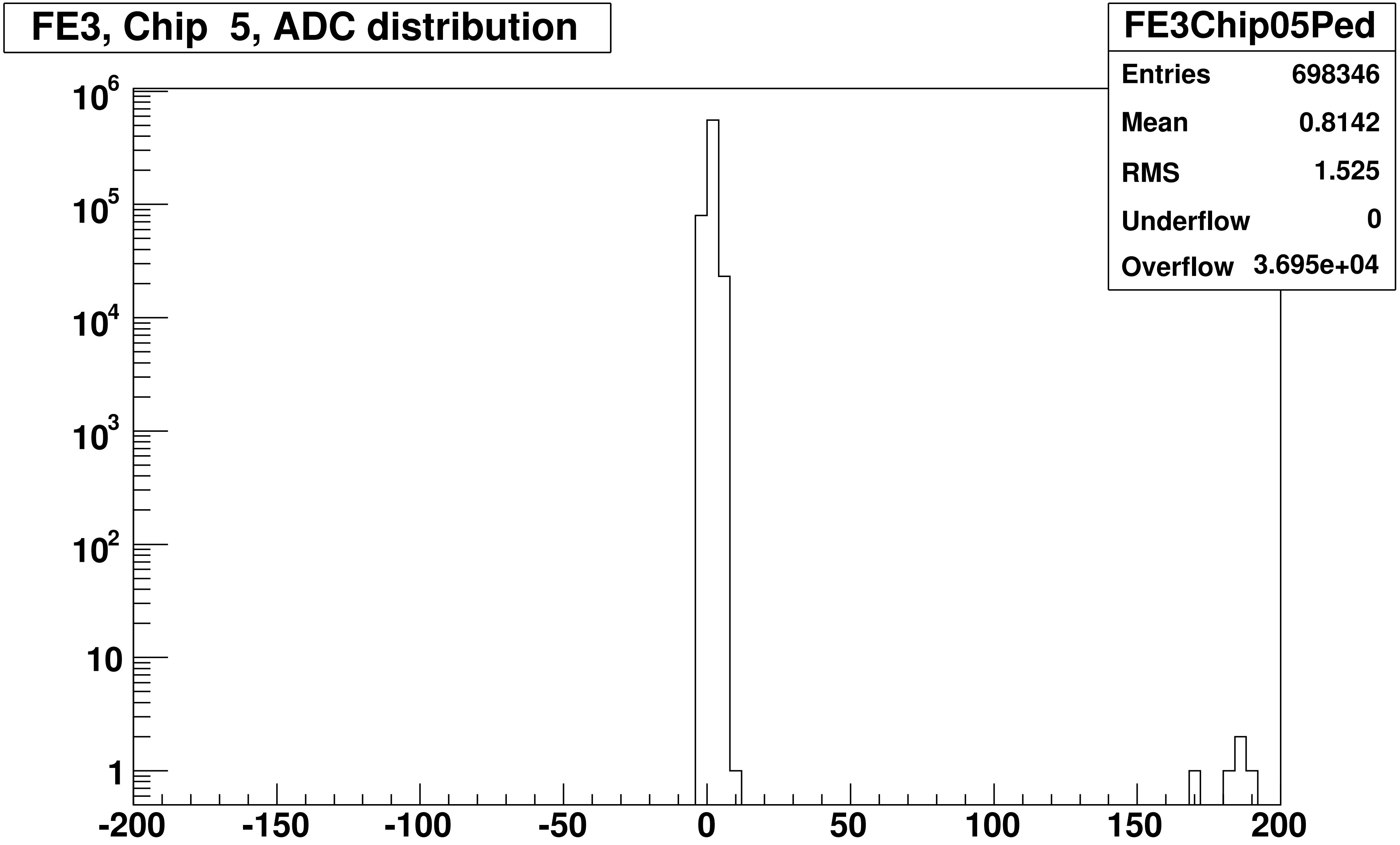
FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 698346
Mean 0.8142
RMS 1.525
Underflow 0
Overflow 3.695e+04
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10^5
10^4
10^3
10^2
10
1
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100
150
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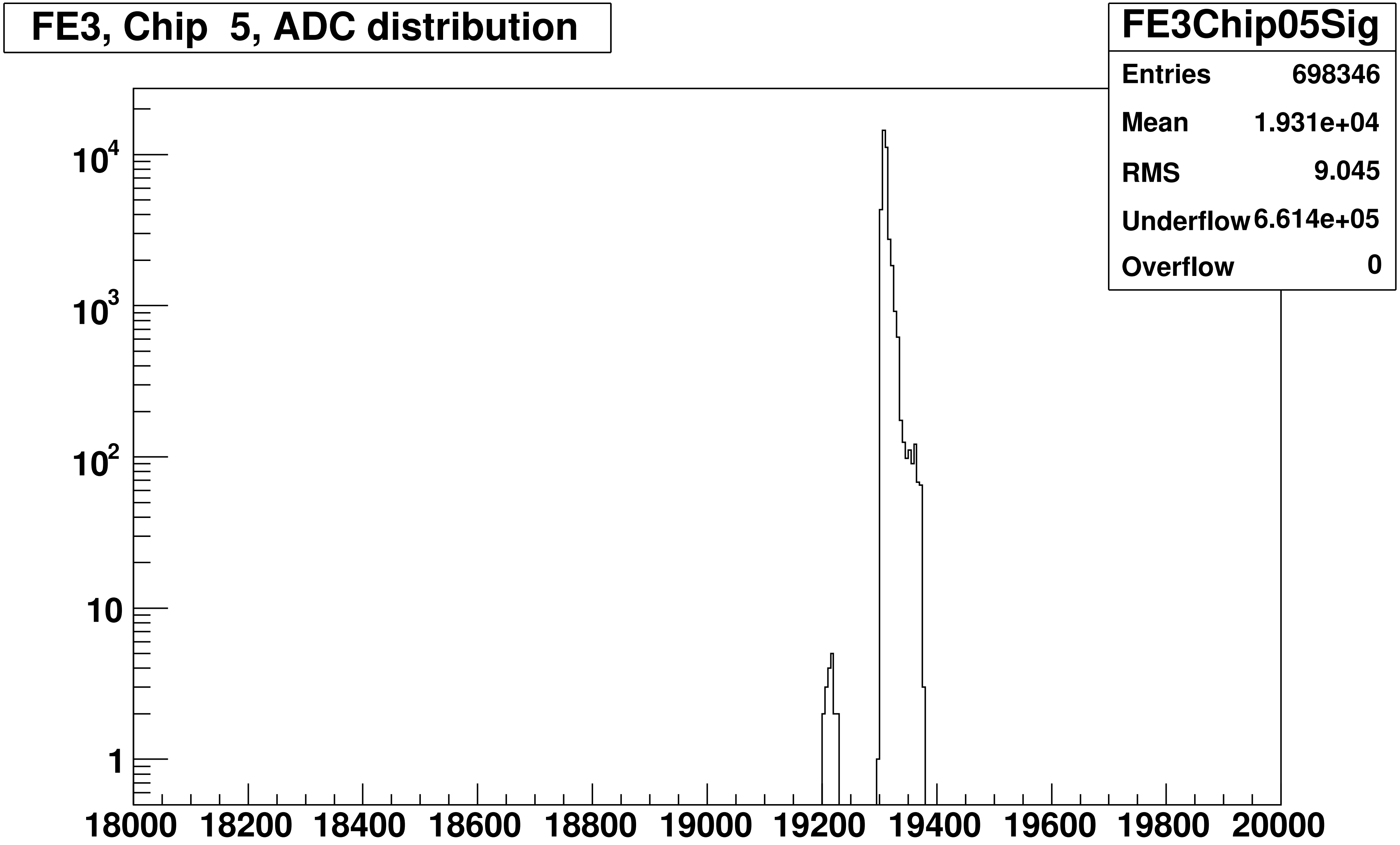
FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 698346
Mean 1.931e+04
RMS 9.045
Underflow 6.614e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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19000
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19400
19600
19800
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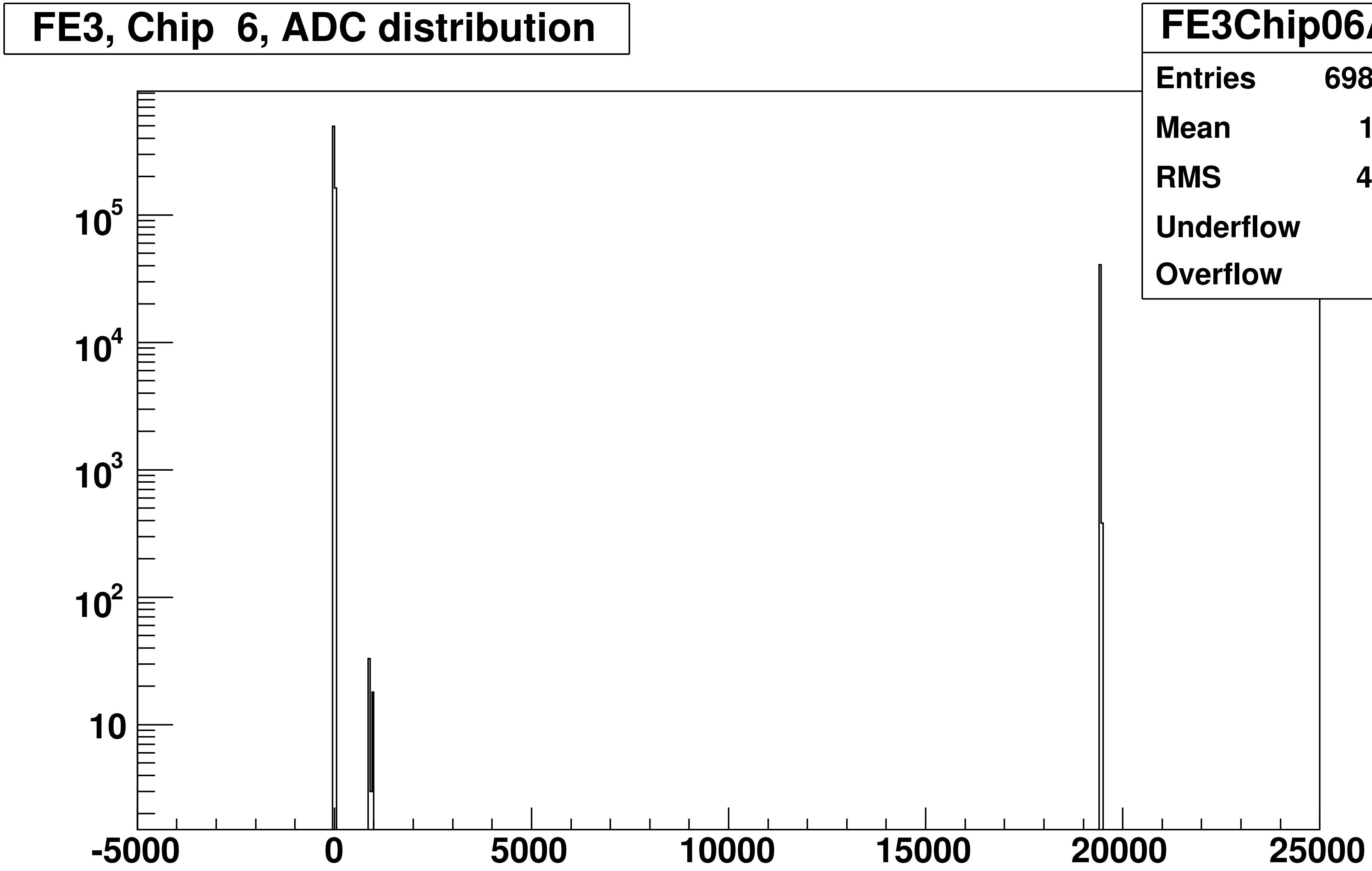
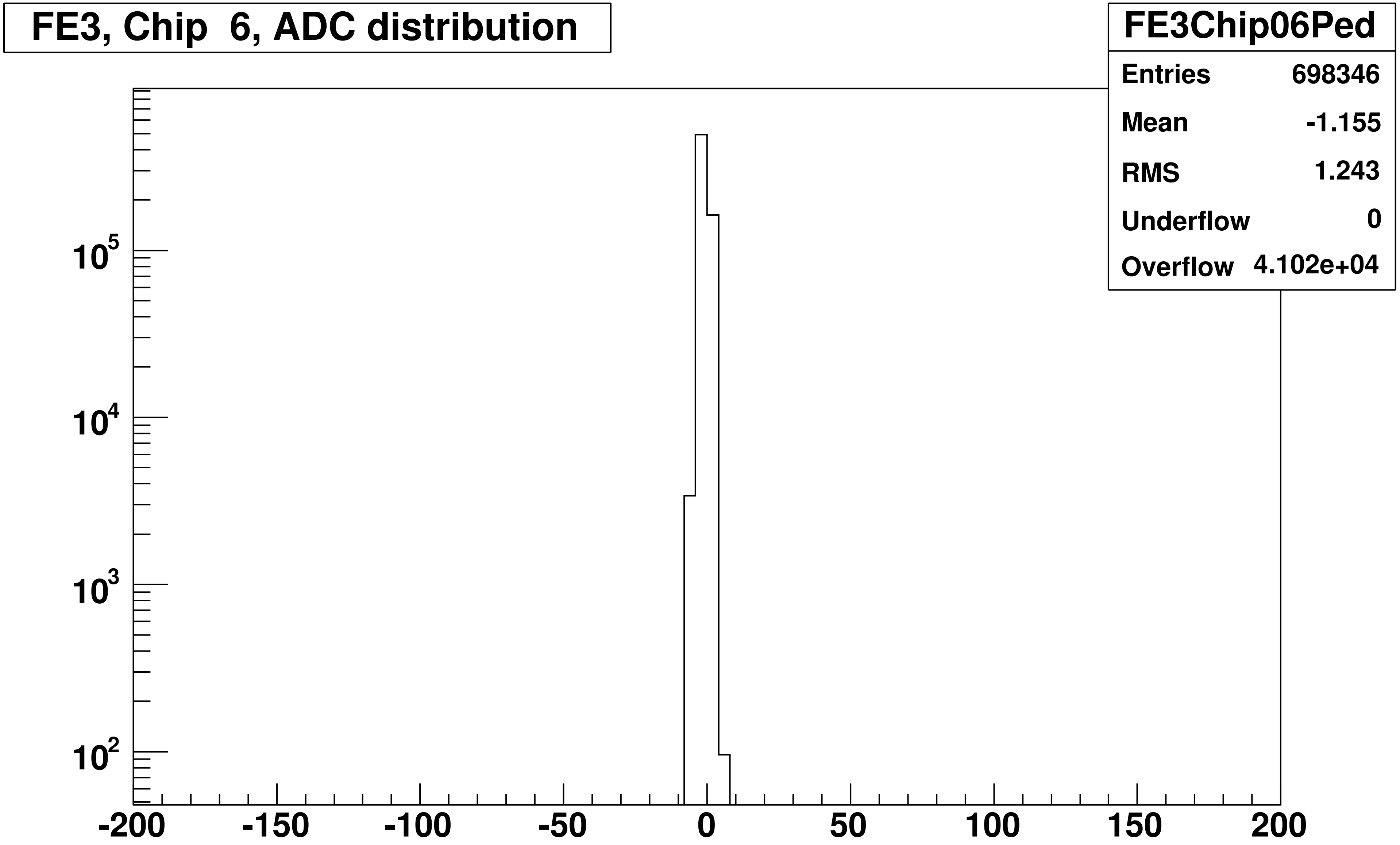

FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 698346
Mean 1138
RMS 4564
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 698346
Mean -1.155
RMS 1.243
Underflow 0
Overflow 4.102e+04
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10^4
10^3
10^2
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-100
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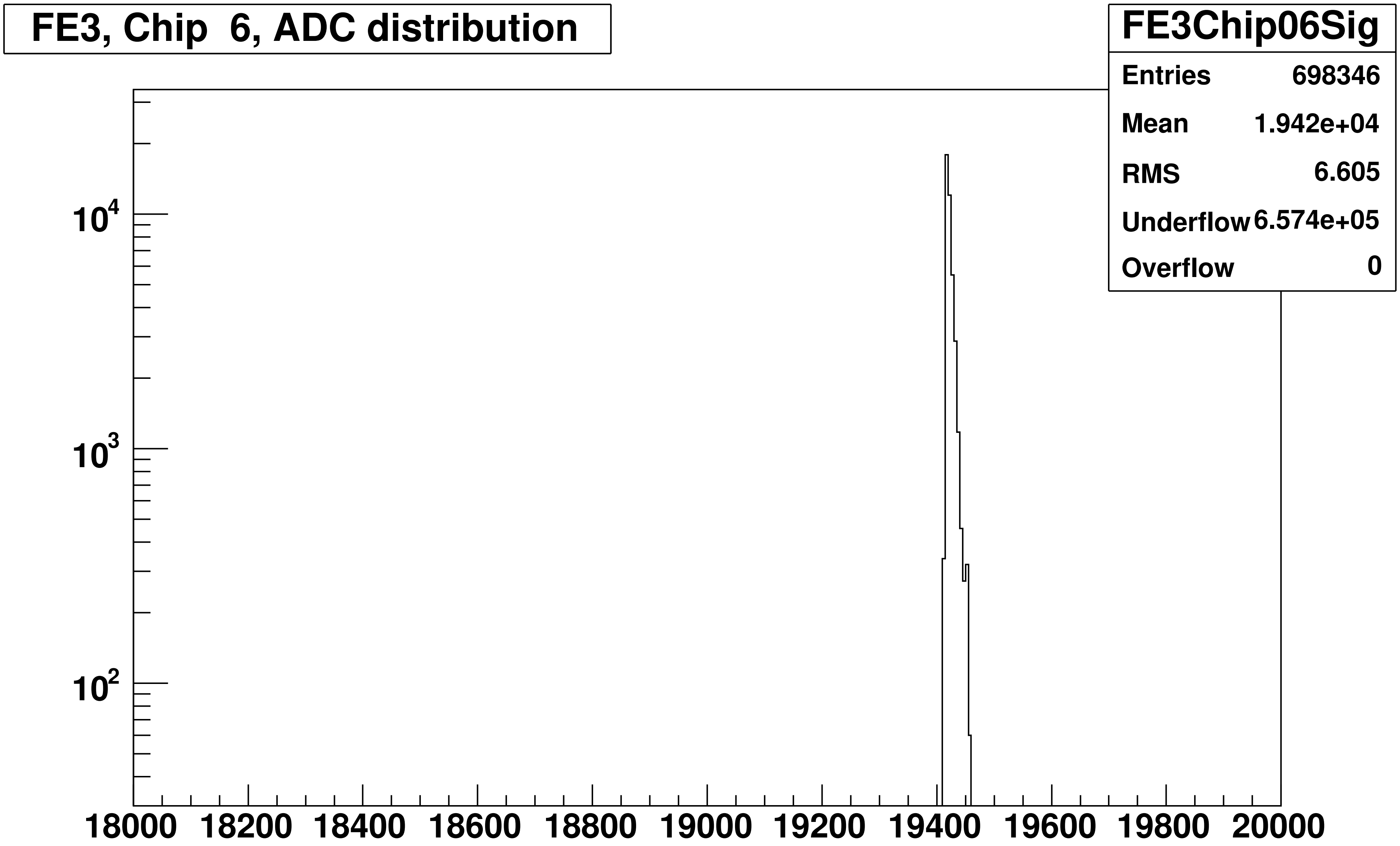
0
50
100
150
200


FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 698346
Mean 1.942e+04
RMS 6.605
Underflow 6.574e+05
Overflow 0
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10^3
10^2
18000
18200
18400
18600
18800
19000
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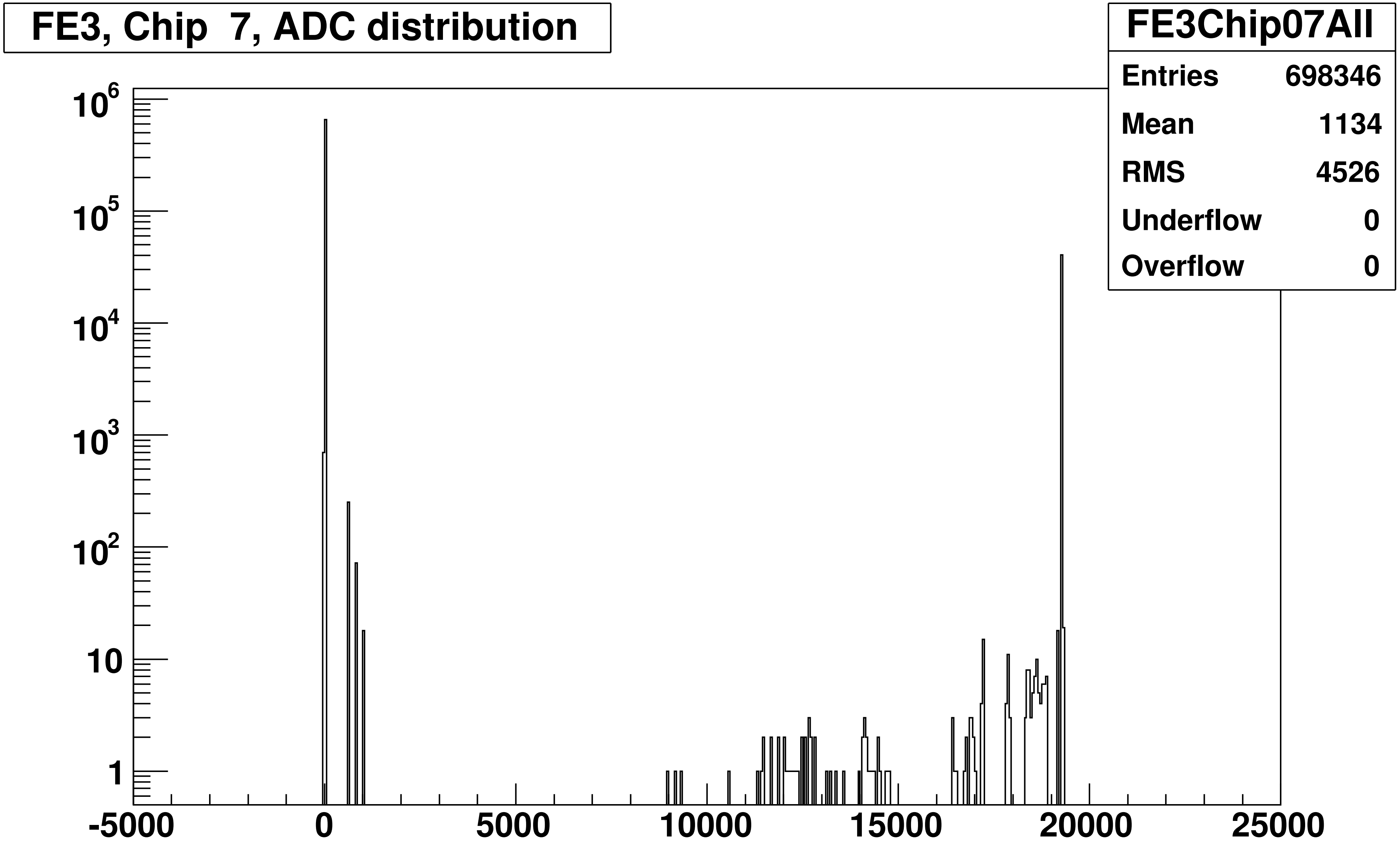
FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 698346
Mean 1134
RMS 4526
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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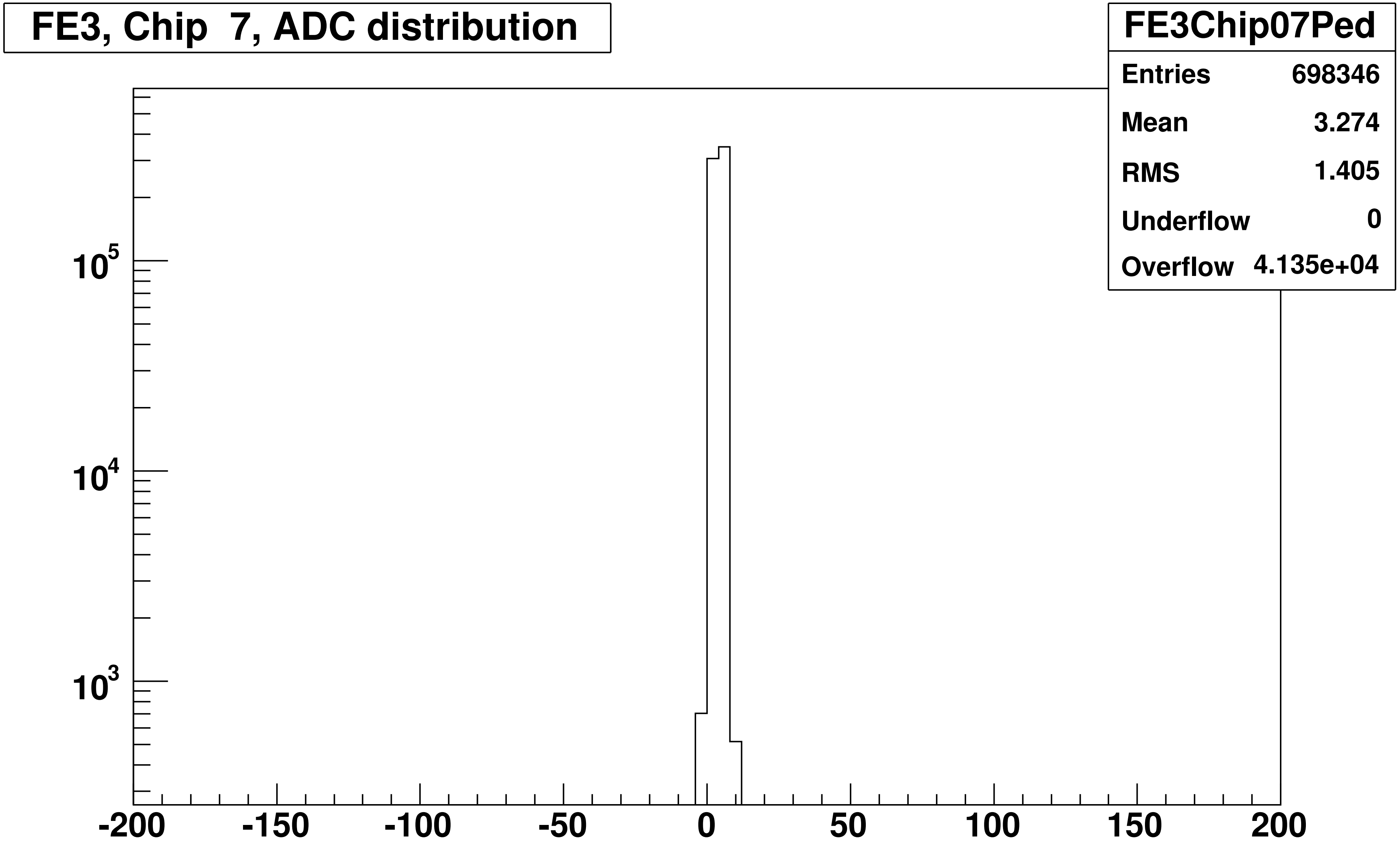
FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 698346
Mean 3.274
RMS 1.405
Underflow 0
Overflow 4.135e+04
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10^4
10^3
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-100
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0
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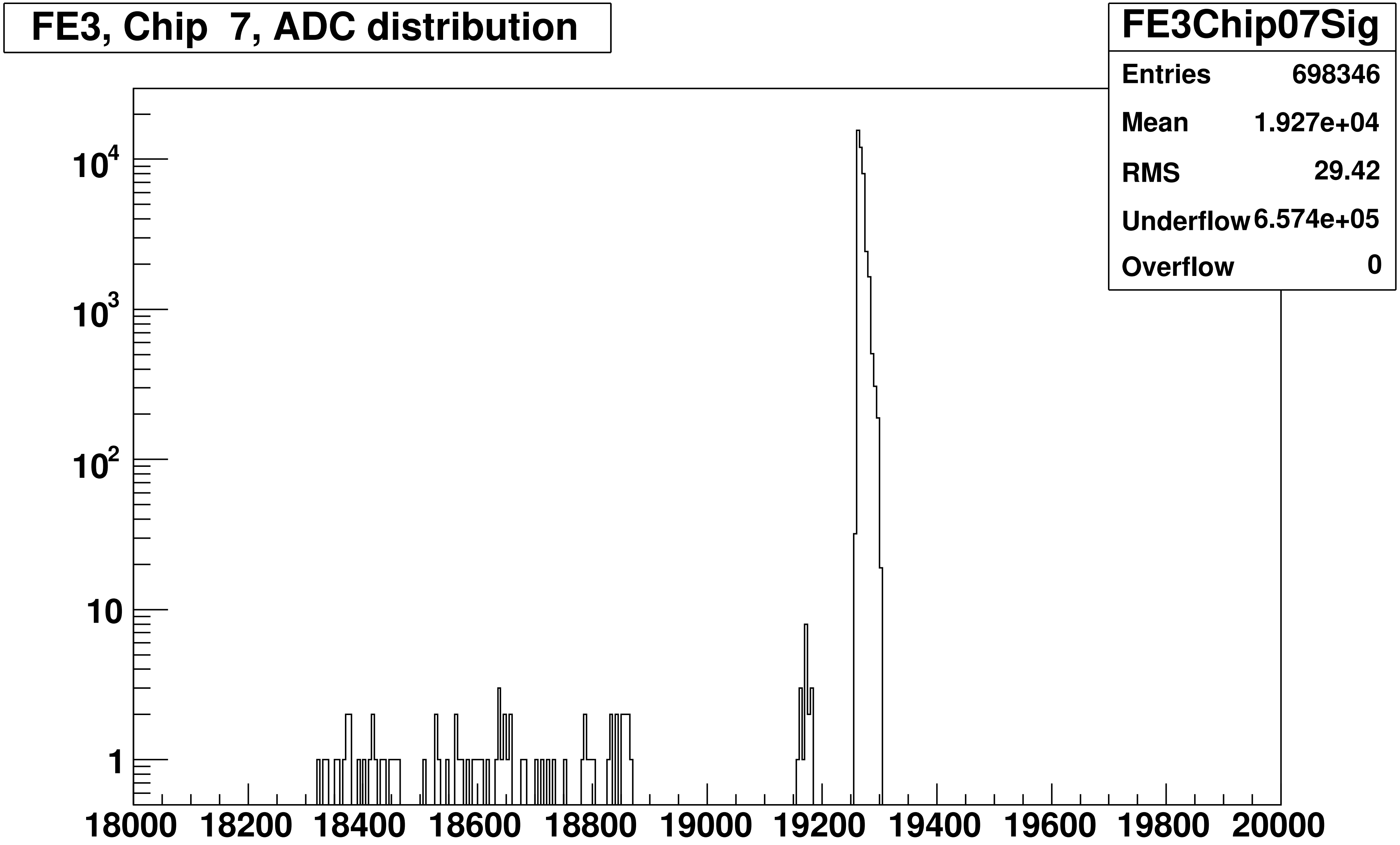
100
150
200



FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 698346
Mean 1.927e+04
RMS 29.42
Underflow 6.574e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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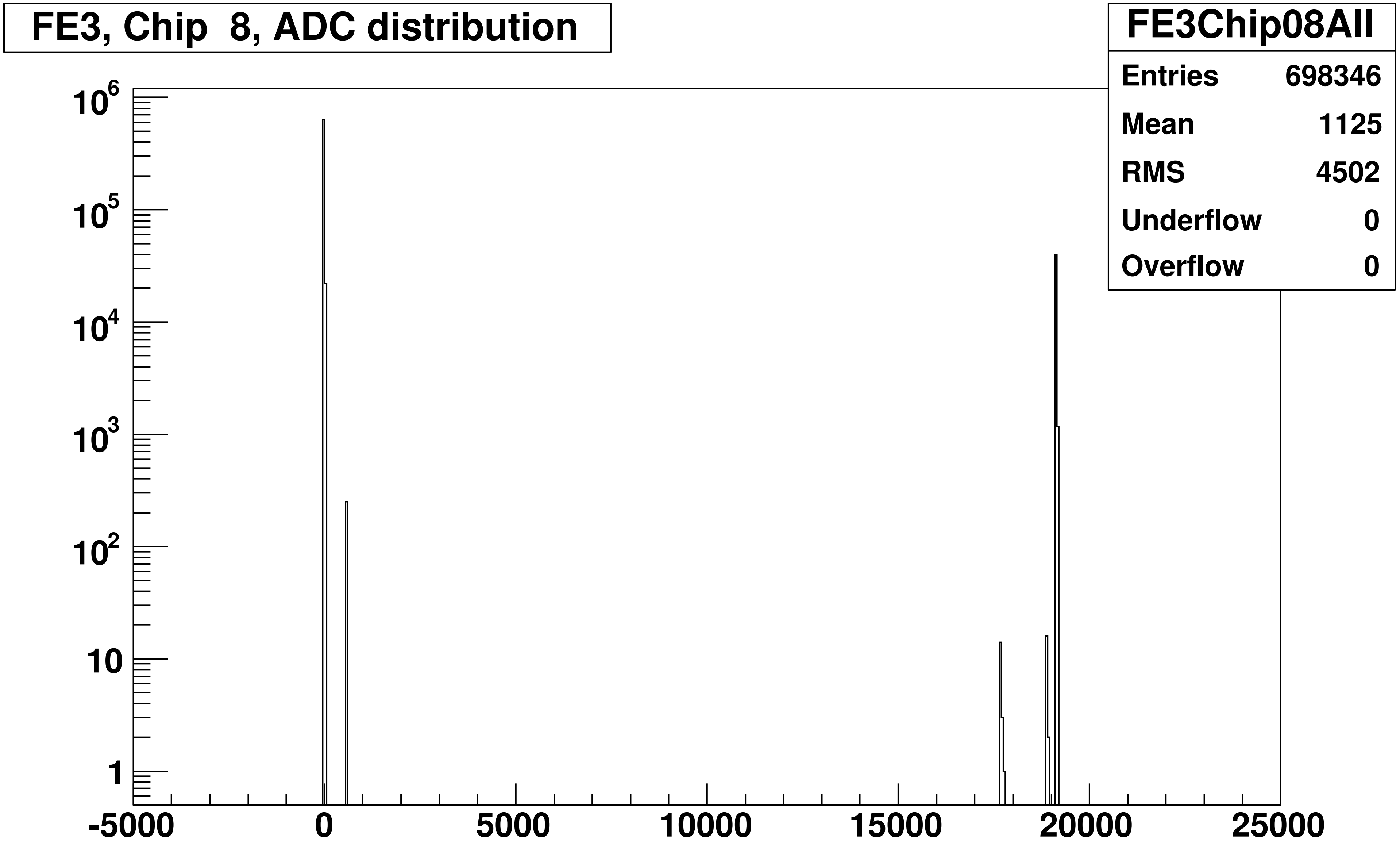
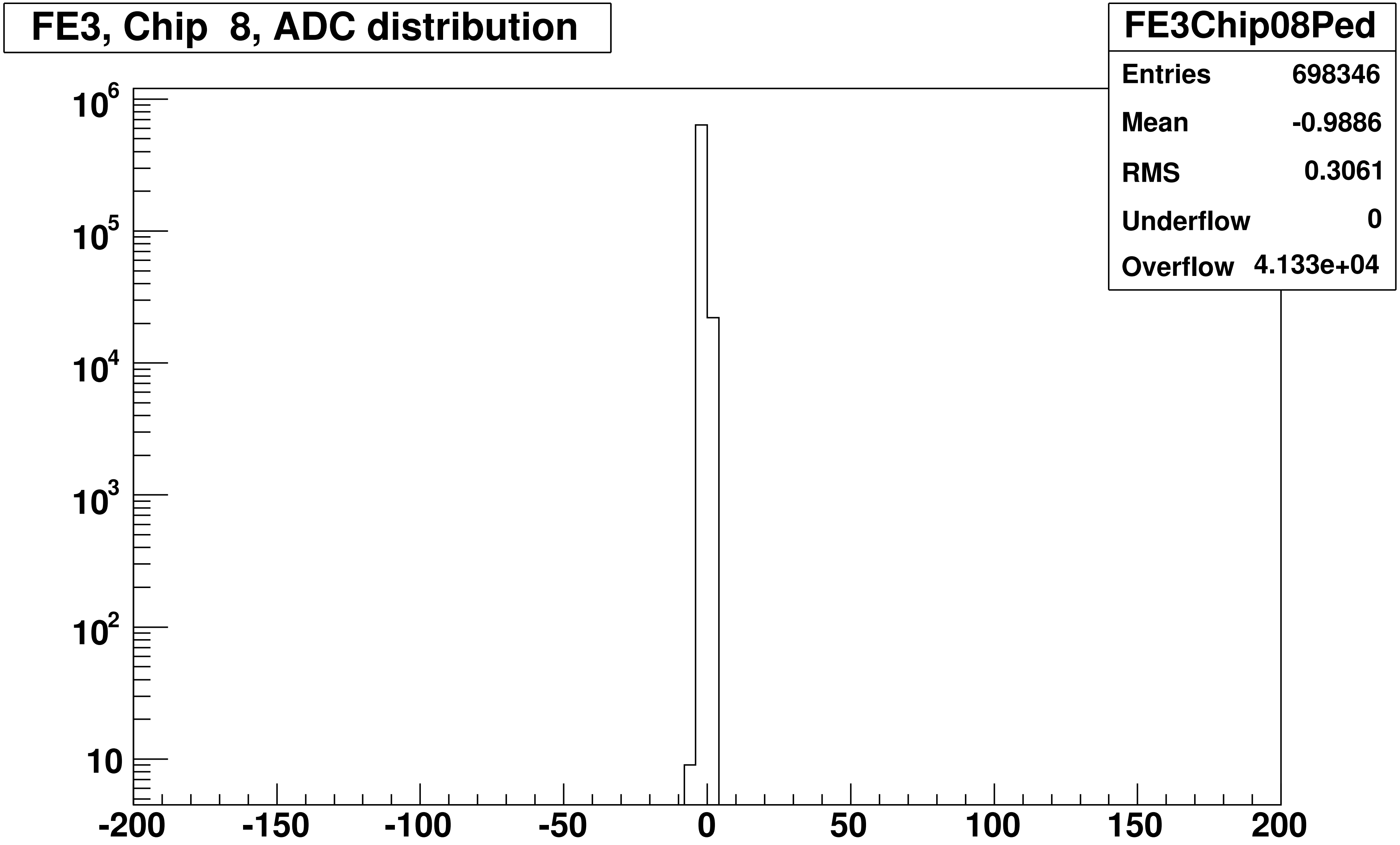
19400
19600
19800
20000



FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 698346
Mean 1125
RMS 4502
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 698346
Mean -0.9886
RMS 0.3061
Underflow 0
Overflow 4.133e+04
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10^5
10^4
10^3
10^2
10
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-100
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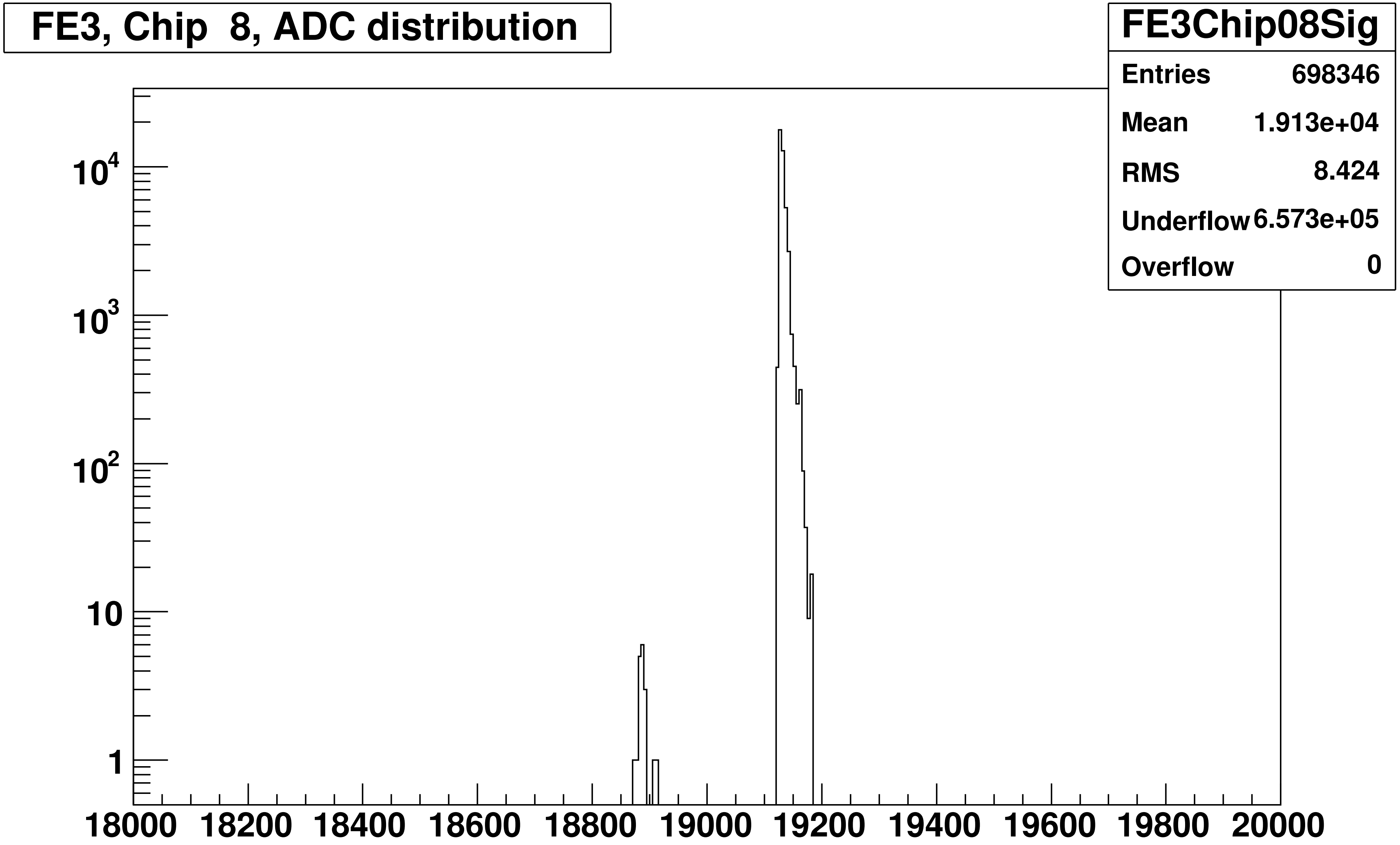
0
50
100
150
200



FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 698346
Mean 1.913e+04
RMS 8.424
Underflow 6.573e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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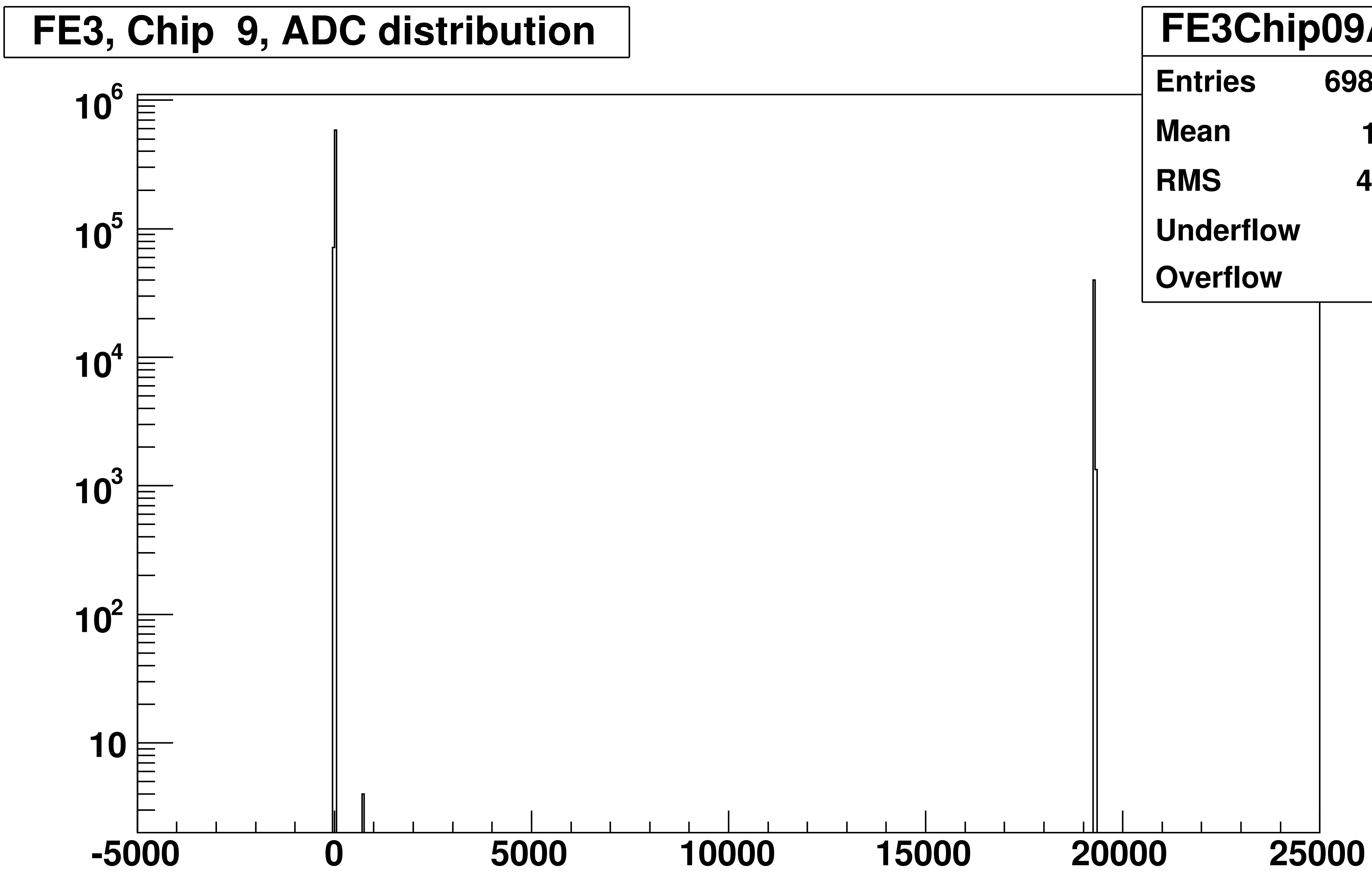
19600
19800
20000



FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 698346
Mean 1141
RMS 4549
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
5000
10000
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20000
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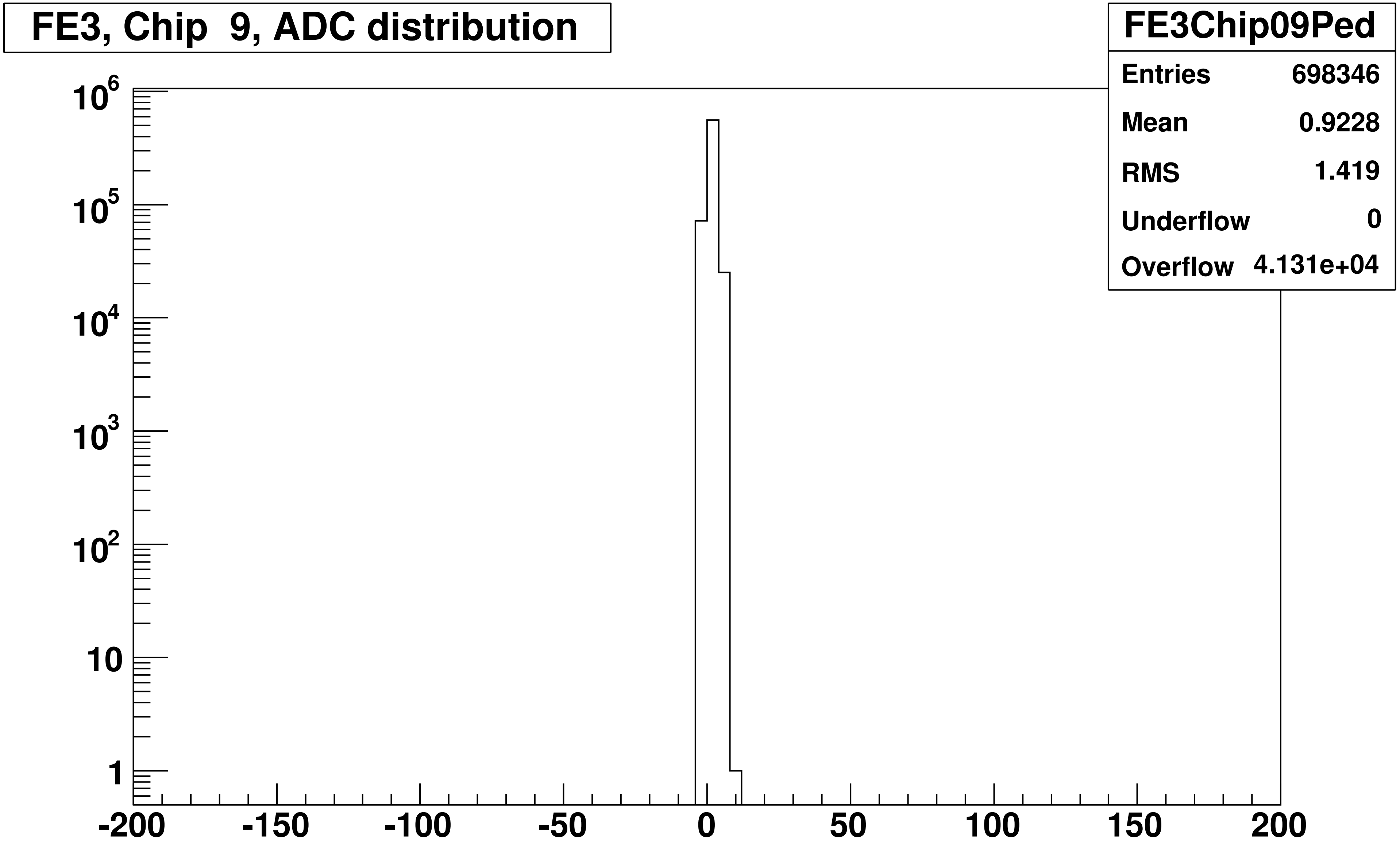

FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 698346
Mean 0.9228
RMS 1.419
Underflow 0
Overflow 4.131e+04
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10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
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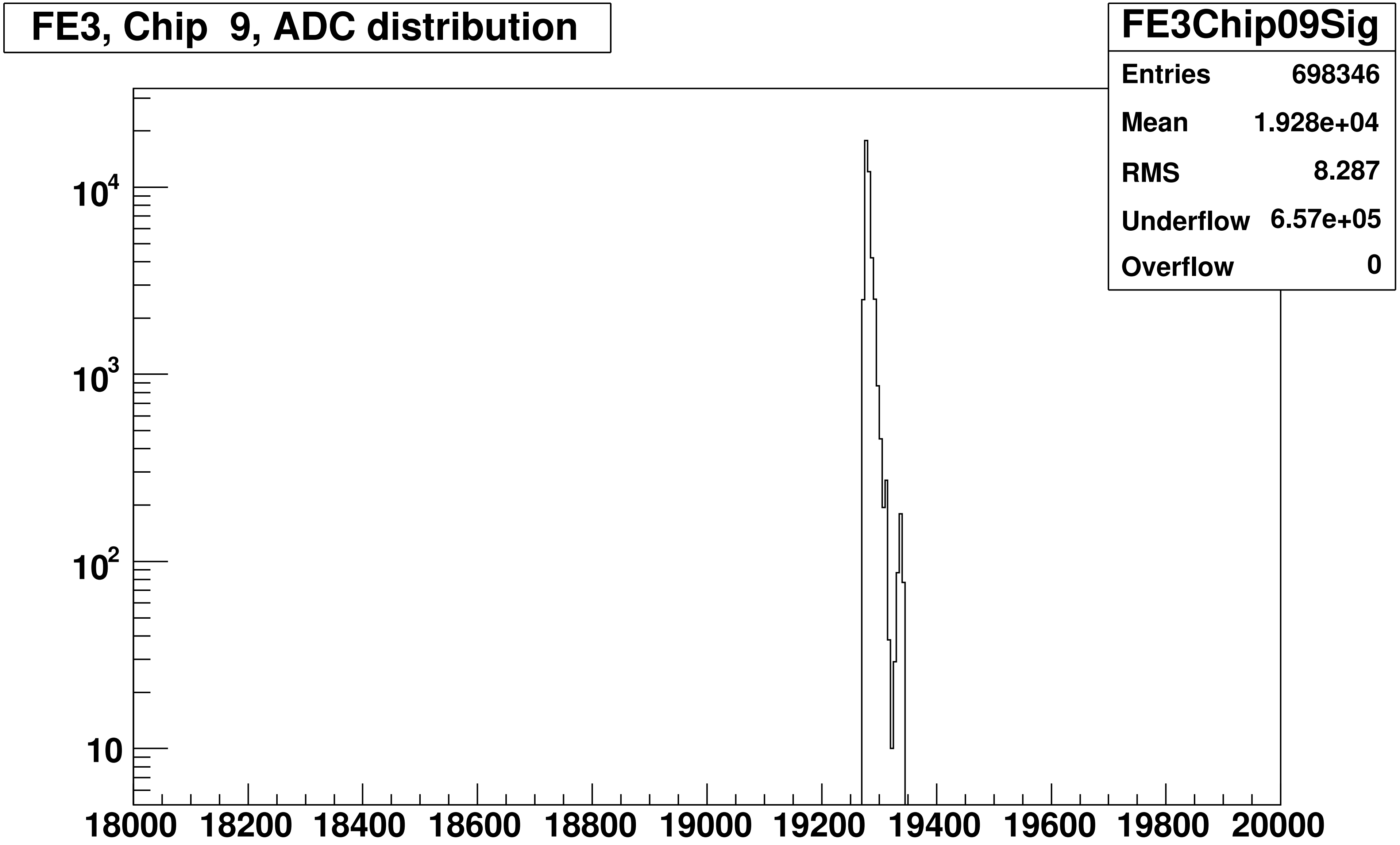
100
150
200


FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 698346
Mean 1.928e+04
RMS 8.287
Underflow 6.57e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
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19000
19200
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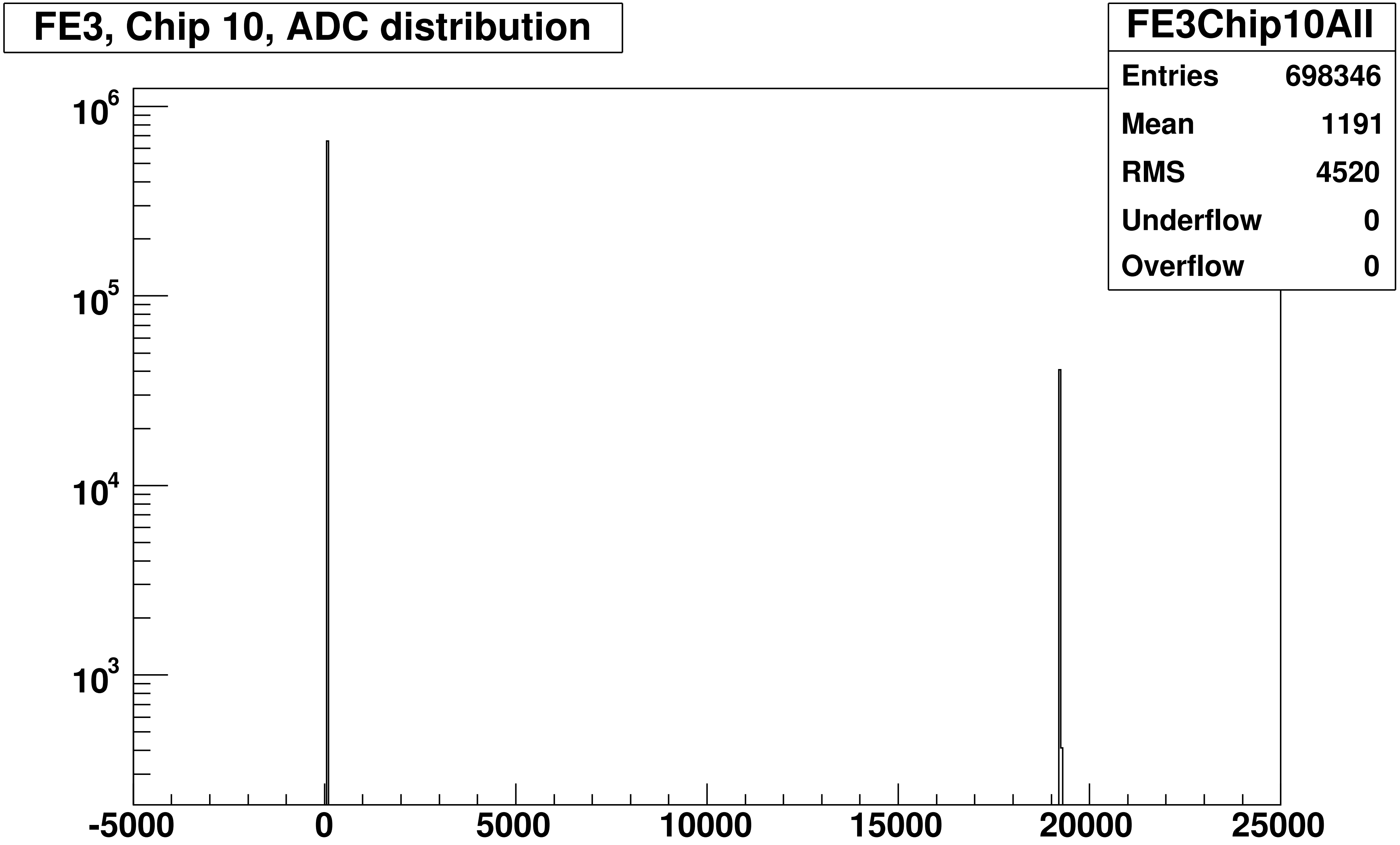
20000



FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 698346
Mean 1191
RMS 4520
Underflow 0
Overflow 0
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10^5
10^4
10^3
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0
5000
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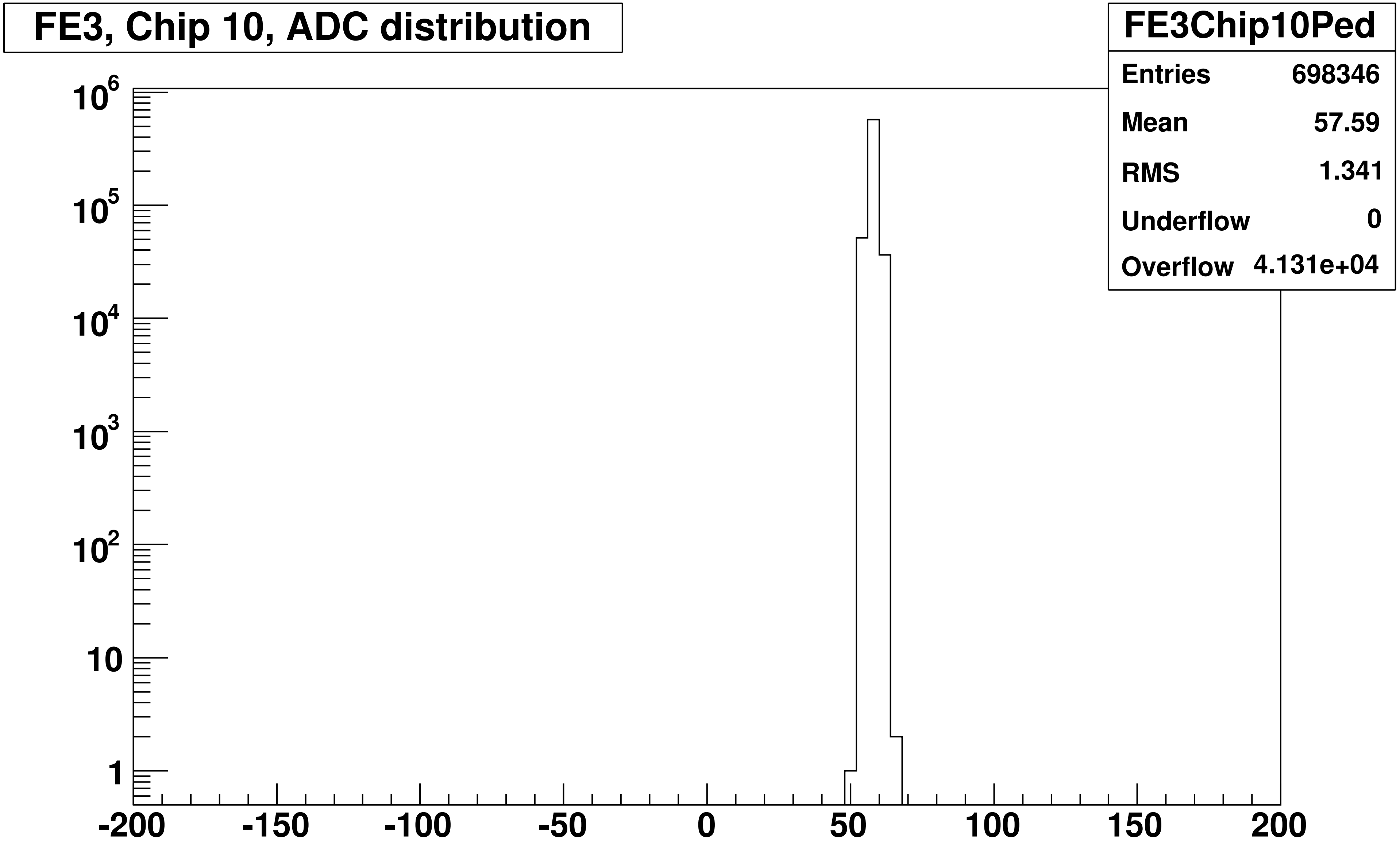
20000
25000


FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 698346
Mean 57.59
RMS 1.341
Underflow 0
Overflow 4.131e+04
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10^5
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10^3
10^2
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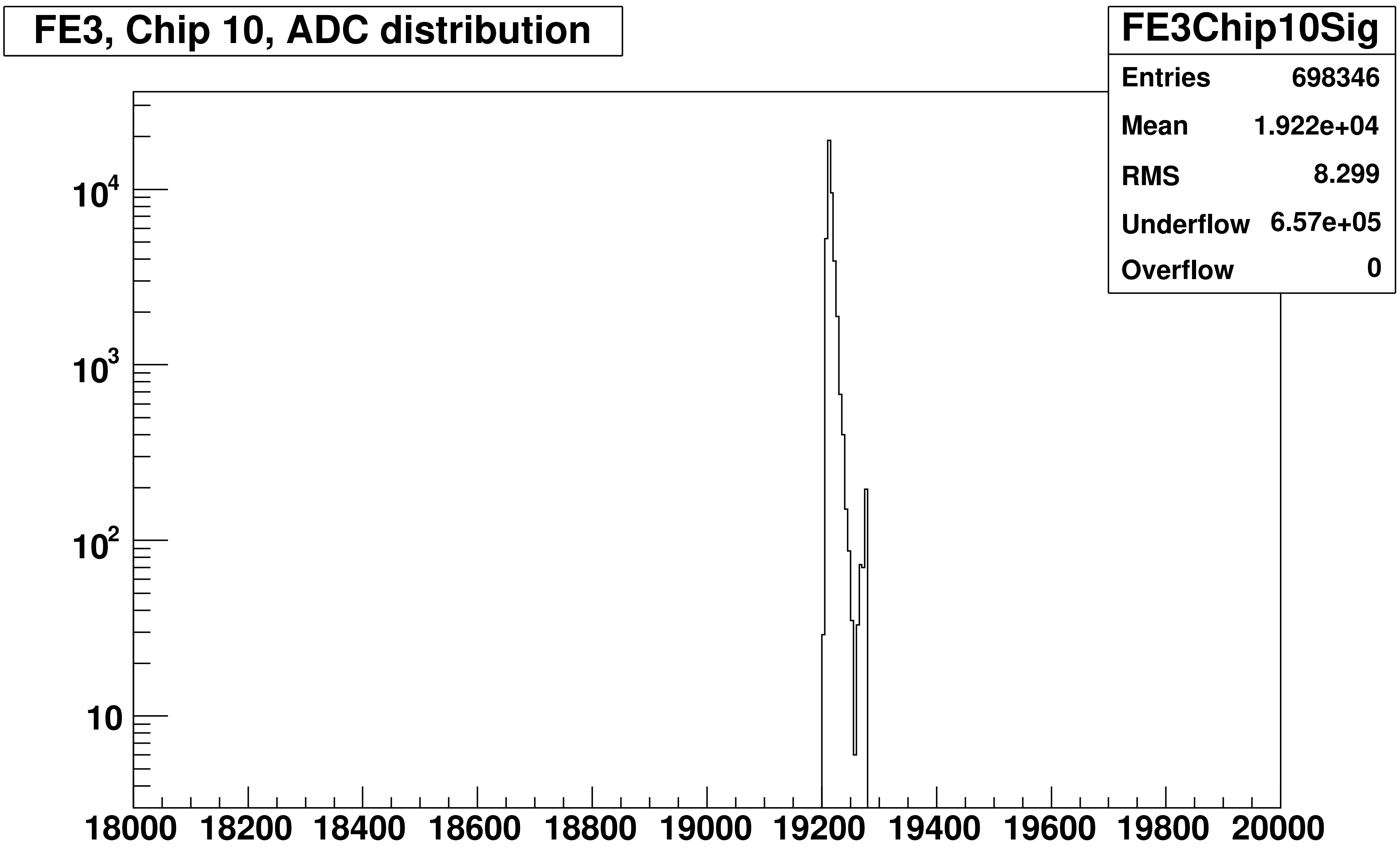
FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 698346
Mean 1.922e+04
RMS 8.299
Underflow 6.57e+05
Overflow 0
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10^3
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10
18000
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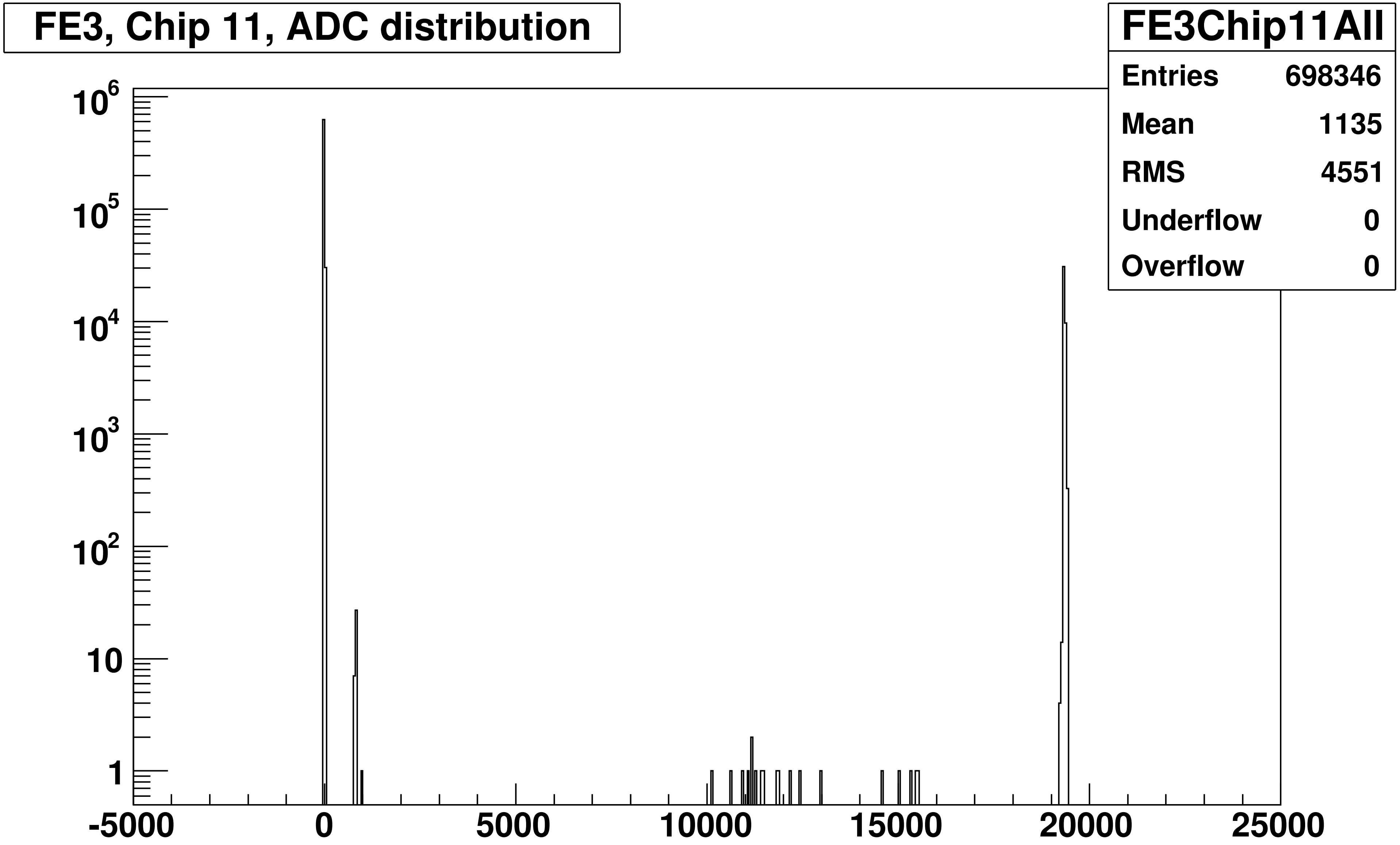
FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 698346
Mean 1135
RMS 4551
Underflow 0
Overflow 0
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10^3
10^2
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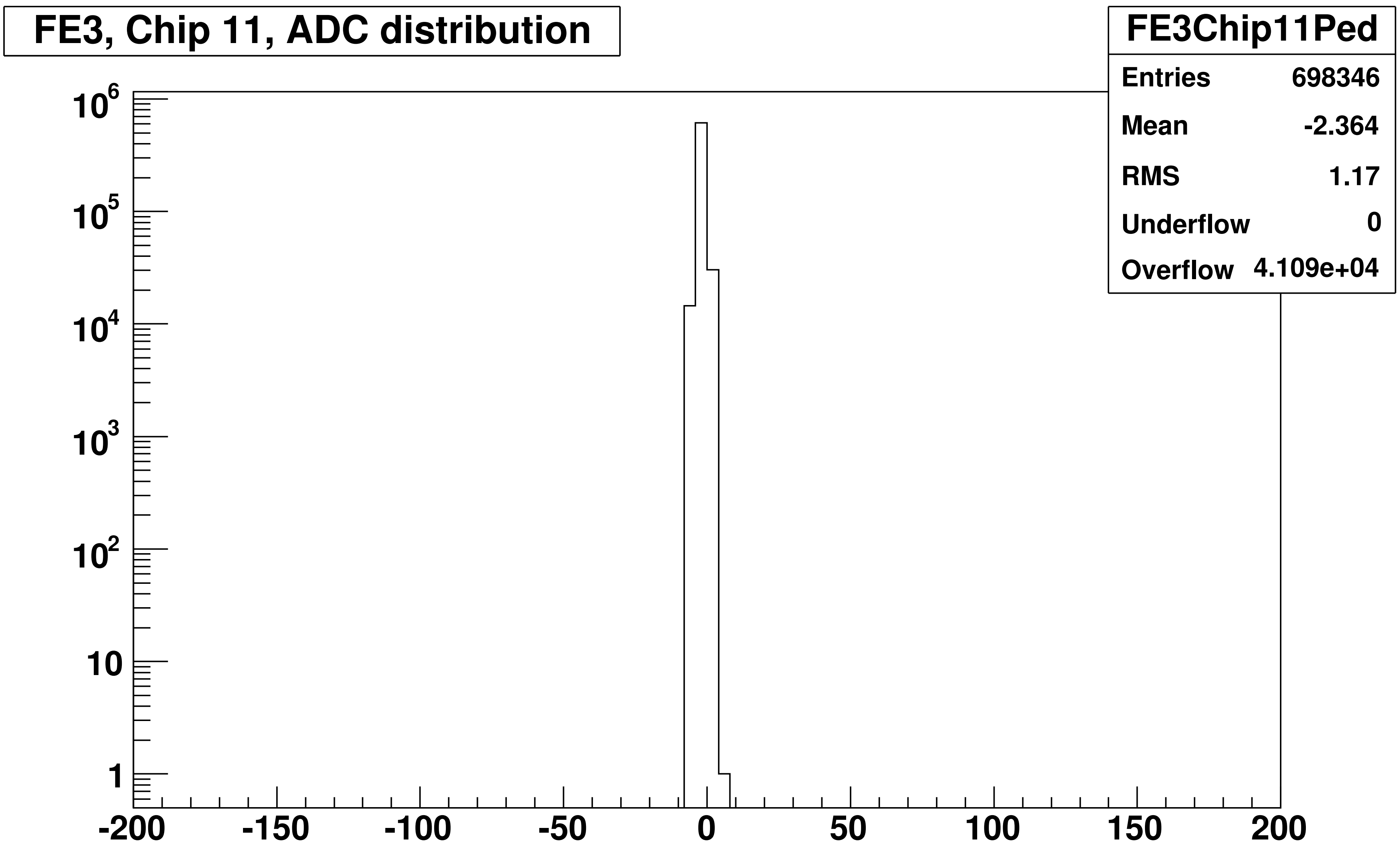
FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 698346
Mean -2.364
RMS 1.17
Underflow 0
Overflow 4.109e+04
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10^3
10^2
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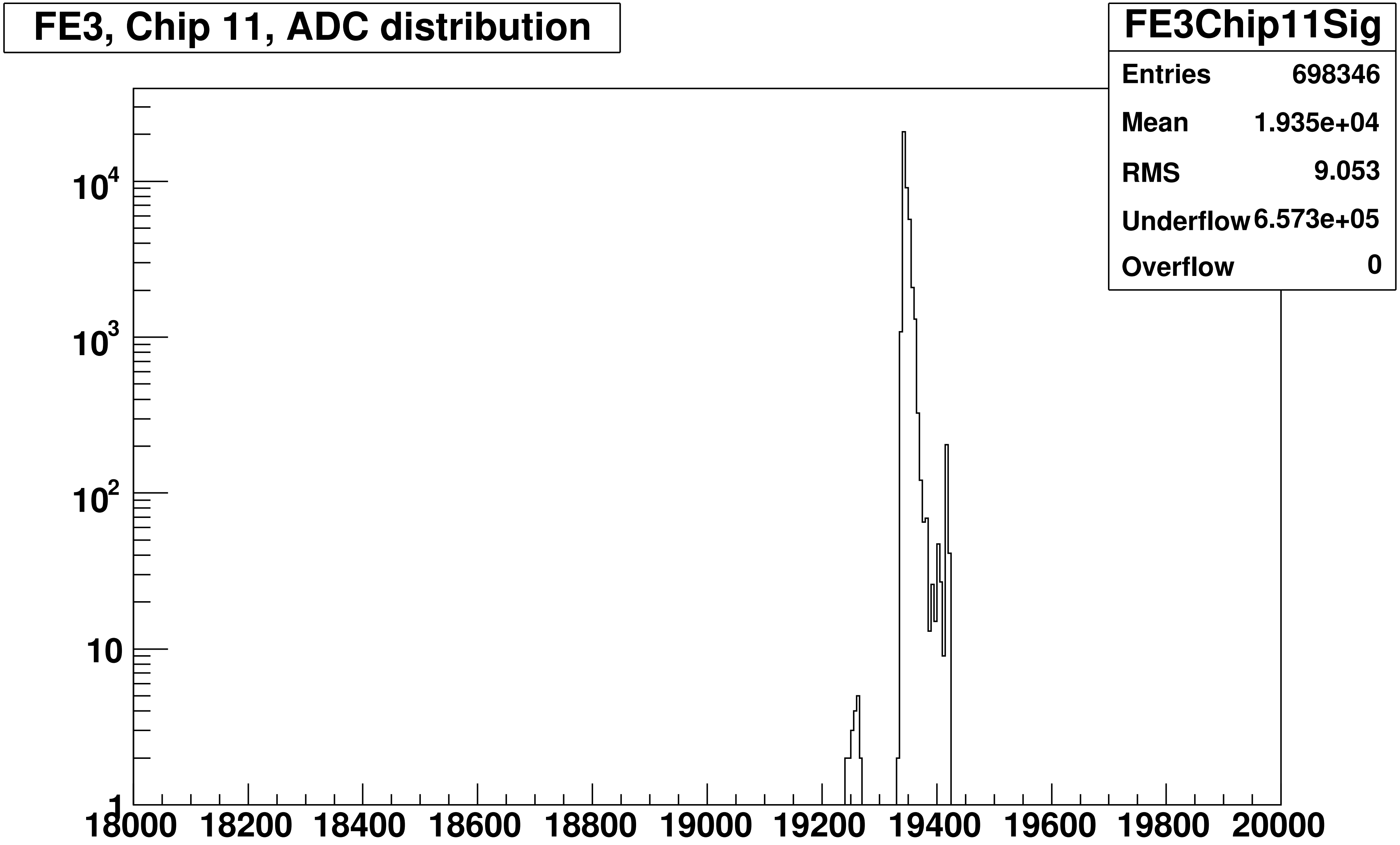
FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 698346
Mean 1.935e+04
RMS 9.053
Underflow 6.573e+05
Overflow 0
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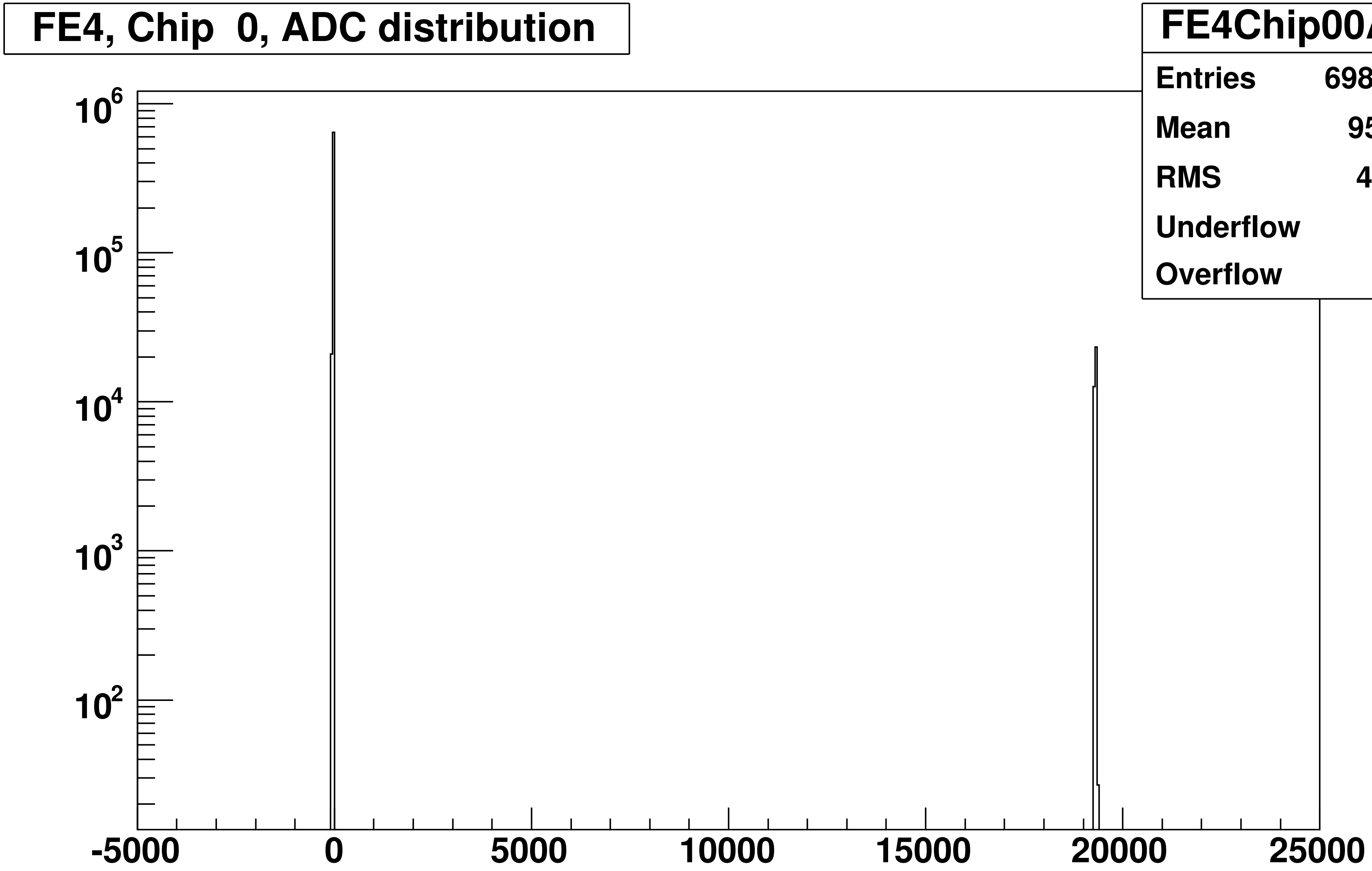
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 698346
Mean 950.9
RMS 4282
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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10000
15000
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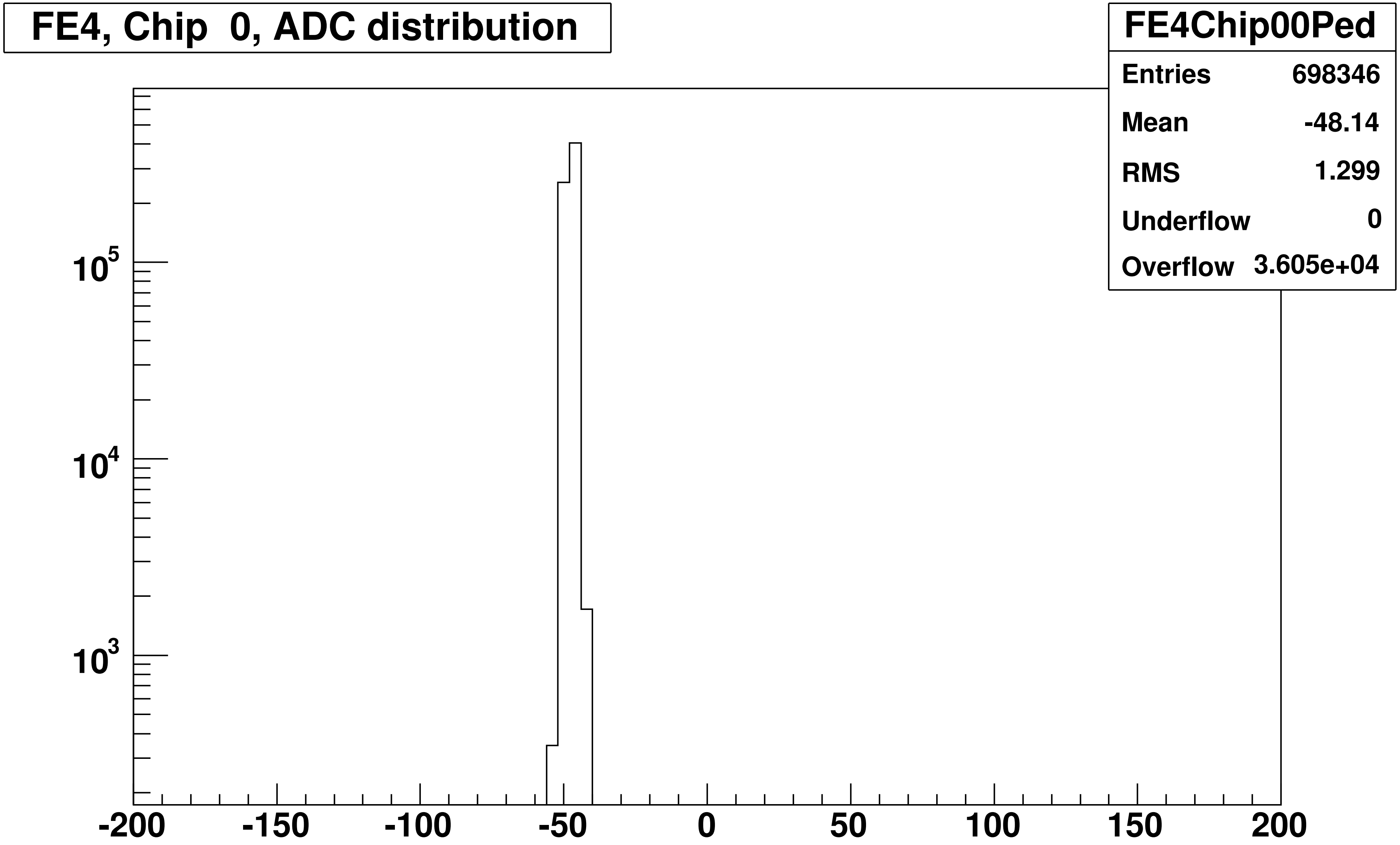

FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 698346
Mean -48.14
RMS 1.299
Underflow 0
Overflow 3.605e+04
10^5
10^4
10^3
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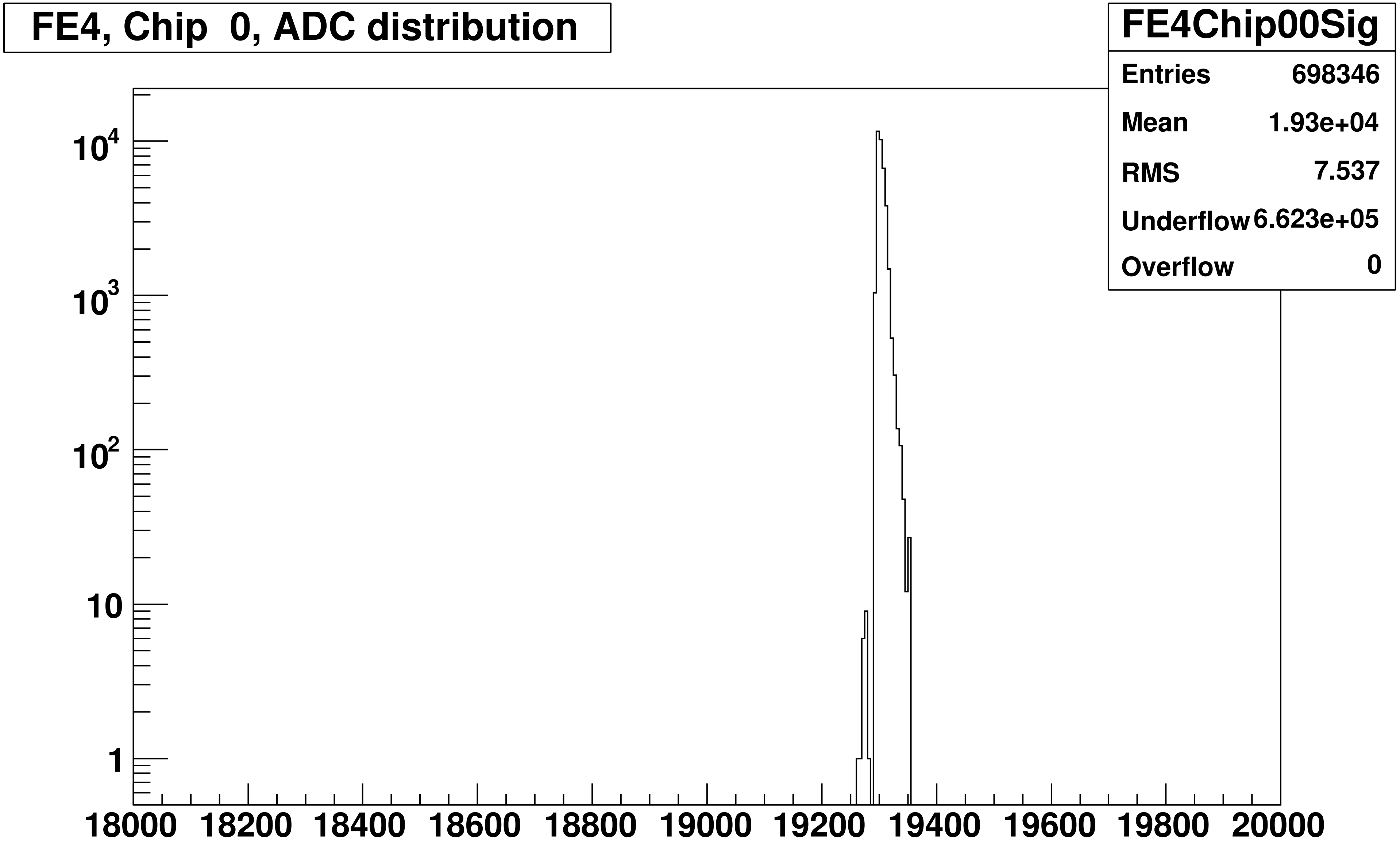
FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 698346
Mean 1.93e+04
RMS 7.537
Underflow 6.623e+05
Overflow 0
10^4
10^3
10^2
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1
18000
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19000
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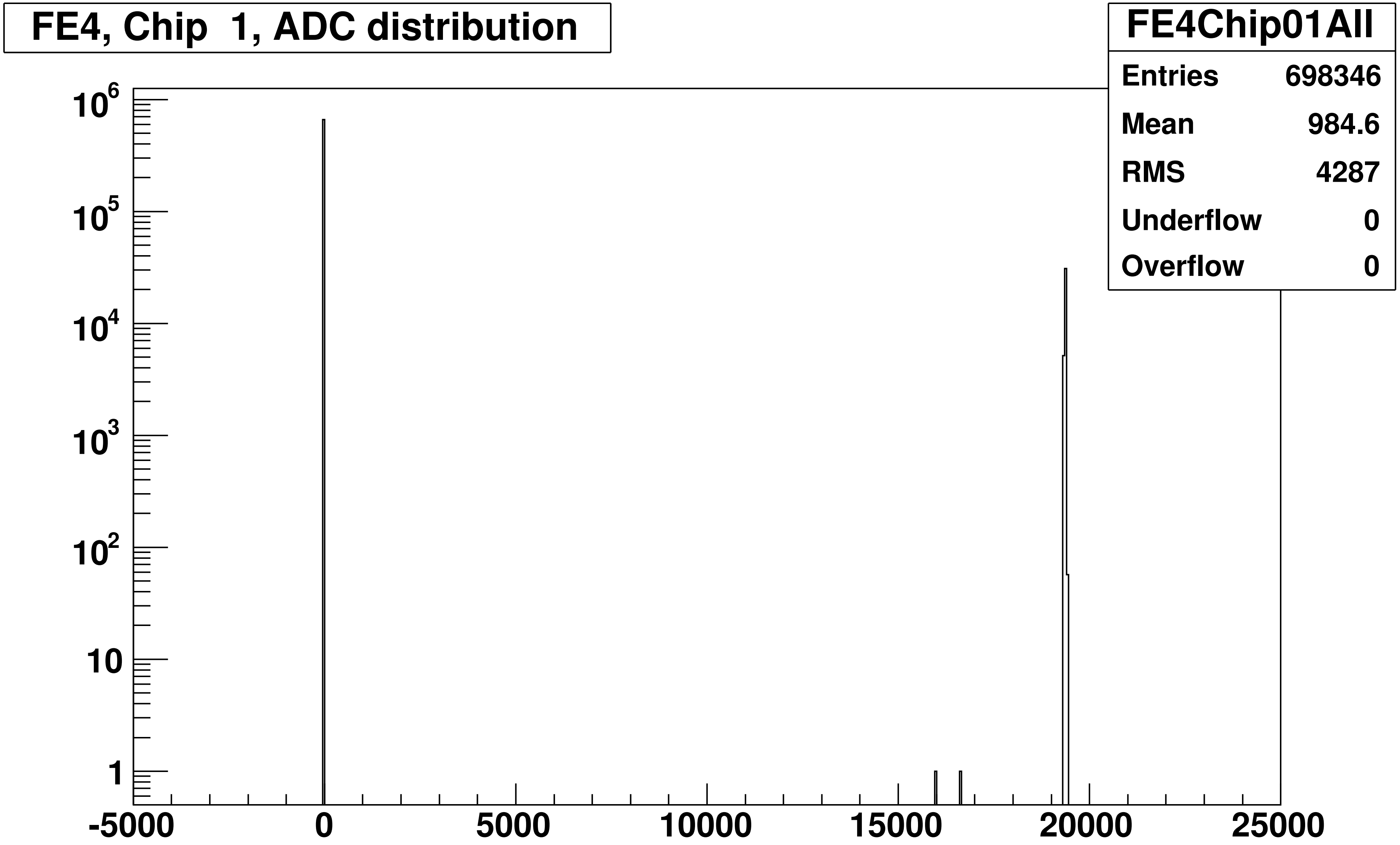
FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 698346
Mean 984.6
RMS 4287
Underflow 0
Overflow 0
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10^4
10^3
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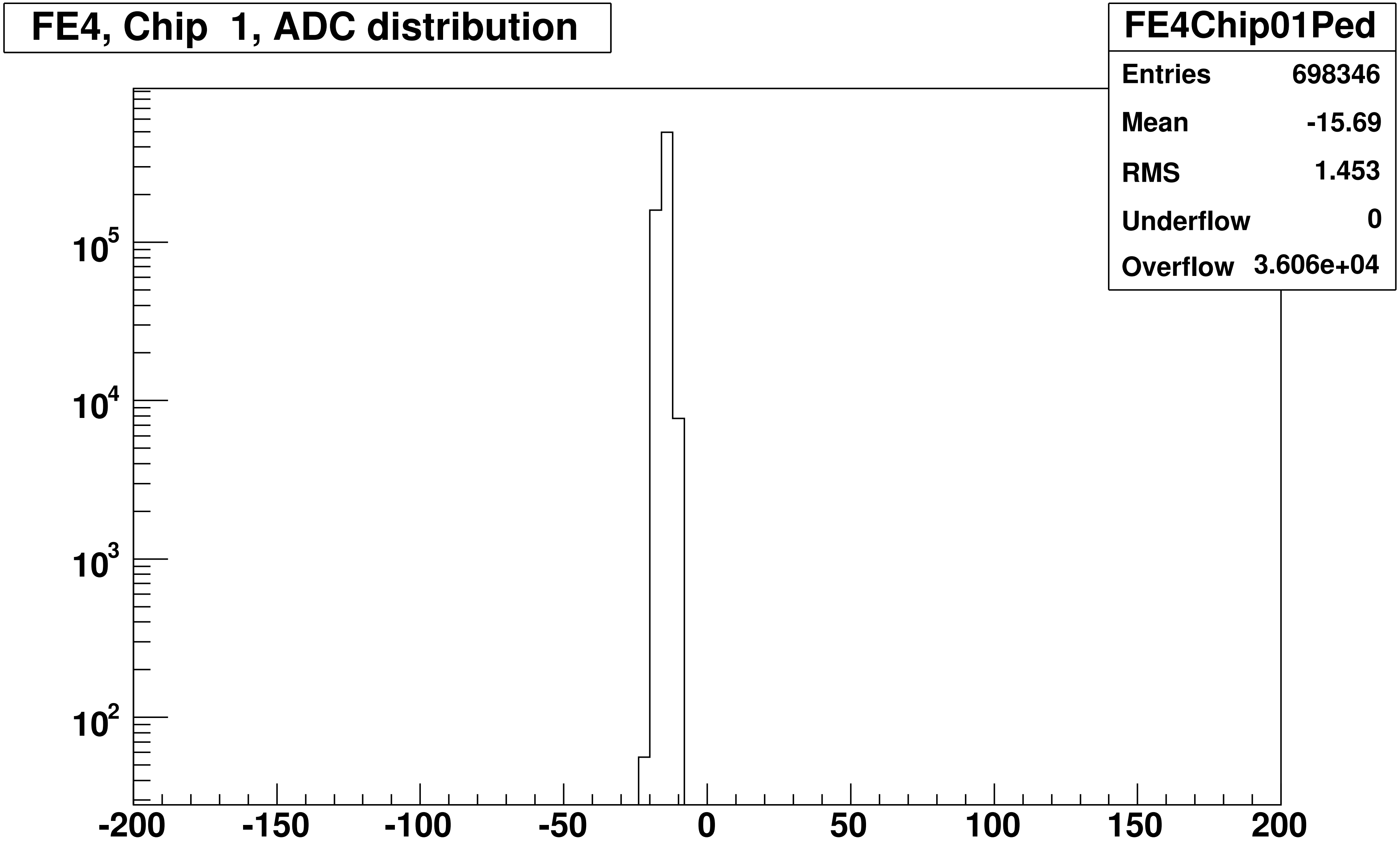
FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 698346
Mean -15.69
RMS 1.453
Underflow 0
Overflow 3.606e+04
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10^2
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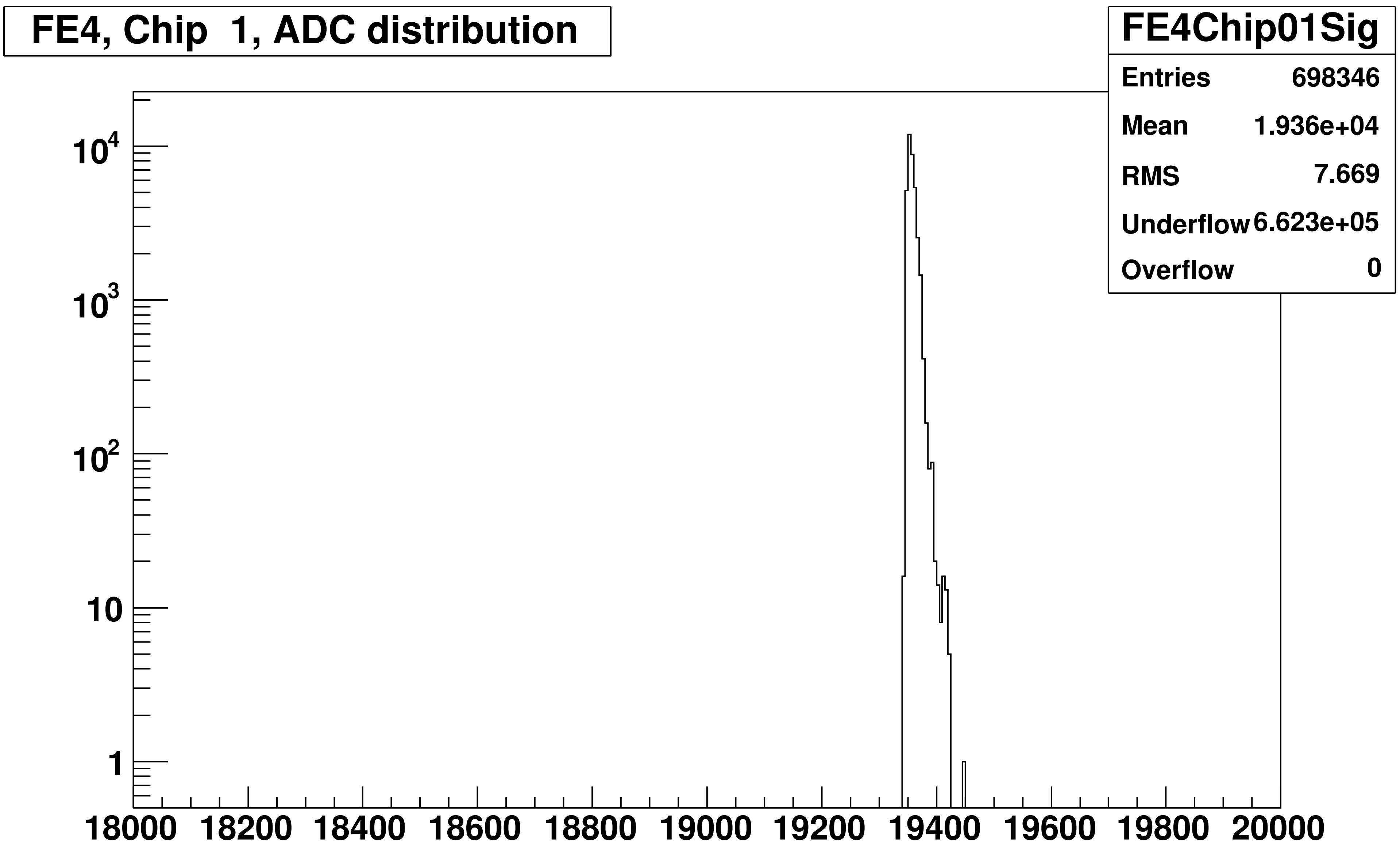
FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 698346
Mean 1.936e+04
RMS 7.669
Underflow 6.623e+05
Overflow 0
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10^3
10^2
10
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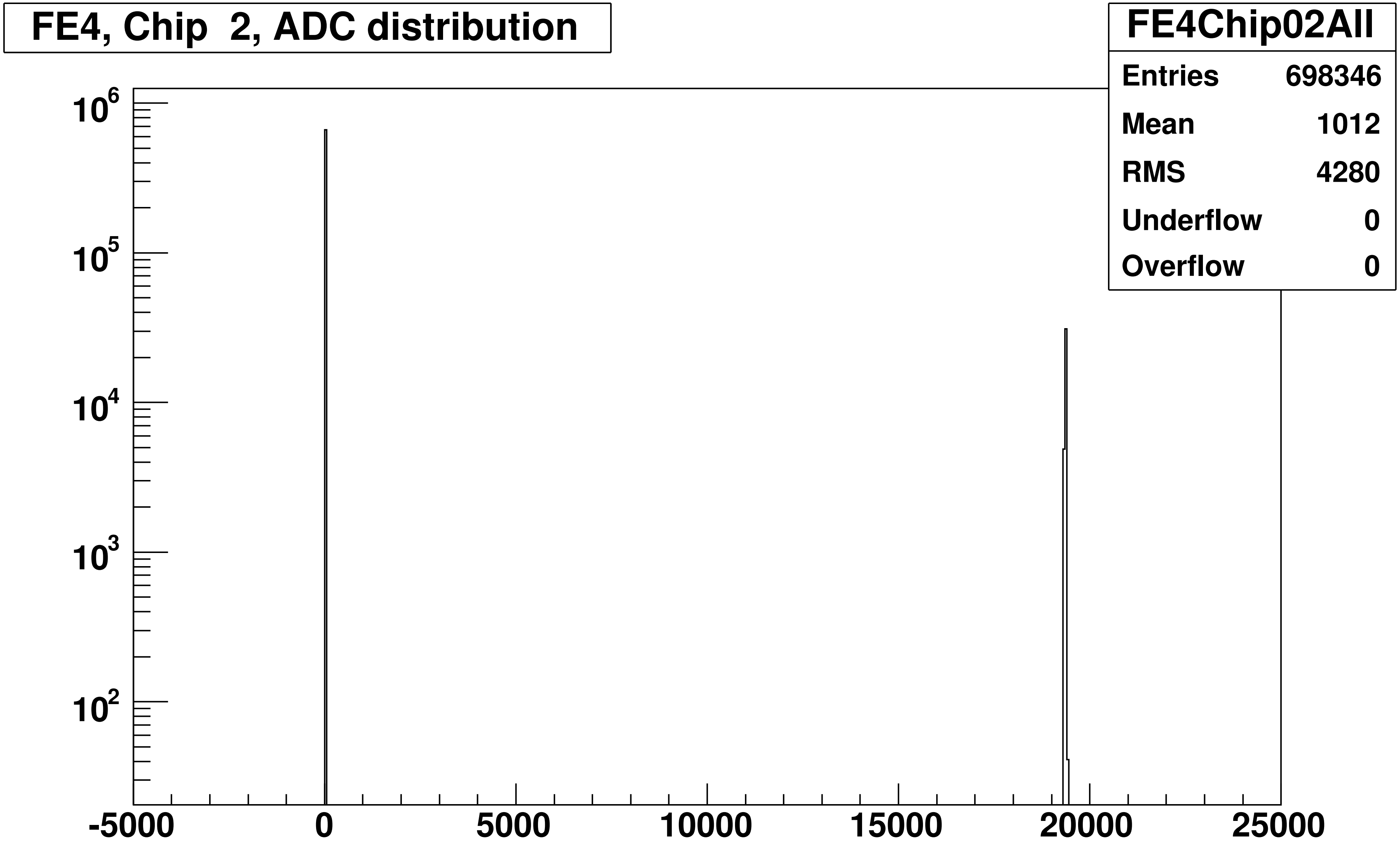
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 698346
Mean 1012
RMS 4280
Underflow 0
Overflow 0
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10^5
10^4
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10^2
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0
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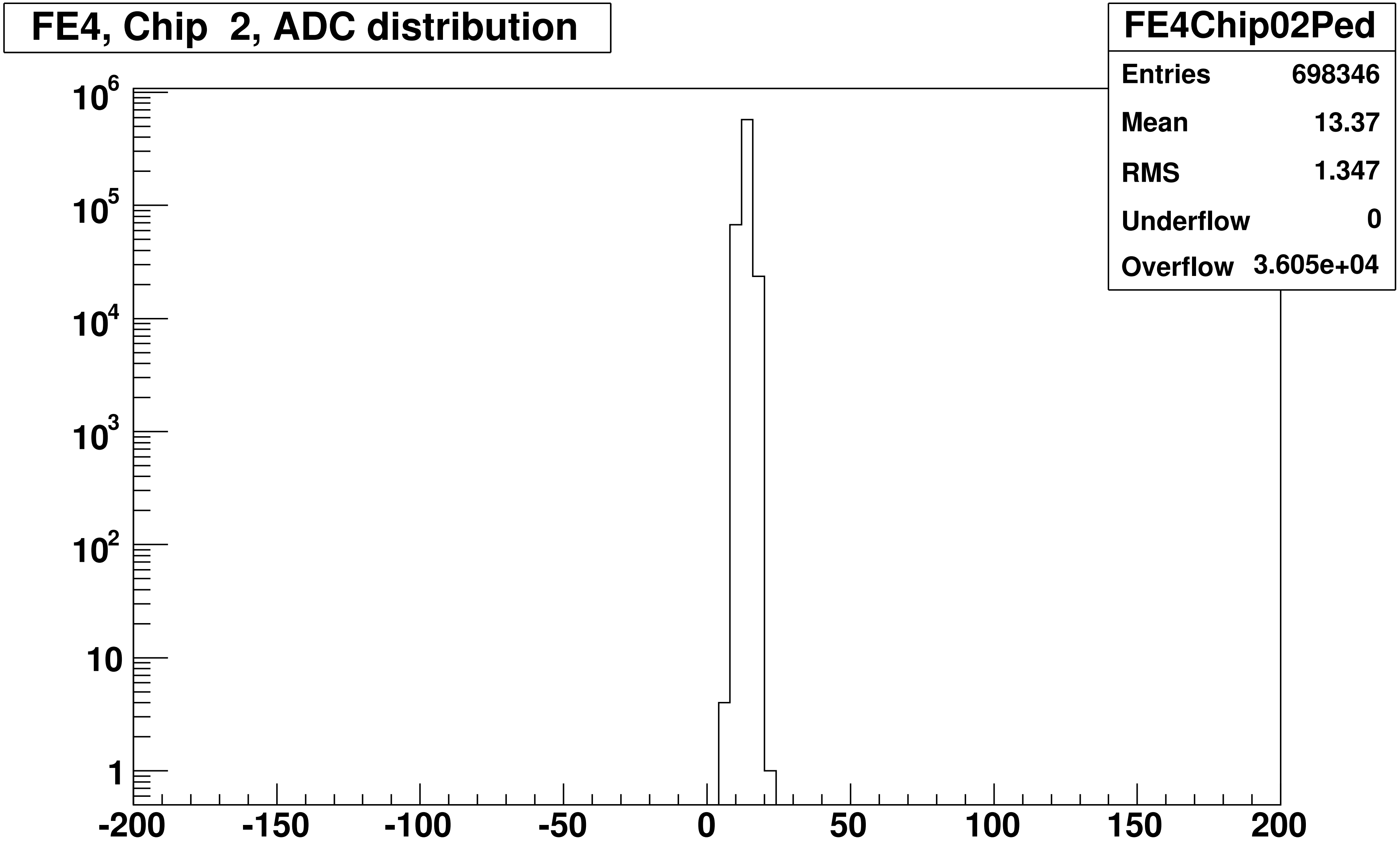
20000
25000


FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 698346
Mean 13.37
RMS 1.347
Underflow 0
Overflow 3.605e+04
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10^3
10^2
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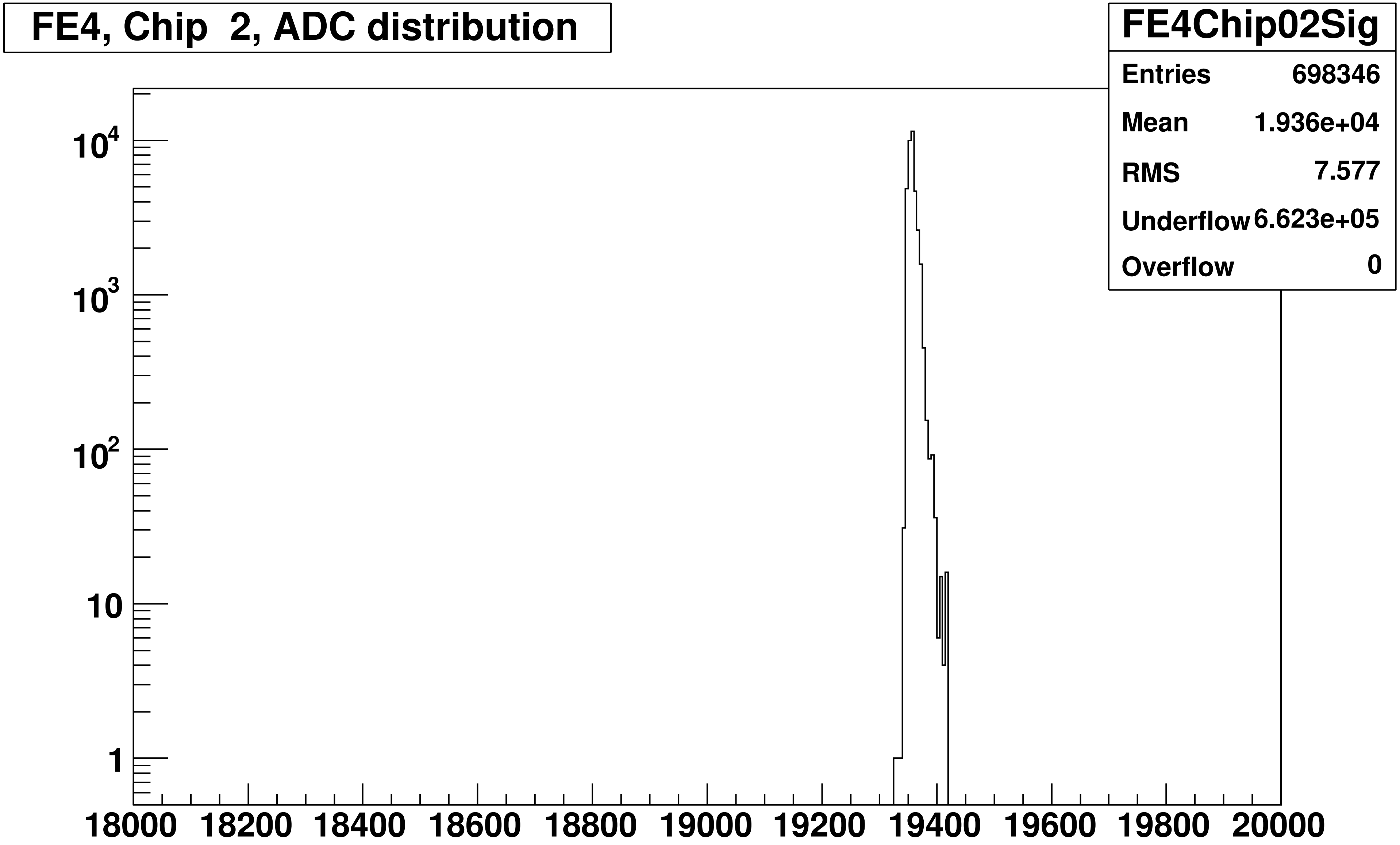
FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 698346
Mean 1.936e+04
RMS 7.577
Underflow 6.623e+05
Overflow 0
10^4
10^3
10^2
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1
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18400
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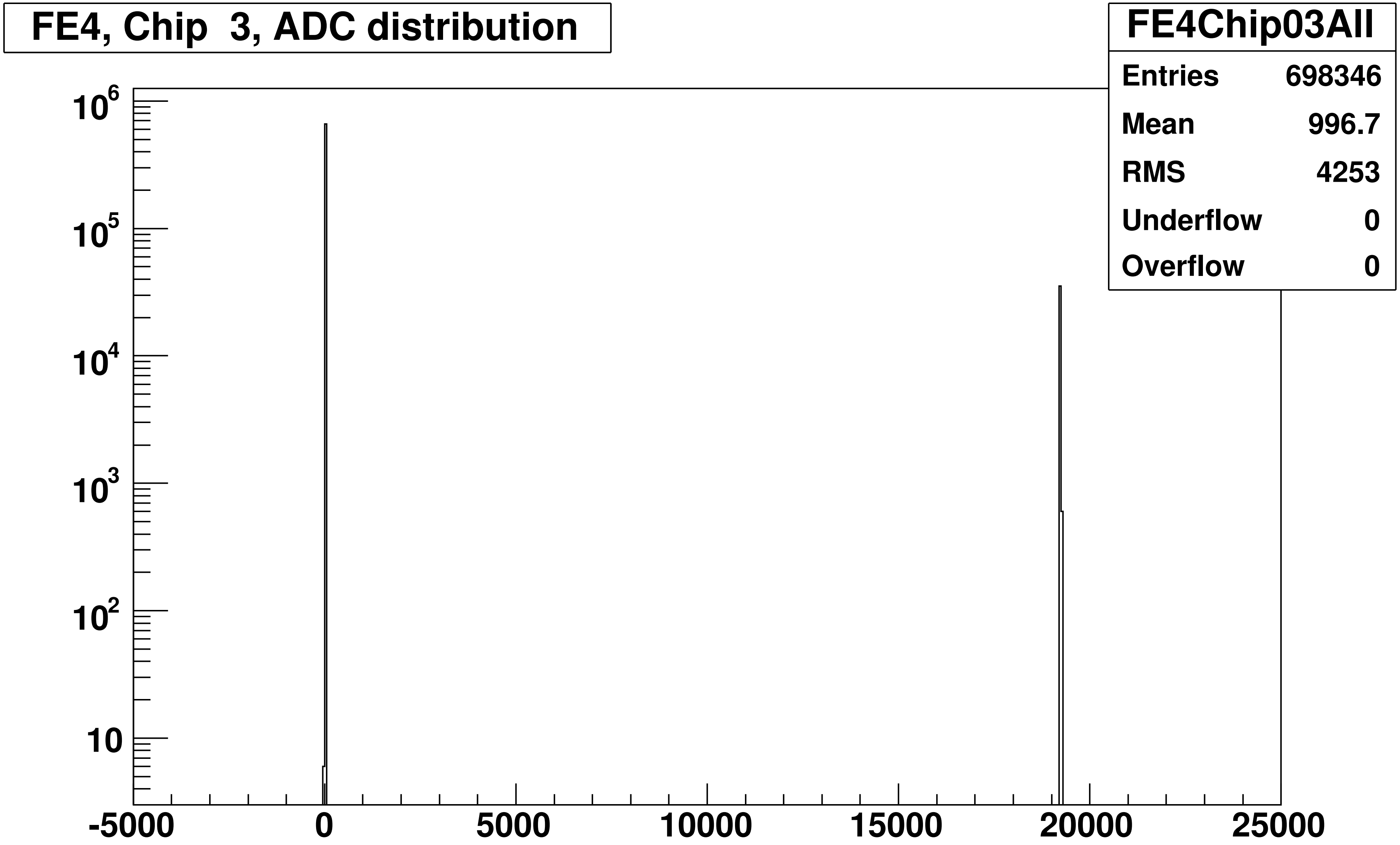
FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 698346
Mean 996.7
RMS 4253
Underflow 0
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10^2
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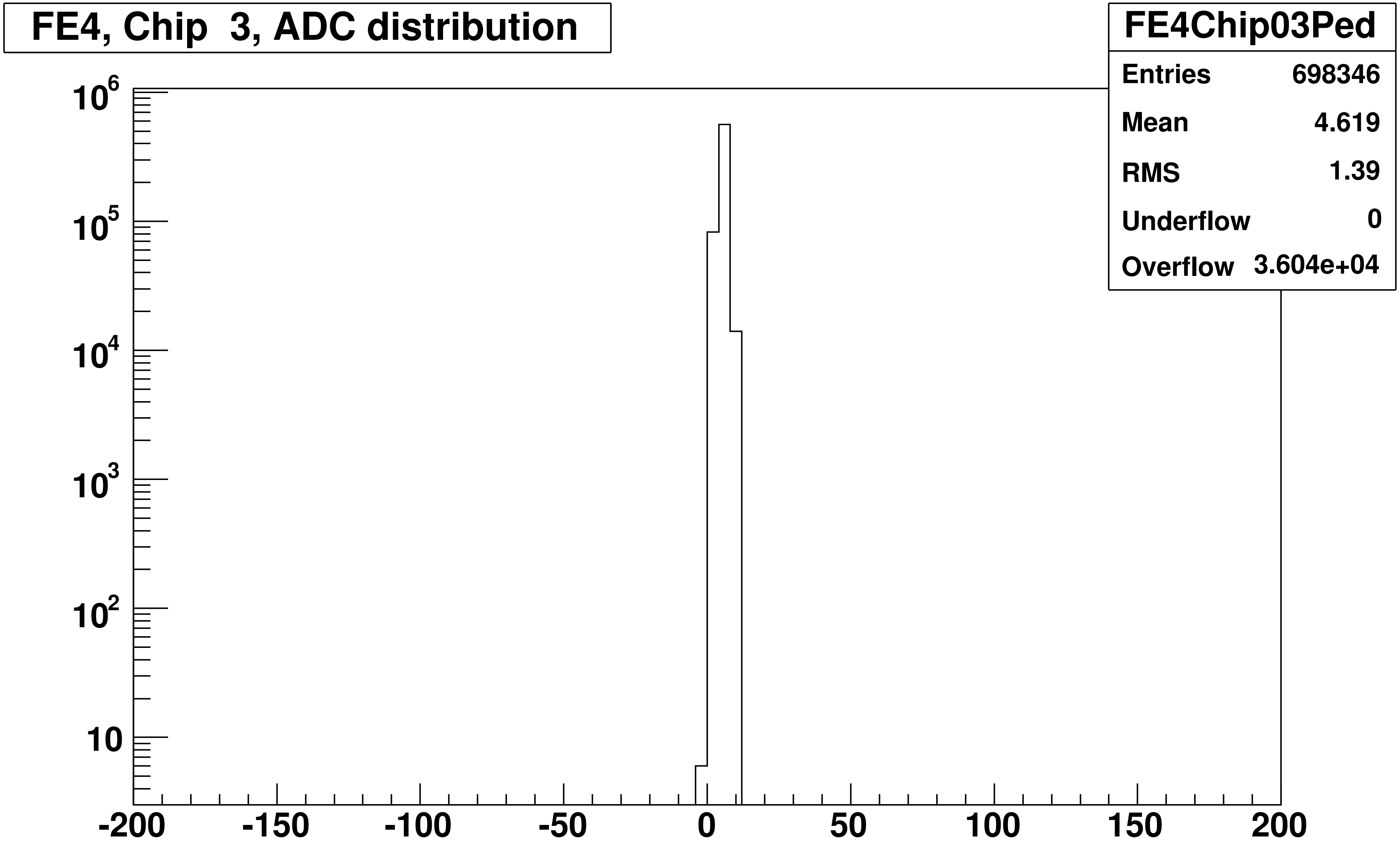
FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 698346
Mean 4.619
RMS 1.39
Underflow 0
Overflow 3.604e+04
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10^3
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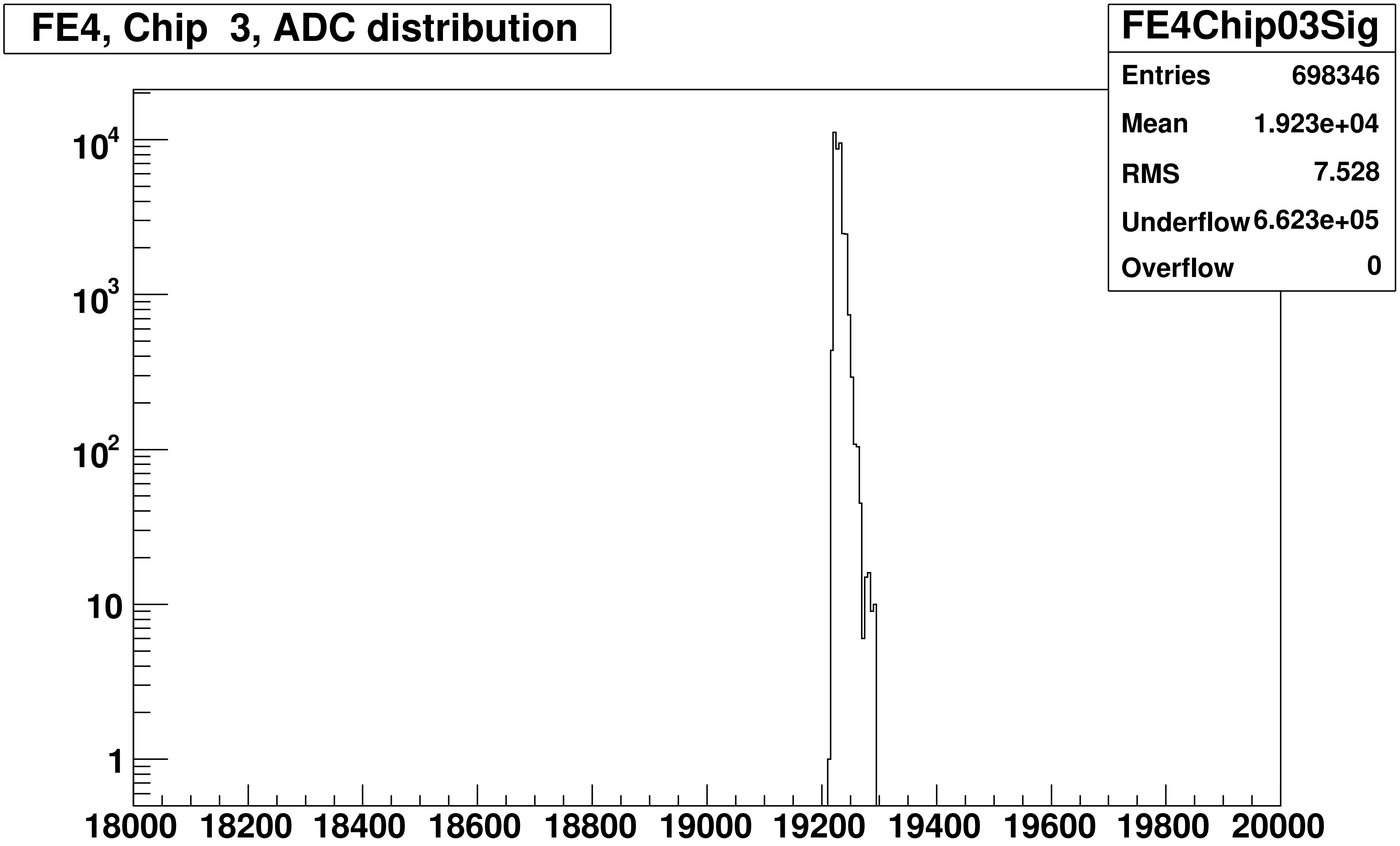
FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 698346
Mean 1.923e+04
RMS 7.528
Underflow 6.623e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
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18600
18800
19000
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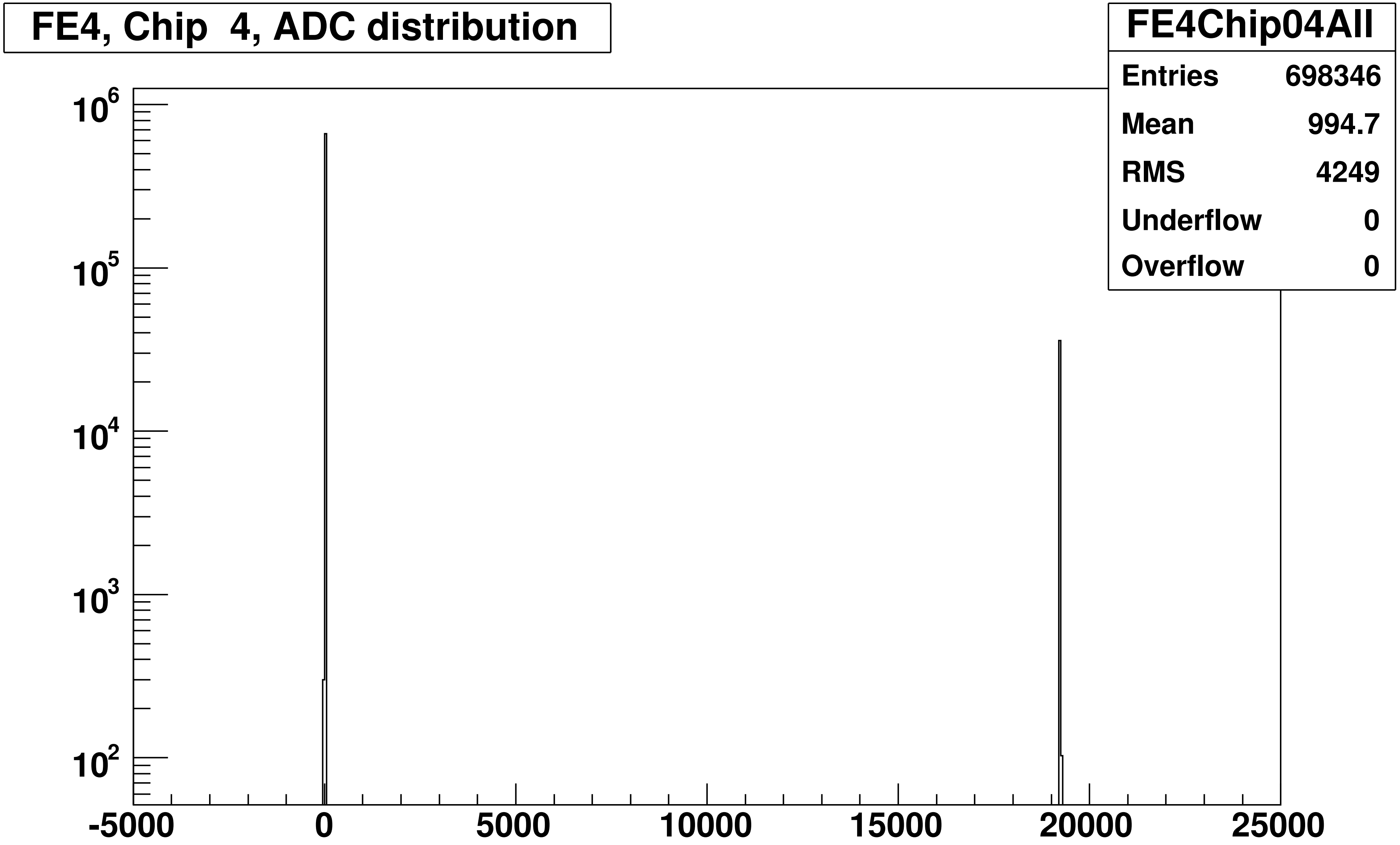
19400
19600
19800
20000



FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 698346
Mean 994.7
RMS 4249
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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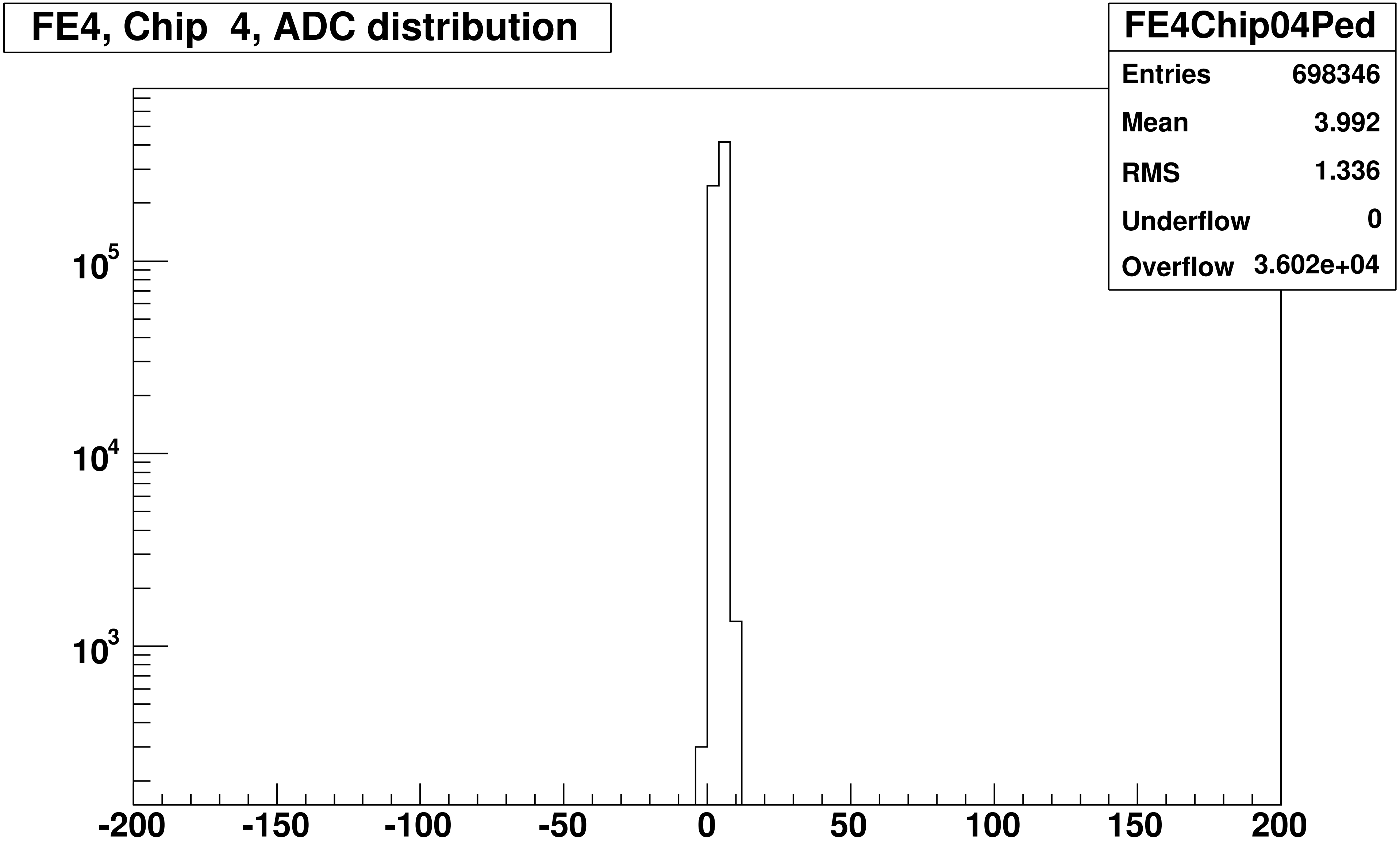
FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 698346
Mean 3.992
RMS 1.336
Underflow 0
Overflow 3.602e+04
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10^4
10^3
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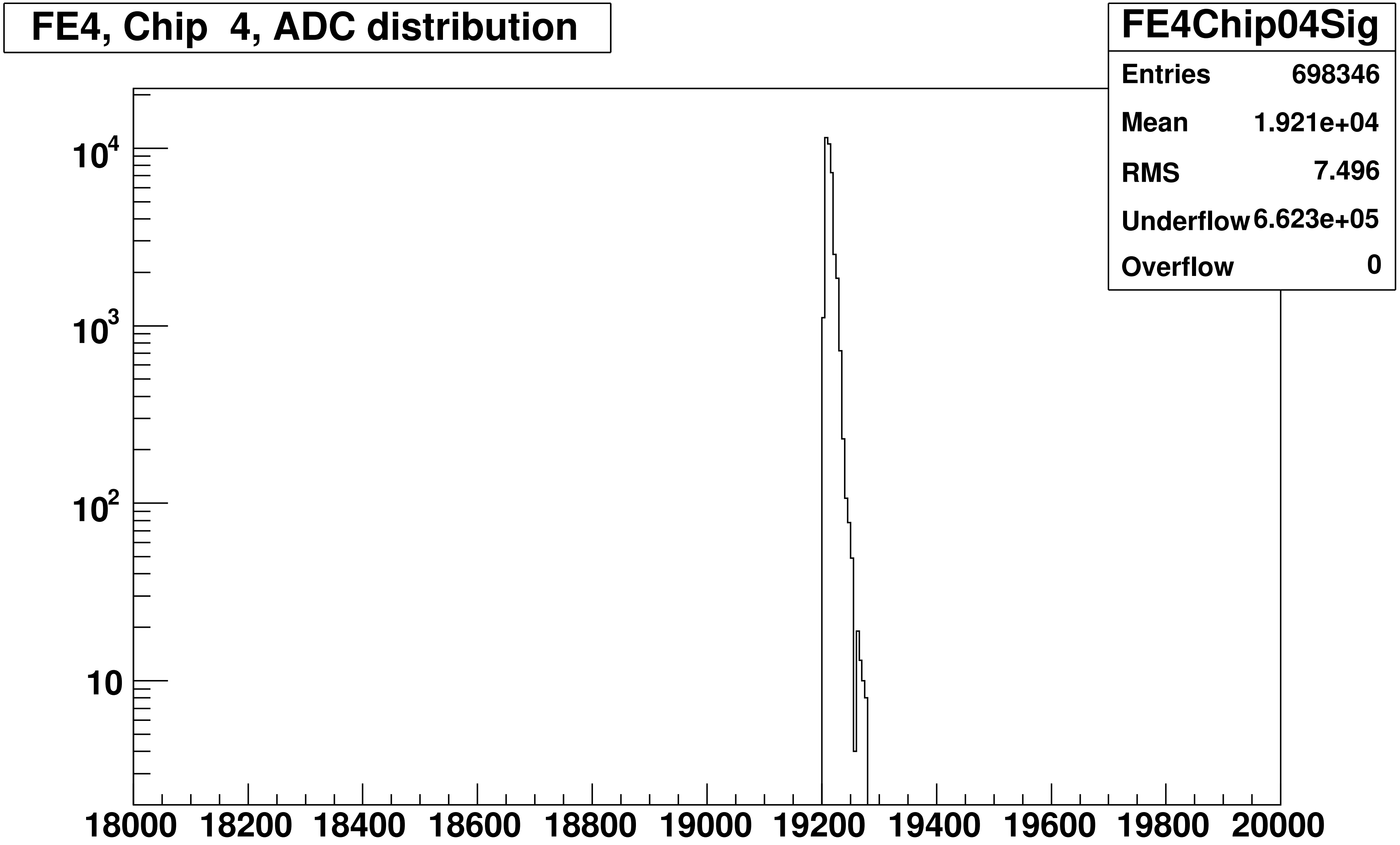

FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 698346
Mean 1.921e+04
RMS 7.496
Underflow 6.623e+05
Overflow 0
10^4
10^3
10^2
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18000
18200
18400
18600
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19000
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19800
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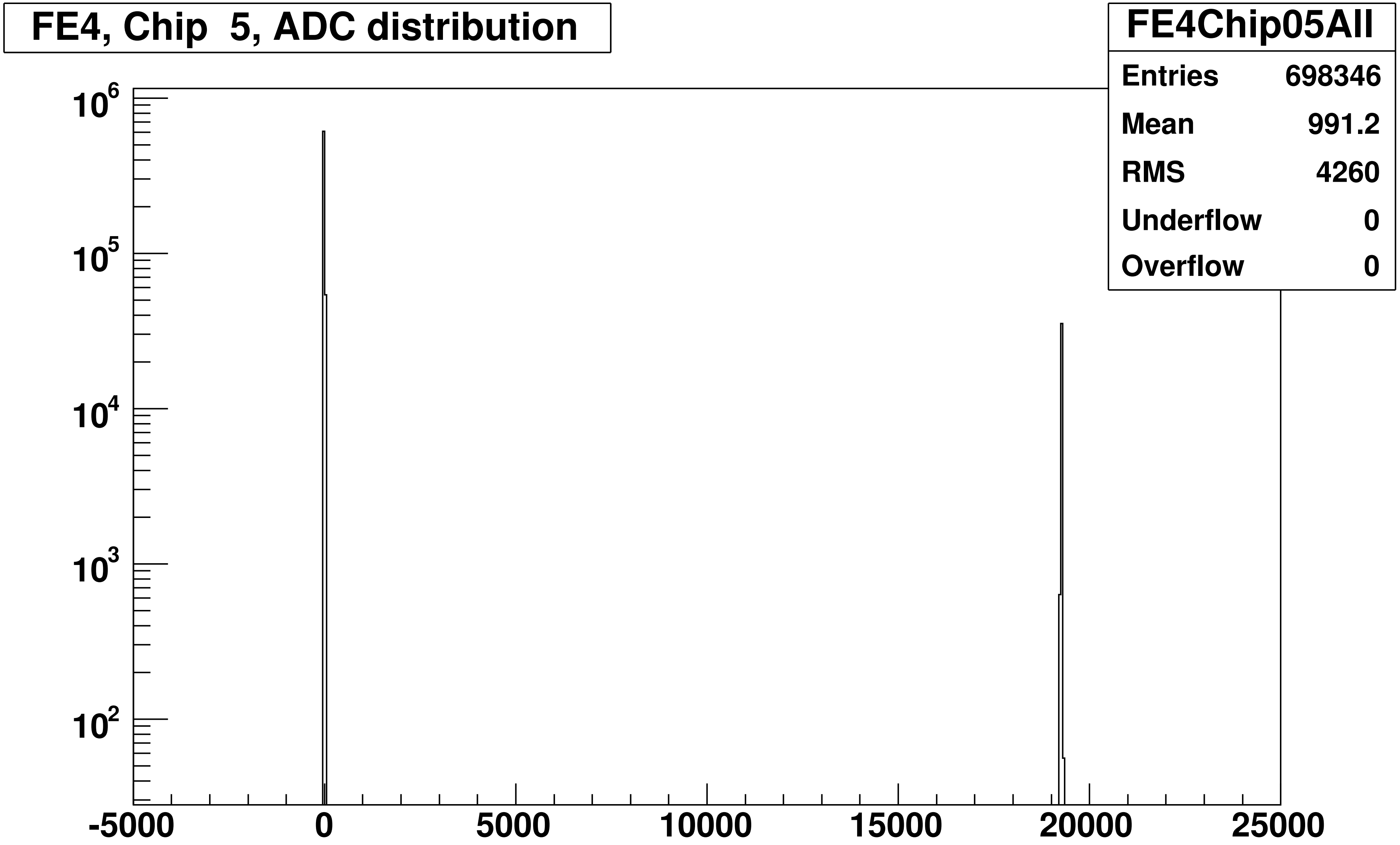

FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 698346
Mean 991.2
RMS 4260
Underflow 0
Overflow 0
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10^5
10^4
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10^2
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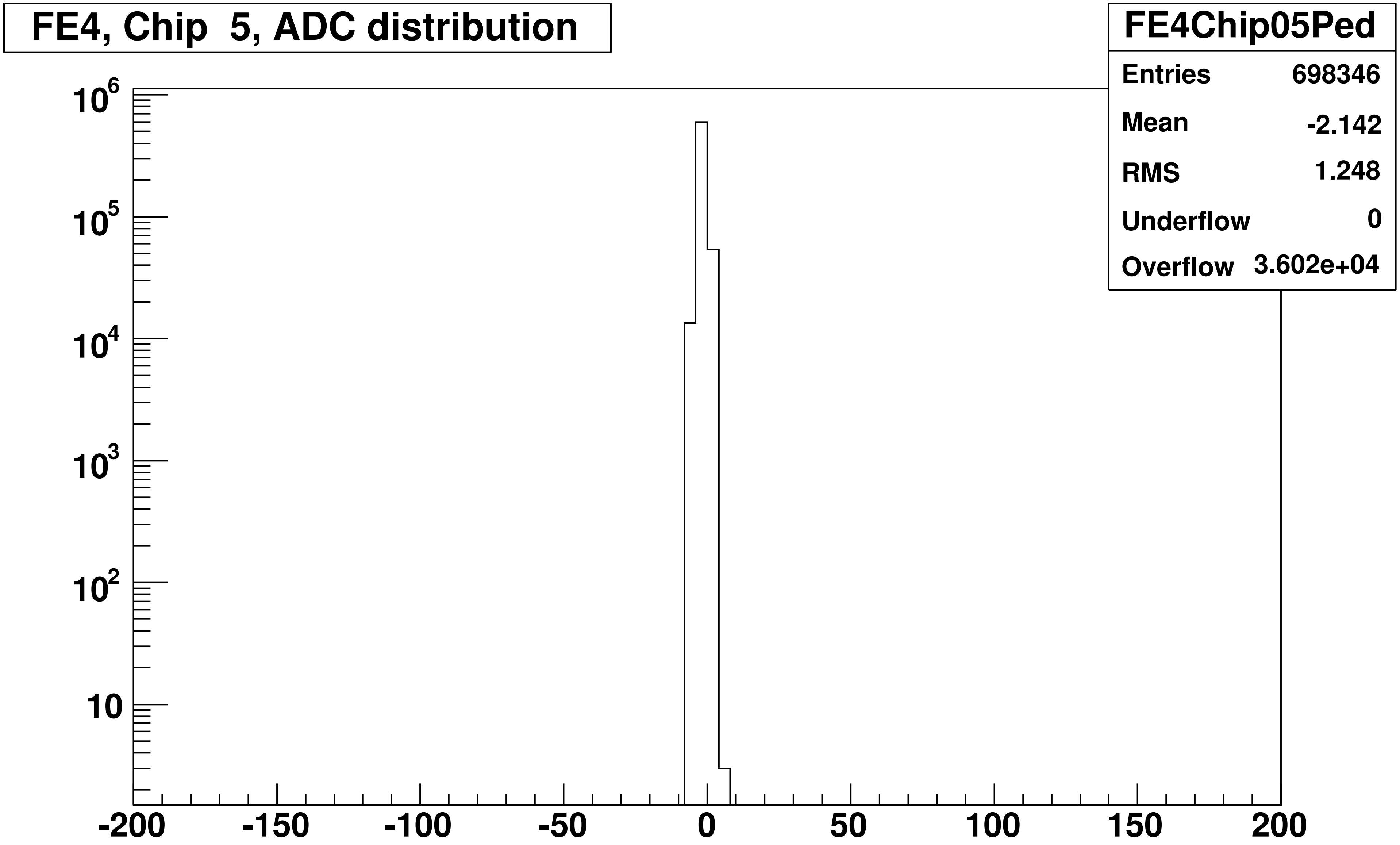
FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 698346
Mean -2.142
RMS 1.248
Underflow 0
Overflow 3.602e+04
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10^5
10^4
10^3
10^2
10
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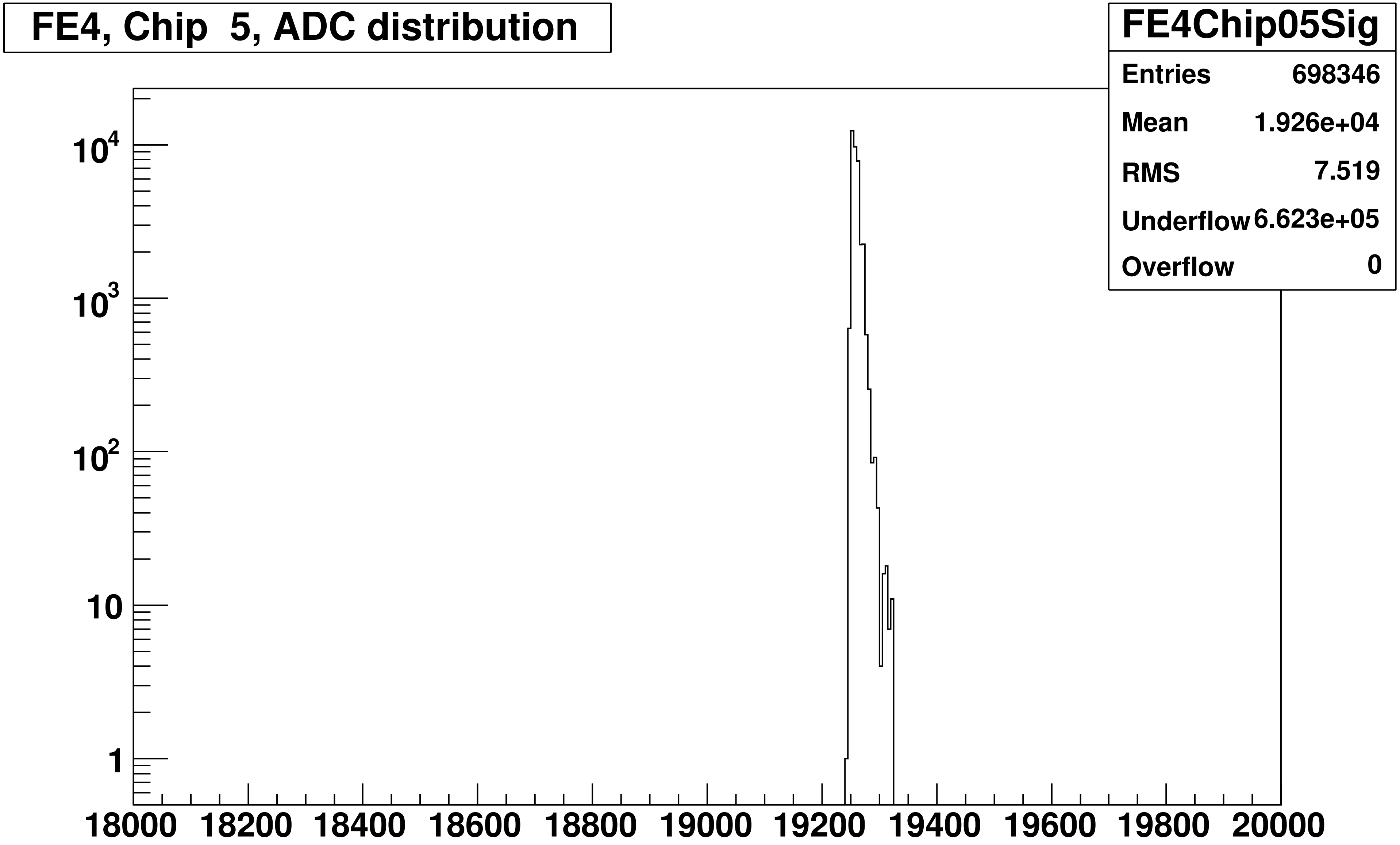
FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 698346
Mean 1.926e+04
RMS 7.519
Underflow 6.623e+05
Overflow 0
10^4
10^3
10^2
10
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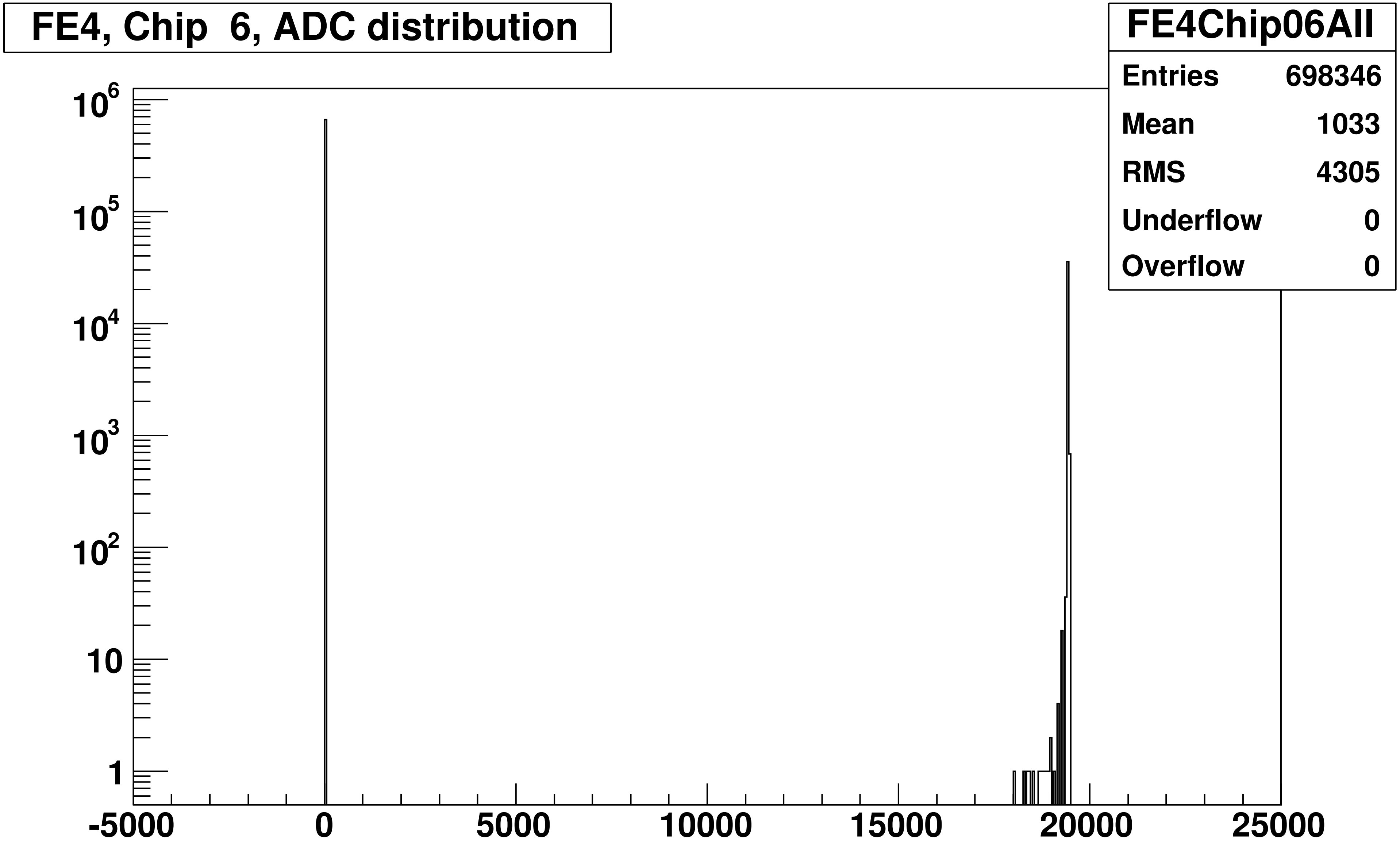
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 698346
Mean 1033
RMS 4305
Underflow 0
Overflow 0
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10^3
10^2
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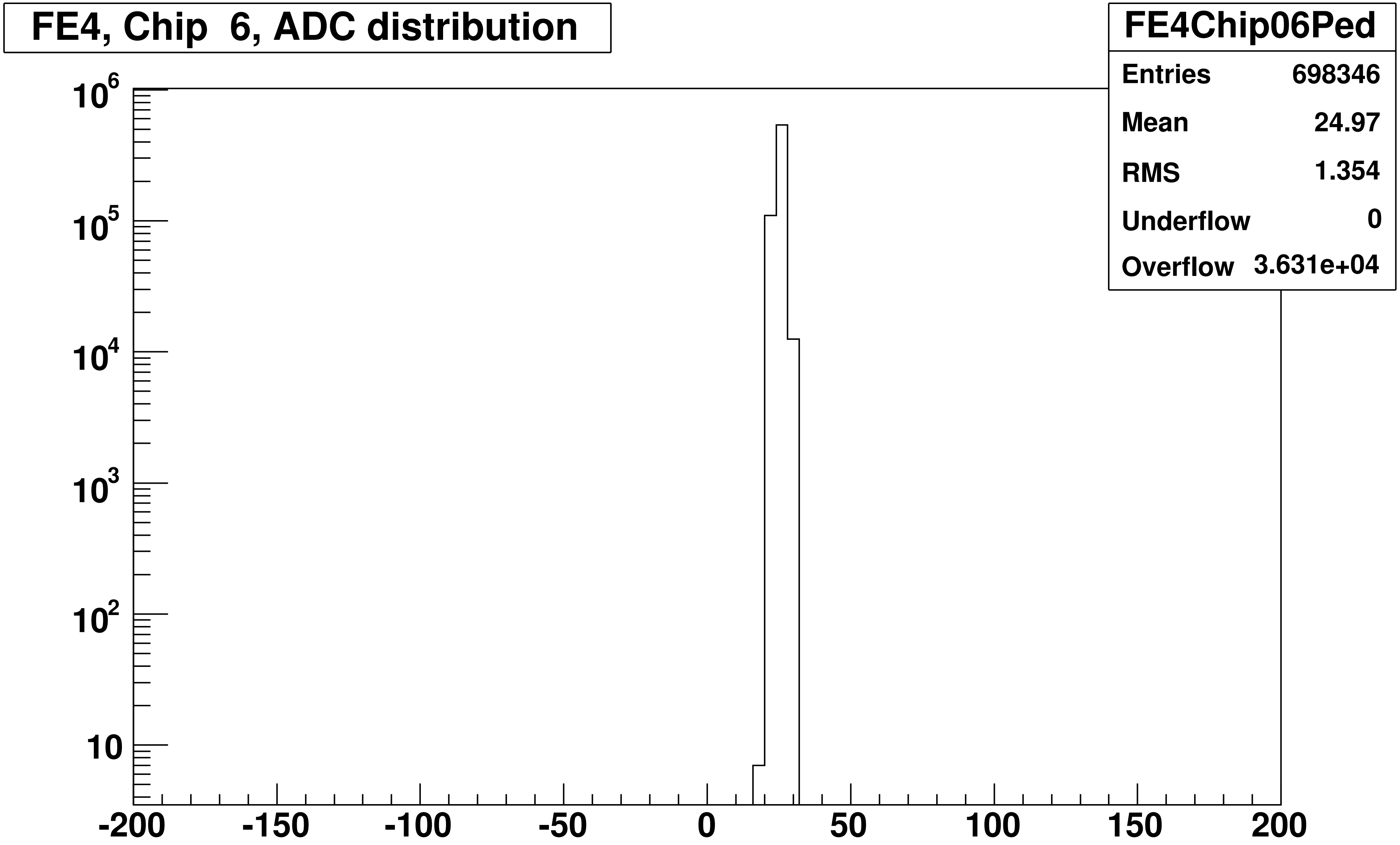

FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 698346
Mean 24.97
RMS 1.354
Underflow 0
Overflow 3.631e+04
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10^5
10^4
10^3
10^2
10
-200
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-100
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100
150
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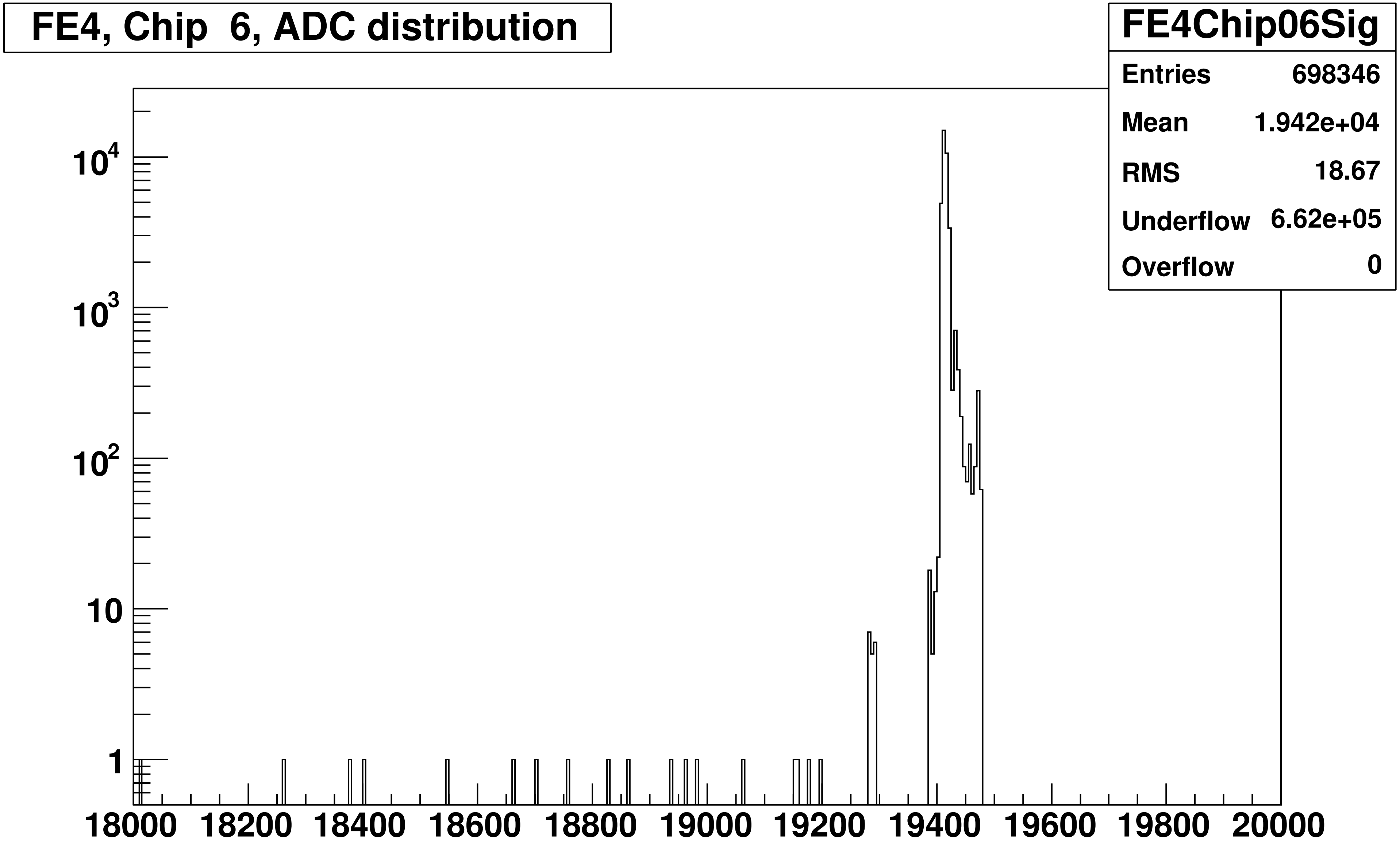

FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 698346
Mean 1.942e+04
RMS 18.67
Underflow 6.62e+05
Overflow 0
10^4
10^3
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10
1
18000
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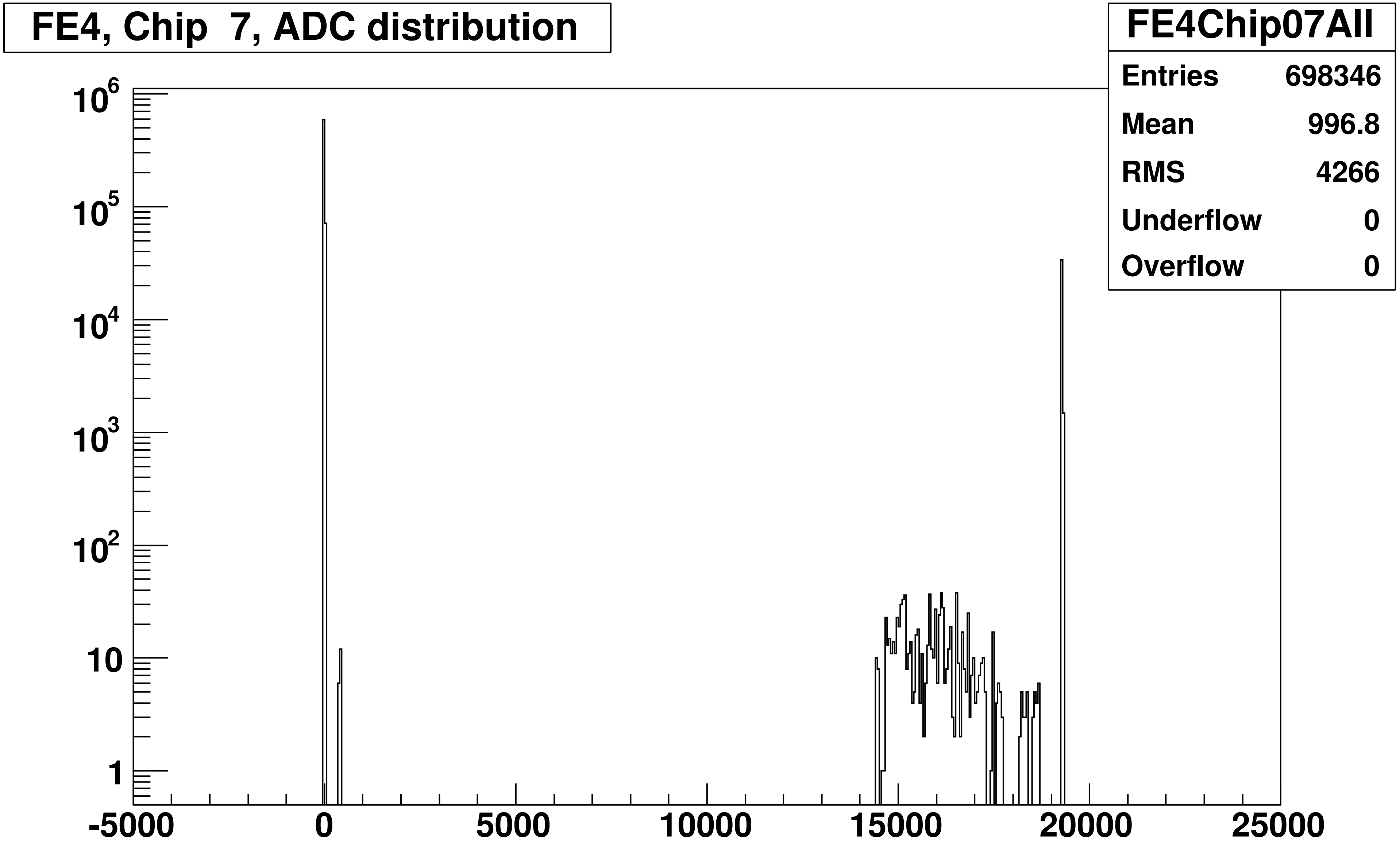
FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 698346
Mean 996.8
RMS 4266
Underflow 0
Overflow 0
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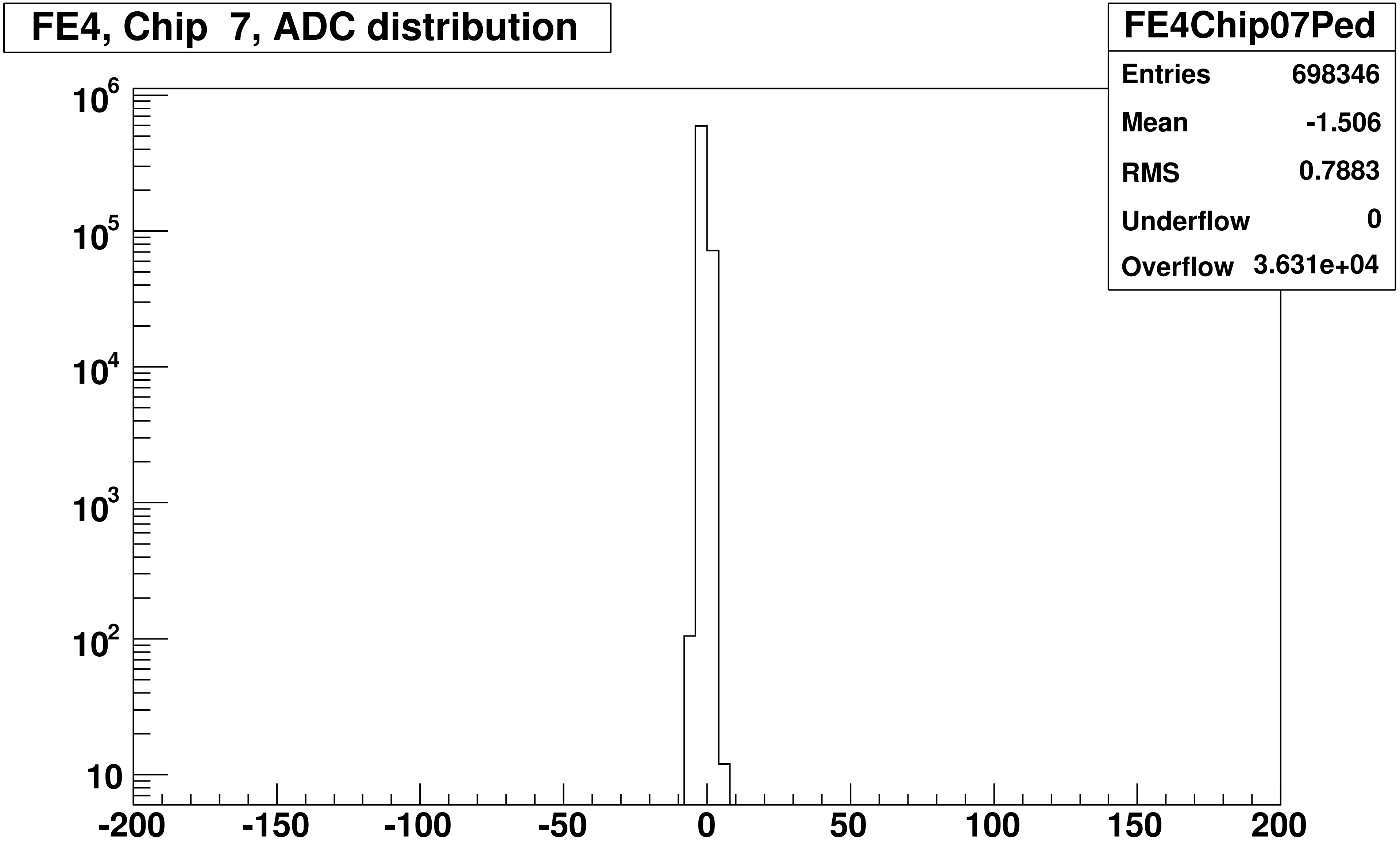
FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 698346
Mean -1.506
RMS 0.7883
Underflow 0
Overflow 3.631e+04
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10^5
10^4
10^3
10^2
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150
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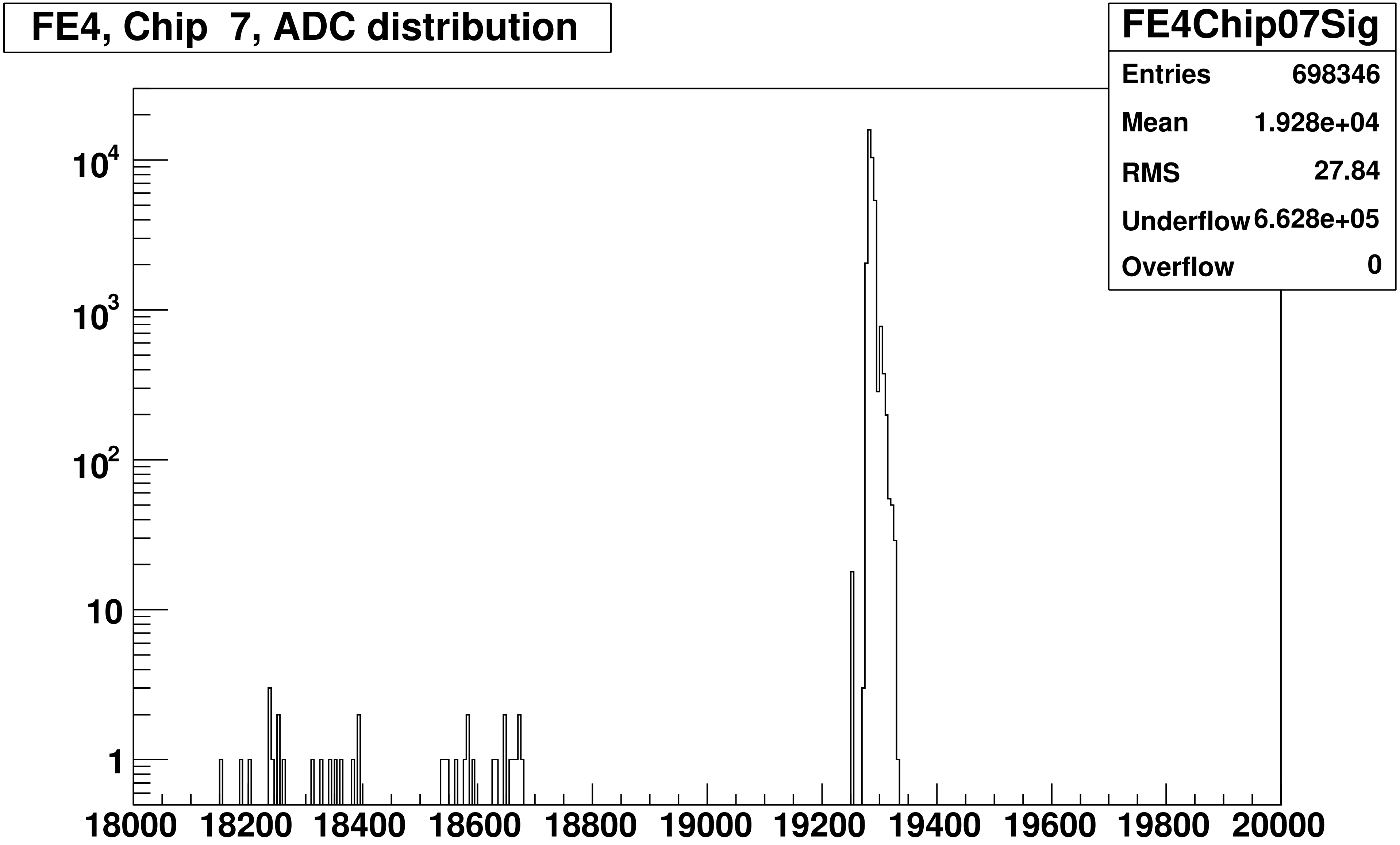
FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 698346
Mean 1.928e+04
RMS 27.84
Underflow 6.628e+05
Overflow 0
1
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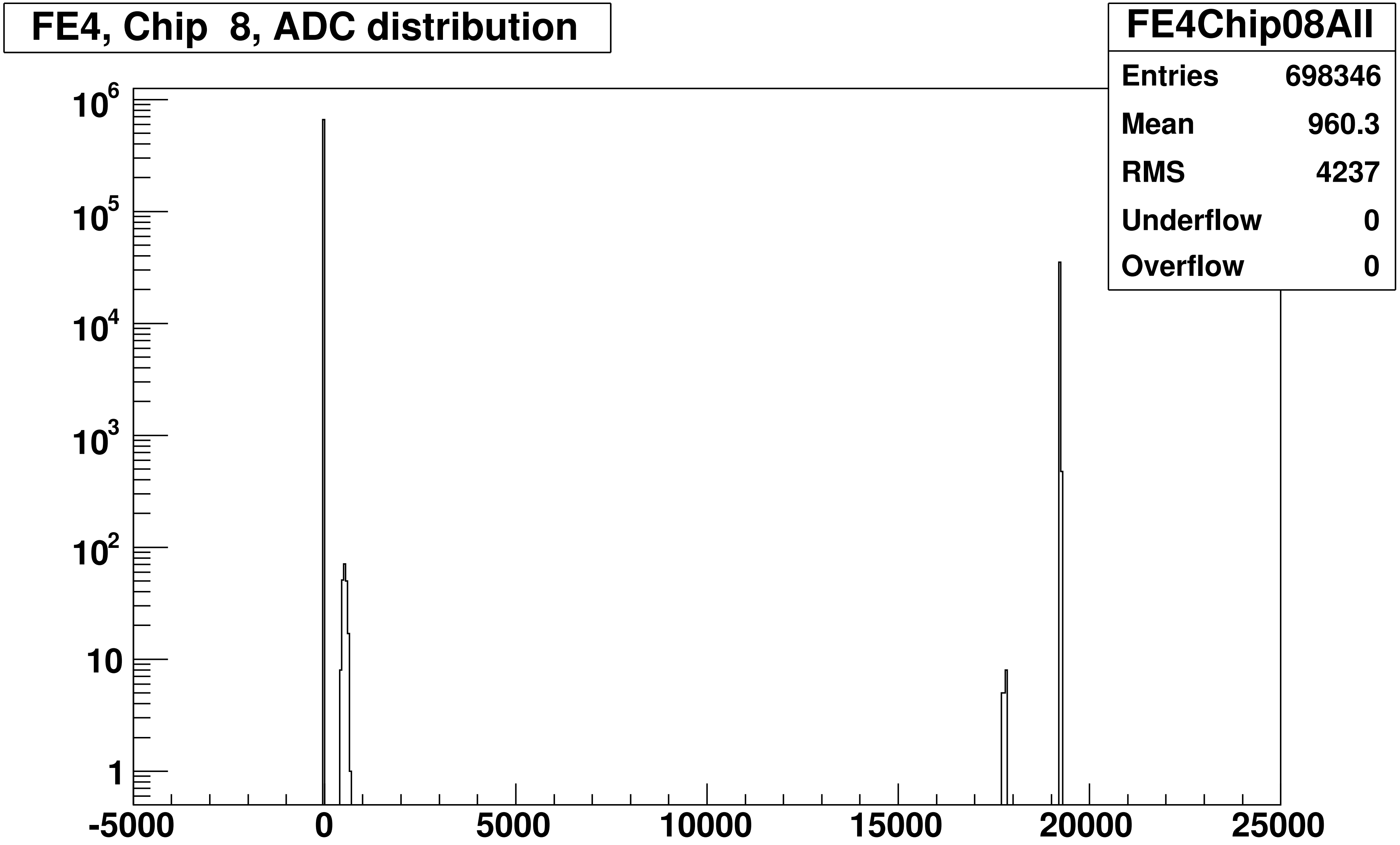
FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 698346
Mean 960.3
RMS 4237
Underflow 0
Overflow 0
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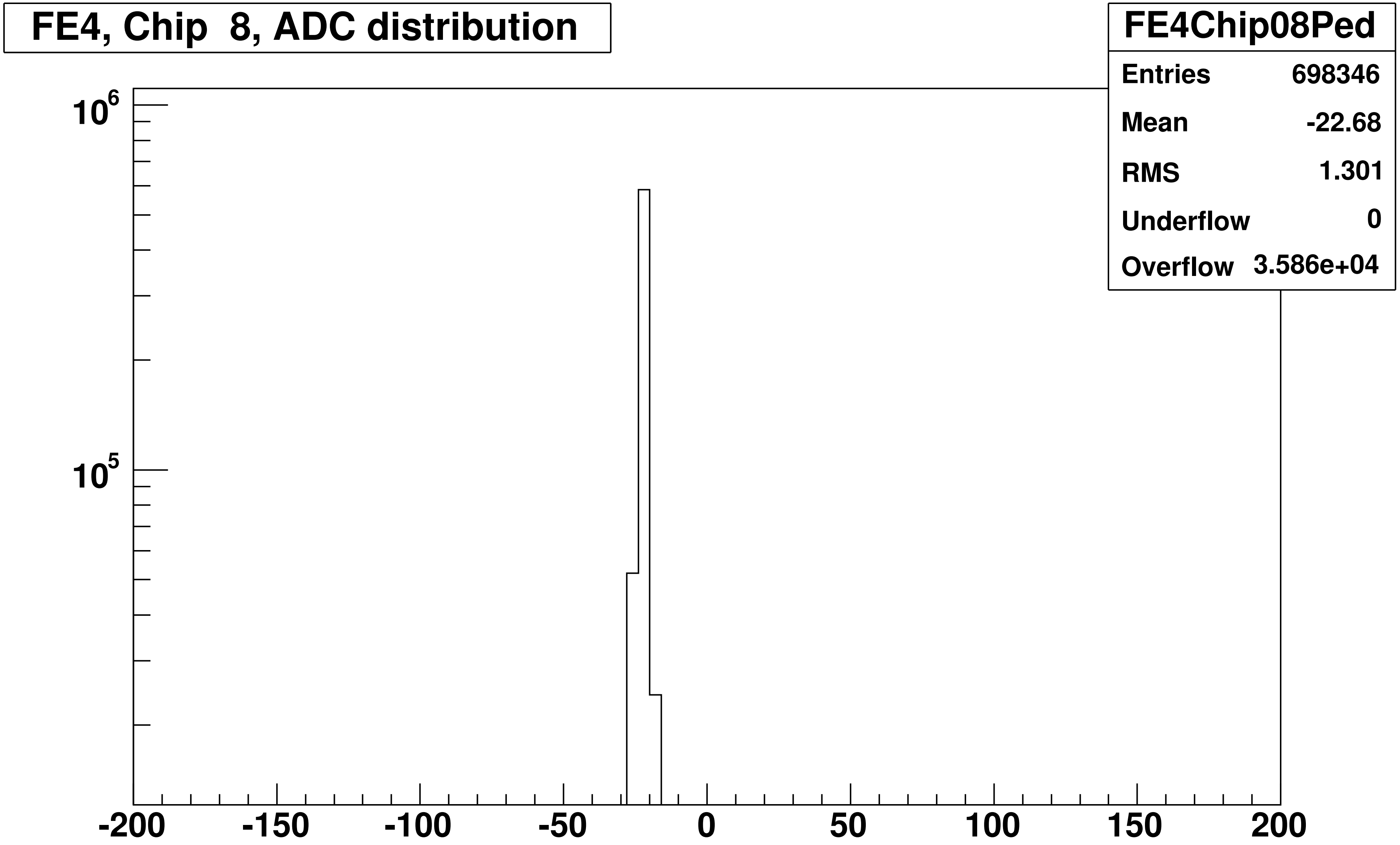
FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 698346
Mean -22.68
RMS 1.301
Underflow 0
Overflow 3.586e+04
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10^5
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0
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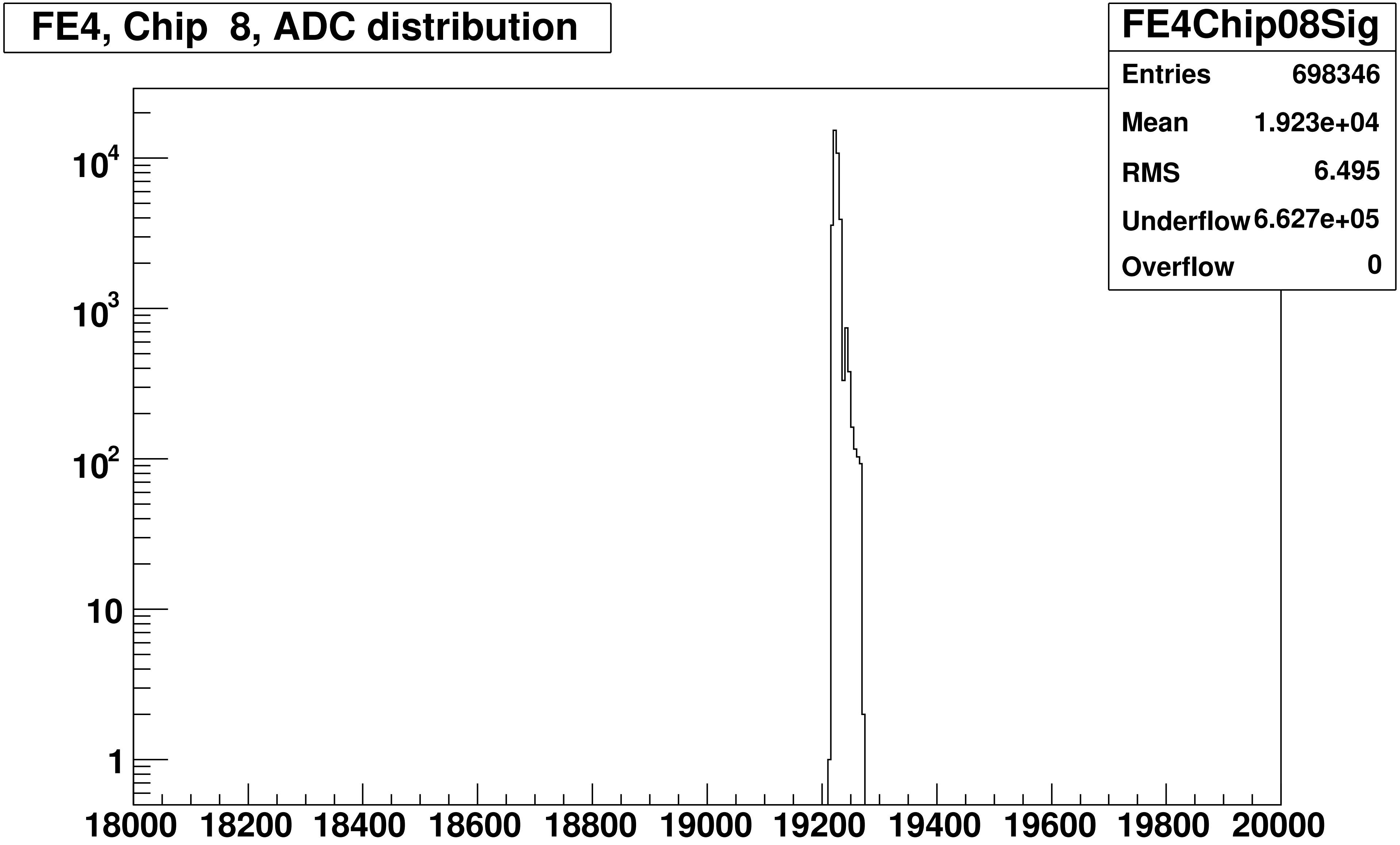
100
150
200


FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 698346
Mean 1.923e+04
RMS 6.495
Underflow 6.627e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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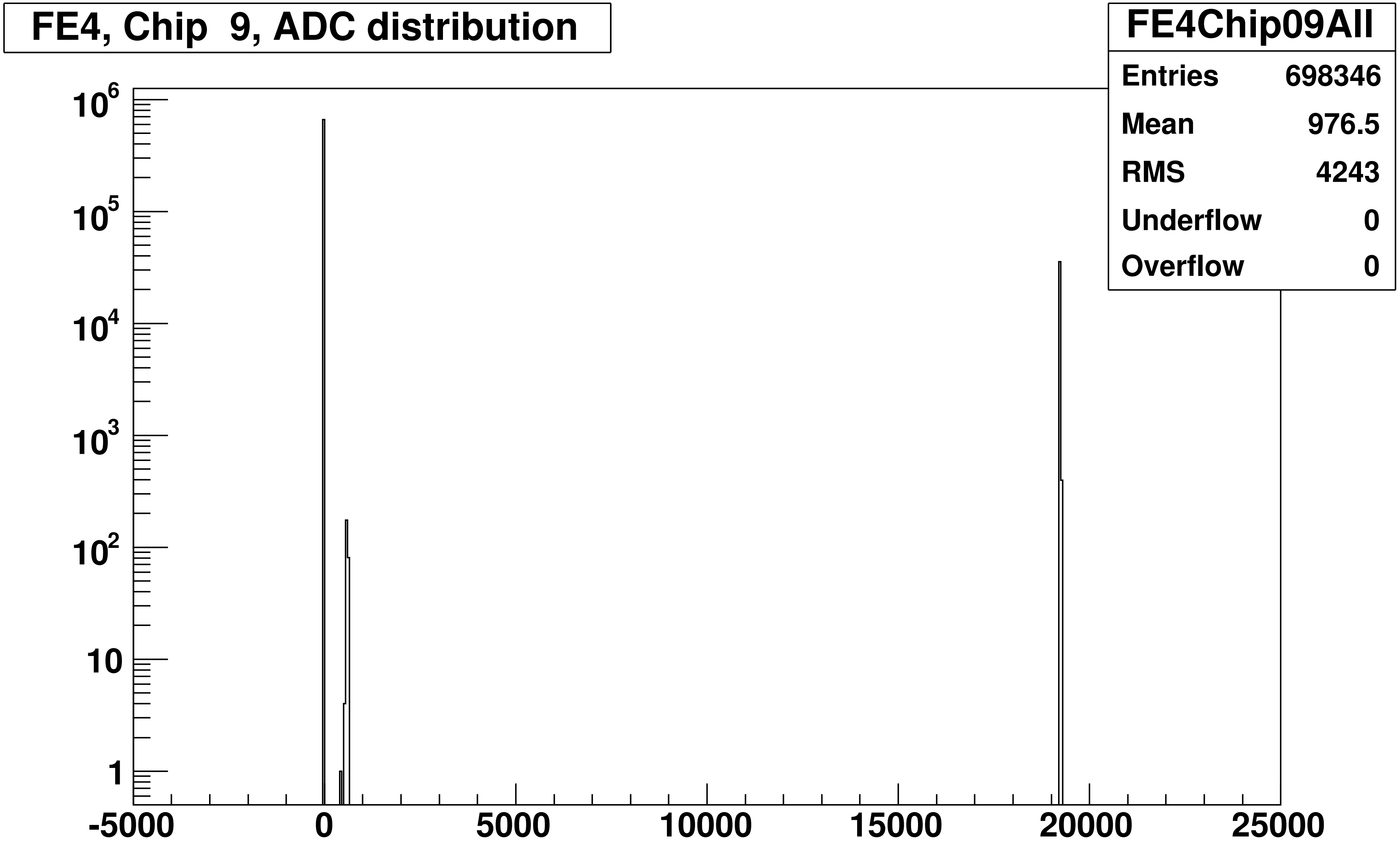
19000
19200
19400
19600
19800
20000



FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 698346
Mean 976.5
RMS 4243
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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0
5000
10000
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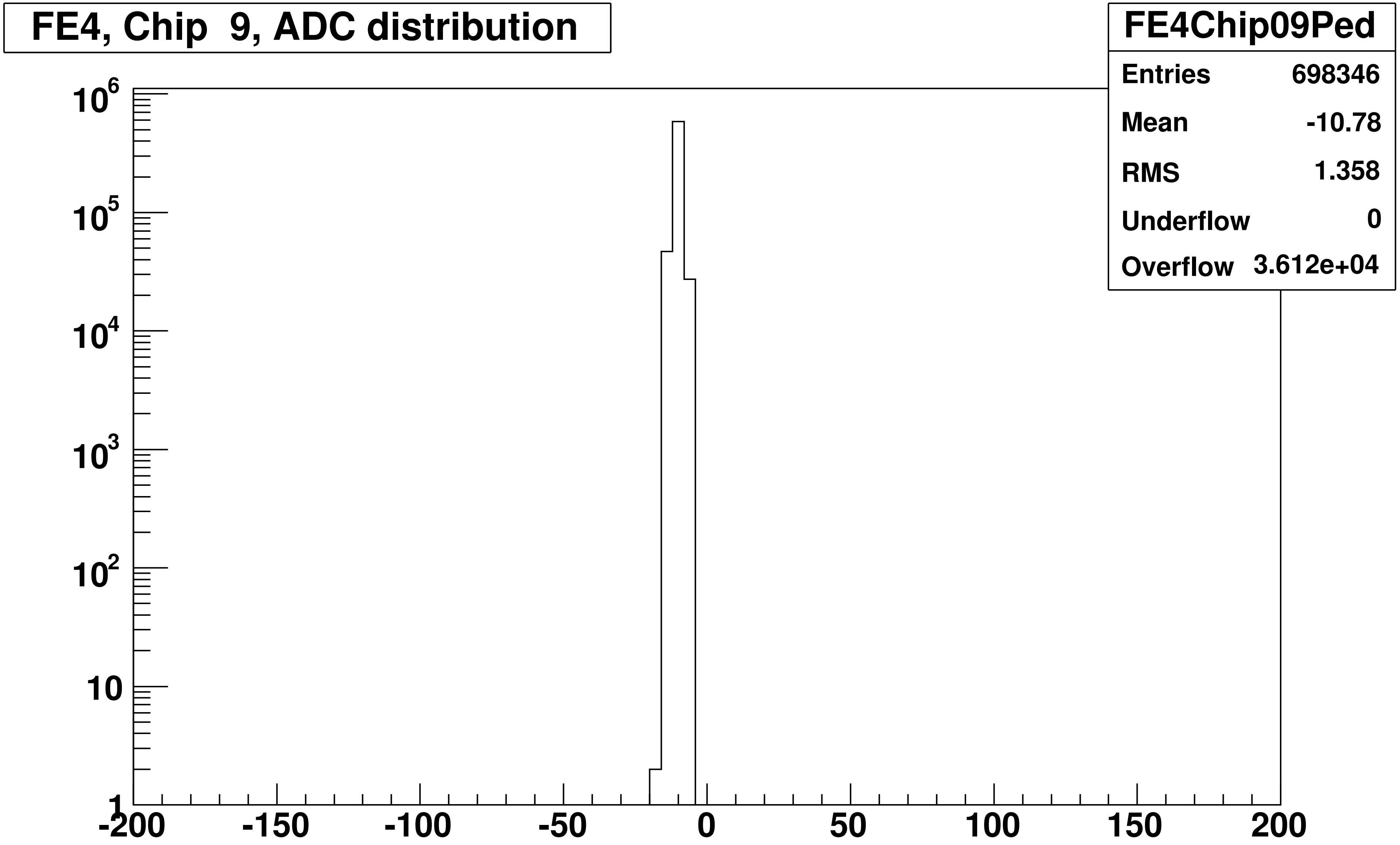
FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 698346
Mean -10.78
RMS 1.358
Underflow 0
Overflow 3.612e+04
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10^3
10^2
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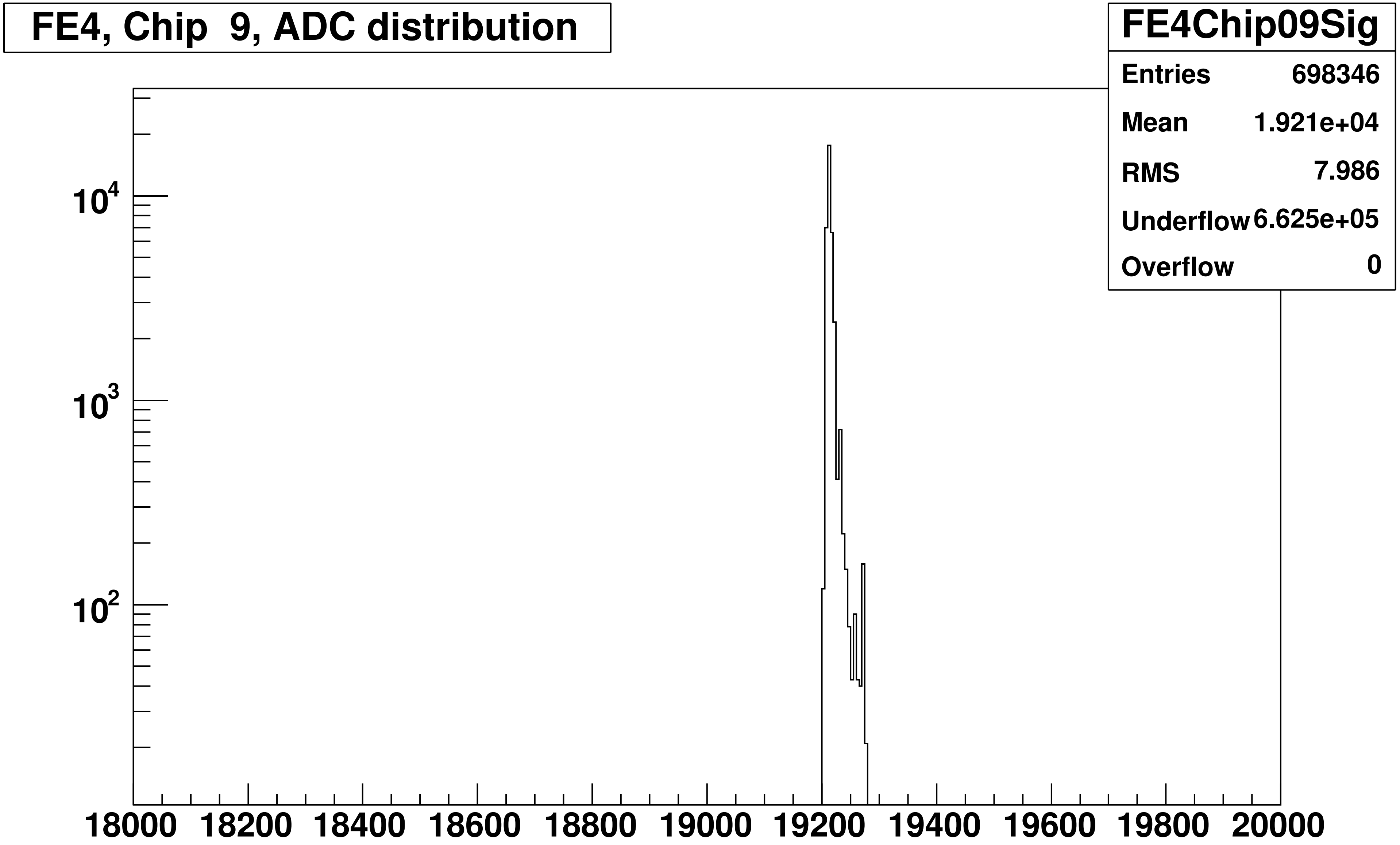

FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 698346
Mean 1.921e+04
RMS 7.986
Underflow 6.625e+05
Overflow 0
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10^3
10^2
18000
18200
18400
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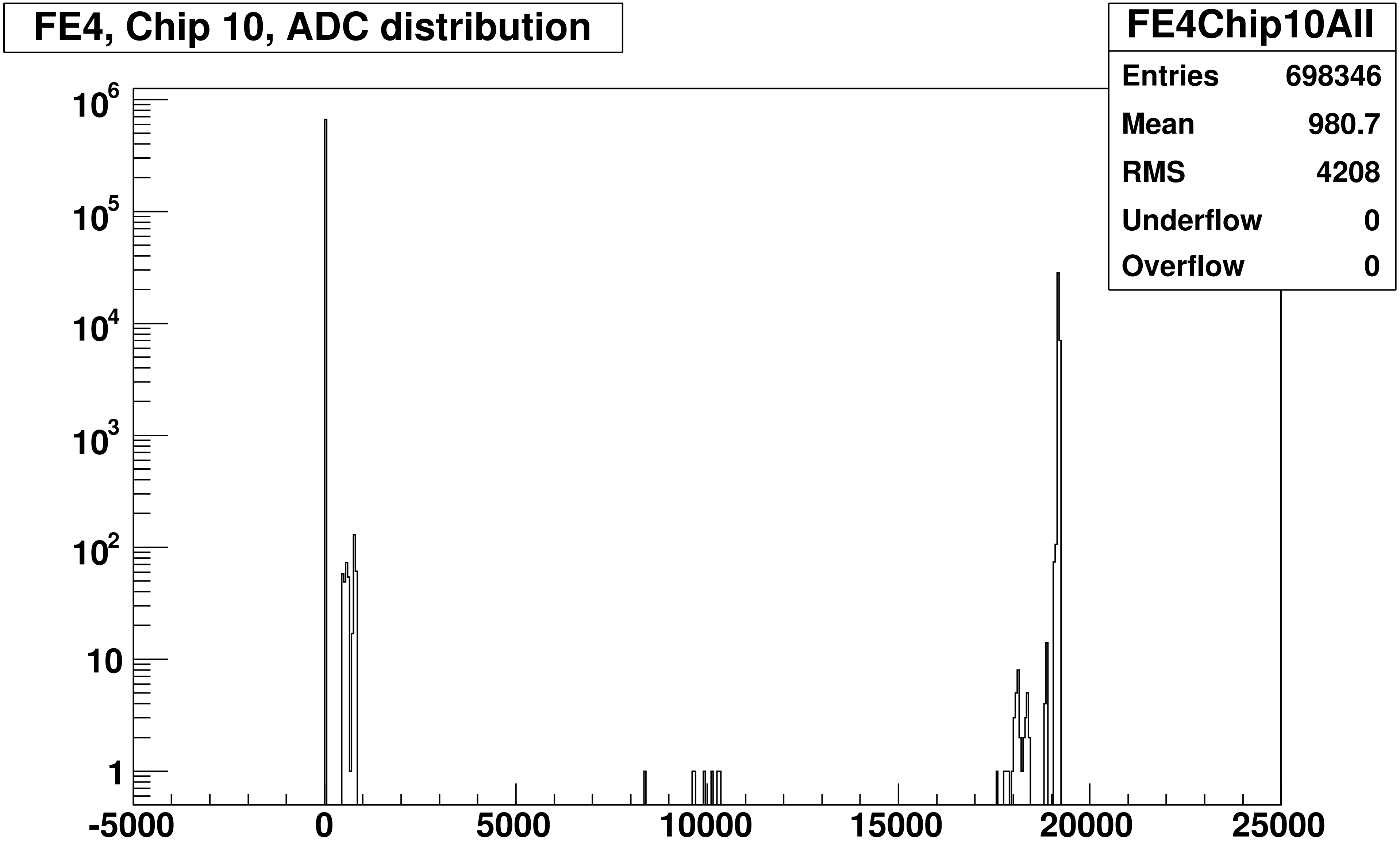
FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 698346
Mean 980.7
RMS 4208
Underflow 0
Overflow 0
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10^3
10^2
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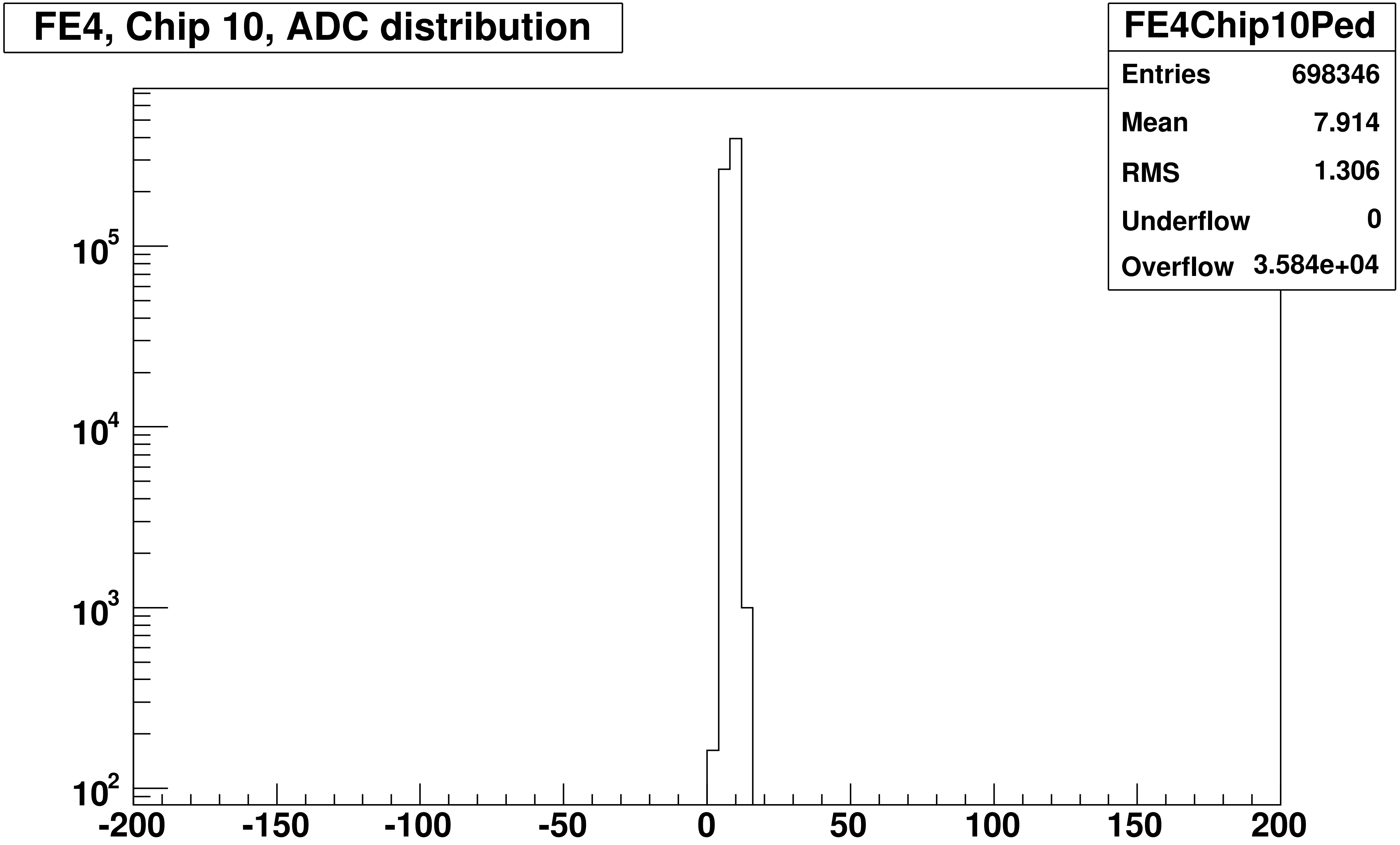

FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 698346
Mean 7.914
RMS 1.306
Underflow 0
Overflow 3.584e+04
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10^4
10^3
10^2
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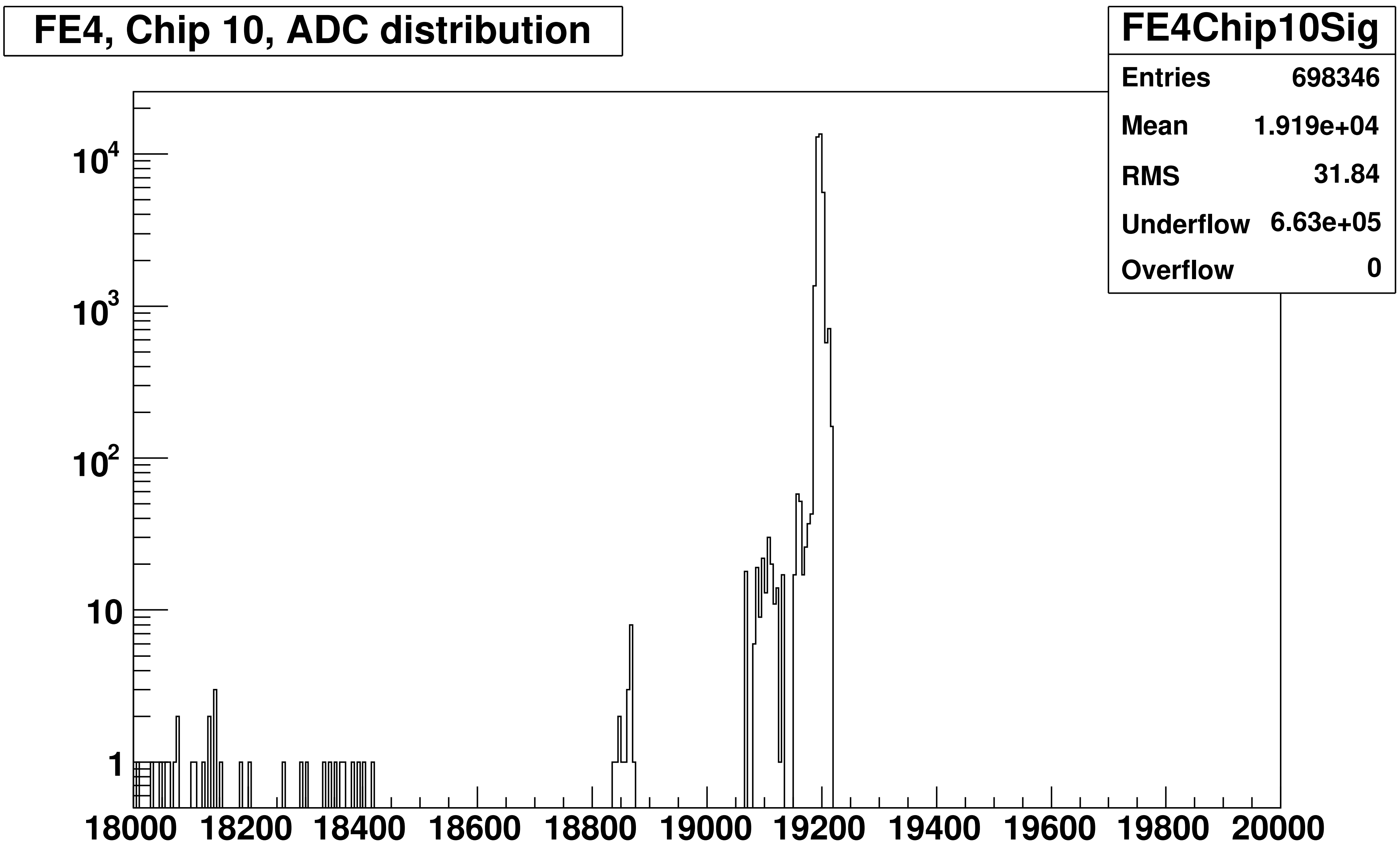
FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 698346
Mean 1.919e+04
RMS 31.84
Underflow 6.63e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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19000
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19800
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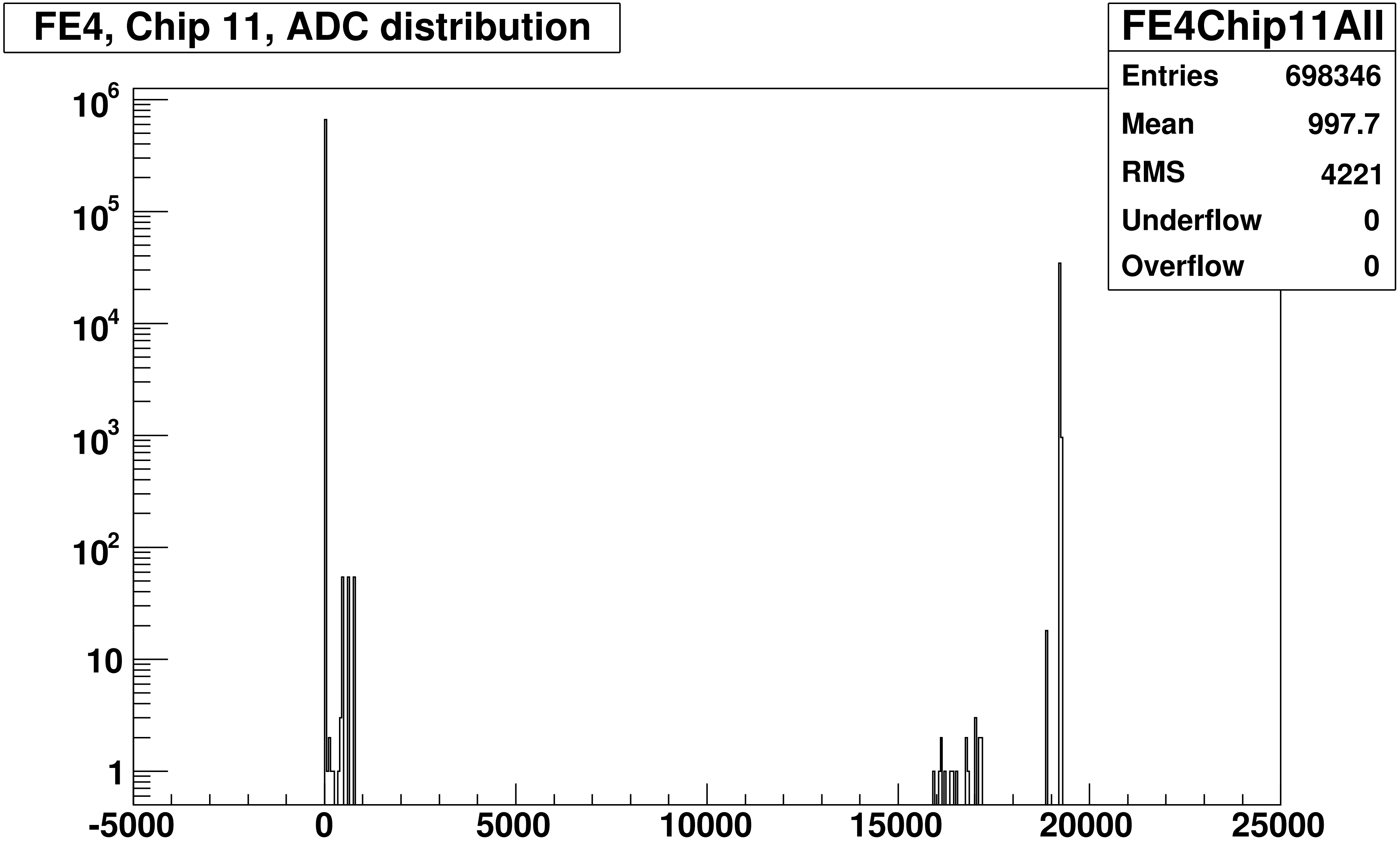

FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 698346
Mean 997.7
RMS 4221
Underflow 0
Overflow 0
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10^3
10^2
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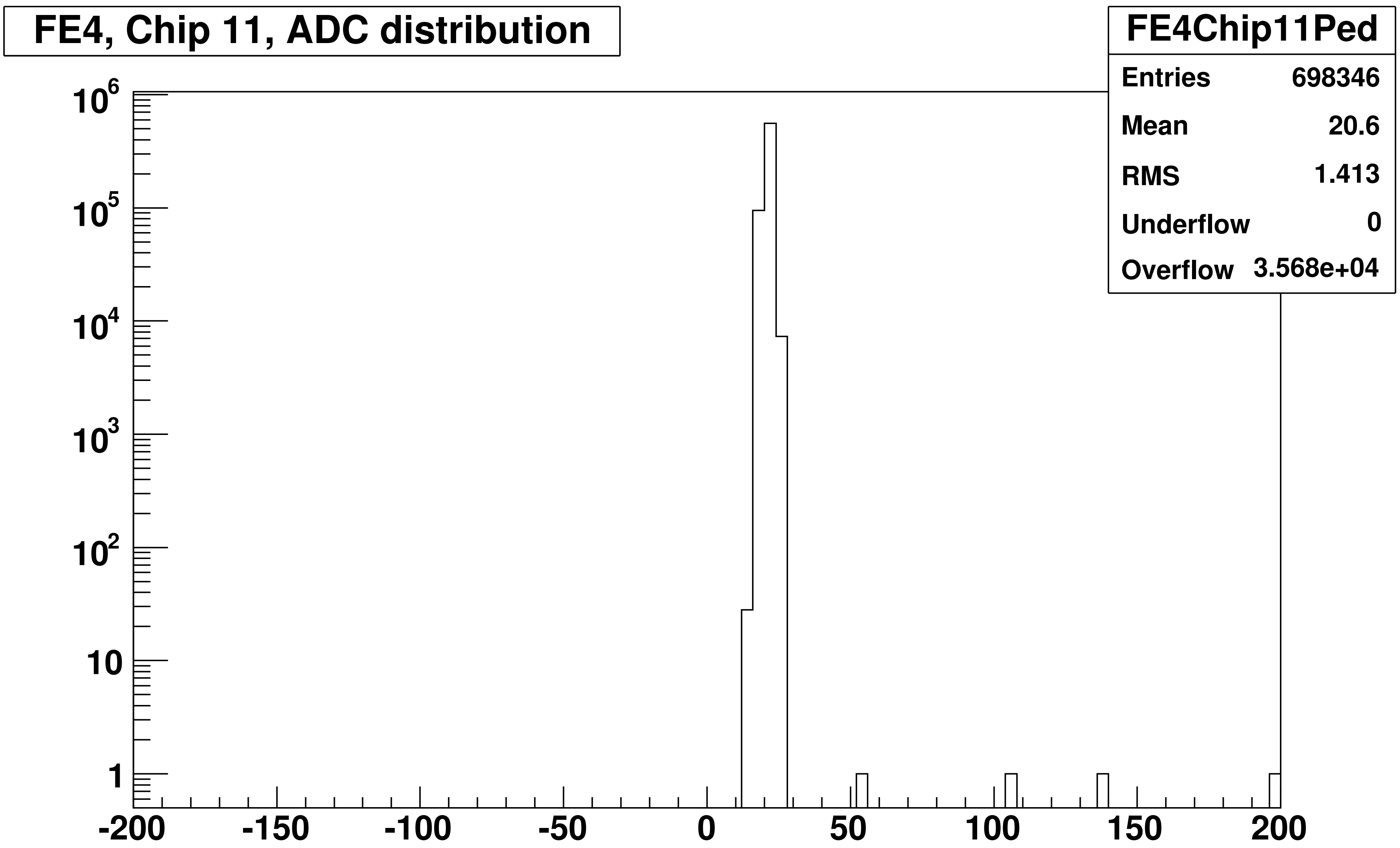

FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 698346
Mean 20.6
RMS 1.413
Underflow 0
Overflow 3.568e+04
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10^3
10^2
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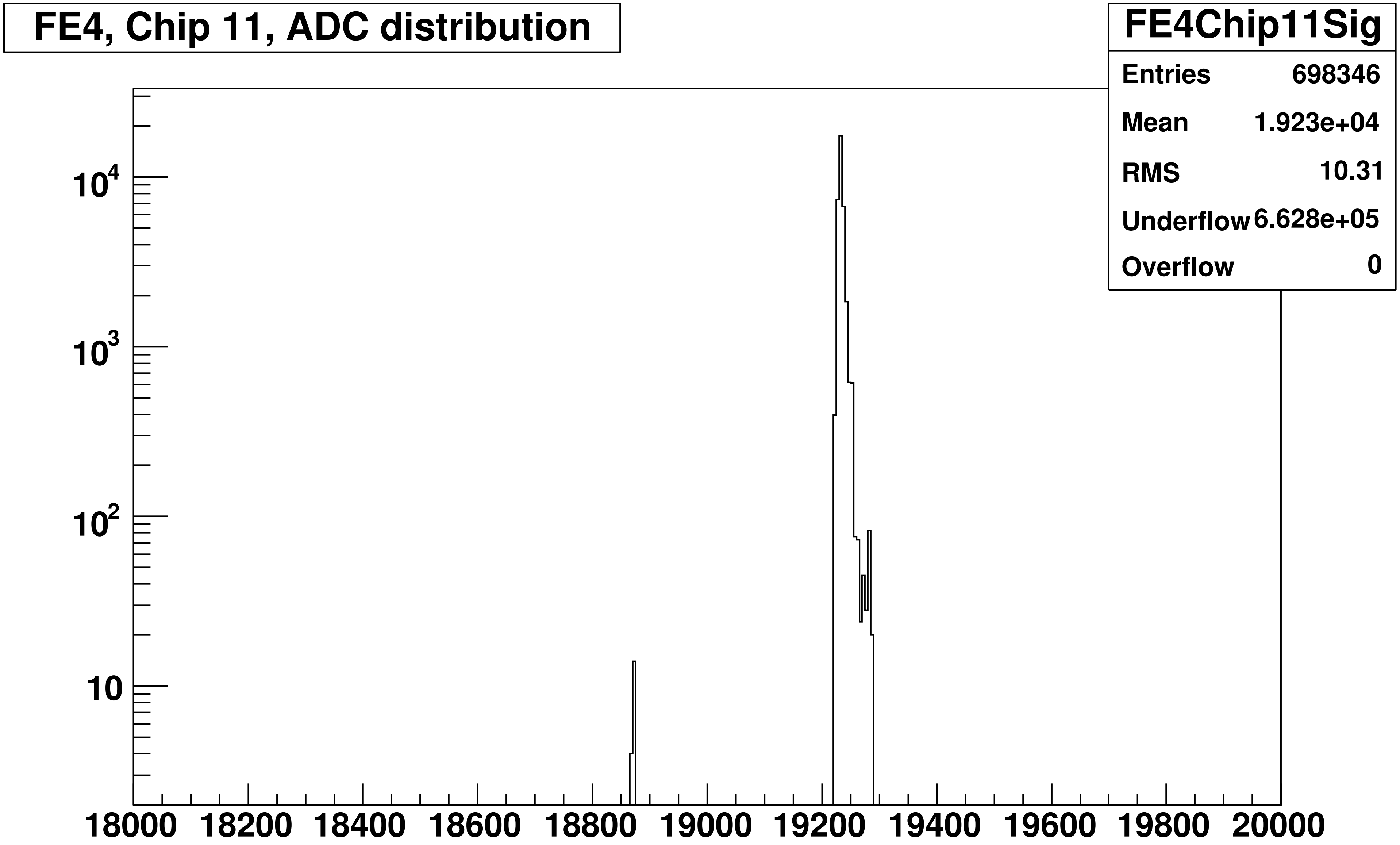
FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 698346
Mean 1.923e+04
RMS 10.31
Underflow 6.628e+05
Overflow 0
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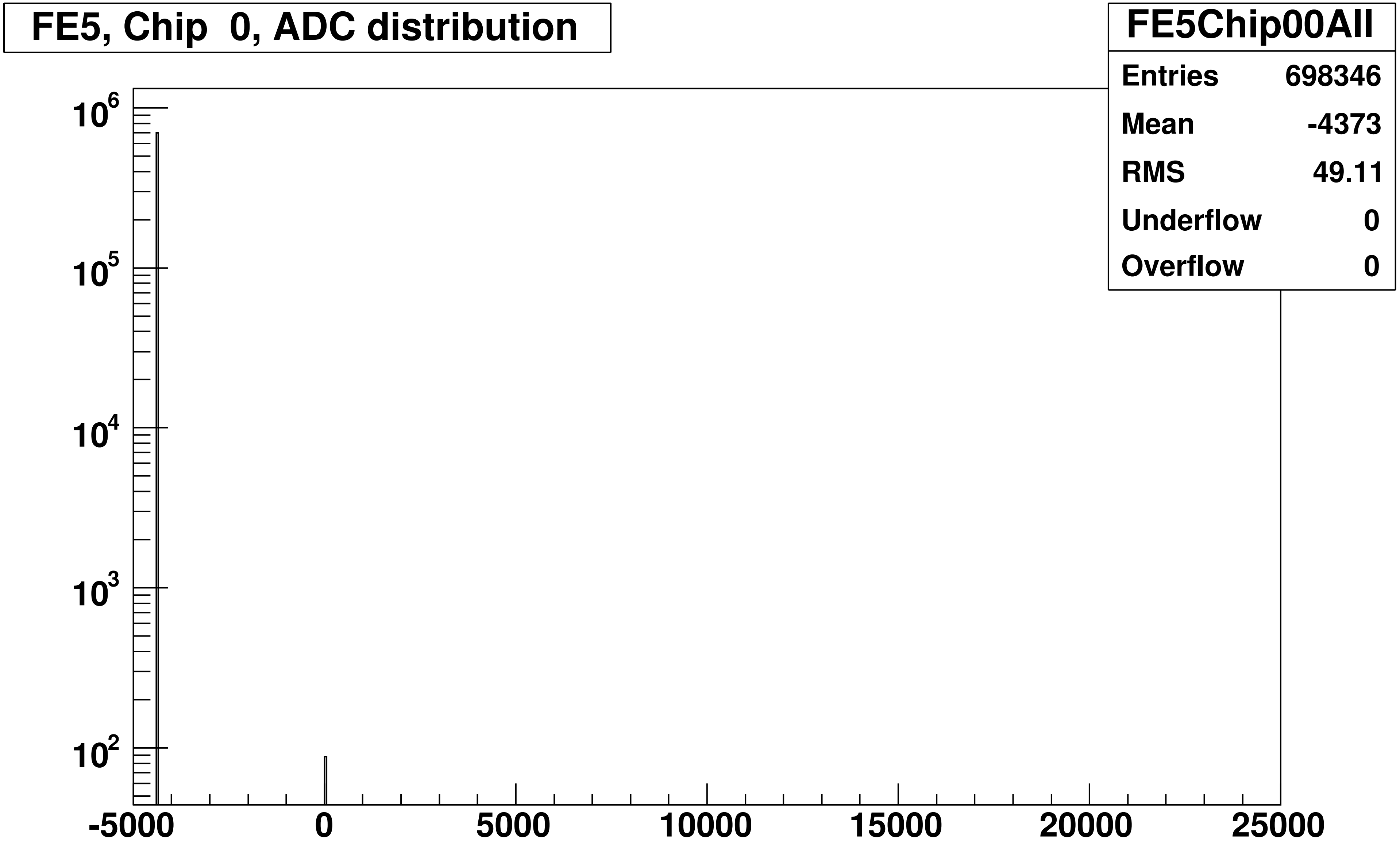
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 698346
Mean -4373
RMS 49.11
Underflow 0
Overflow 0
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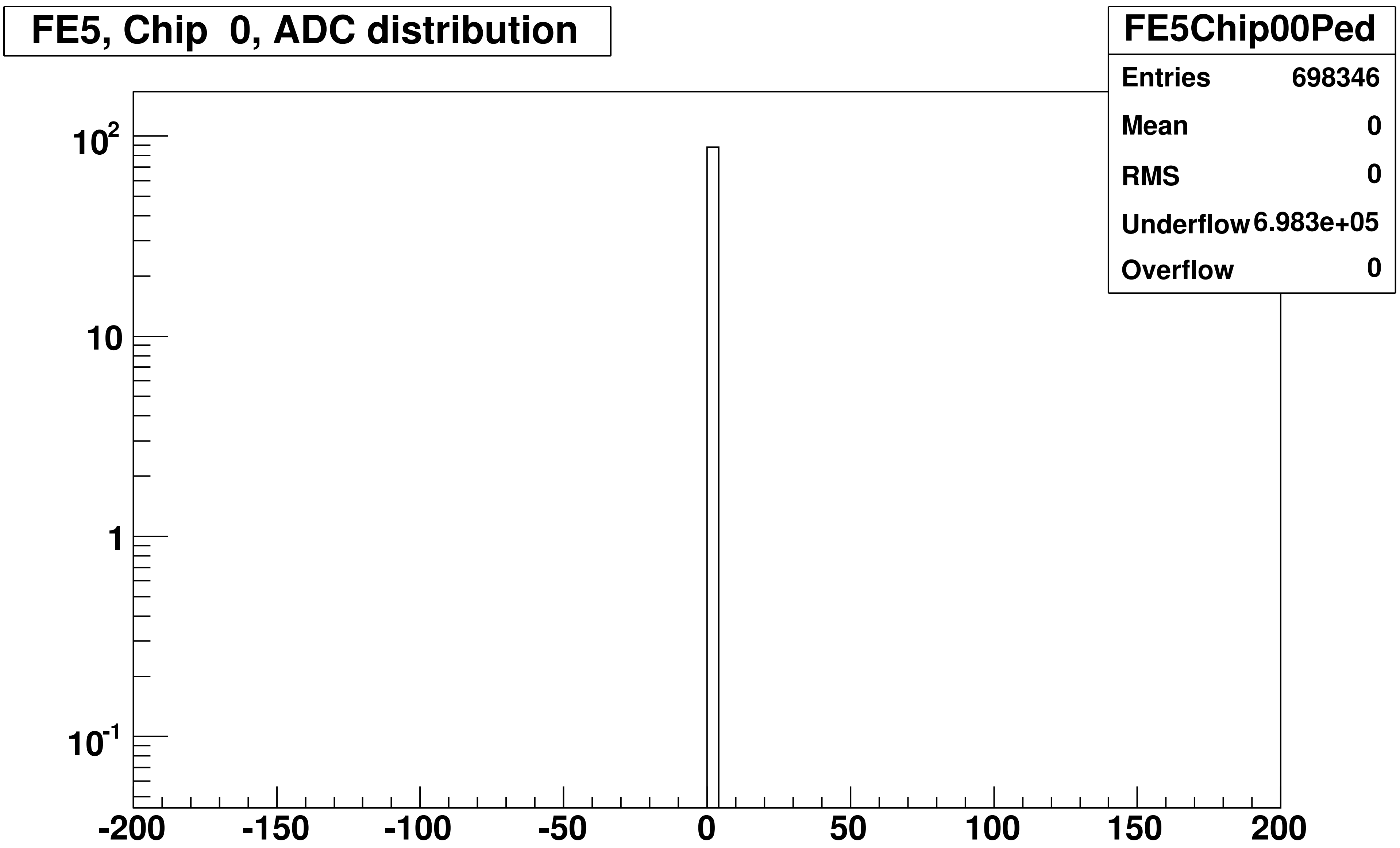

FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 698346
Mean 0
RMS 0
Underflow 6.983e+05
Overflow 0
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1
10^{-1}
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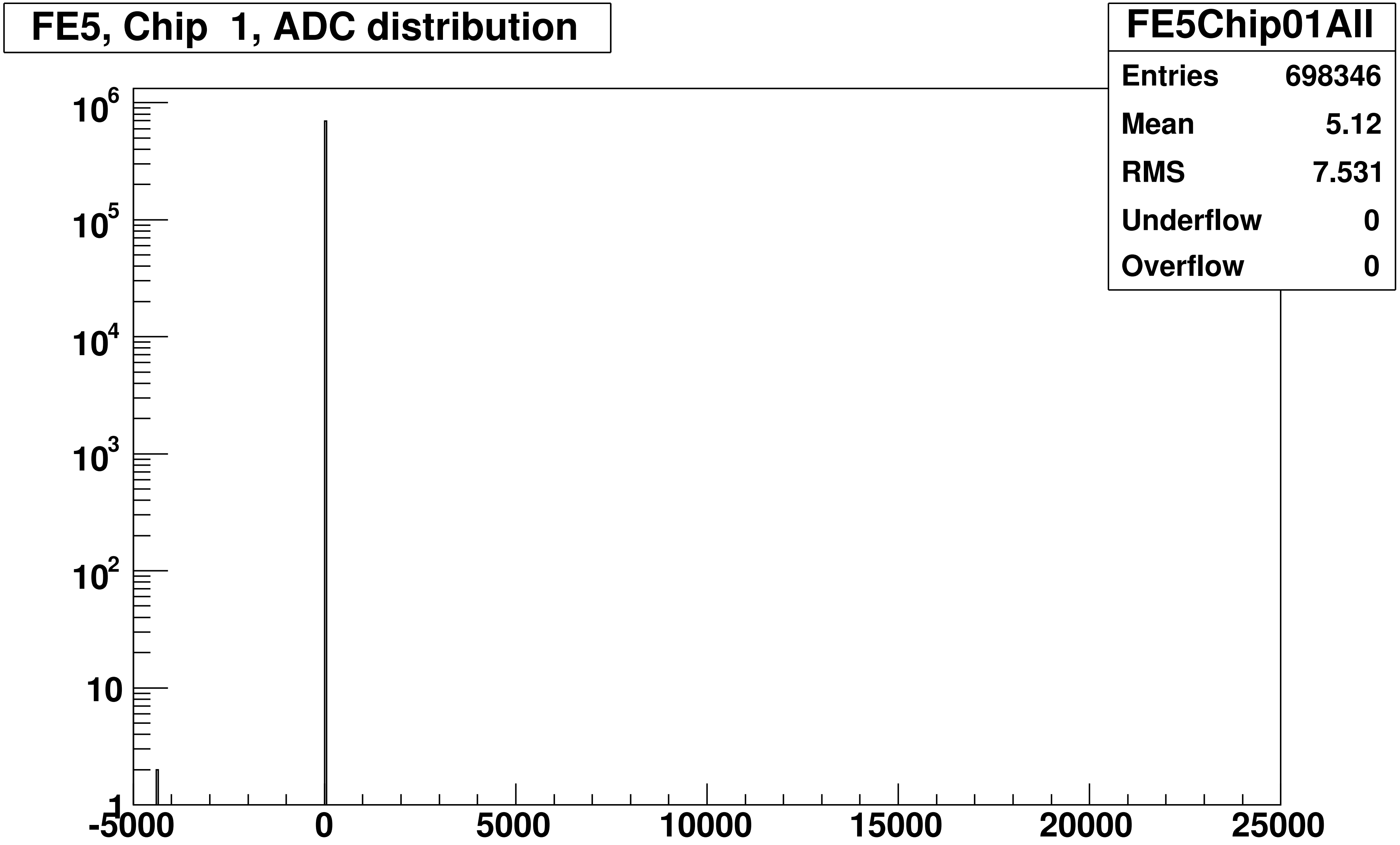
FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 698346
Mean 5.12
RMS 7.531
Underflow 0
Overflow 0
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10^3
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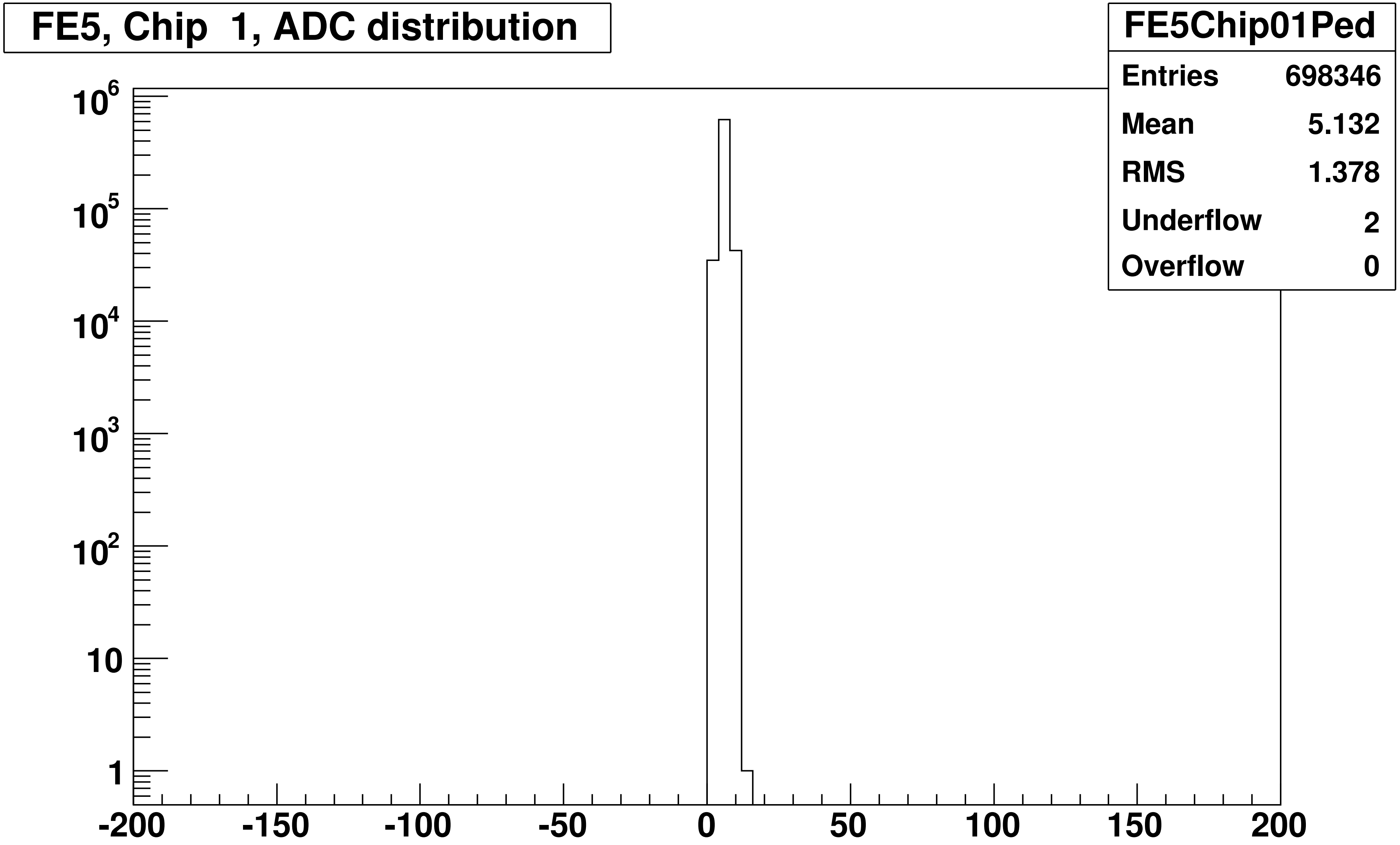
FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 698346
Mean 5.132
RMS 1.378
Underflow 2
Overflow 0
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10^3
10^2
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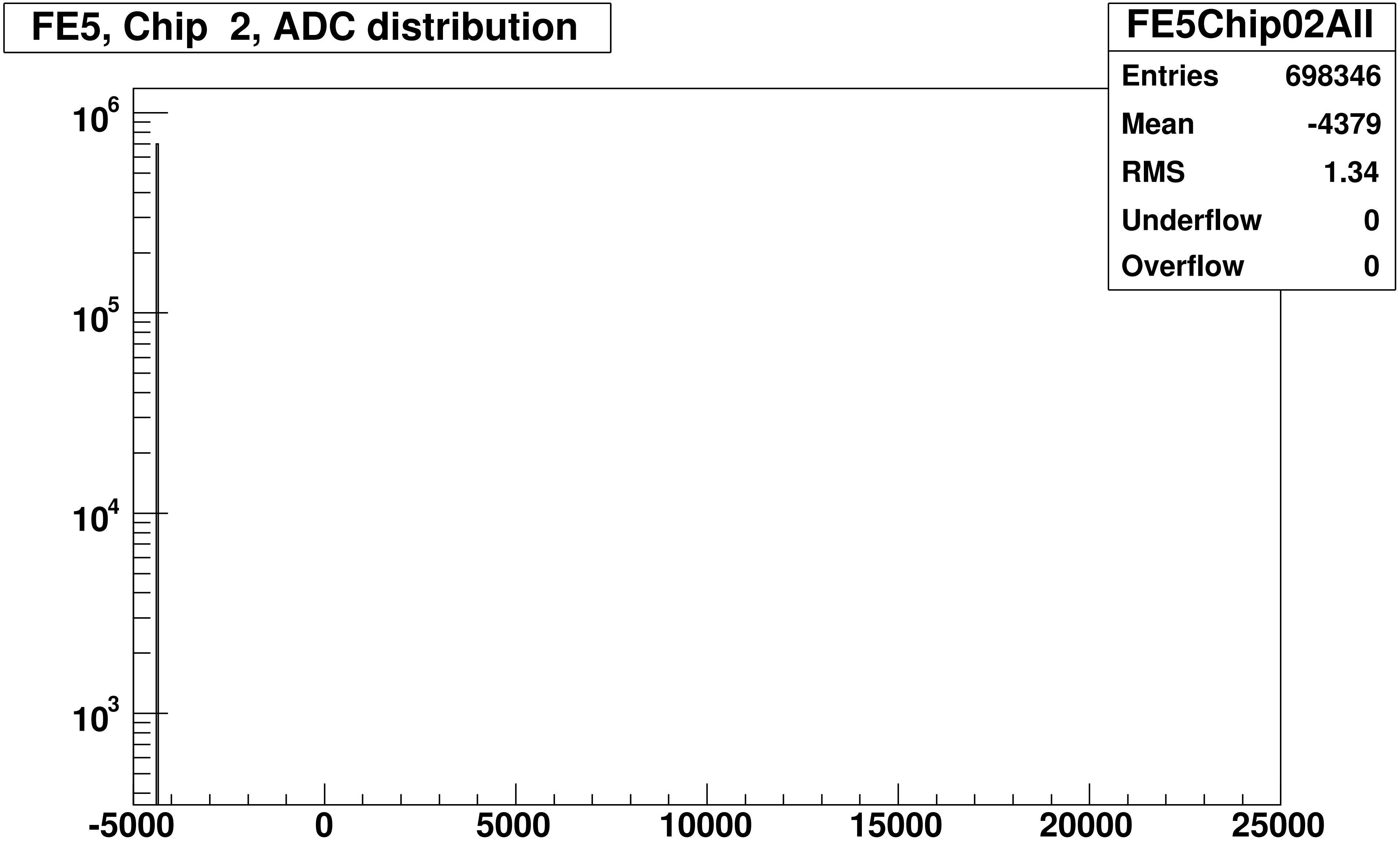
FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 698346
Mean -4379
RMS 1.34
Underflow 0
Overflow 0
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10^4
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0
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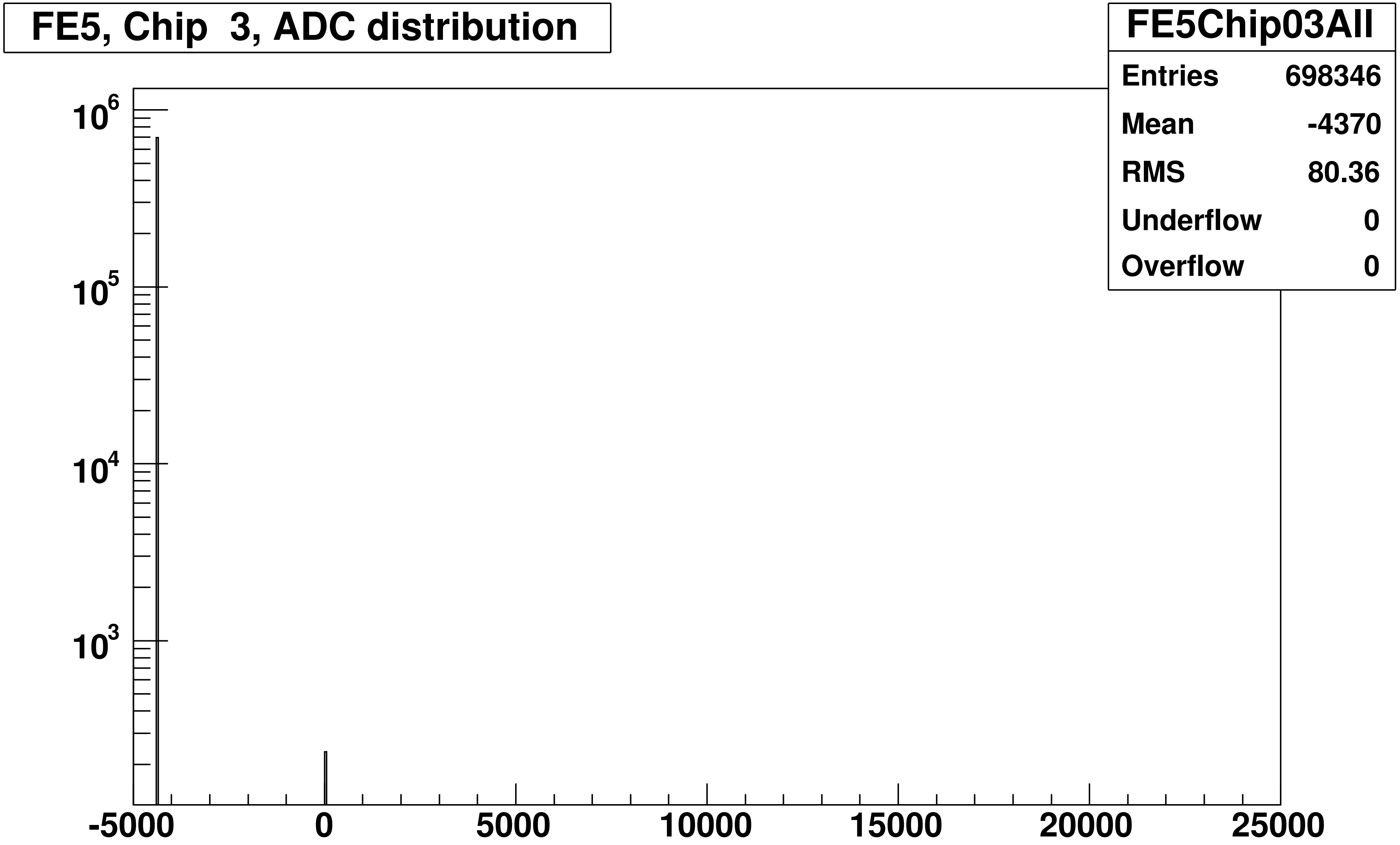
20000
25000


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 698346
Mean -4370
RMS 80.36
Underflow 0
Overflow 0
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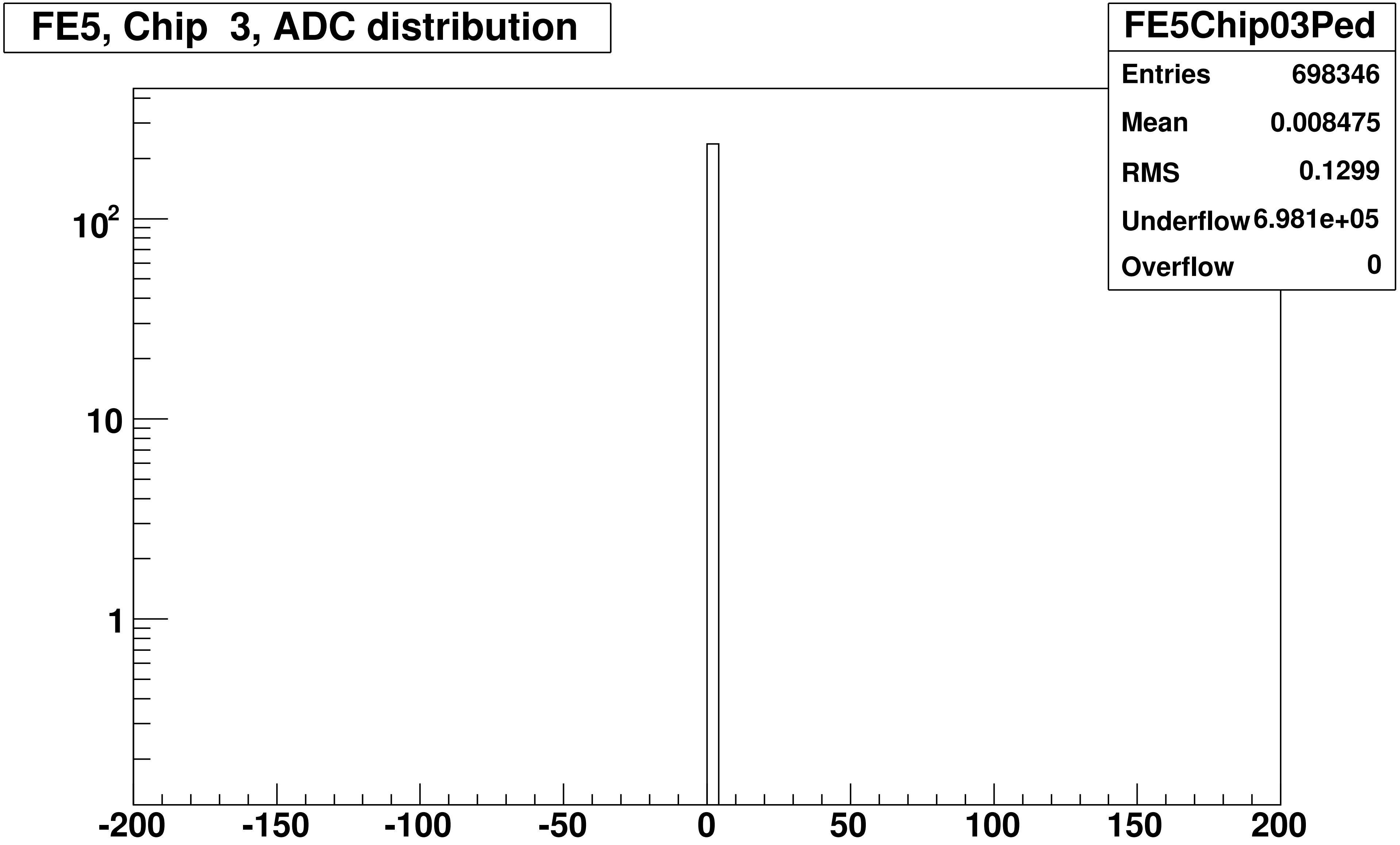
FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 698346
Mean 0.008475
RMS 0.1299
Underflow 6.981e+05
Overflow 0
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10
1
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-100
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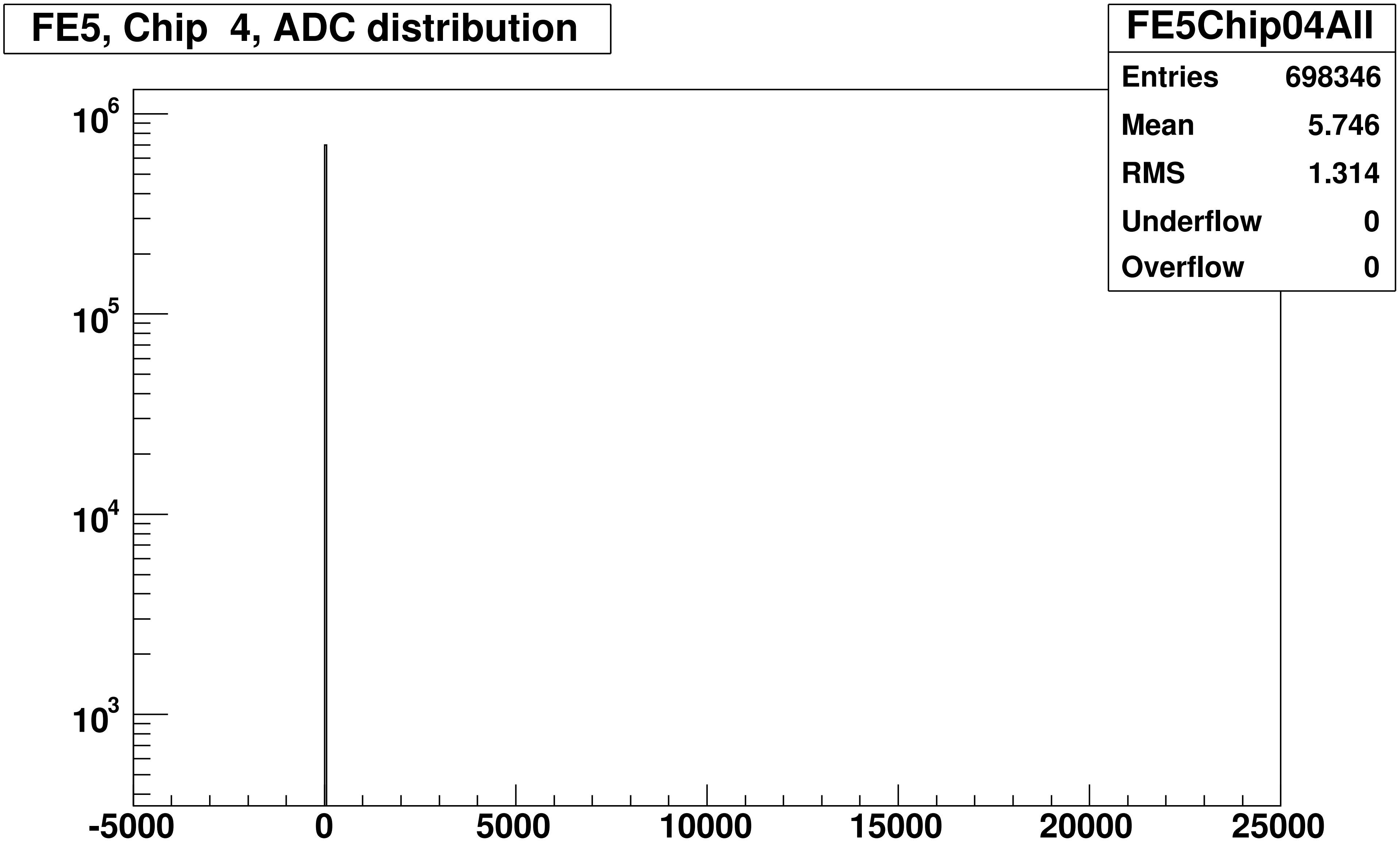
FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 698346
Mean 5.746
RMS 1.314
Underflow 0
Overflow 0
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10^5
10^4
10^3
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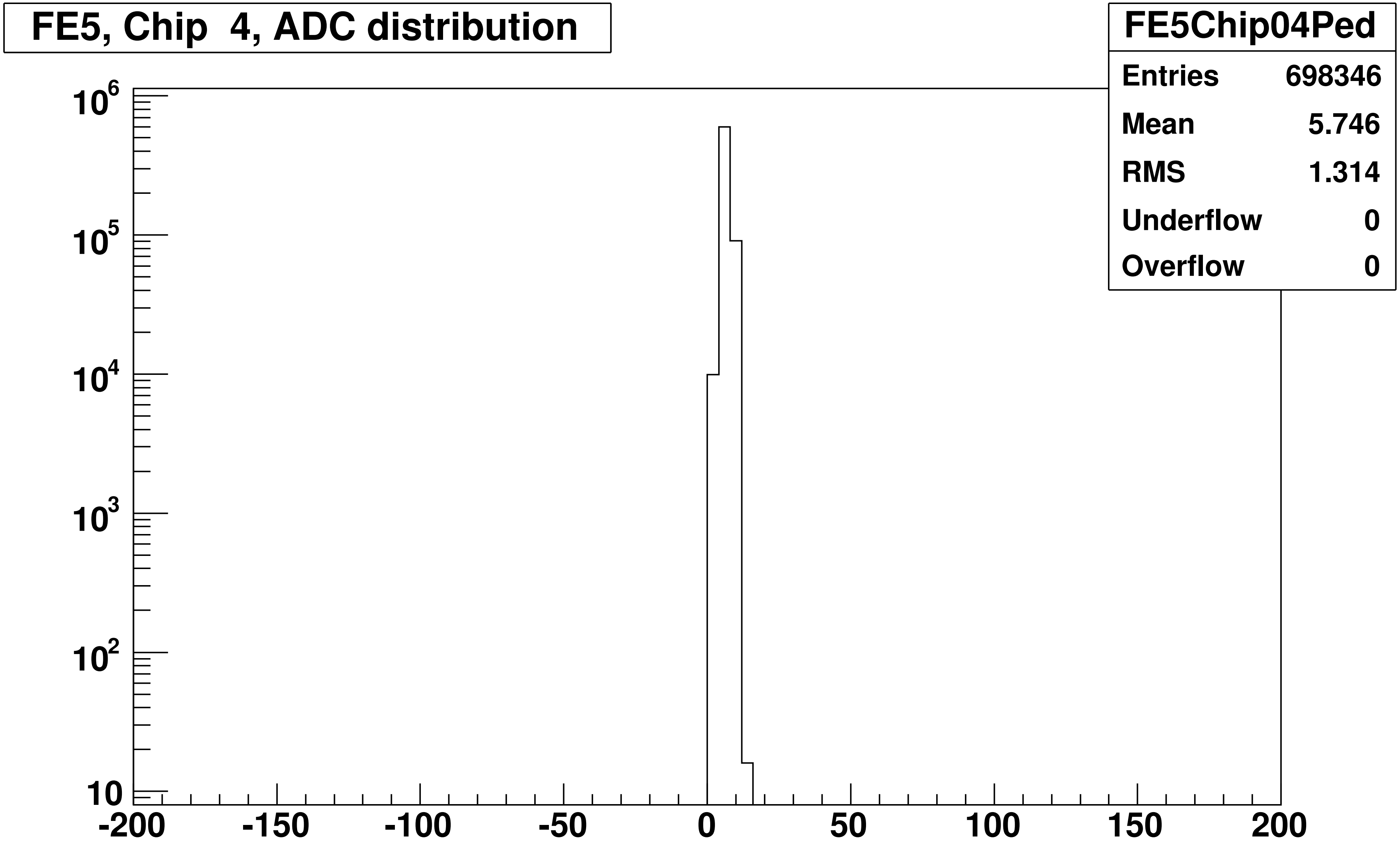
FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 698346
Mean 5.746
RMS 1.314
Underflow 0
Overflow 0
10
10^2
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10^4
10^5
10^6
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-100
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100
150
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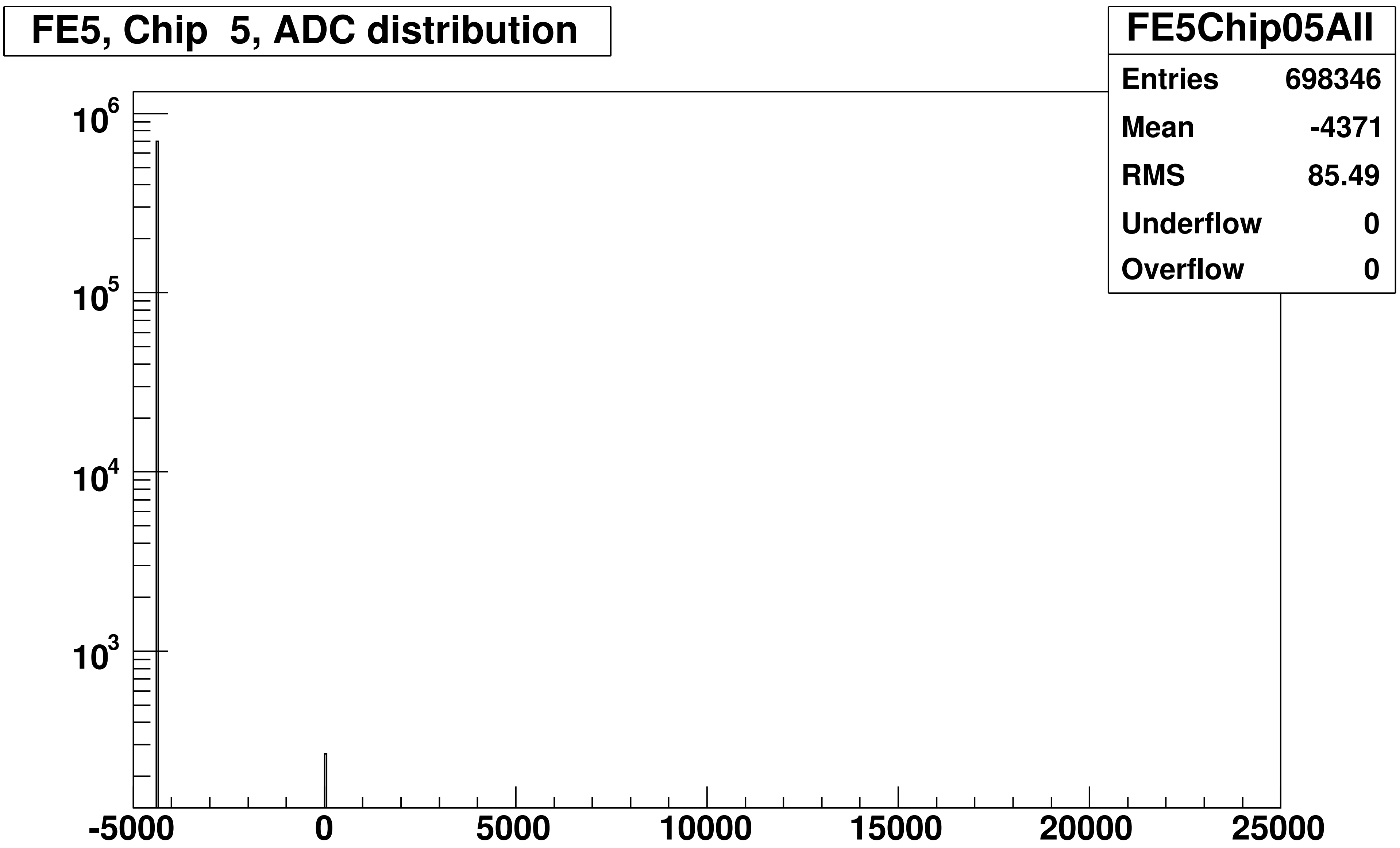

FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 698346
Mean -4371
RMS 85.49
Underflow 0
Overflow 0
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10^4
10^3
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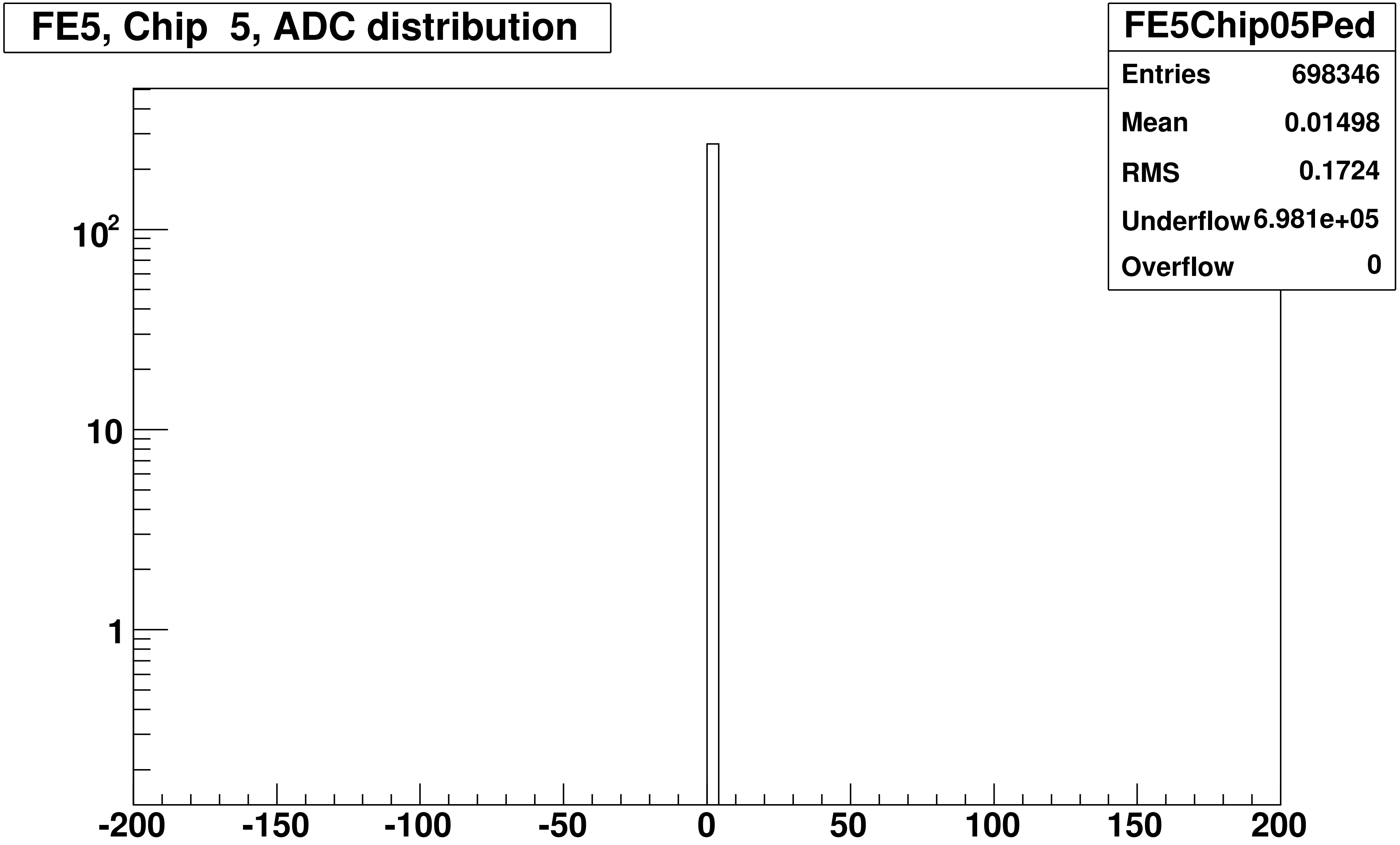

FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 698346
Mean 0.01498
RMS 0.1724
Underflow 6.981e+05
Overflow 0
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1
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-100
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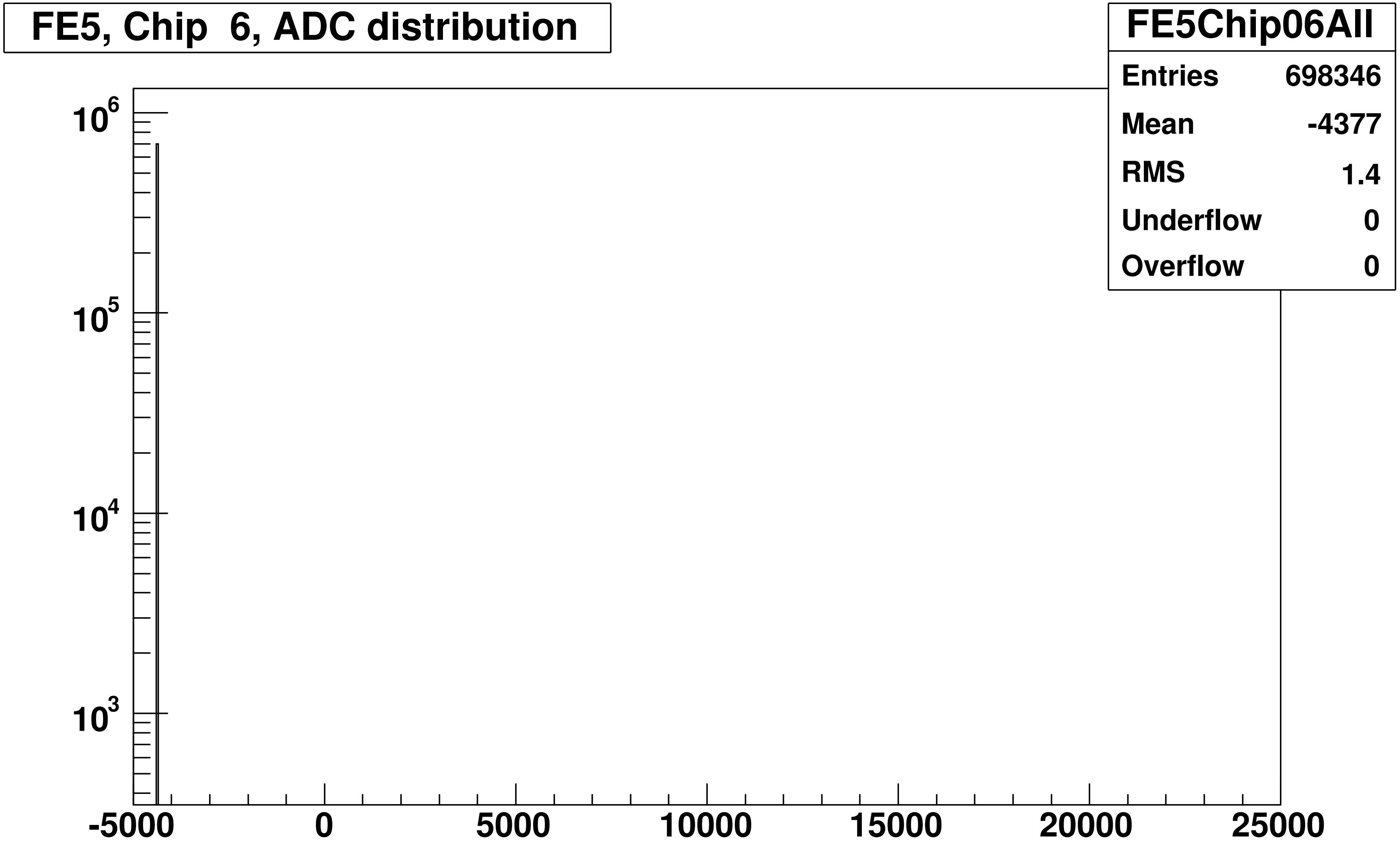
0
50
100
150
200


FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 698346
Mean -4377
RMS 1.4
Underflow 0
Overflow 0
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10^4
10^3
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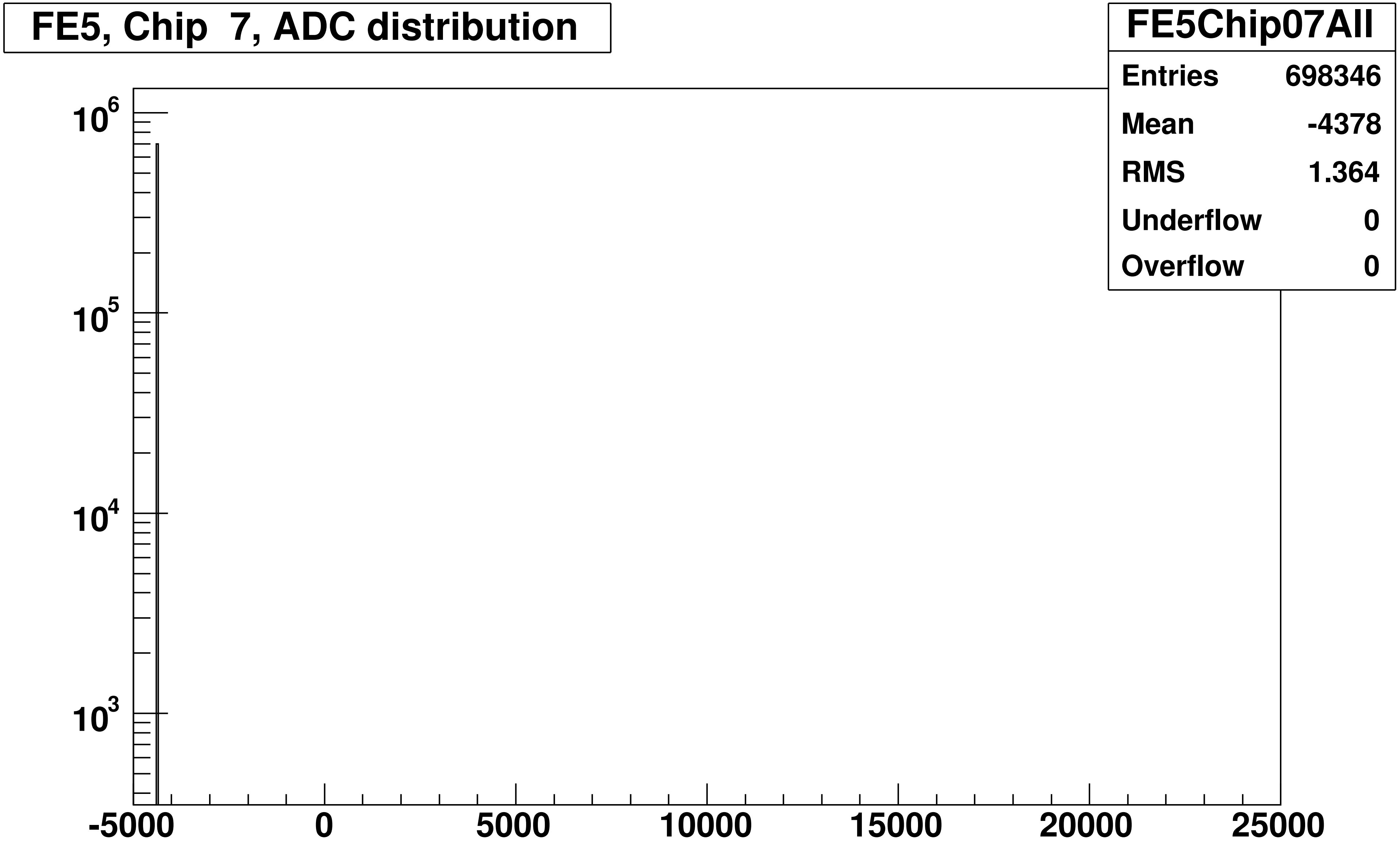
20000
25000



FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 698346
Mean -4378
RMS 1.364
Underflow 0
Overflow 0
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10^4
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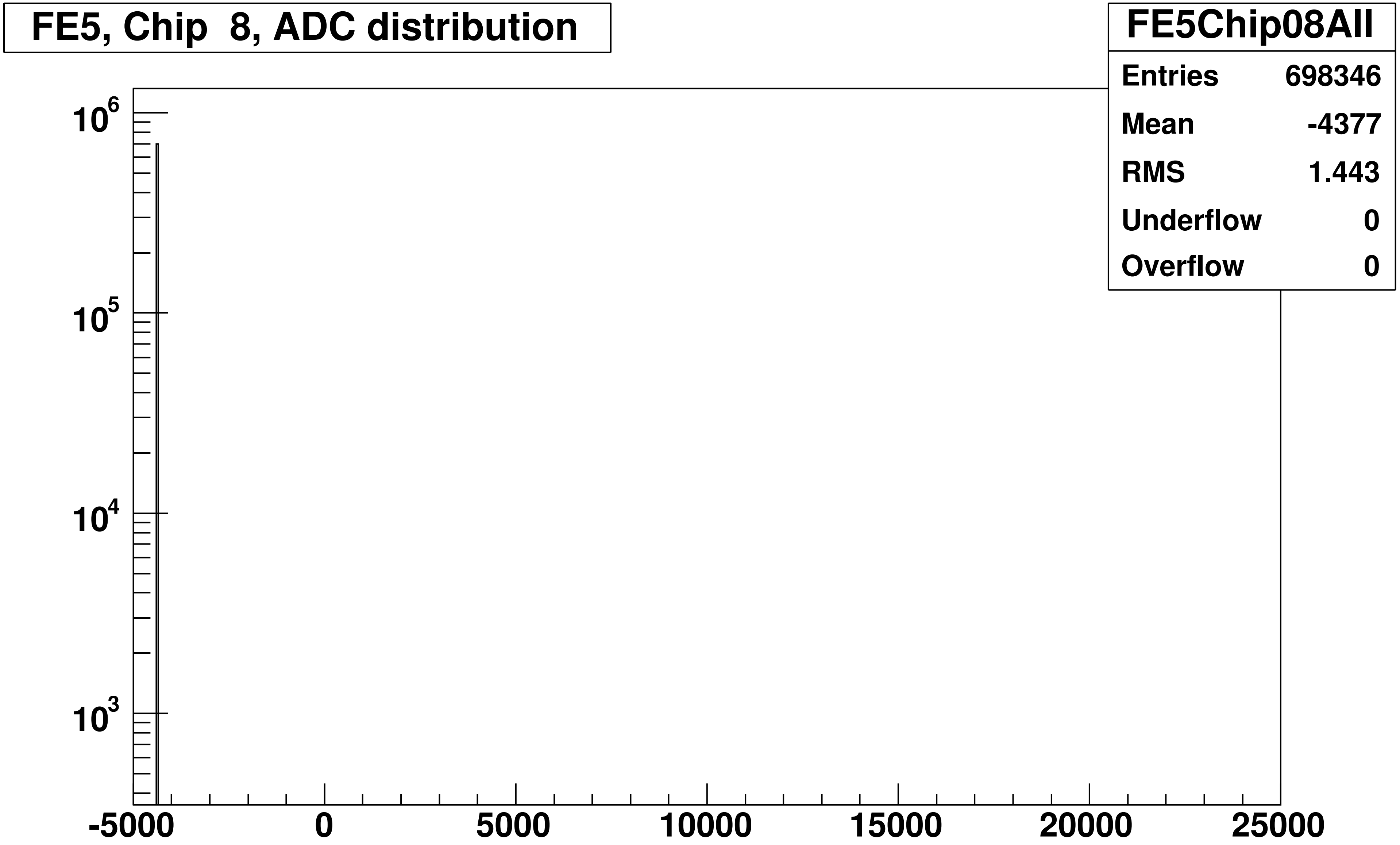

FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 698346
Mean -4377
RMS 1.443
Underflow 0
Overflow 0
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10^4
10^3
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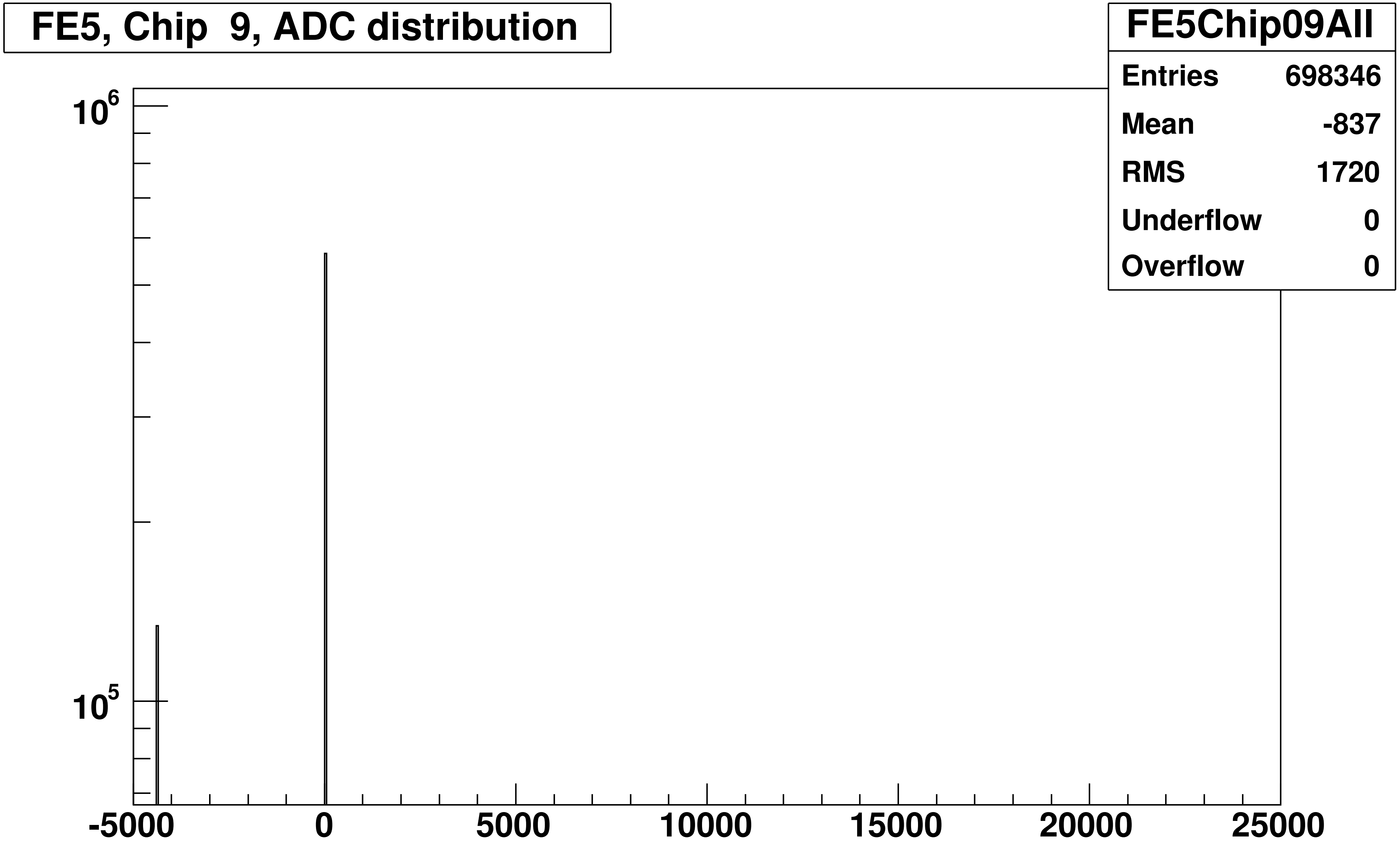

FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 698346
Mean -837
RMS 1720
Underflow 0
Overflow 0
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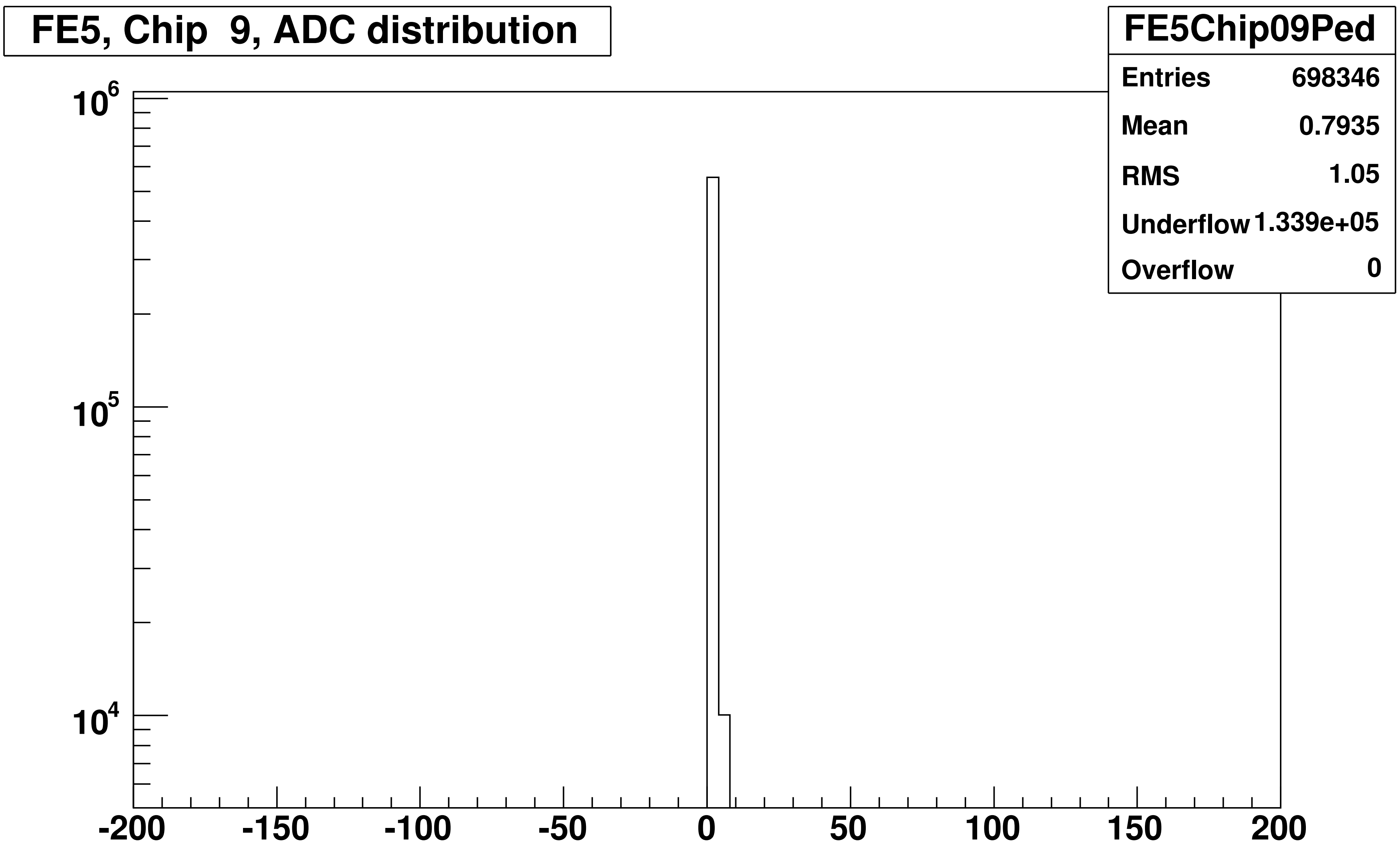
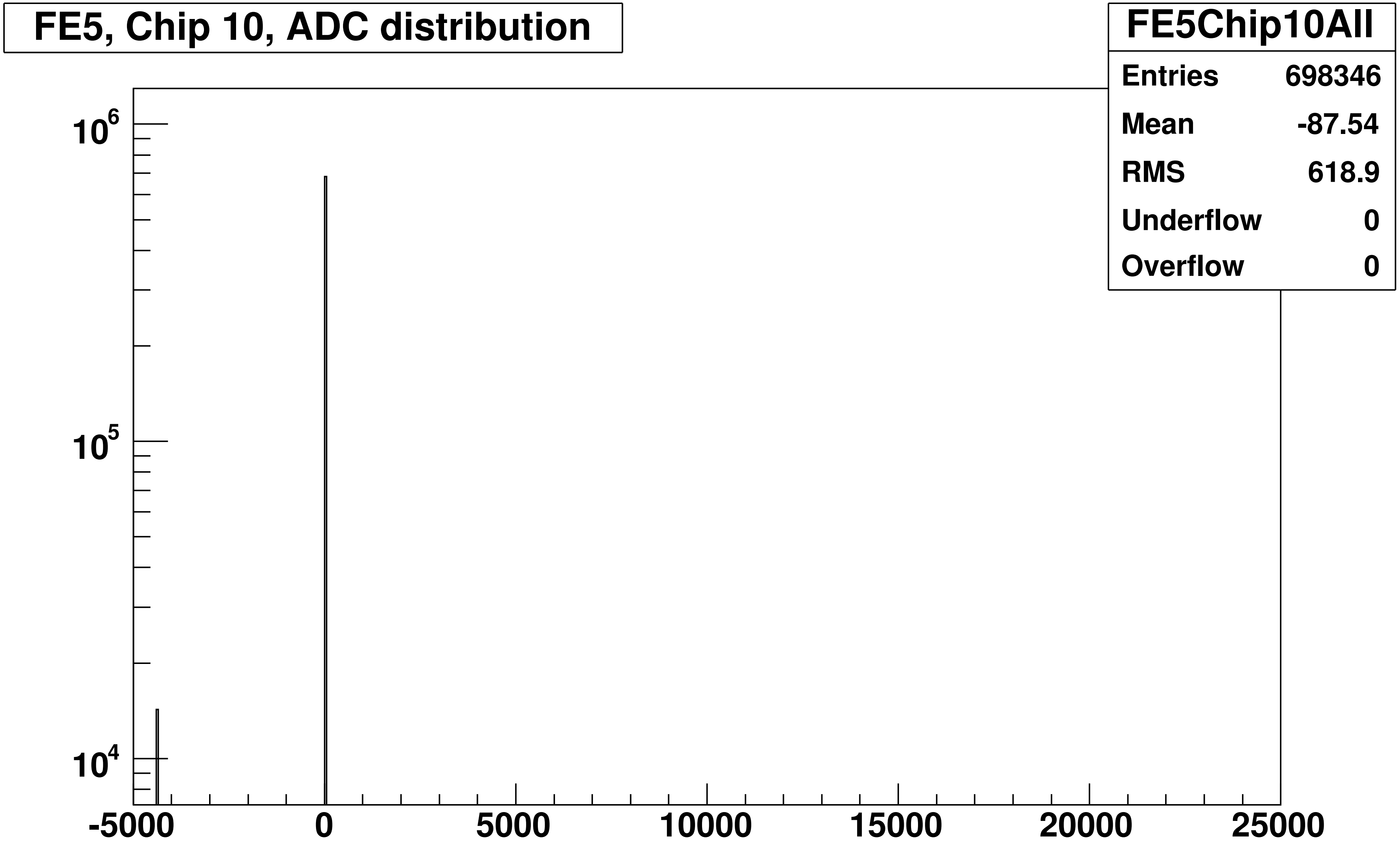

FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 698346
Mean 0.7935
RMS 1.05
Underflow 1.339e+05
Overflow 0
10^6
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 698346
Mean -87.54
RMS 618.9
Underflow 0
Overflow 0
10^6
10^5
10^4
-5000
0
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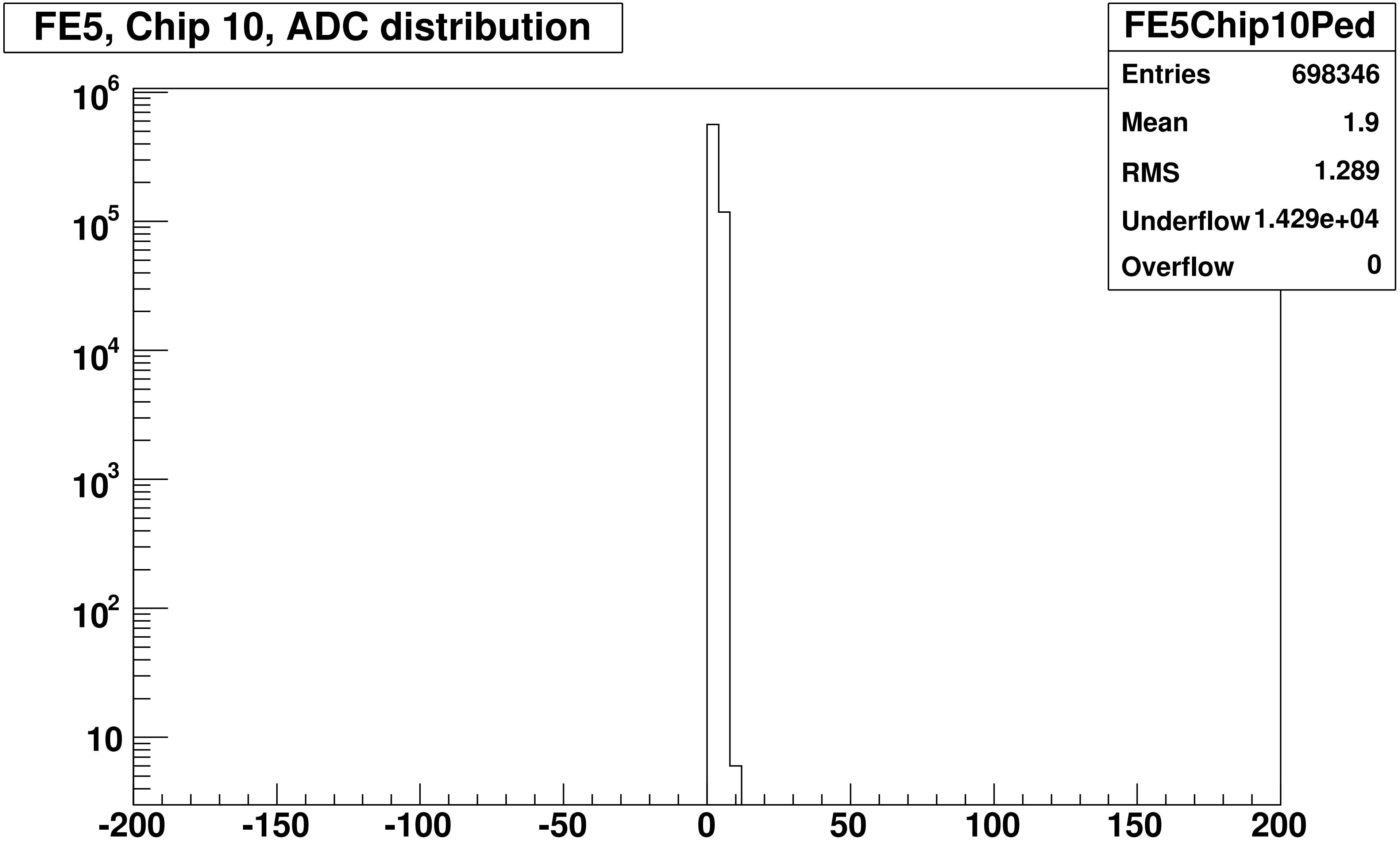
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 698346
Mean 1.9
RMS 1.289
Underflow 1.429e+04
Overflow 0
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10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
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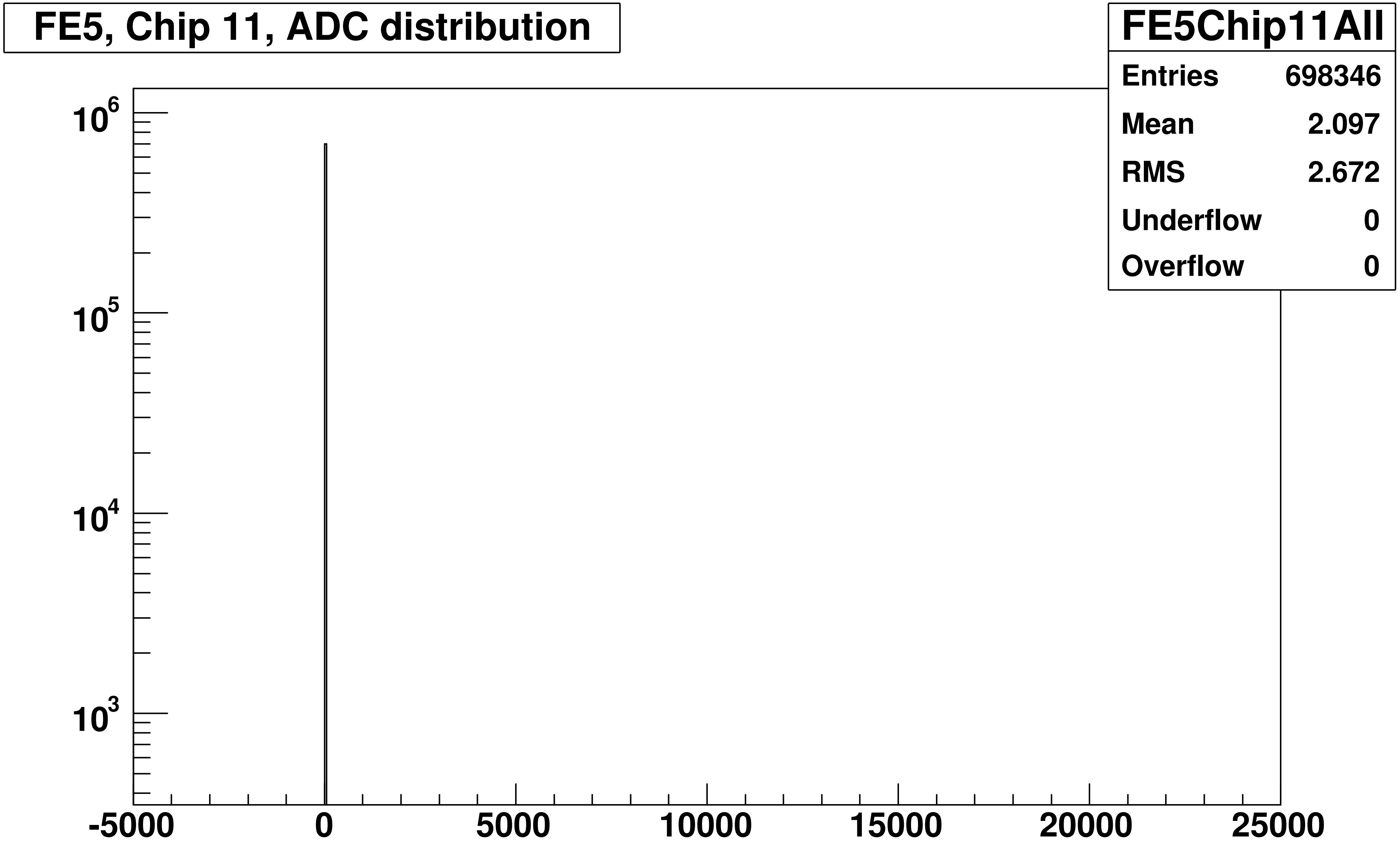
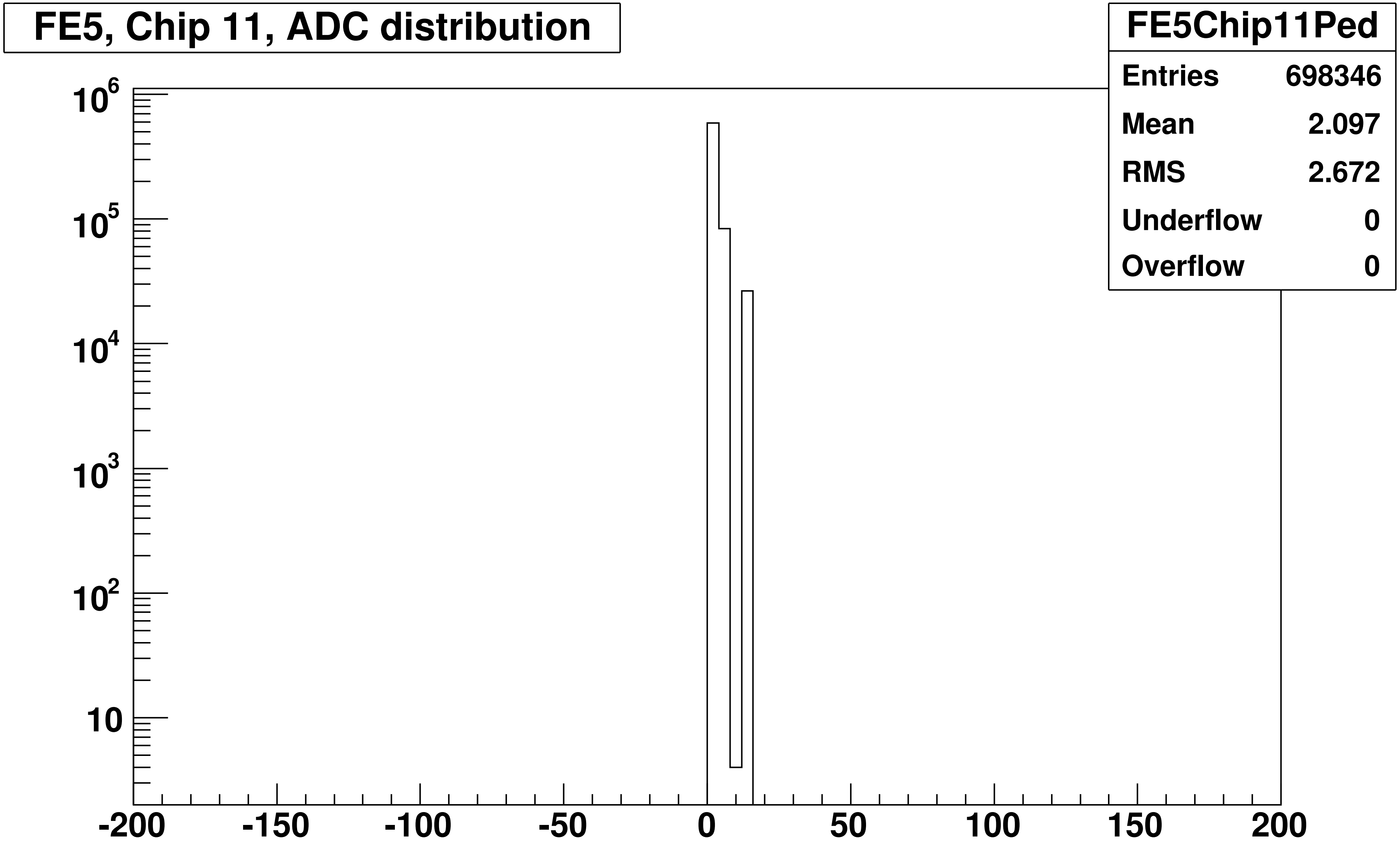
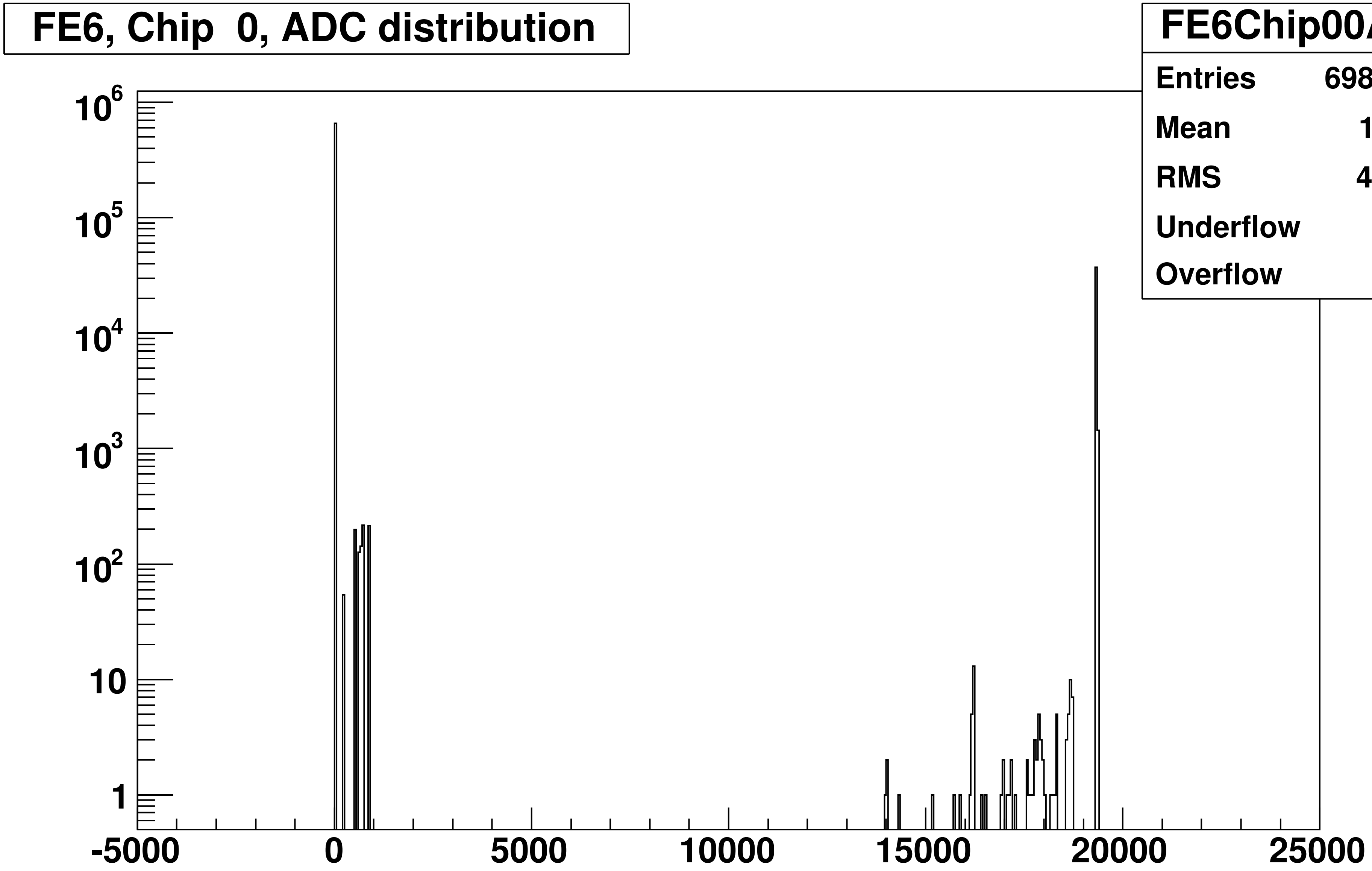
100
150
200


FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 698346
Mean 2.097
RMS 2.672
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 698346
Mean 2.097
RMS 2.672
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 698346
Mean 1105
RMS 4425
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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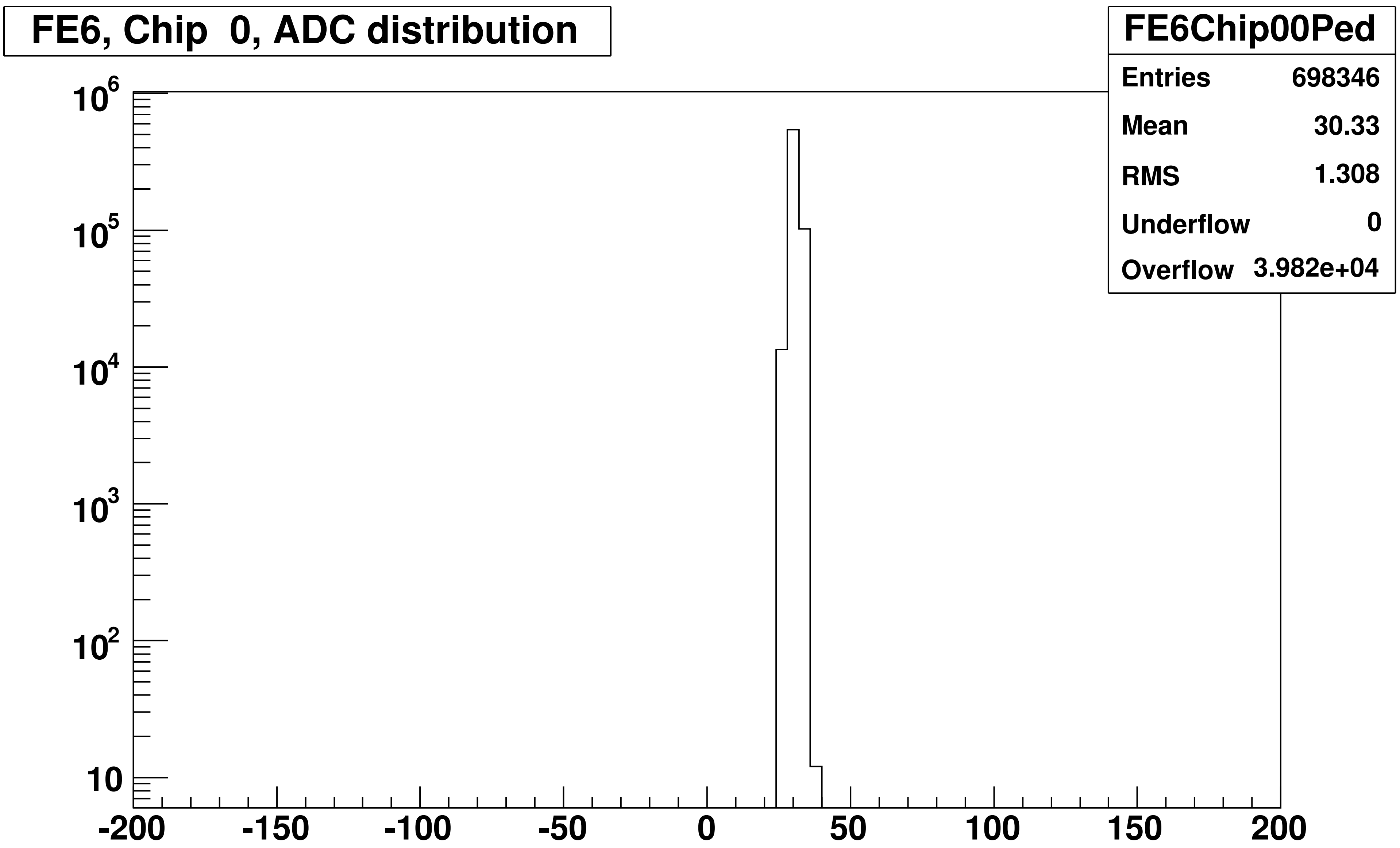
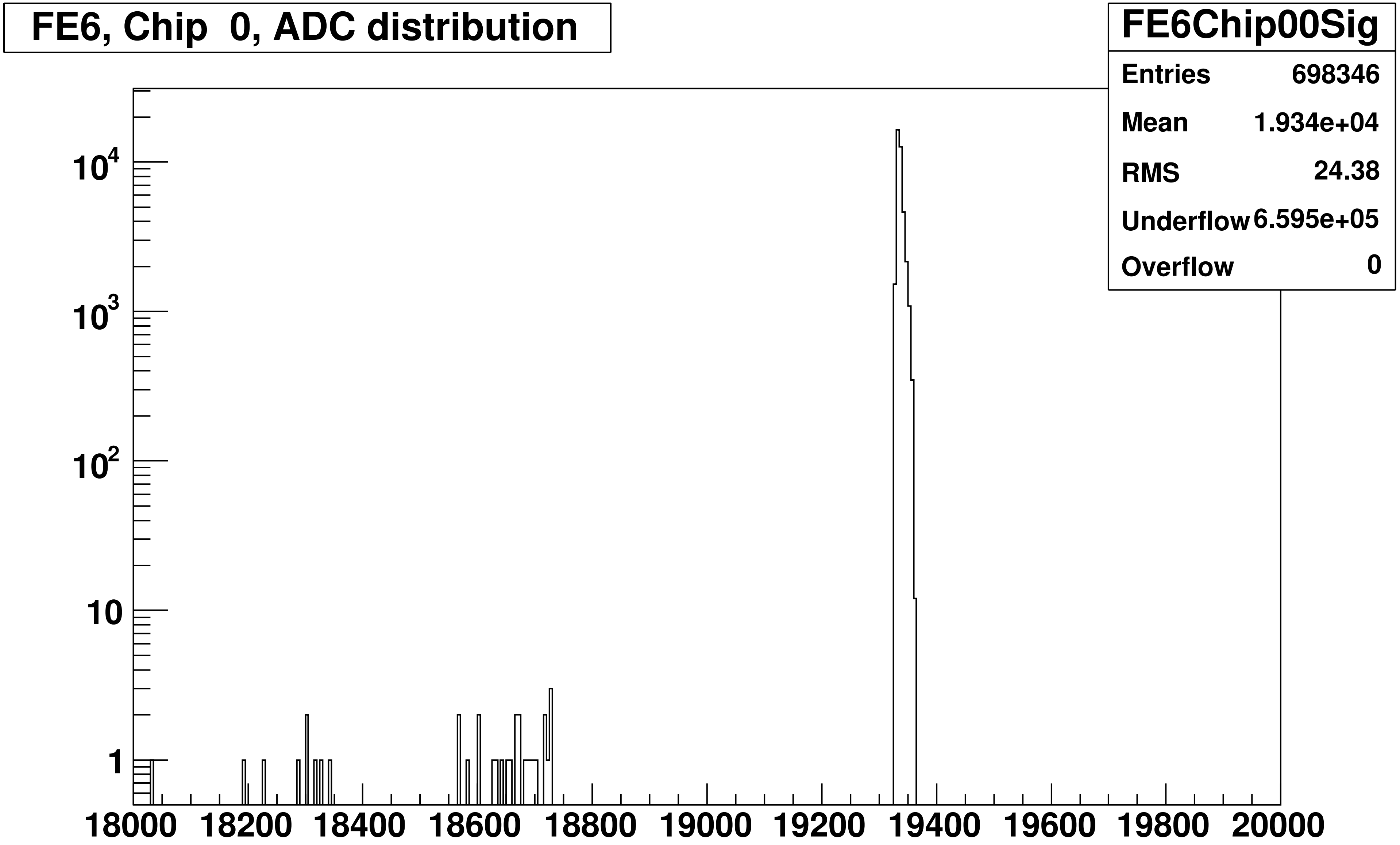
20000
25000


FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 698346
Mean 30.33
RMS 1.308
Underflow 0
Overflow 3.982e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 698346
Mean 1.934e+04
RMS 24.38
Underflow 6.595e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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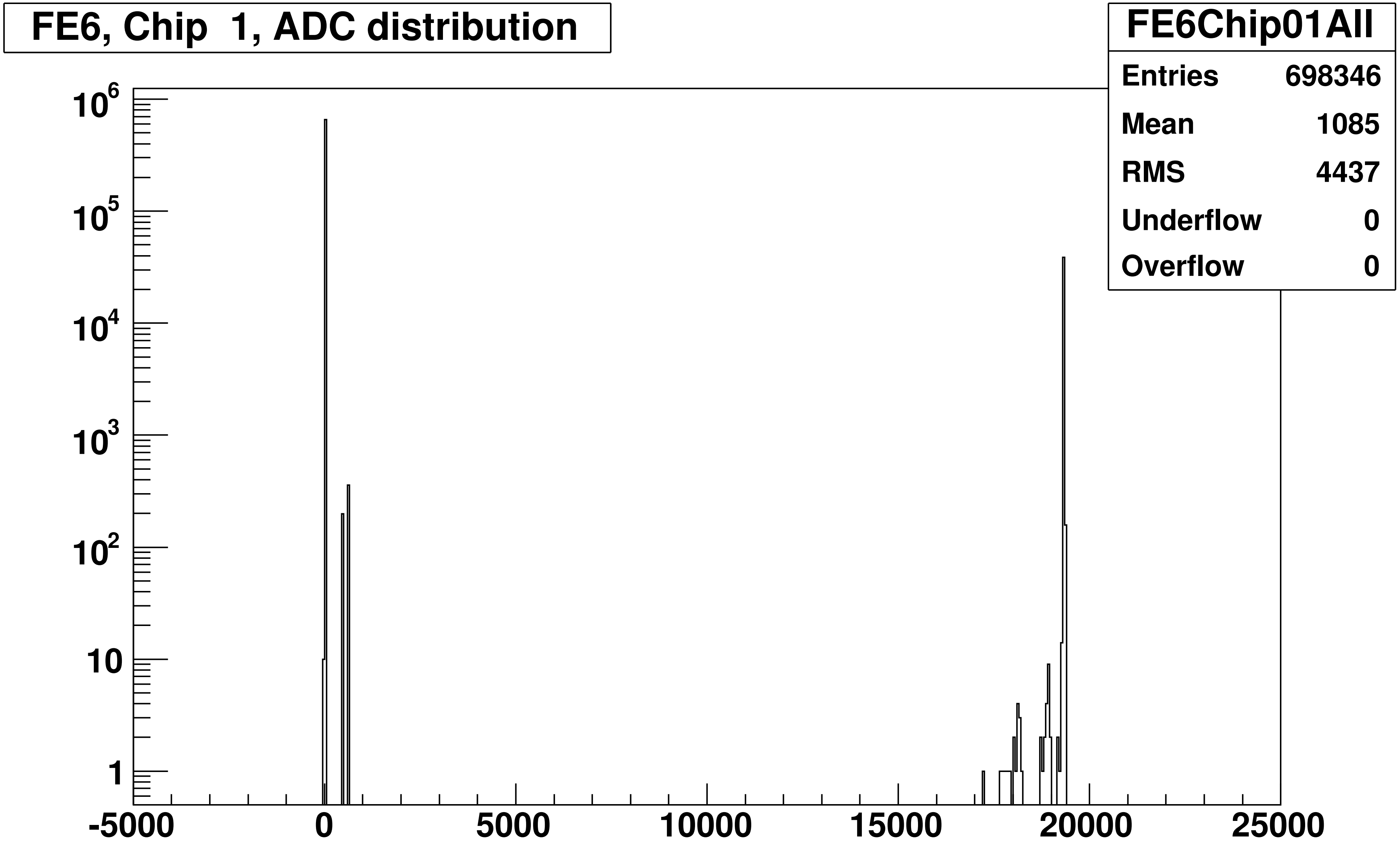
19600
19800
20000


FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 698346
Mean 1085
RMS 4437
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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10000
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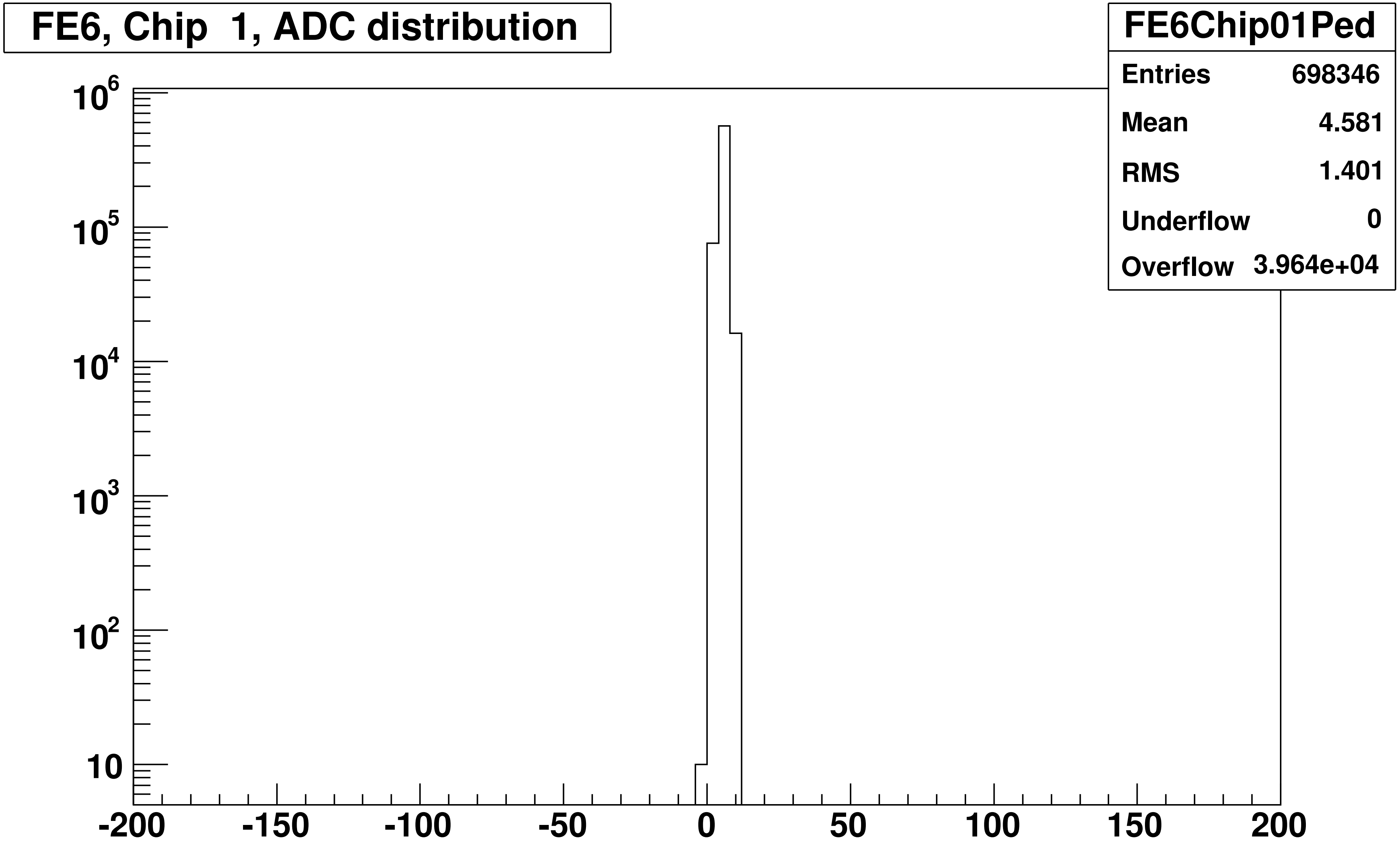
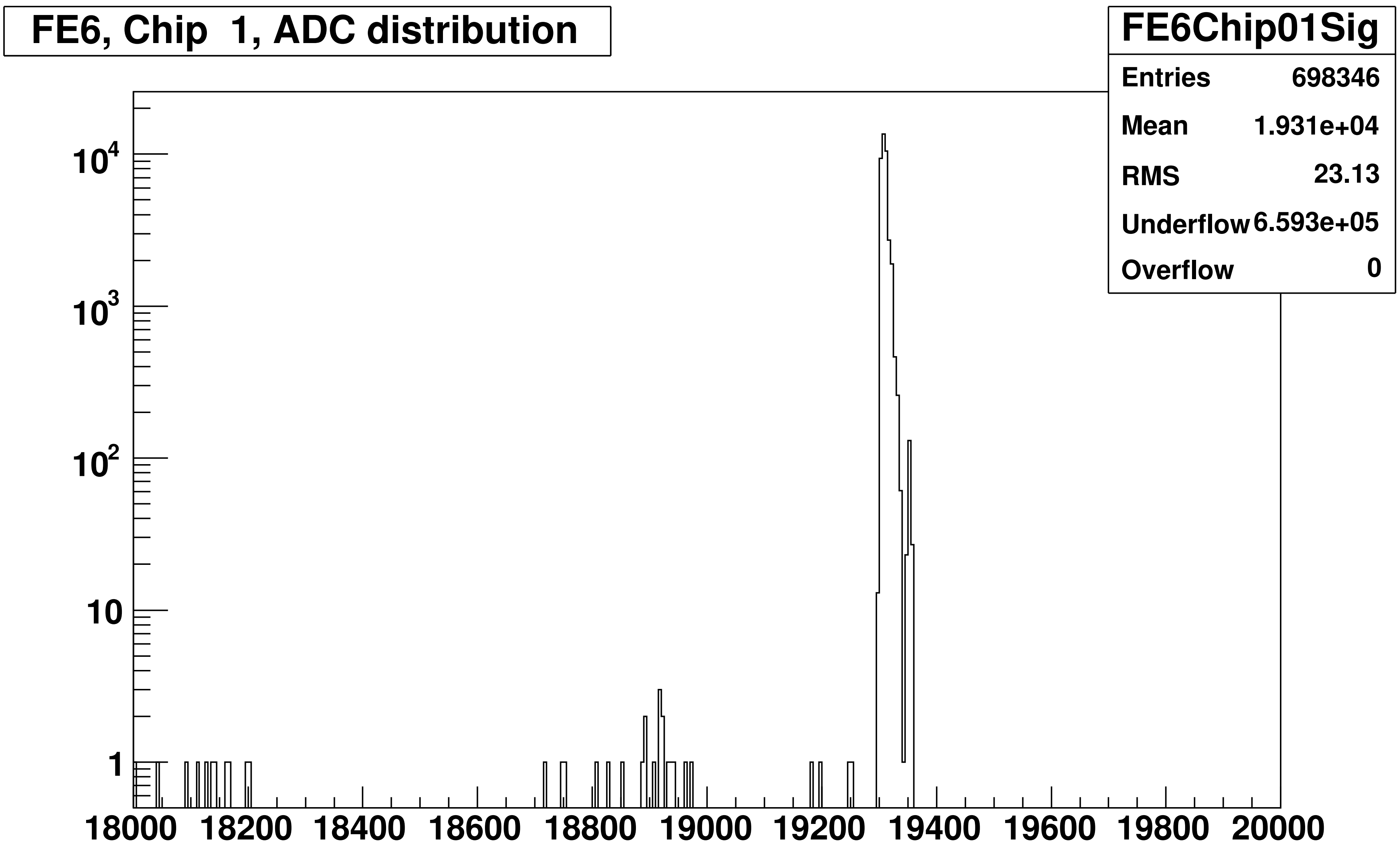
20000
25000



FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 698346
Mean 4.581
RMS 1.401
Underflow 0
Overflow 3.964e+04
10
10^2
10^3
10^4
10^5
10^6
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 698346
Mean 1.931e+04
RMS 23.13
Underflow 6.593e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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19000
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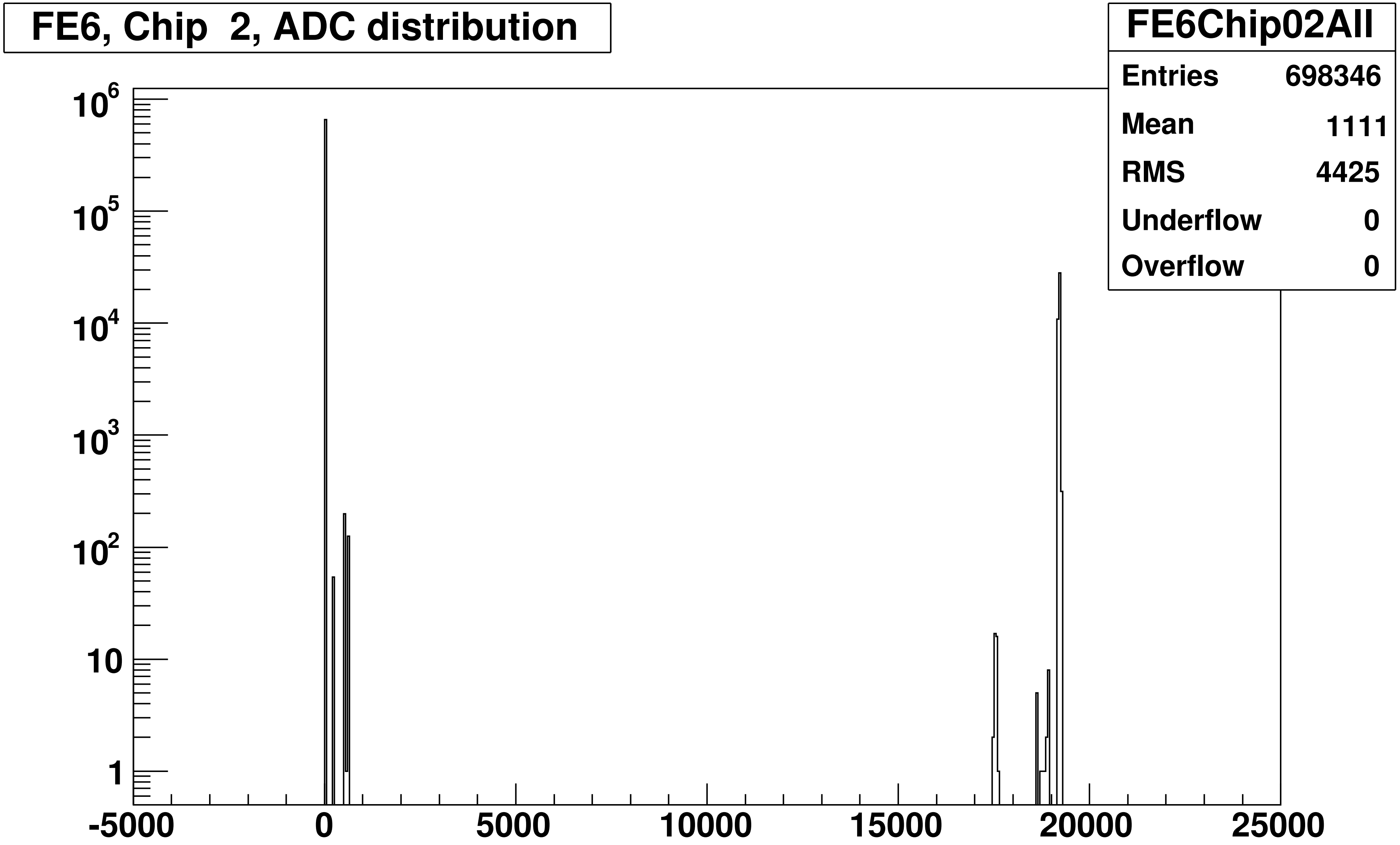
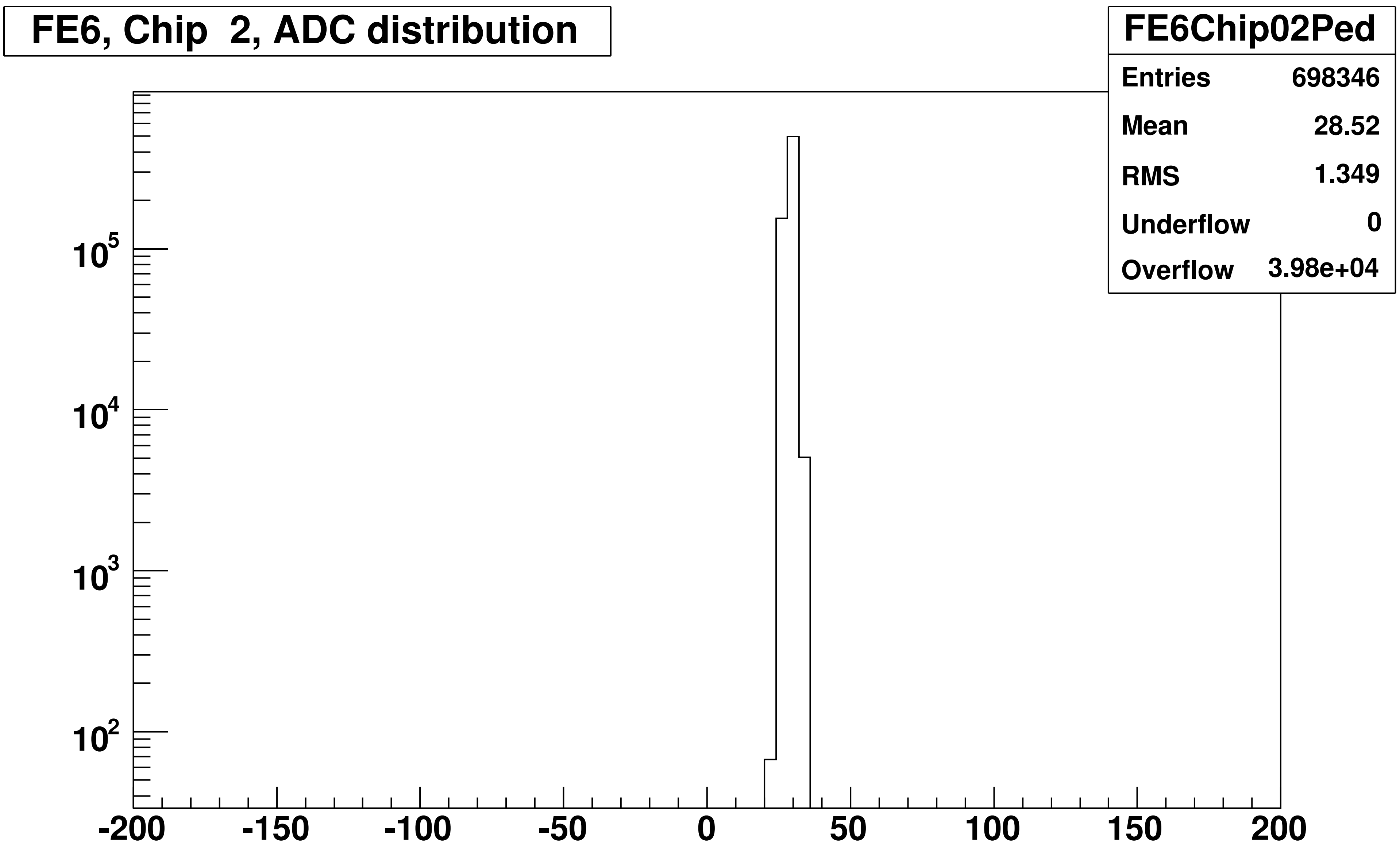
19600
19800
20000



FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 698346
Mean 1111
RMS 4425
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 698346
Mean 28.52
RMS 1.349
Underflow 0
Overflow 3.98e+04
10^5
10^4
10^3
10^2
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-100
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0
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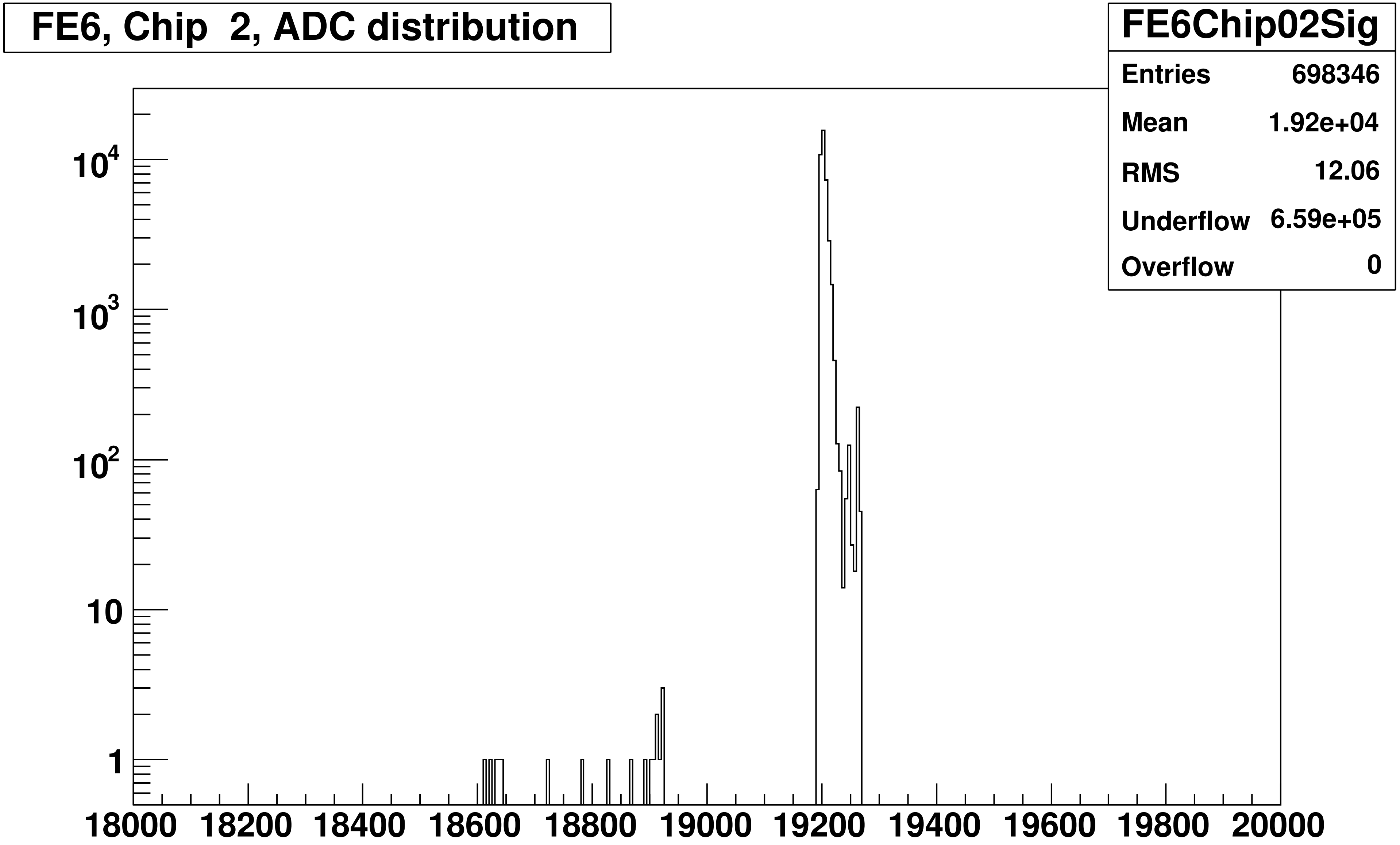
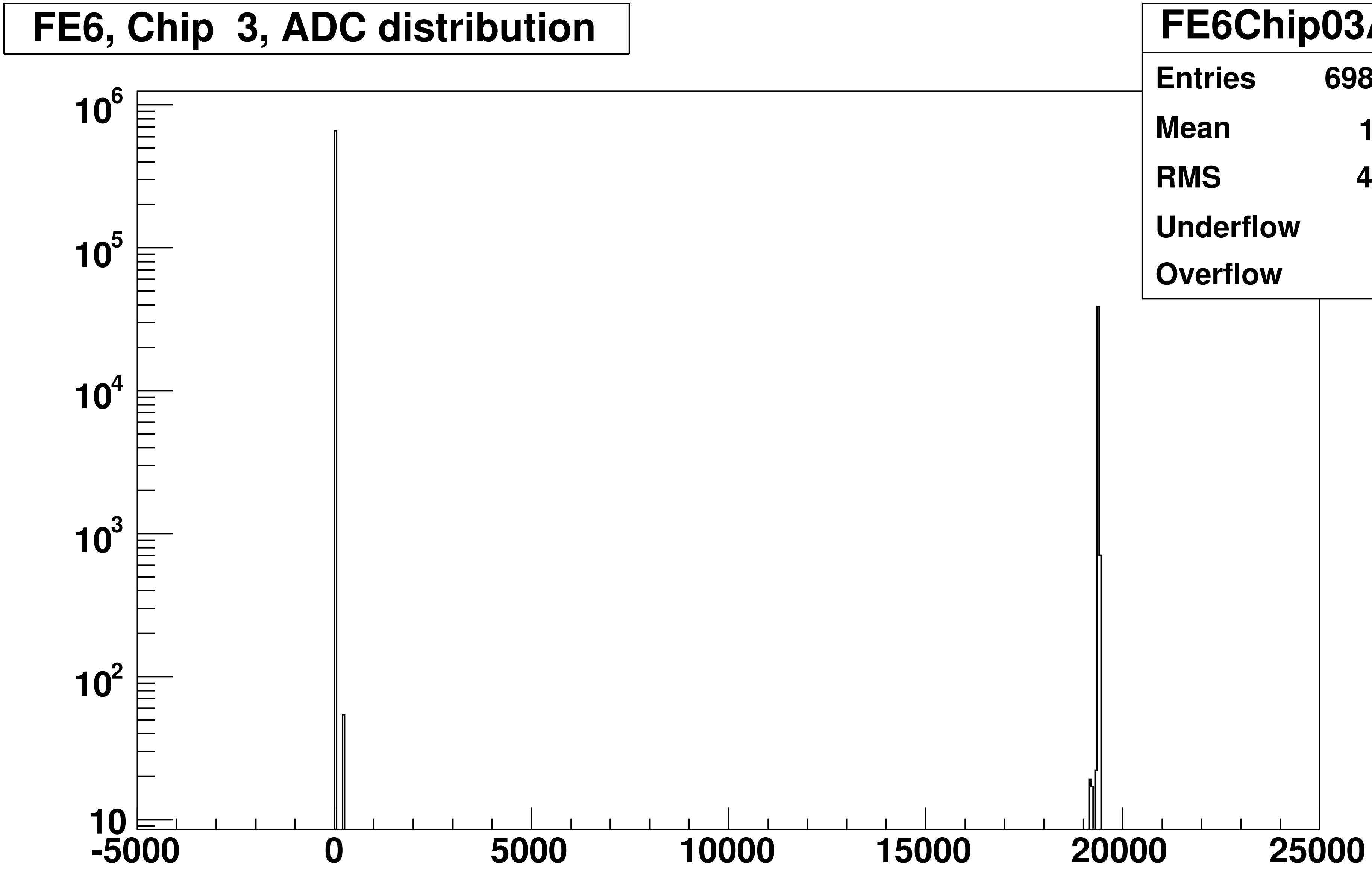
100
150
200


FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 698346
Mean 1.92e+04
RMS 12.06
Underflow 6.59e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 698346
Mean 1124
RMS 4480
Underflow 0
Overflow 0
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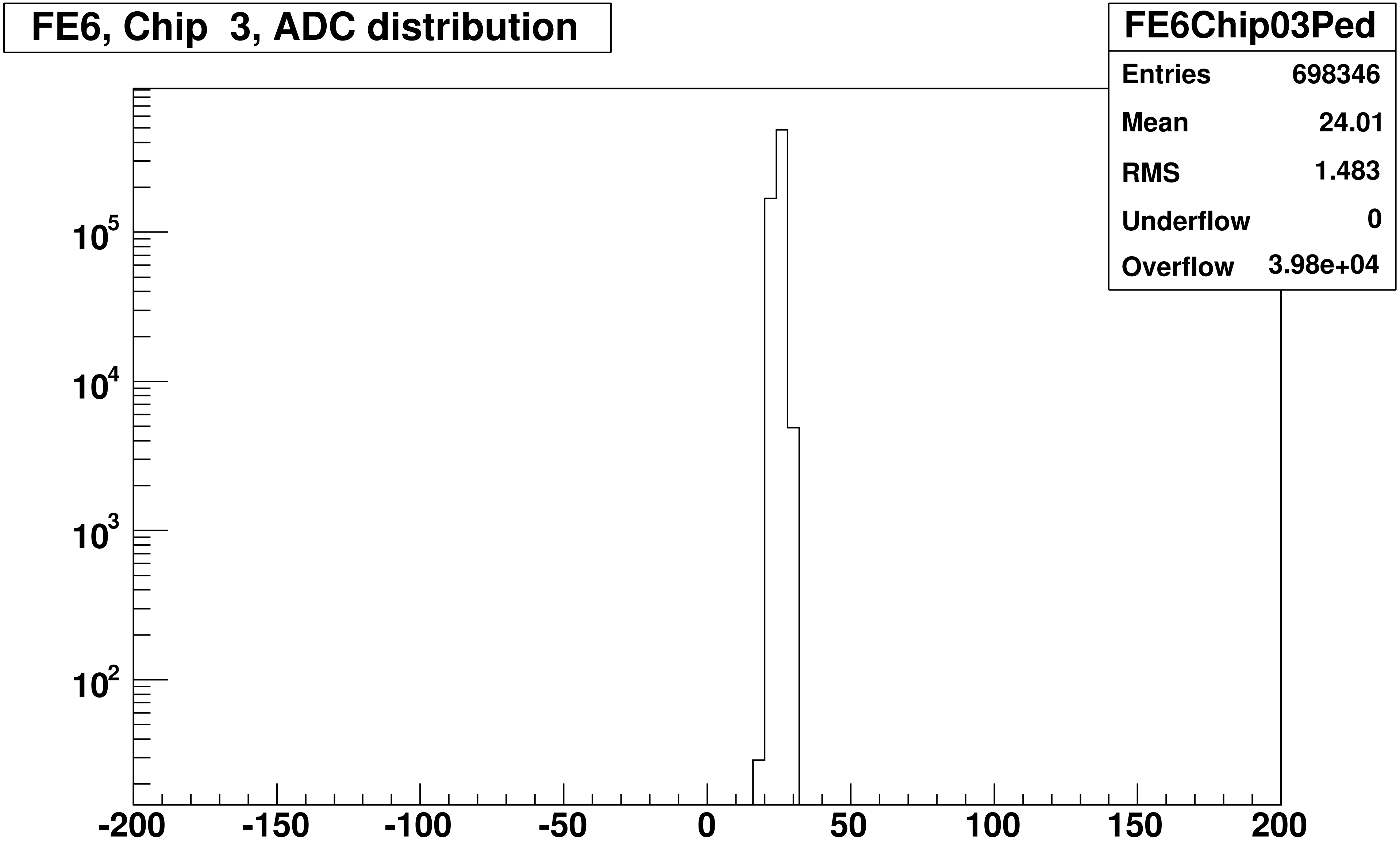
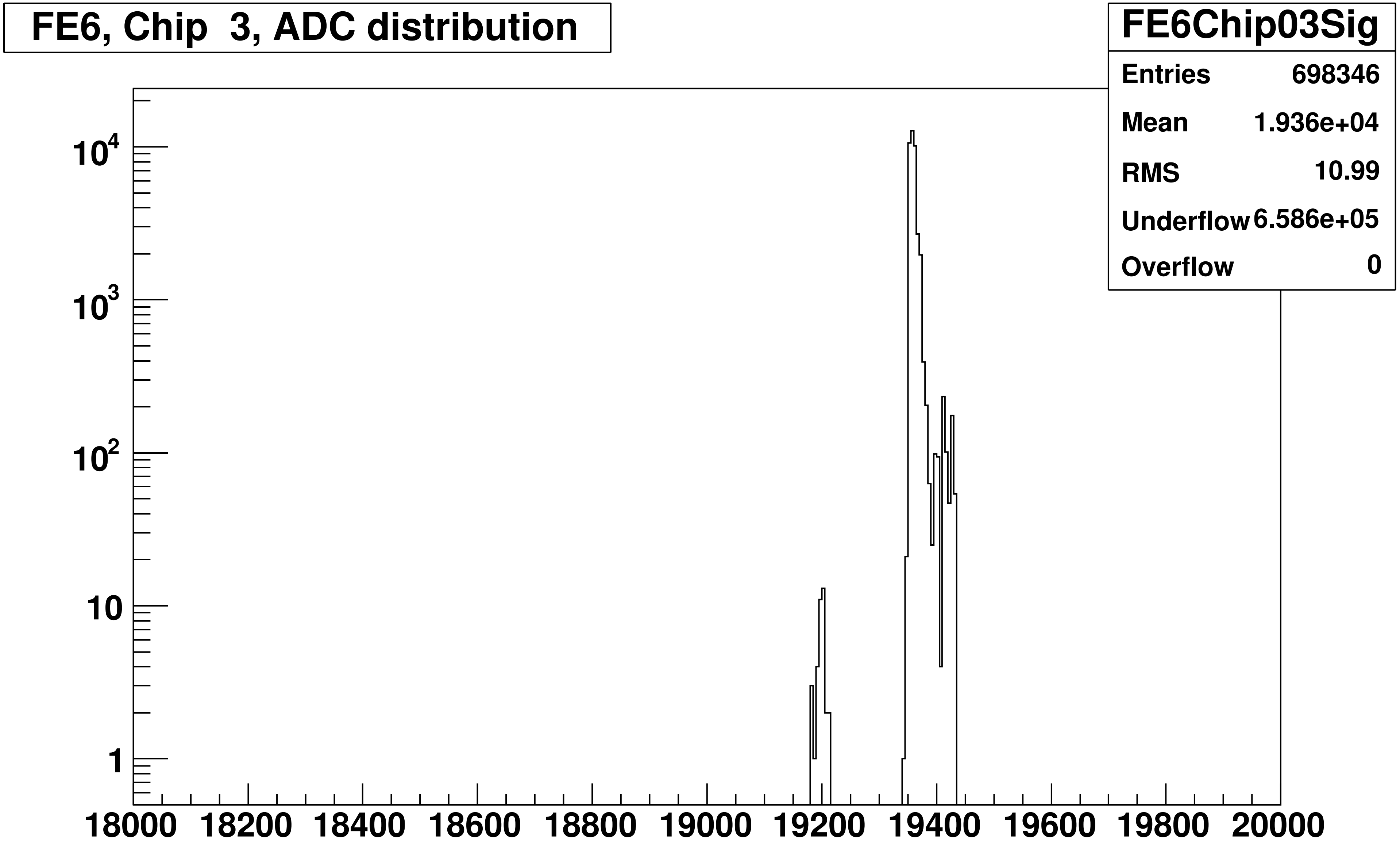
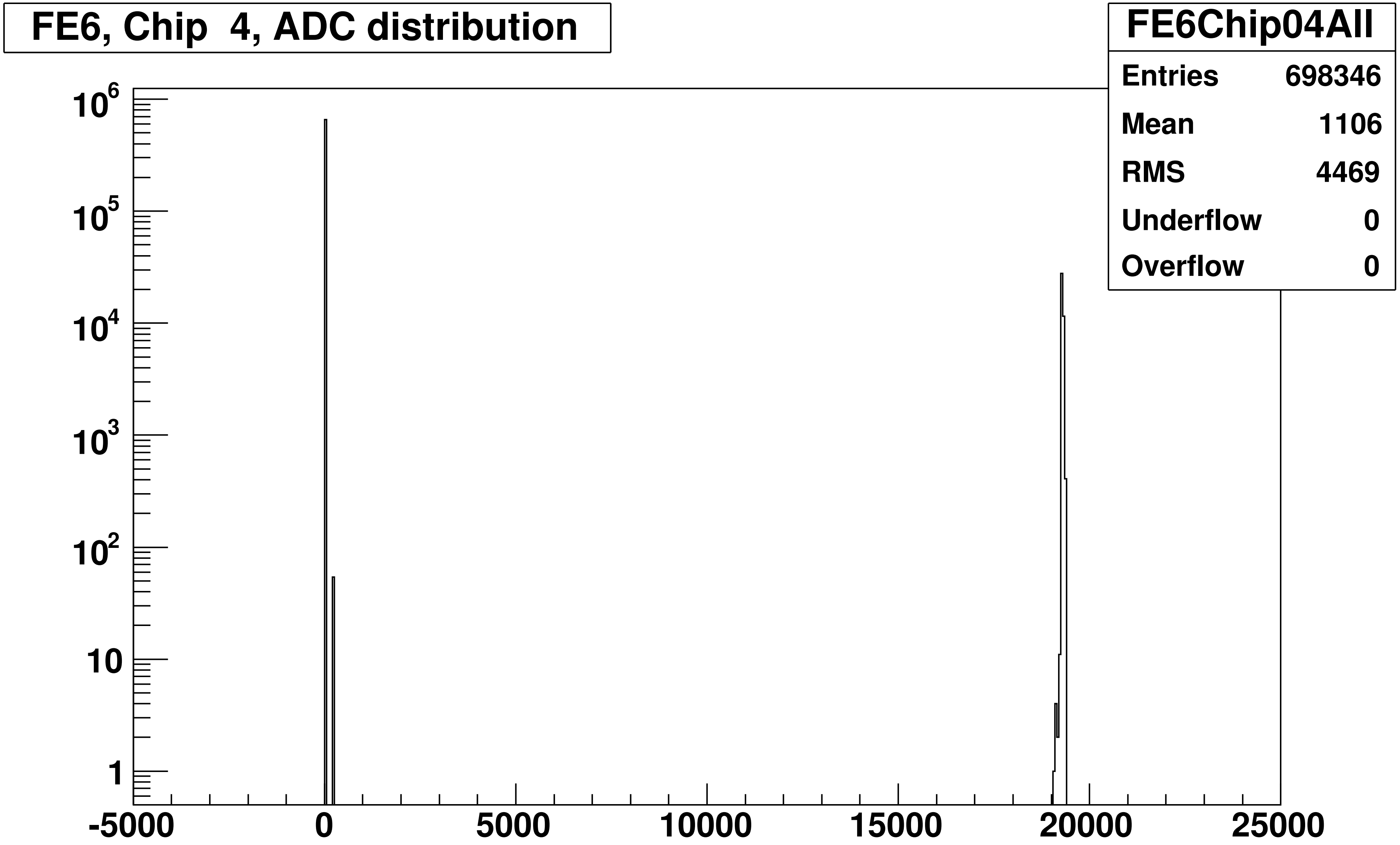
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 698346
Mean 24.01
RMS 1.483
Underflow 0
Overflow 3.98e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 698346
Mean 1.936e+04
RMS 10.99
Underflow 6.586e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 698346
Mean 1106
RMS 4469
Underflow 0
Overflow 0
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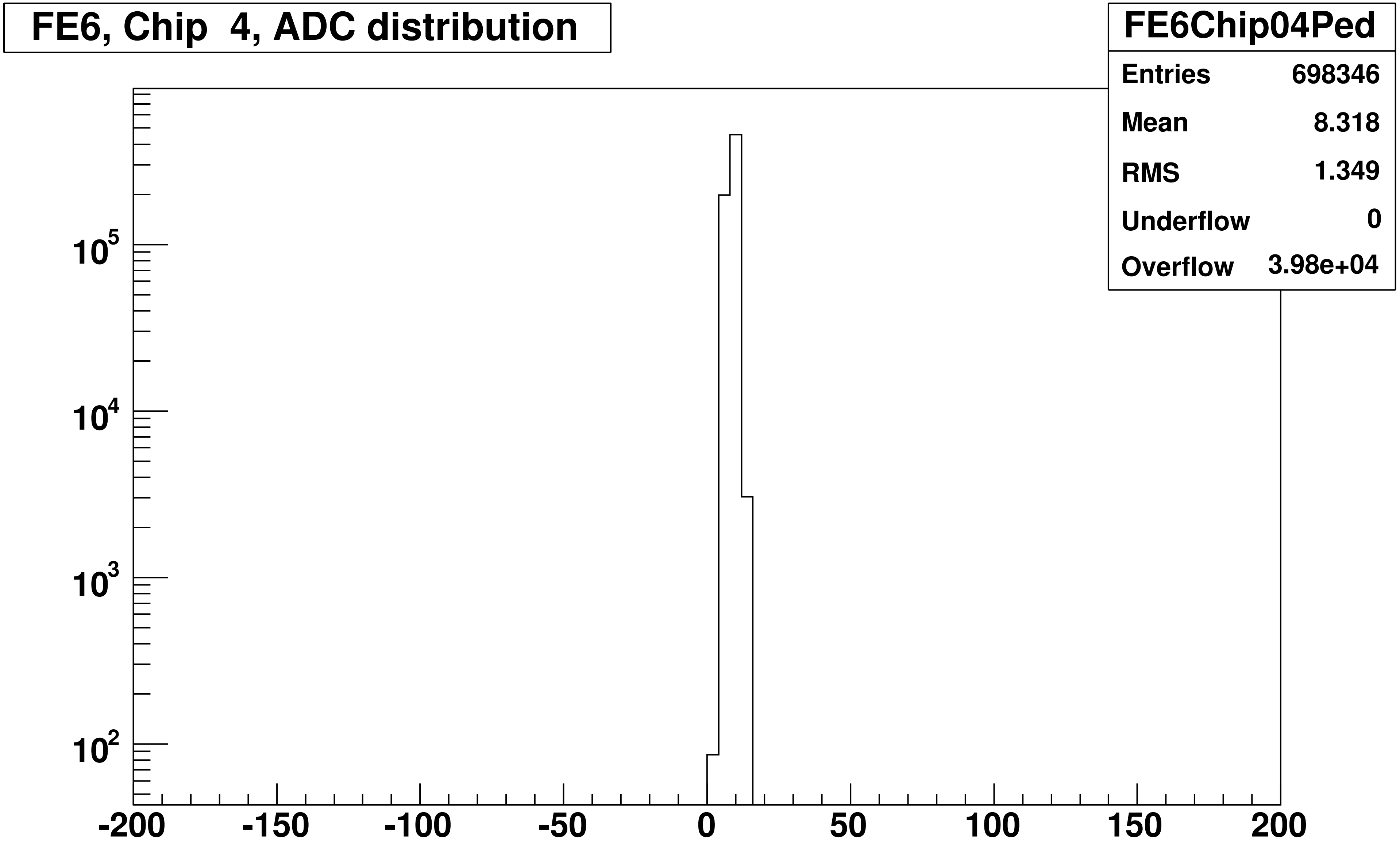
5000
10000
15000
20000
25000
1
10
10^2
10^3
10^4
10^5
10^6


FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 698346
Mean 8.318
RMS 1.349
Underflow 0
Overflow 3.98e+04
10^5
10^4
10^3
10^2
-200
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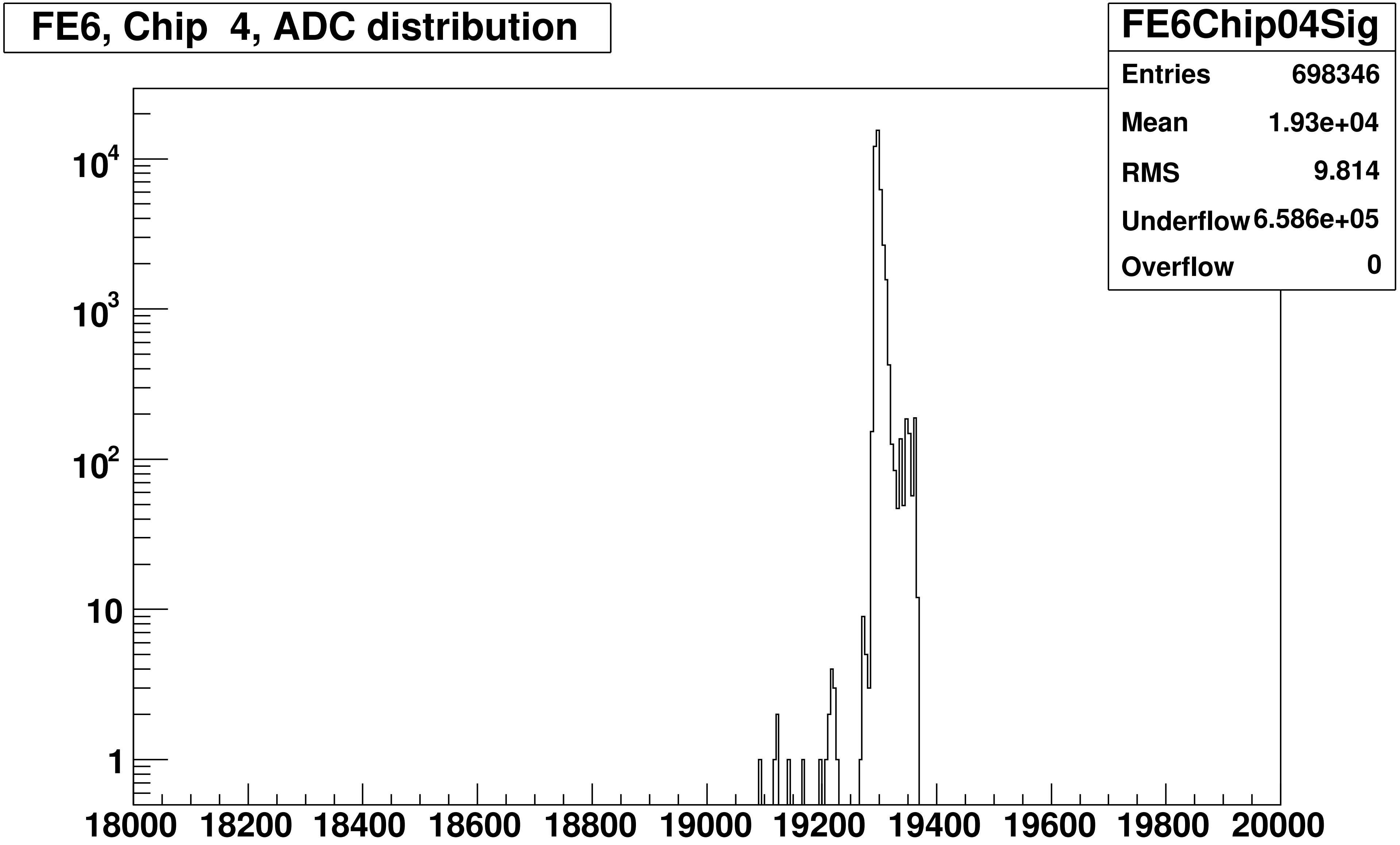
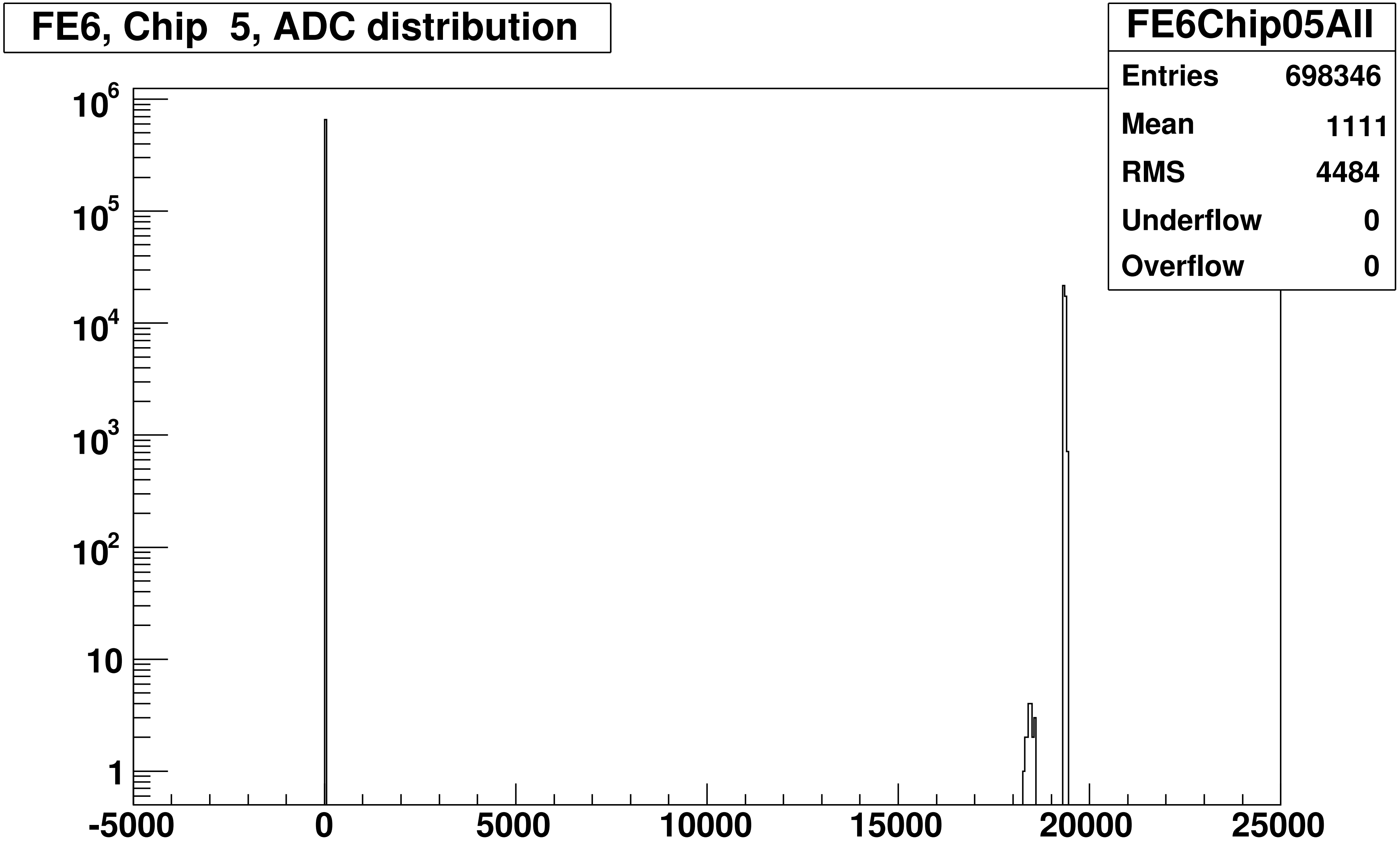
-100
-50
0
50
100
150
200


FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 698346
Mean 1.93e+04
RMS 9.814
Underflow 6.586e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 698346
Mean 1111
RMS 4484
Underflow 0
Overflow 0
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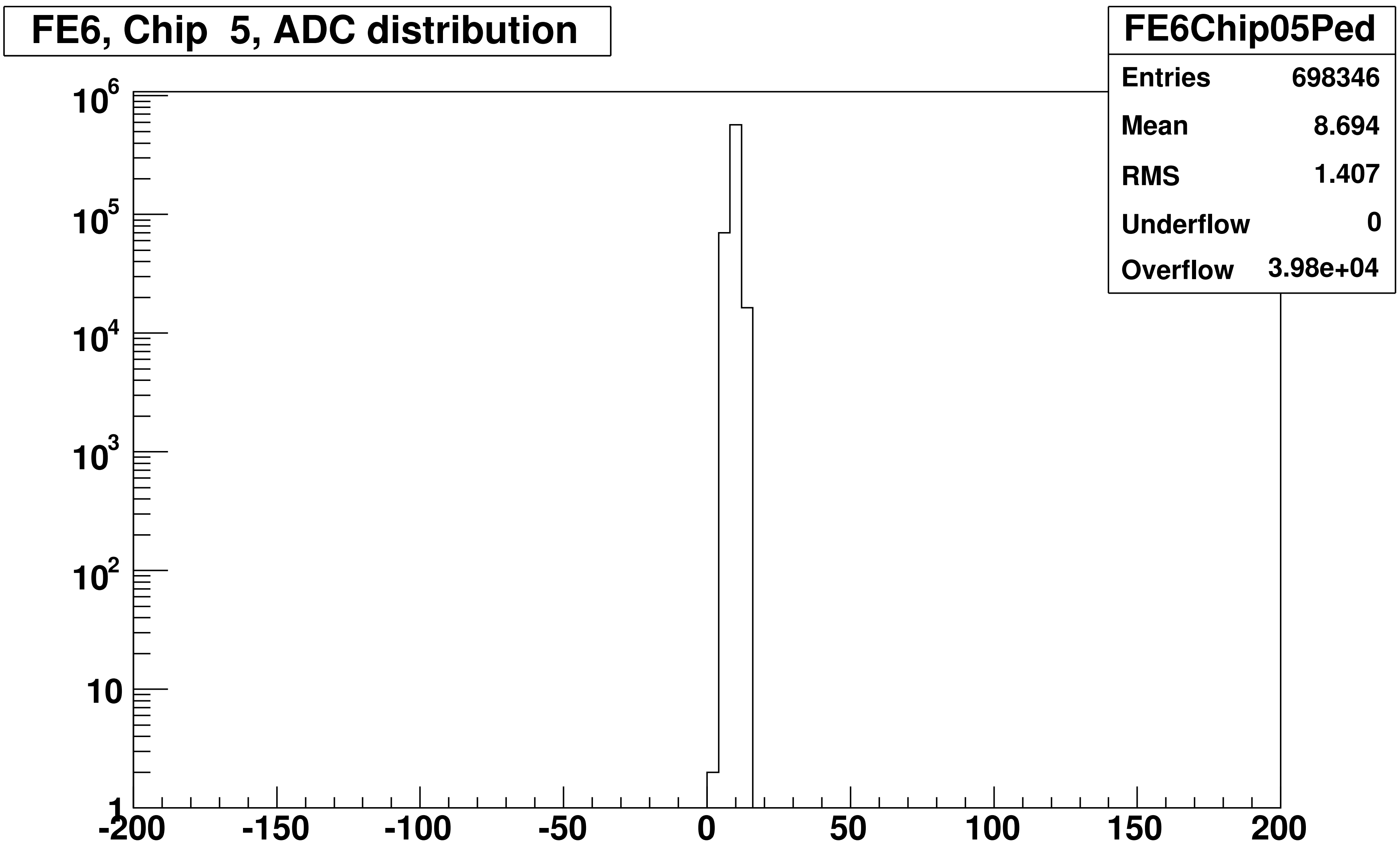
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 698346
Mean 8.694
RMS 1.407
Underflow 0
Overflow 3.98e+04
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10^5
10^4
10^3
10^2
10
1
-200
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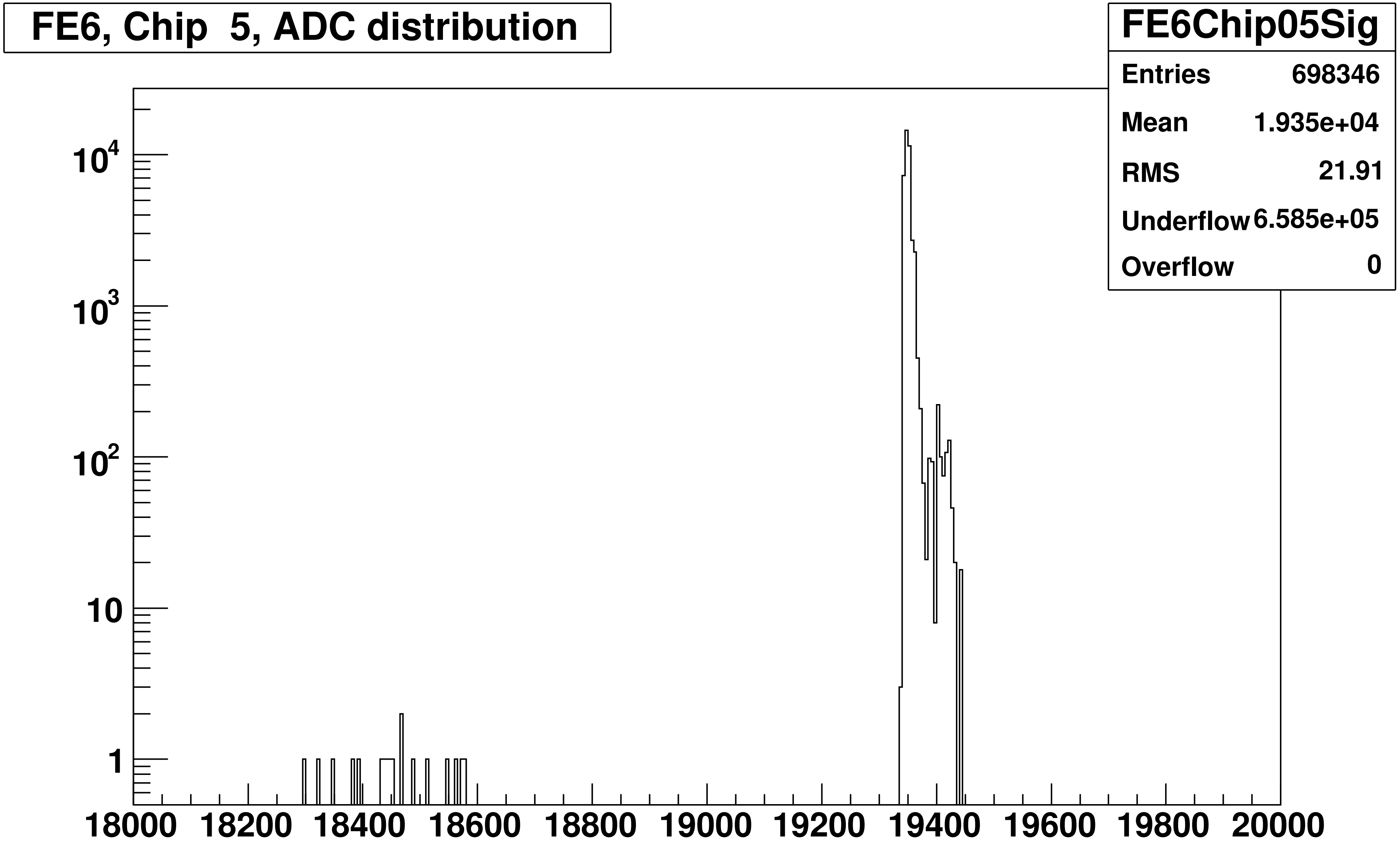
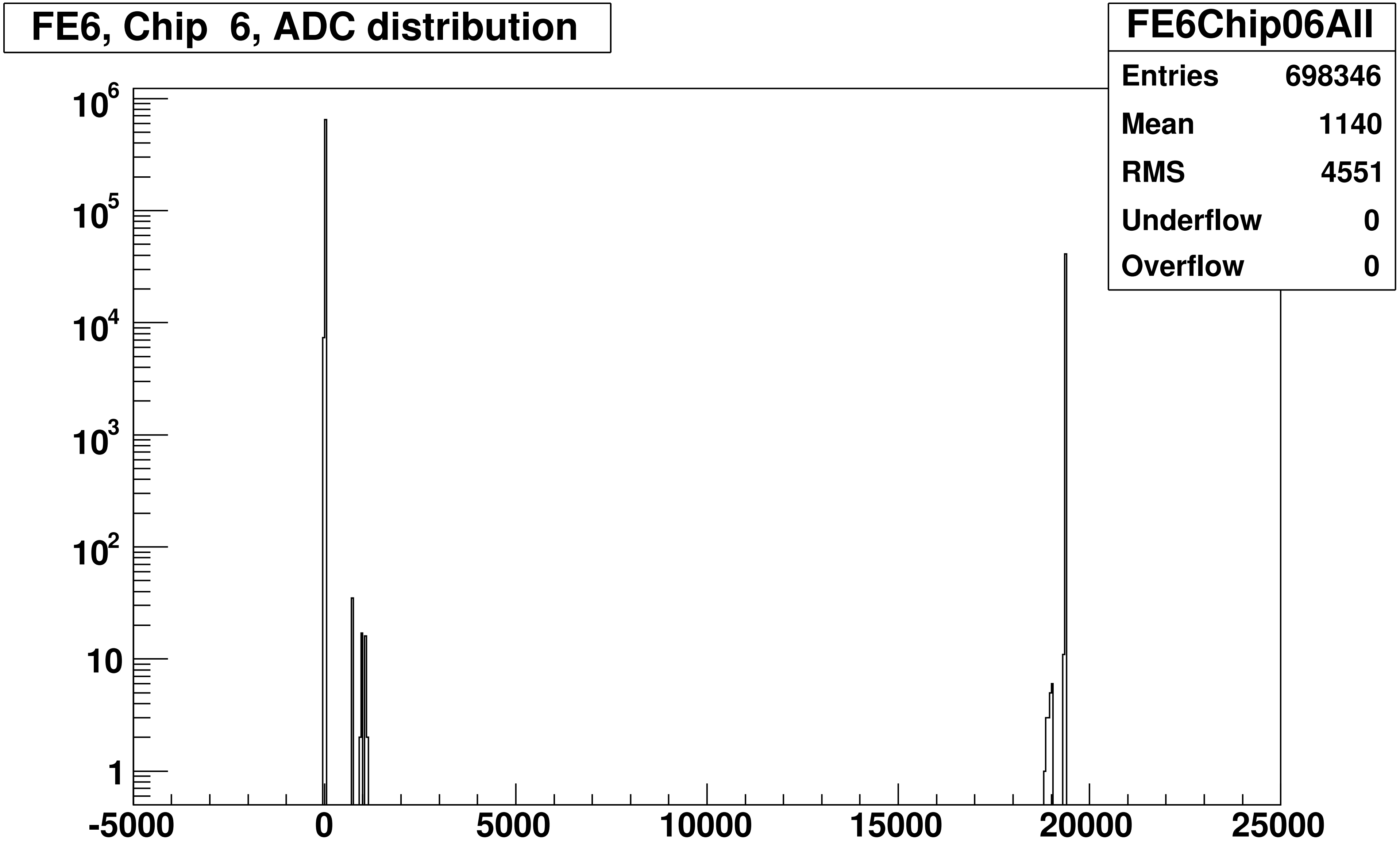
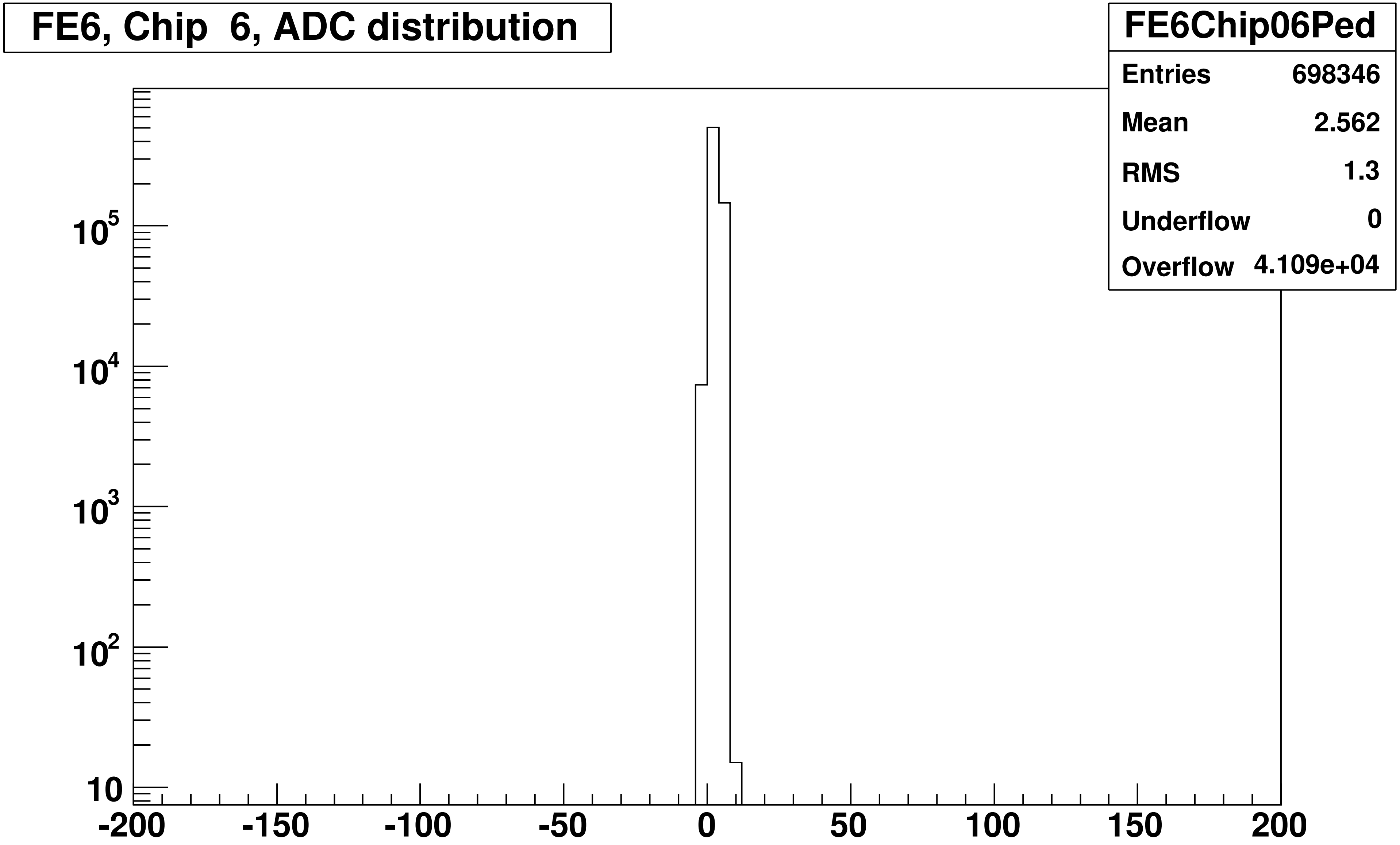
-100
-50
0
50
100
150
200


FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 698346
Mean 1.935e+04
RMS 21.91
Underflow 6.585e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 698346
Mean 1140
RMS 4551
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 698346
Mean 2.562
RMS 1.3
Underflow 0
Overflow 4.109e+04
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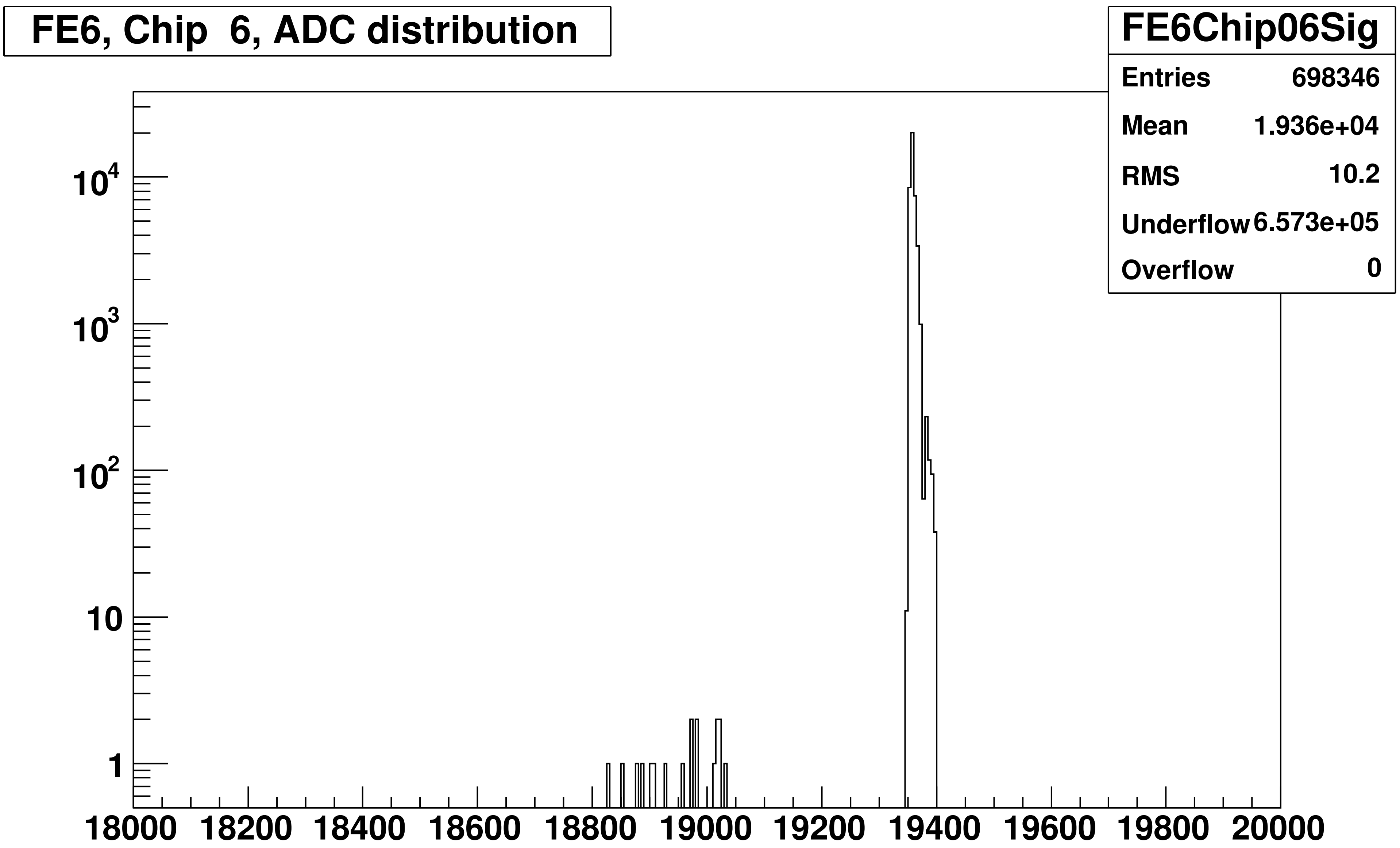
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 698346
Mean 1.936e+04
RMS 10.2
Underflow 6.573e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
18400
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19000
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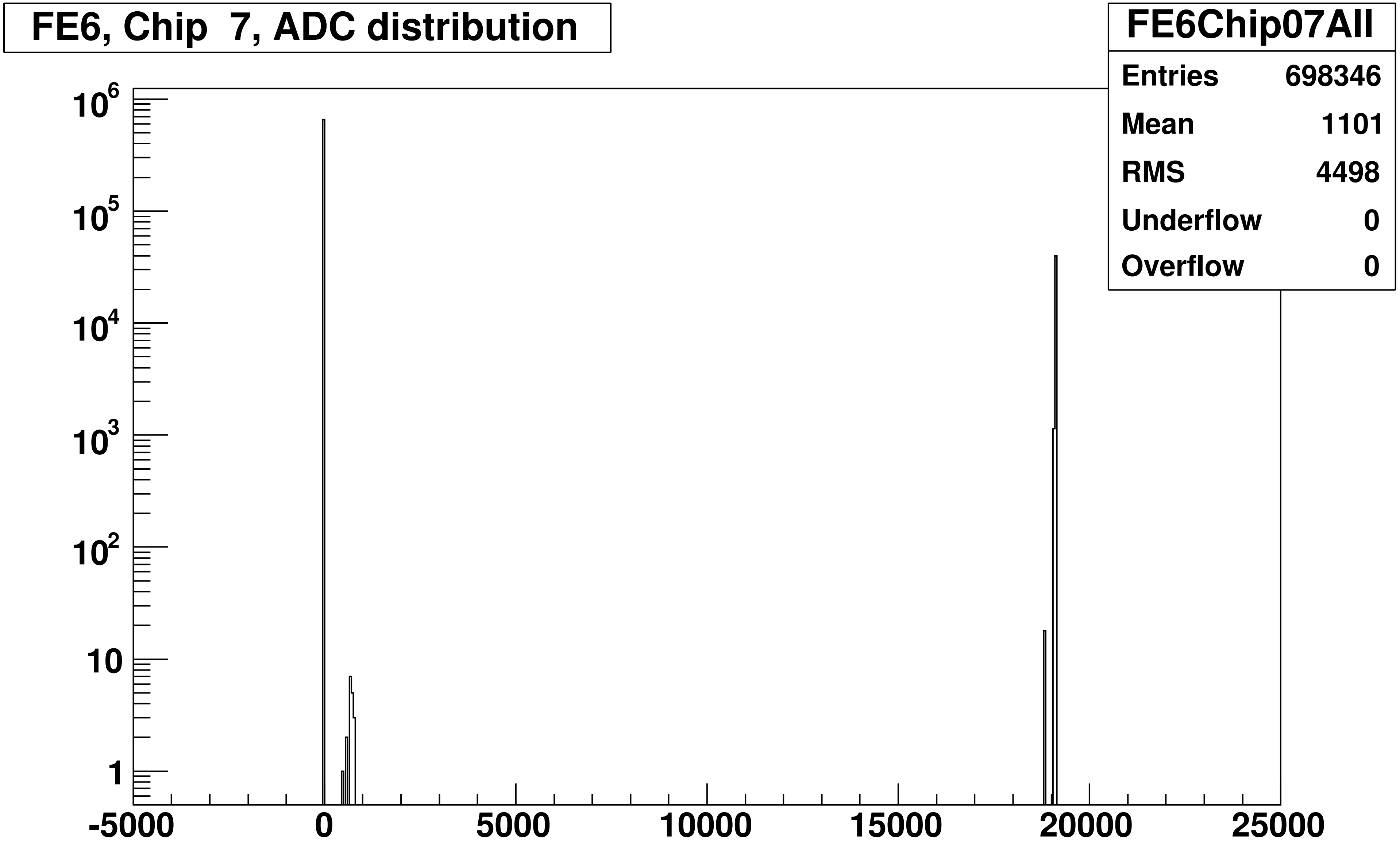
19800
20000


FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 698346
Mean 1101
RMS 4498
Underflow 0
Overflow 0
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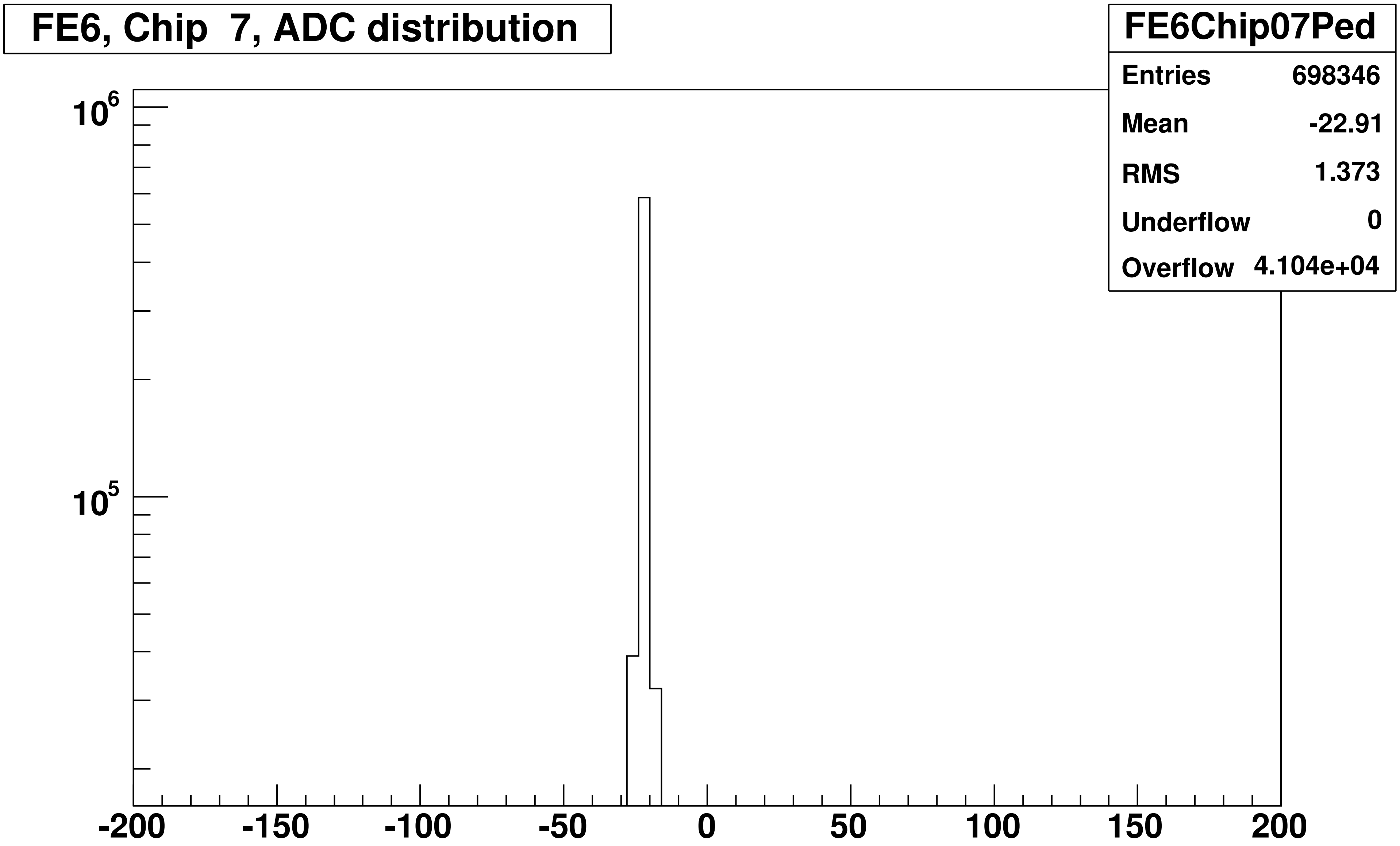
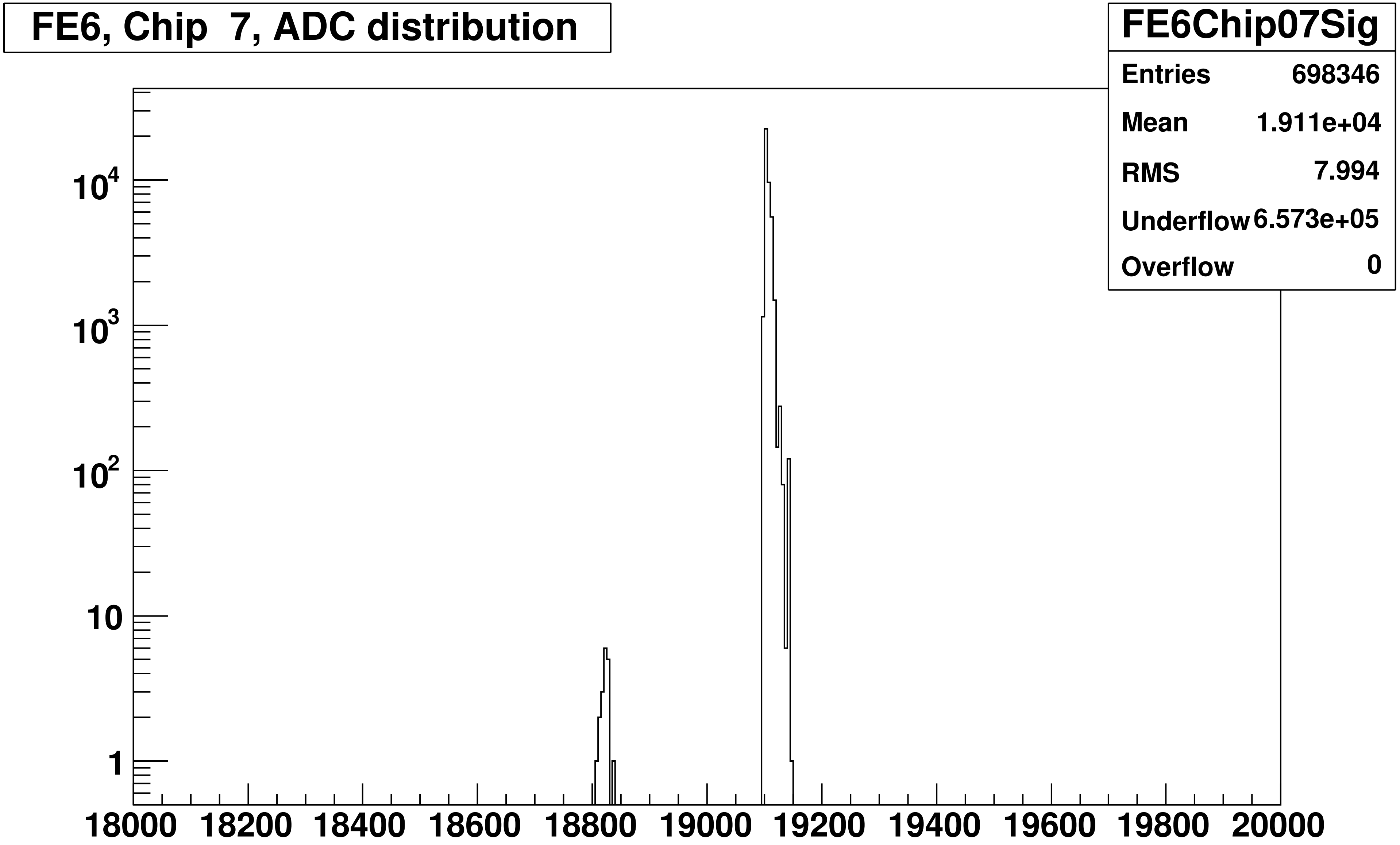
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 698346
Mean -22.91
RMS 1.373
Underflow 0
Overflow 4.104e+04
10^6
10^5
-200
-150
-100
-50
0
50
100
150
200



FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 698346
Mean 1.911e+04
RMS 7.994
Underflow 6.573e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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19000
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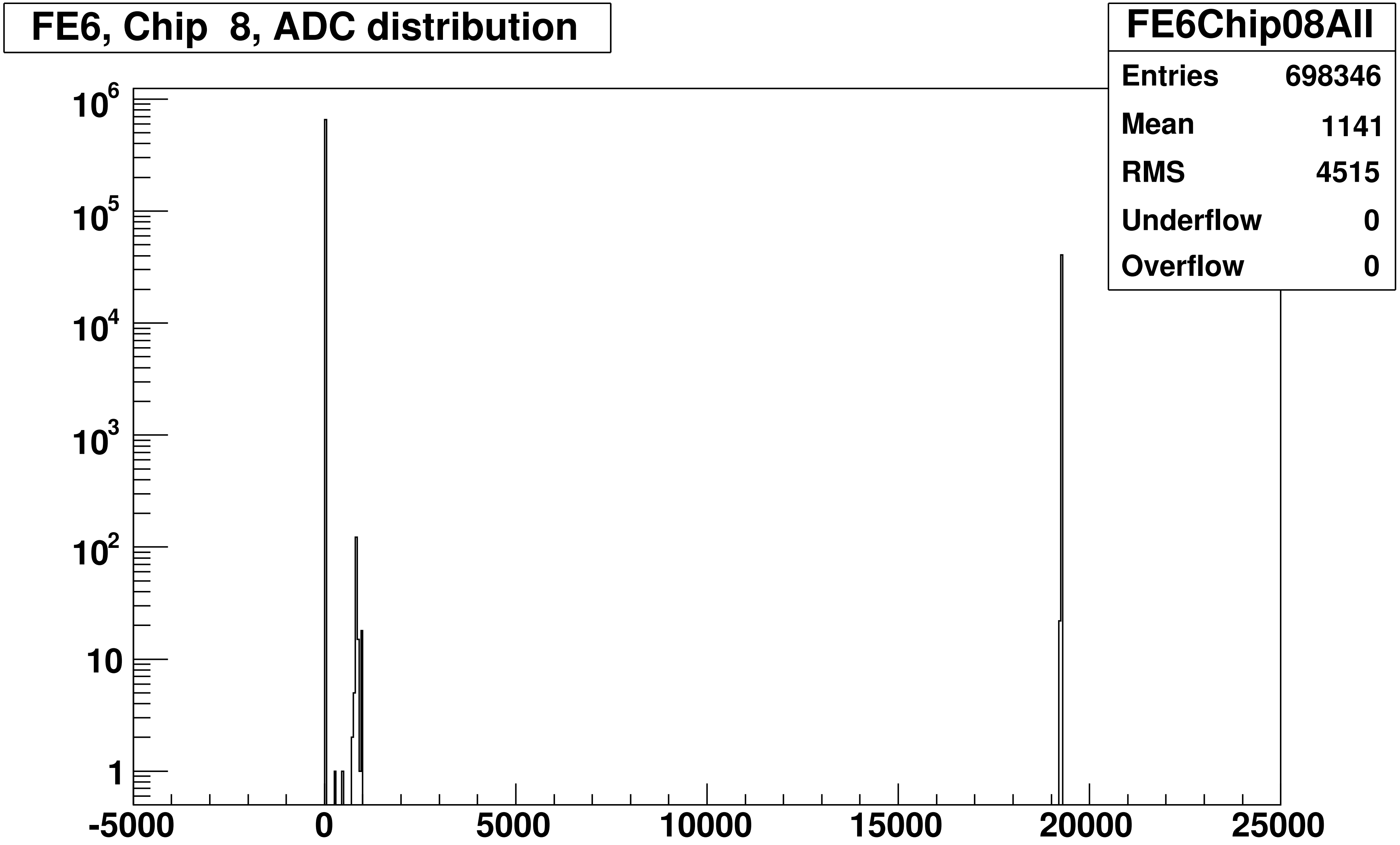
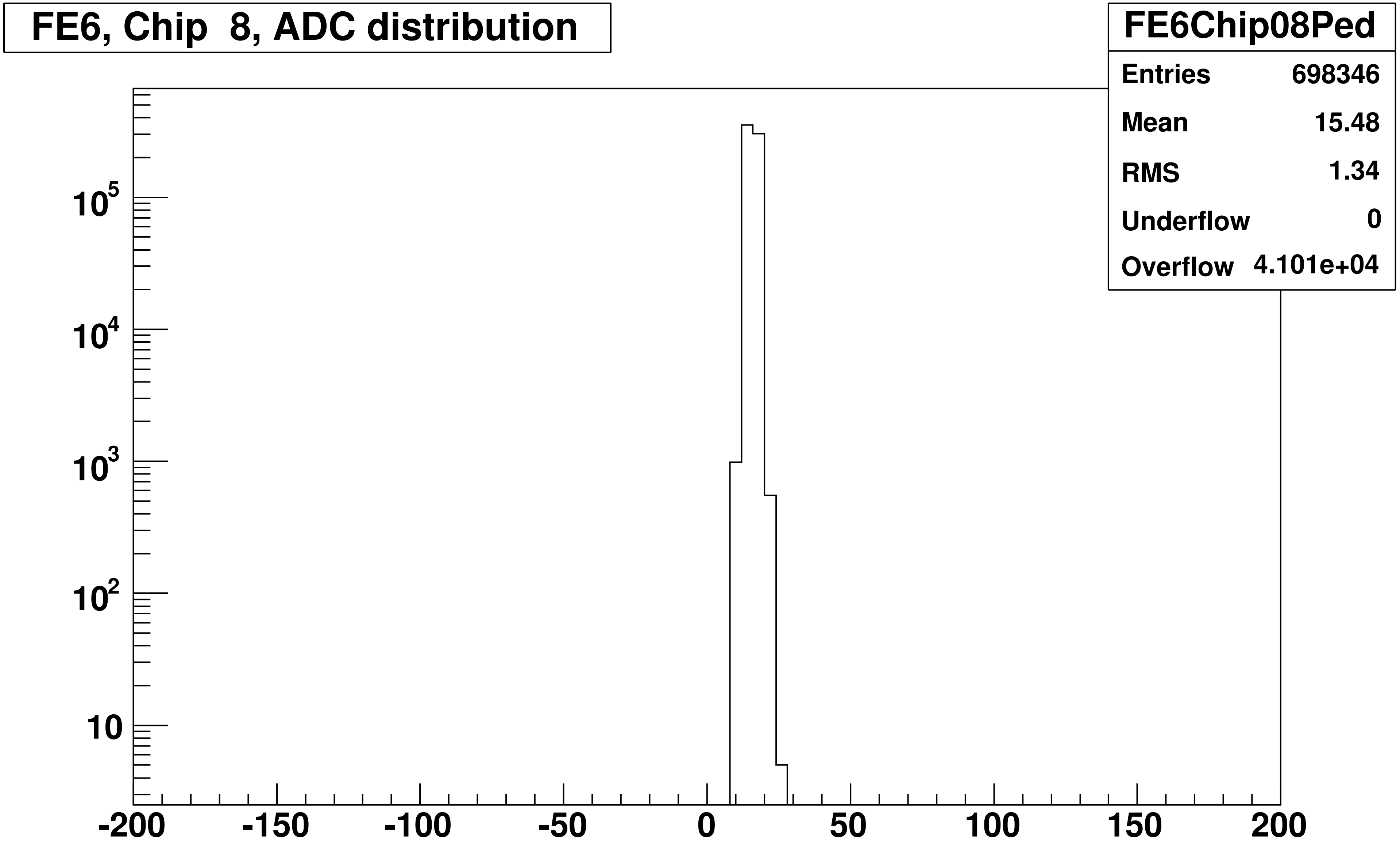
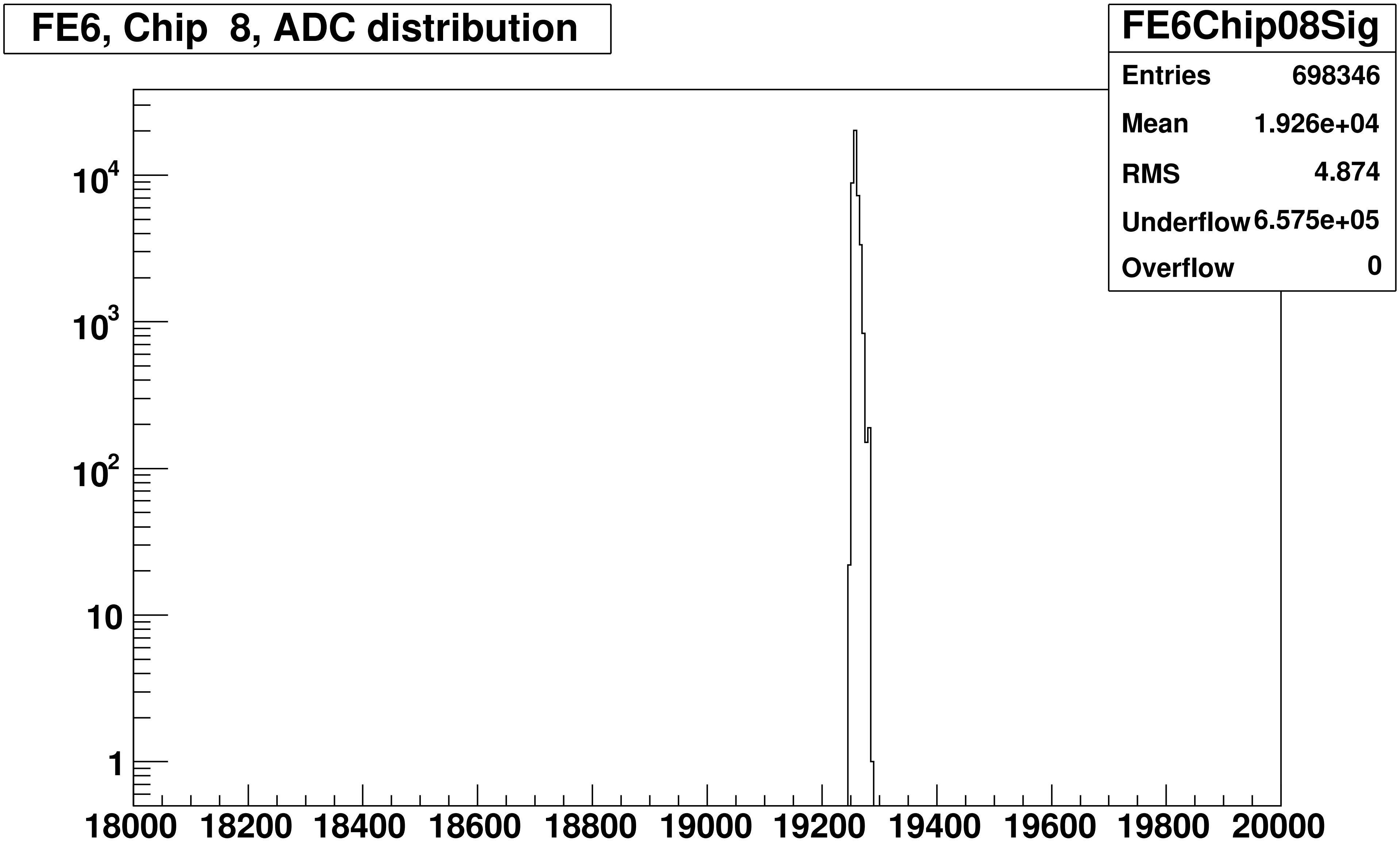
19400
19600
19800
20000



FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 698346
Mean 1141
RMS 4515
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 698346
Mean 15.48
RMS 1.34
Underflow 0
Overflow 4.101e+04
10^5
10^4
10^3
10^2
10
-200 -150 -100 -50 0 50 100 150 200



FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 698346
Mean 1.926e+04
RMS 4.874
Underflow 6.575e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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19000
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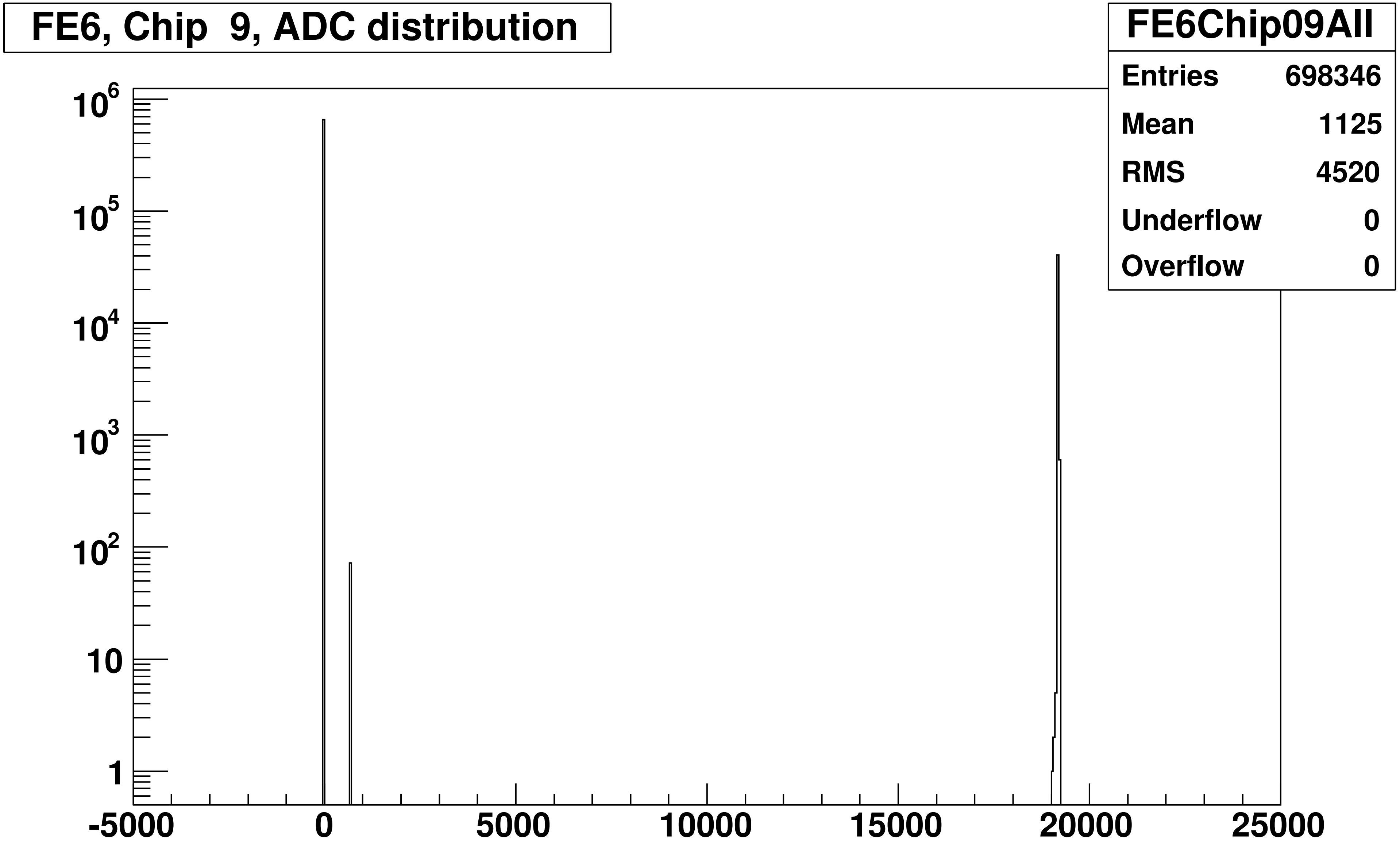
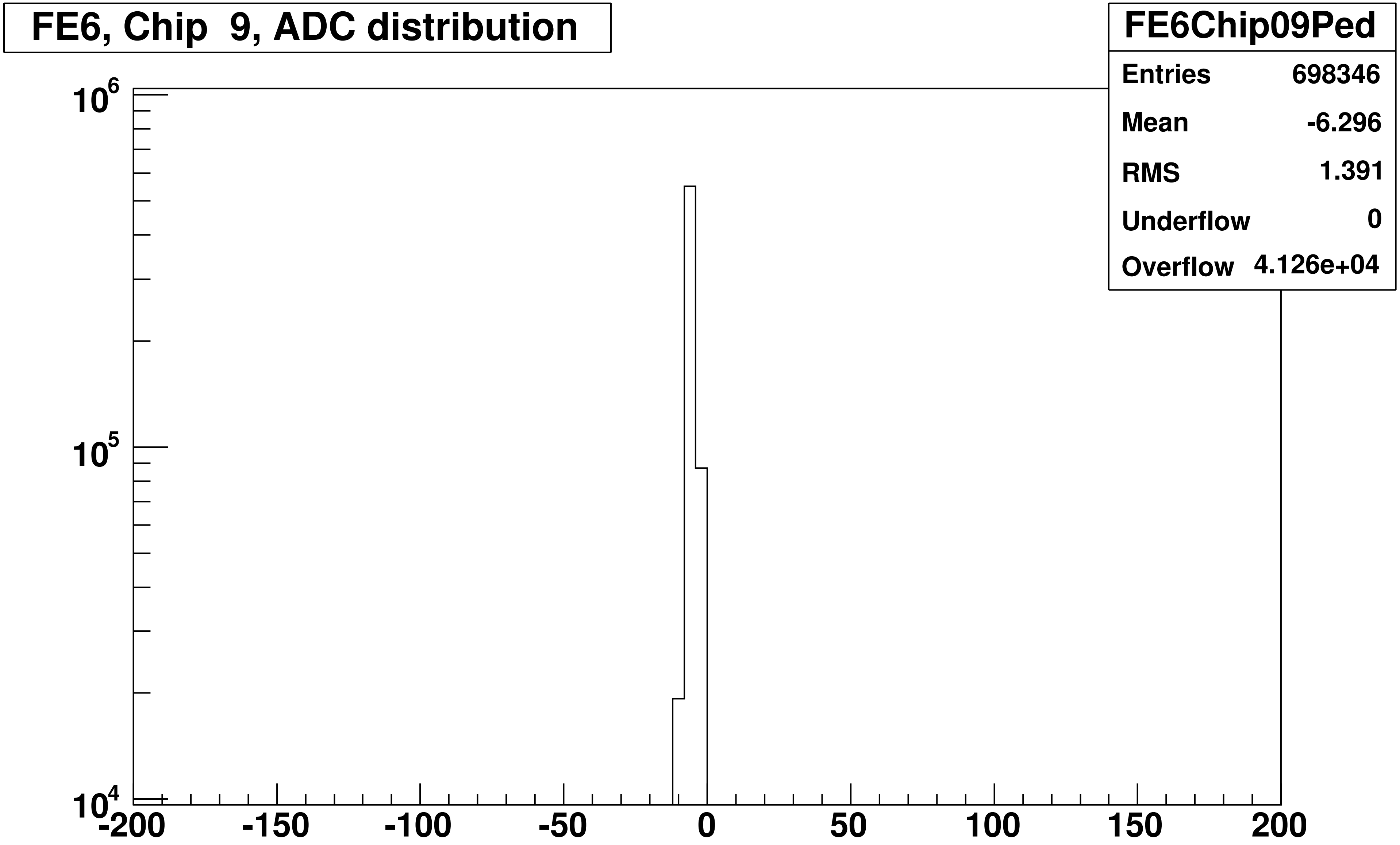
19400
19600
19800
20000



FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 698346
Mean 1125
RMS 4520
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 698346
Mean -6.296
RMS 1.391
Underflow 0
Overflow 4.126e+04
10^6
10^5
10^4
-200
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-100
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0
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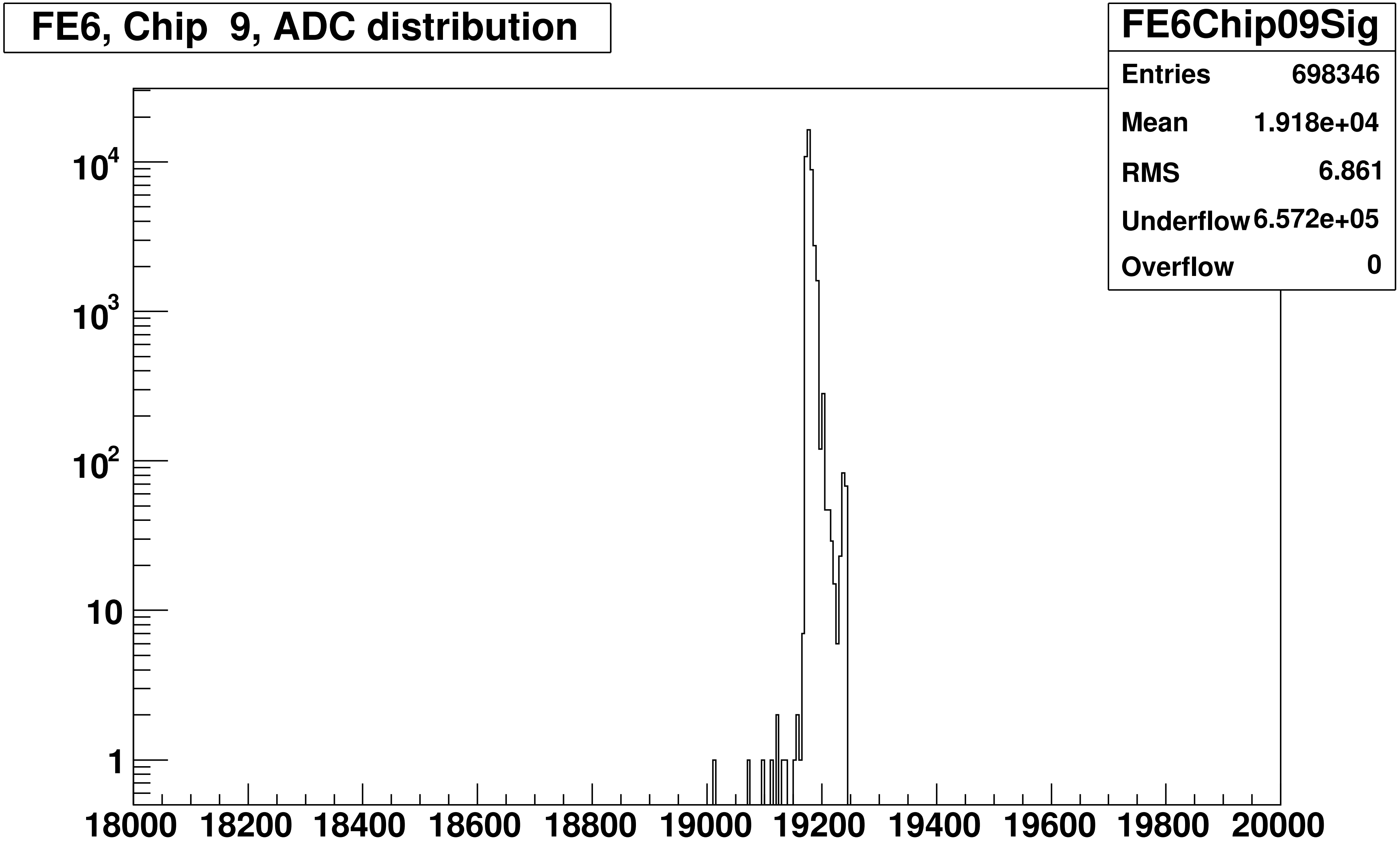
150
200


FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 698346
Mean 1.918e+04
RMS 6.861
Underflow 6.572e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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19000
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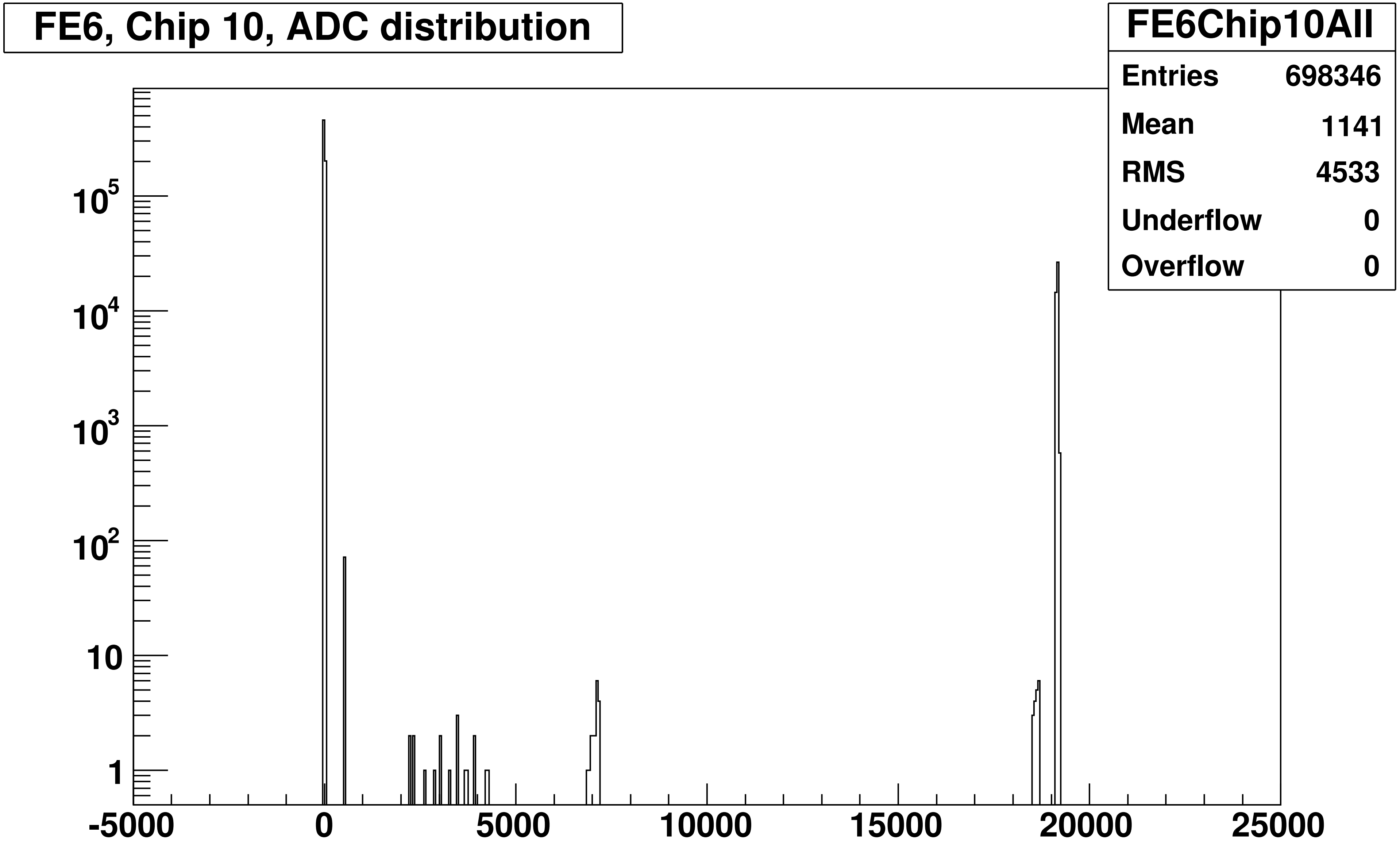
19400
19600
19800
20000



FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 698346
Mean 1141
RMS 4533
Underflow 0
Overflow 0
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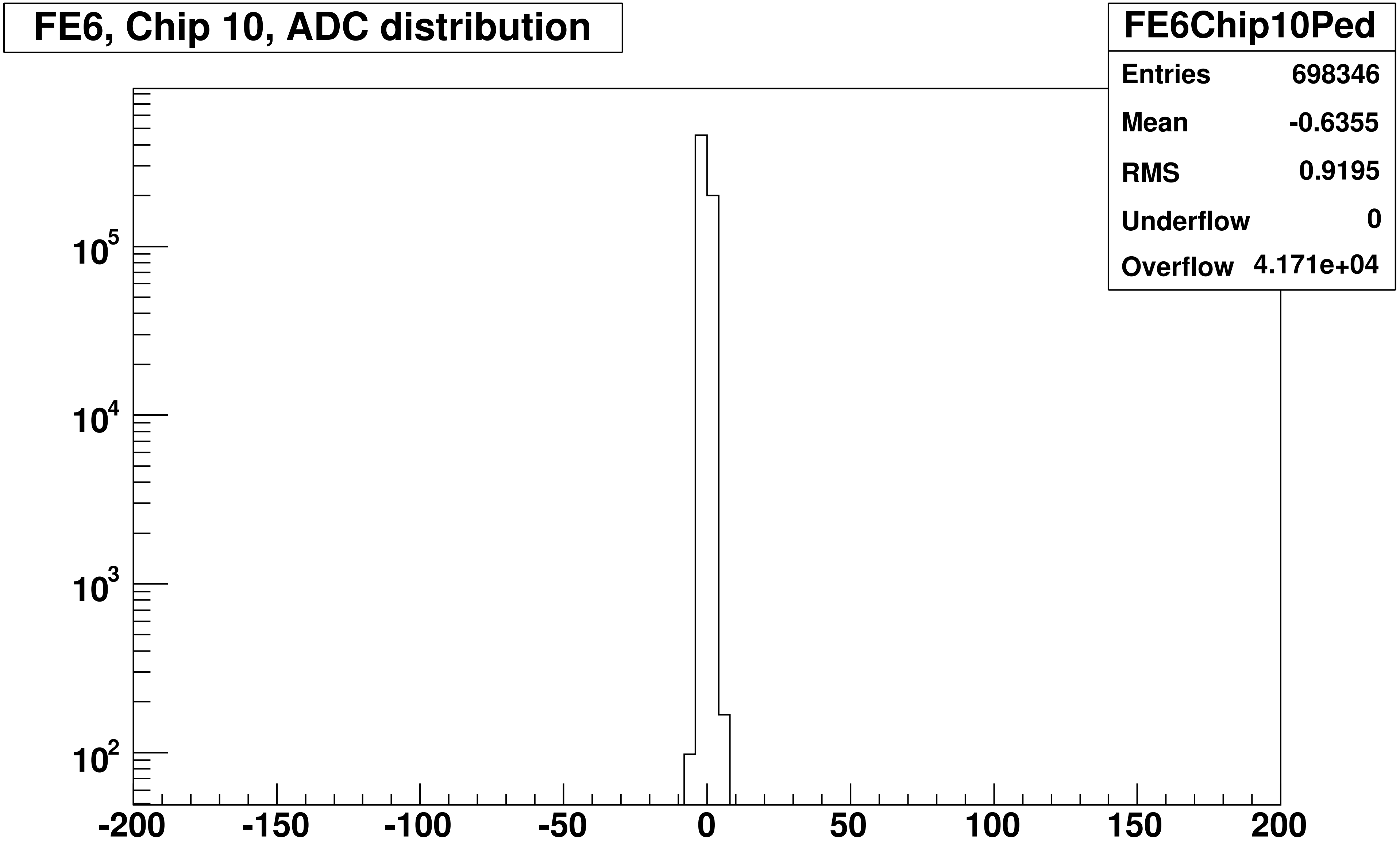
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 698346
Mean -0.6355
RMS 0.9195
Underflow 0
Overflow 4.171e+04
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10^4
10^3
10^2
-200
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-100
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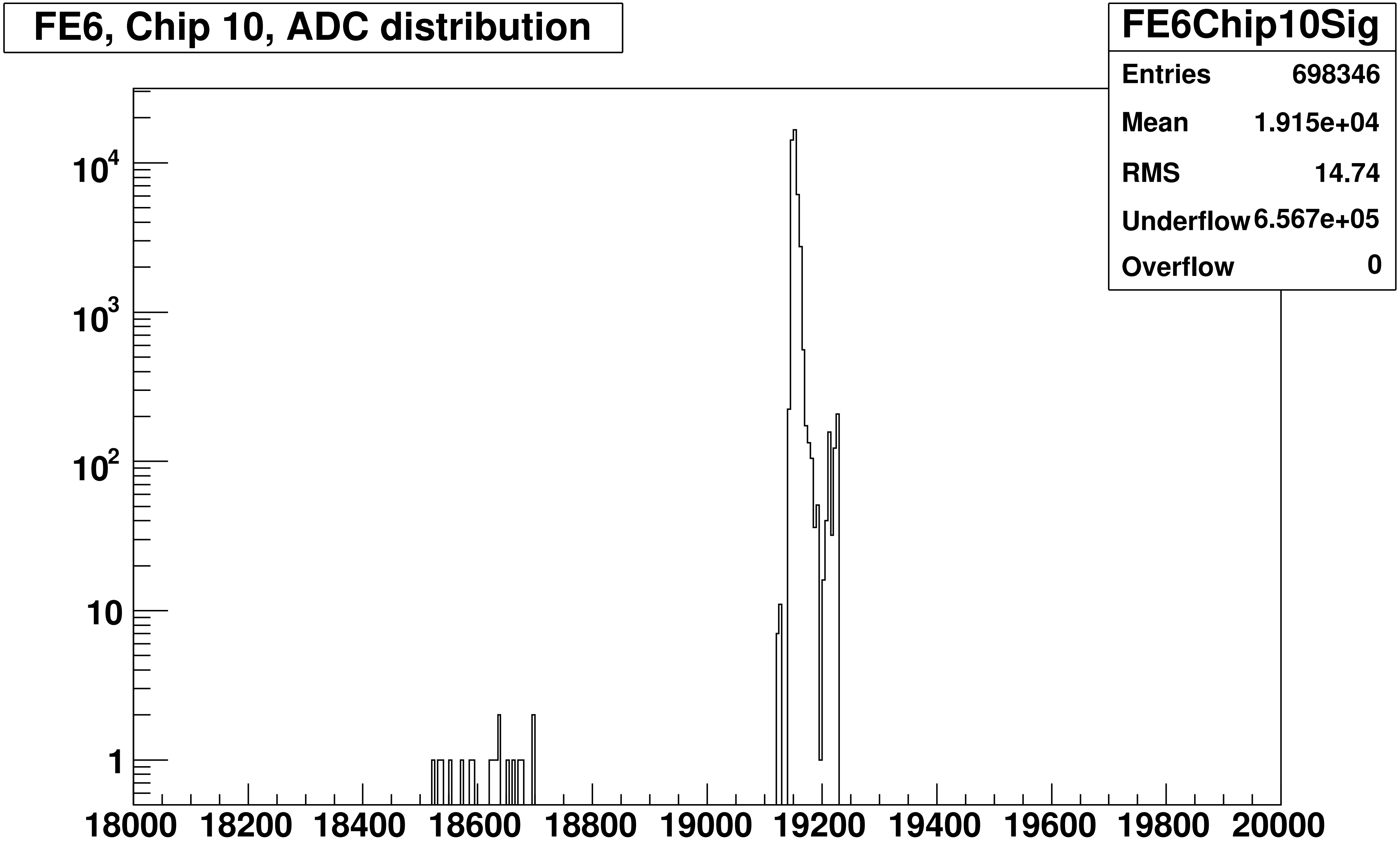
0
50
100
150
200


FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 698346
Mean 1.915e+04
RMS 14.74
Underflow 6.567e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
18400
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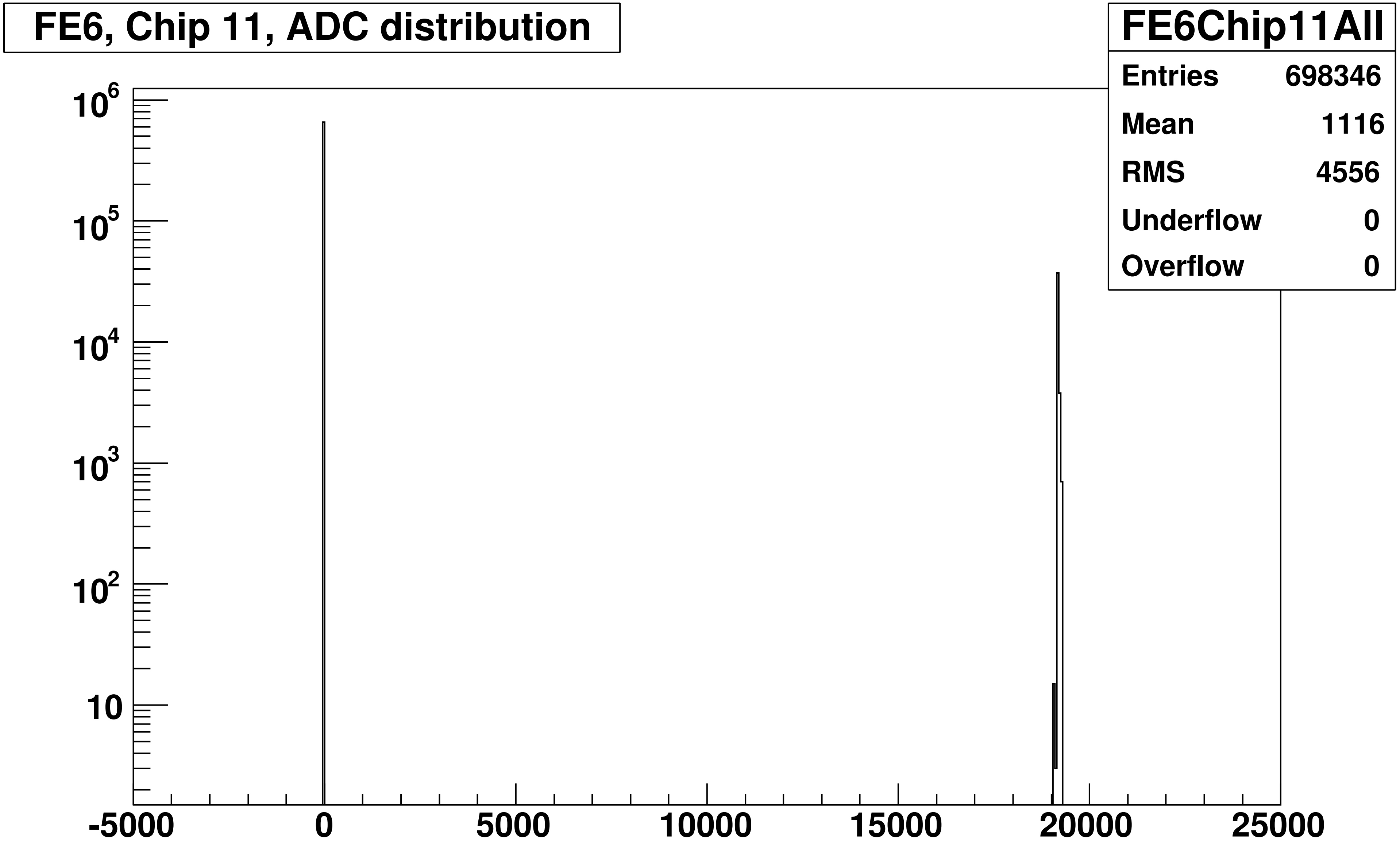
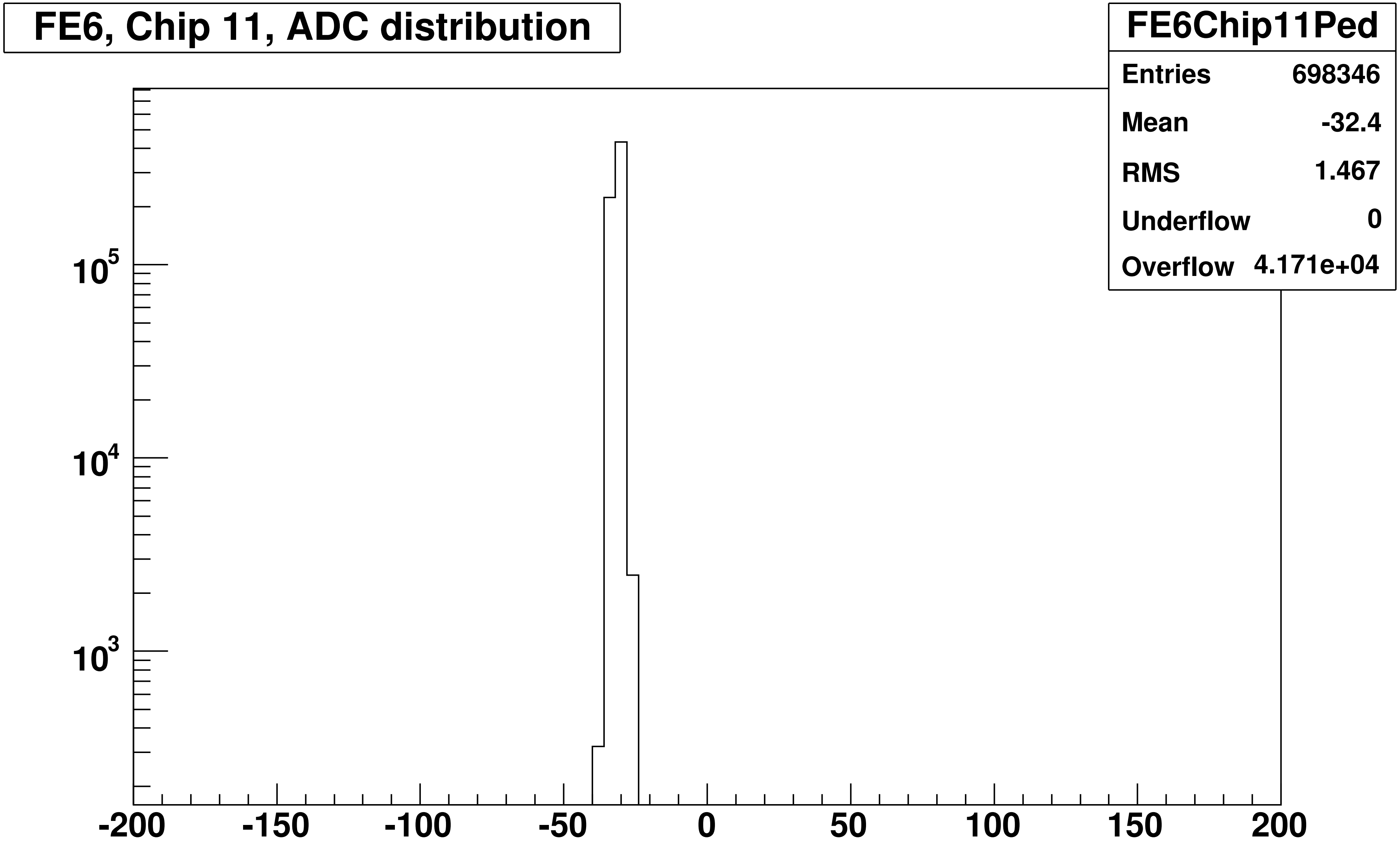
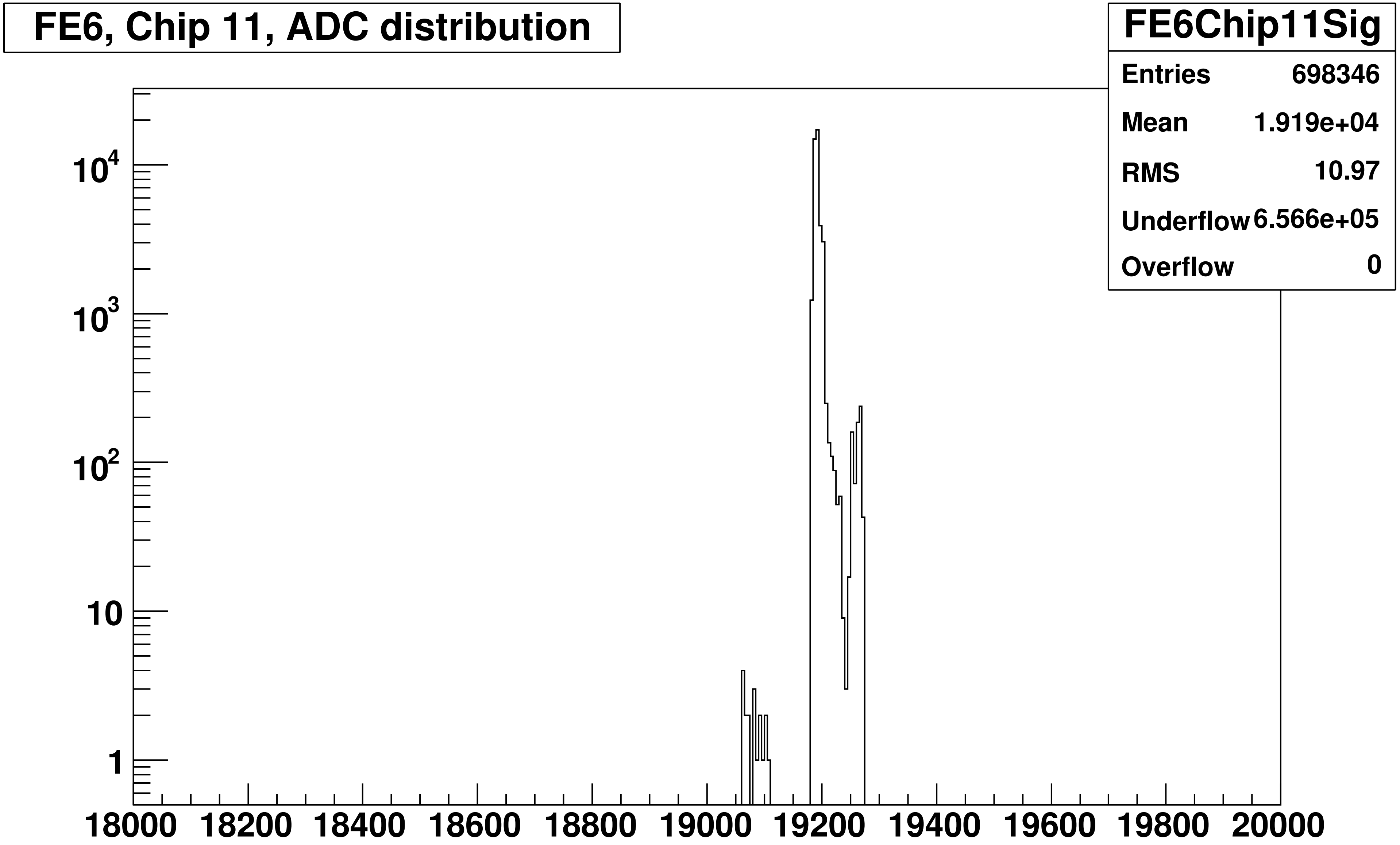
19800
20000

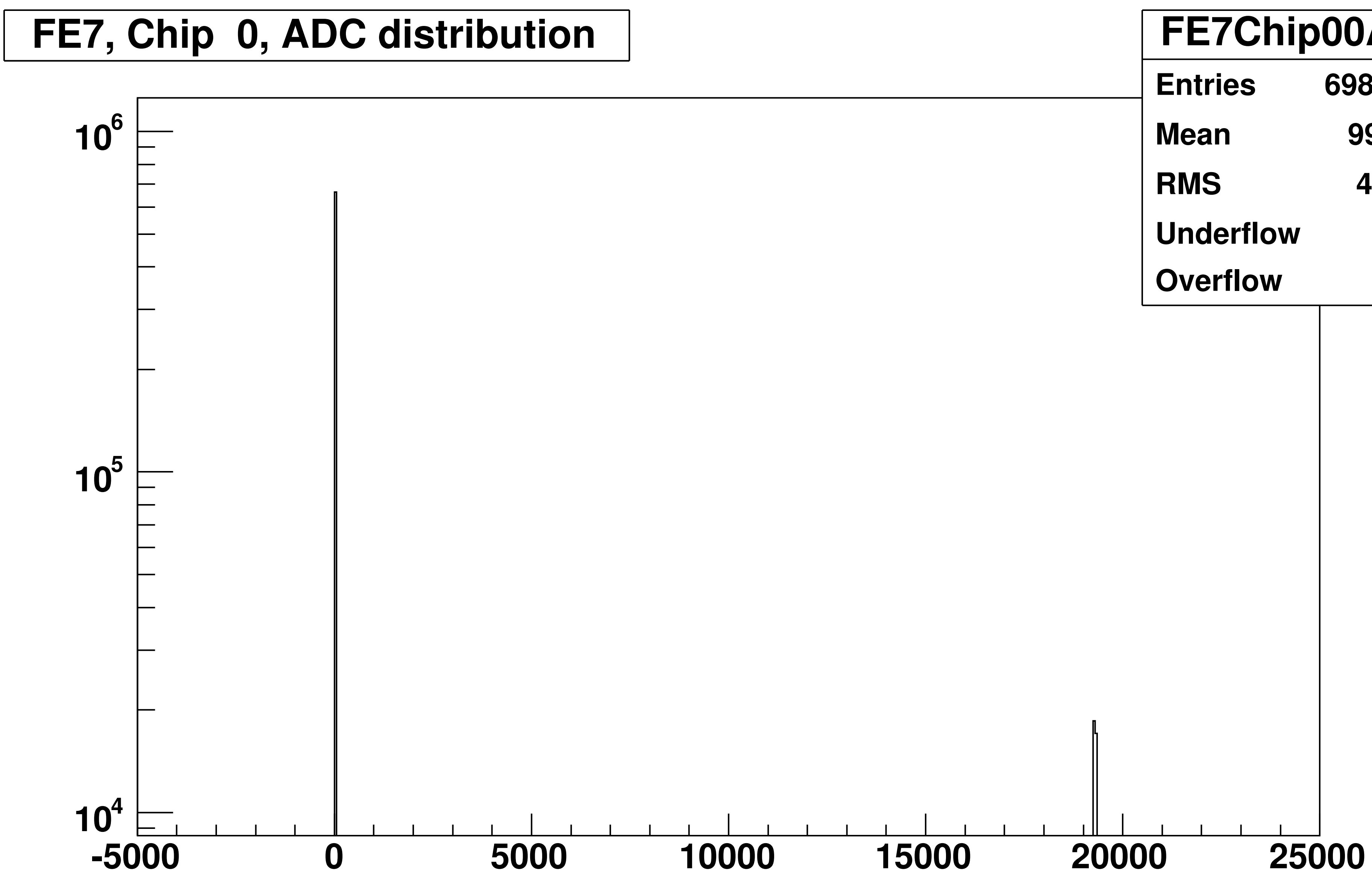

FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 698346
Mean 1116
RMS 4556
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

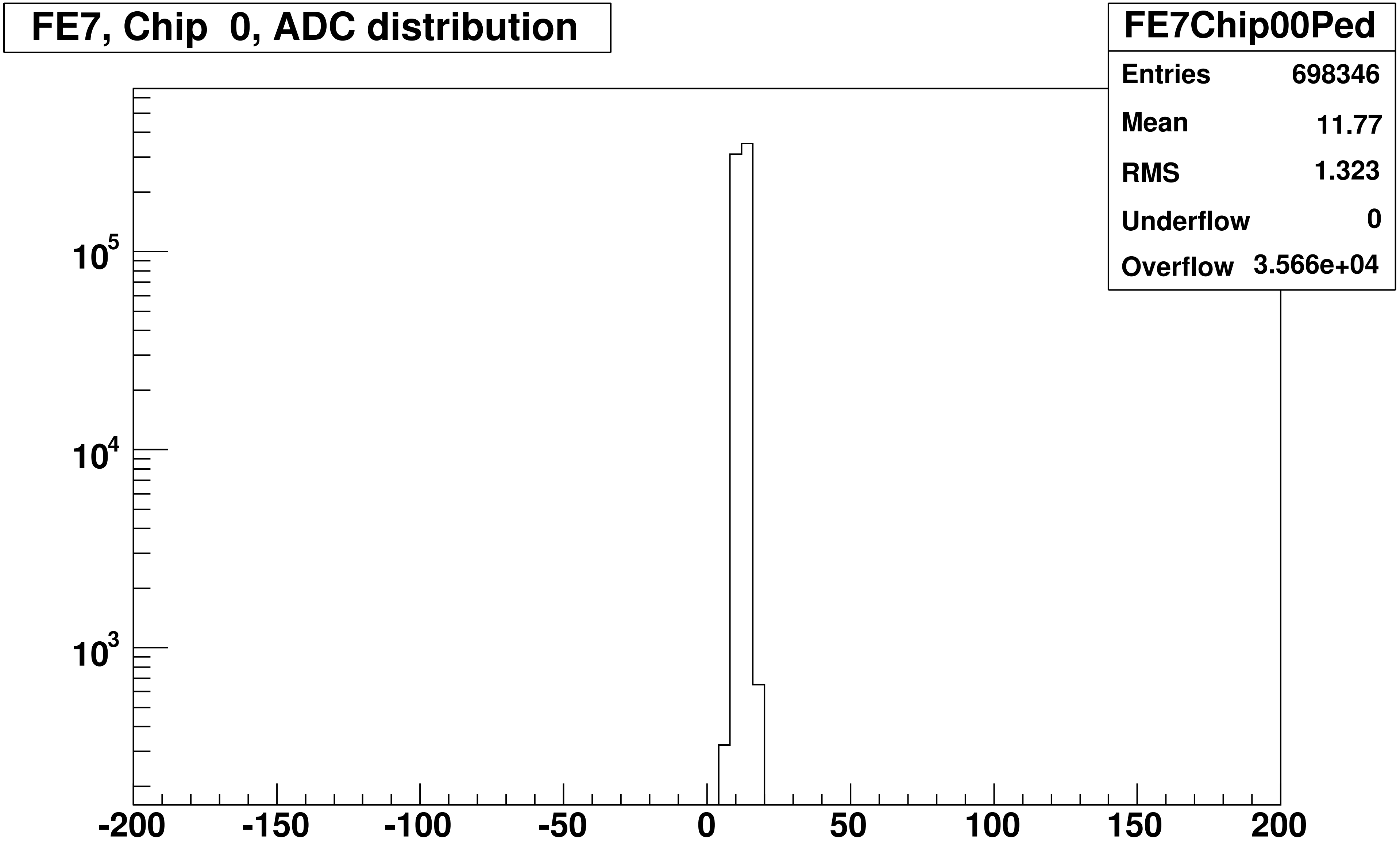


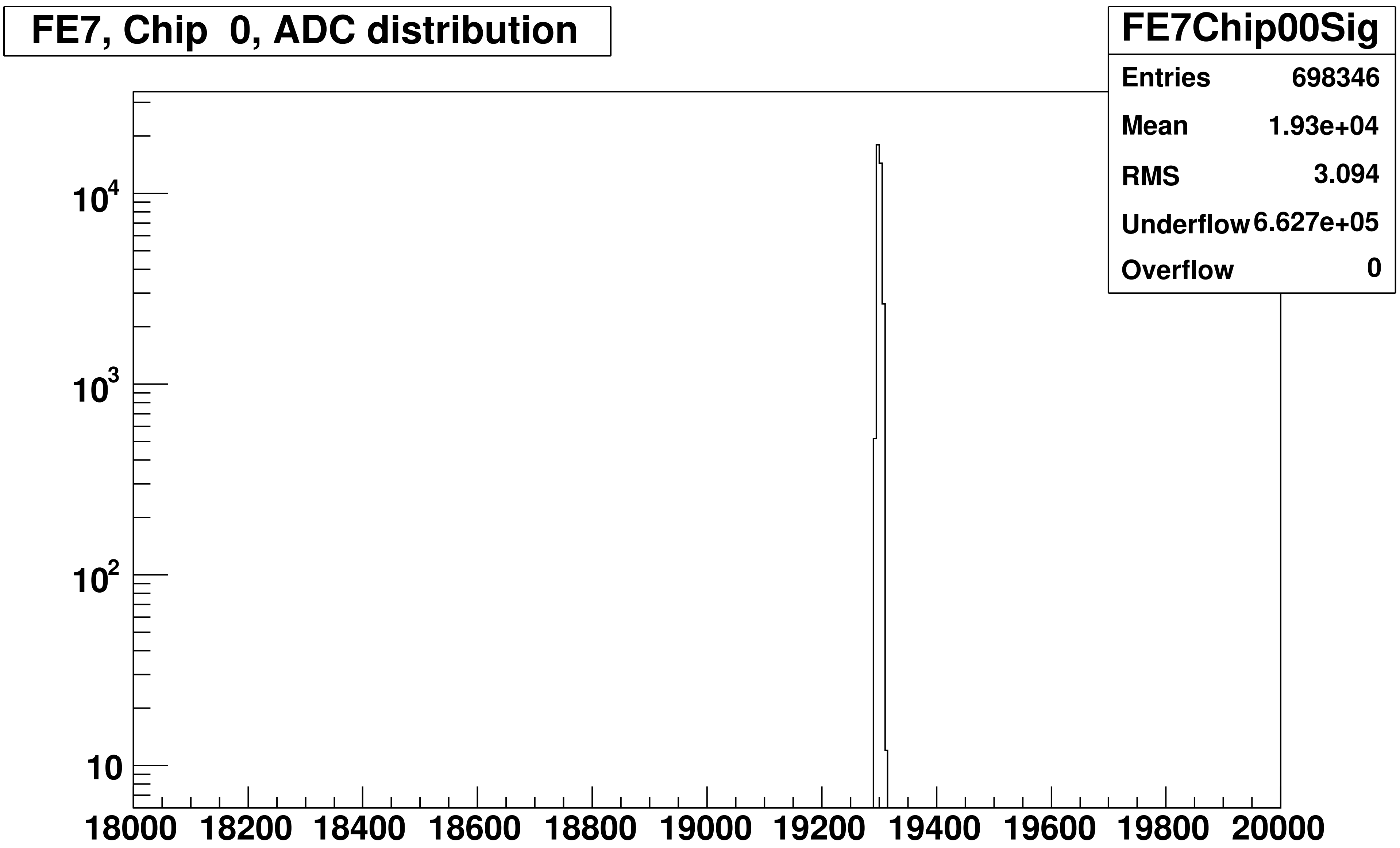
FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 698346
Mean -32.4
RMS 1.467
Underflow 0
Overflow 4.171e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200



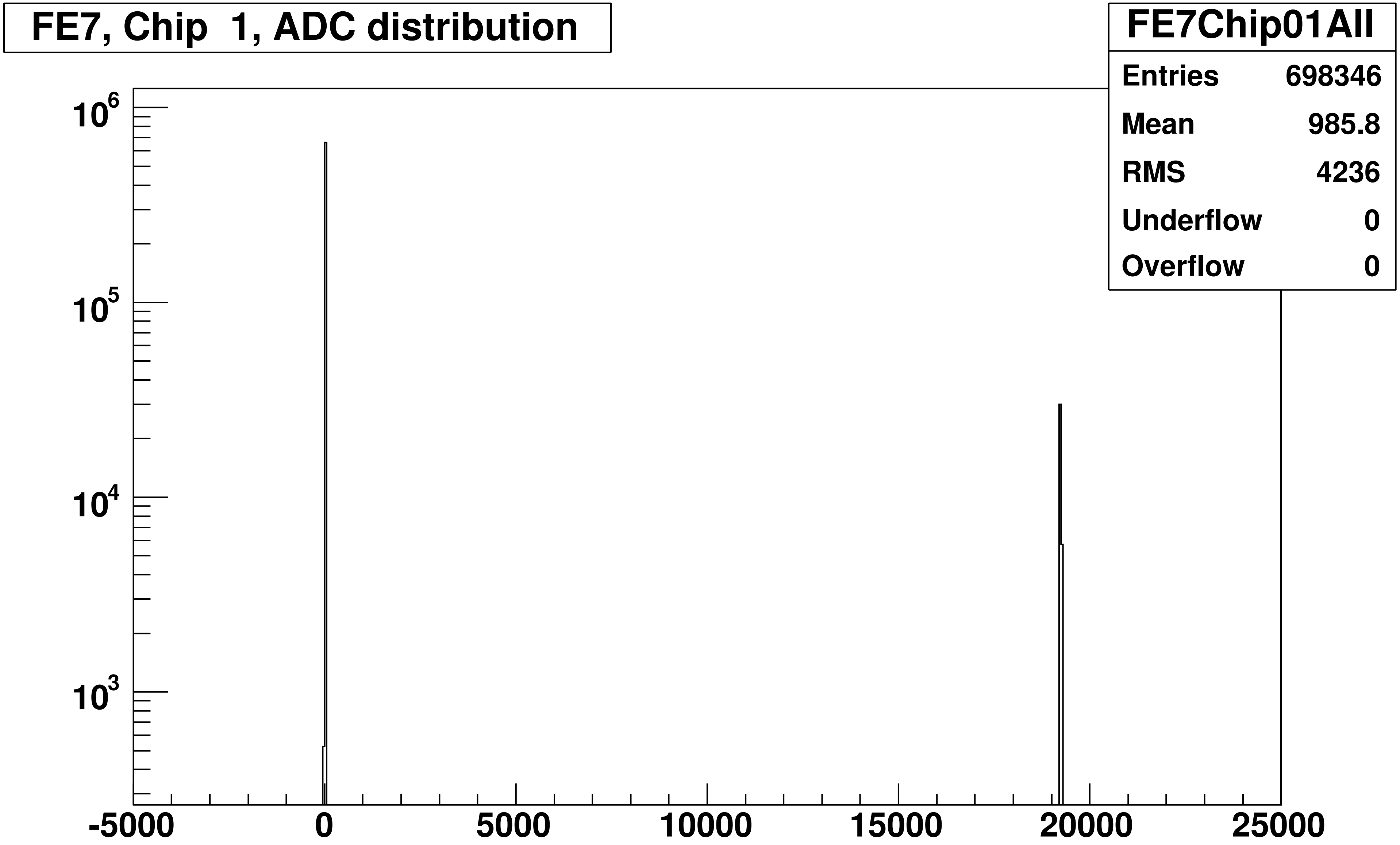
FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 698346
Mean 1.919e+04
RMS 10.97
Underflow 6.566e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 698346
Mean 996.6
RMS 4246
Underflow 0
Overflow 0
10^6
10^5
10^4
-5000
0
5000
10000
15000
20000
25000

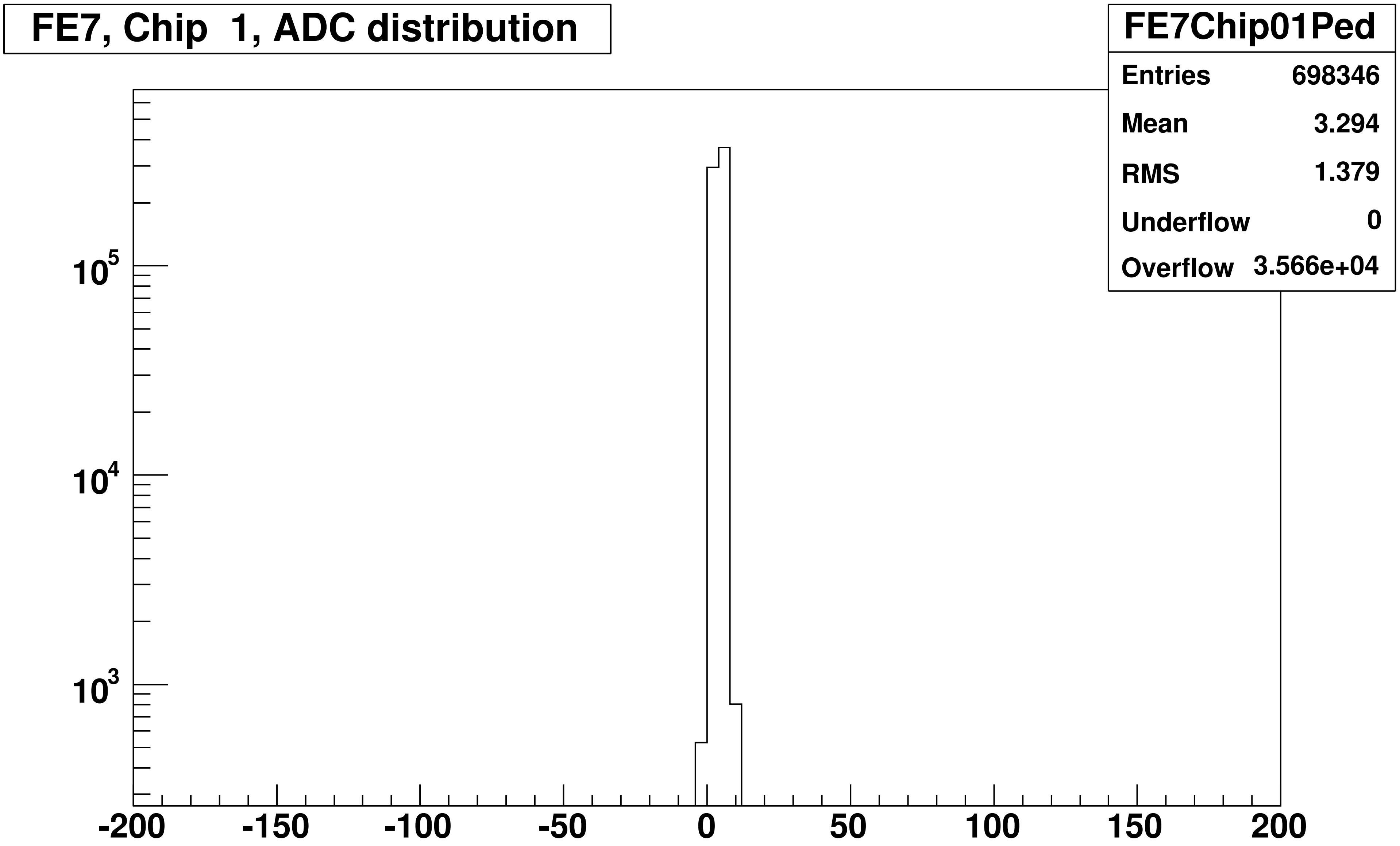

FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 698346
Mean 11.77
RMS 1.323
Underflow 0
Overflow 3.566e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200



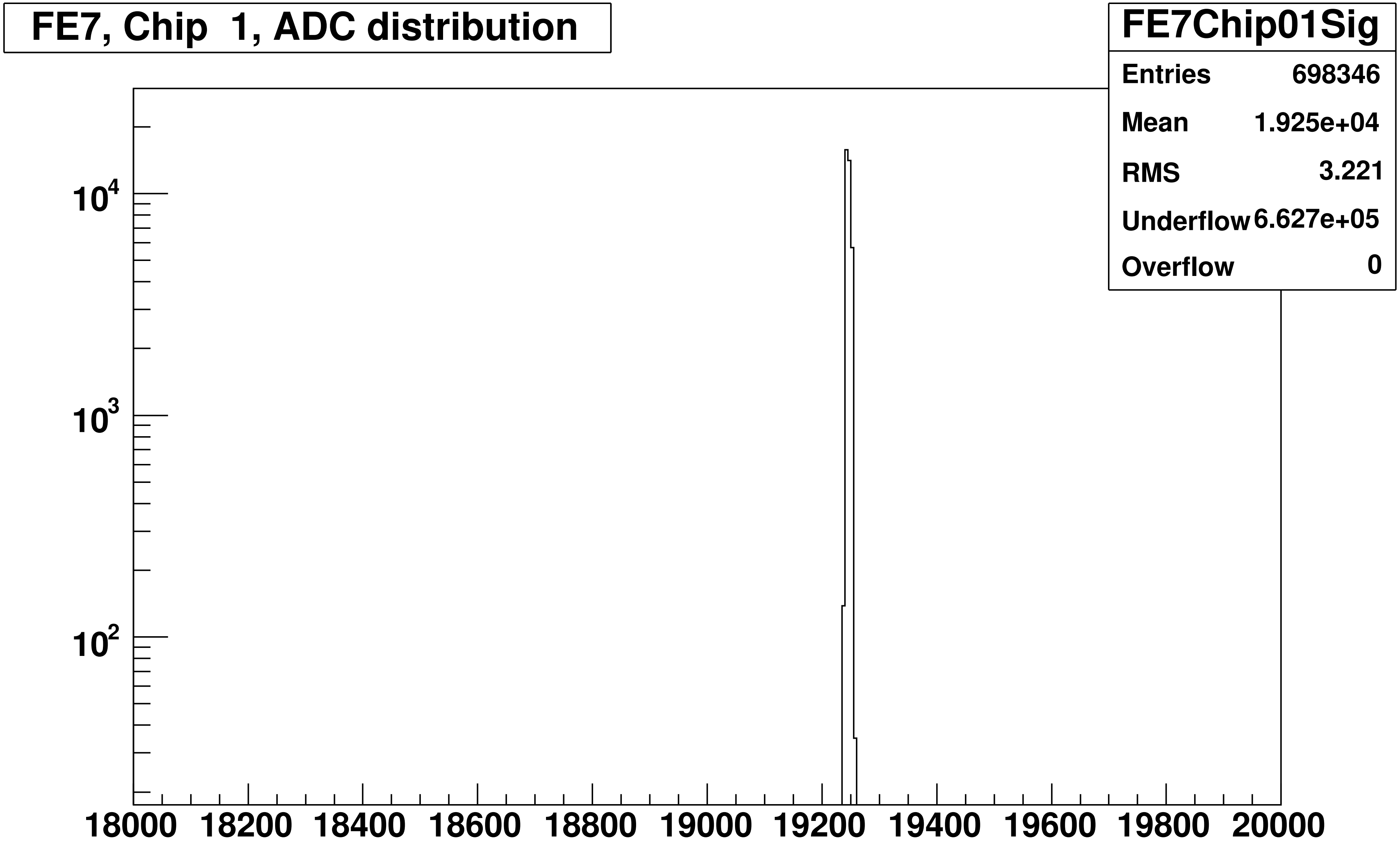
FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 698346
Mean 1.93e+04
RMS 3.094
Underflow 6.627e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

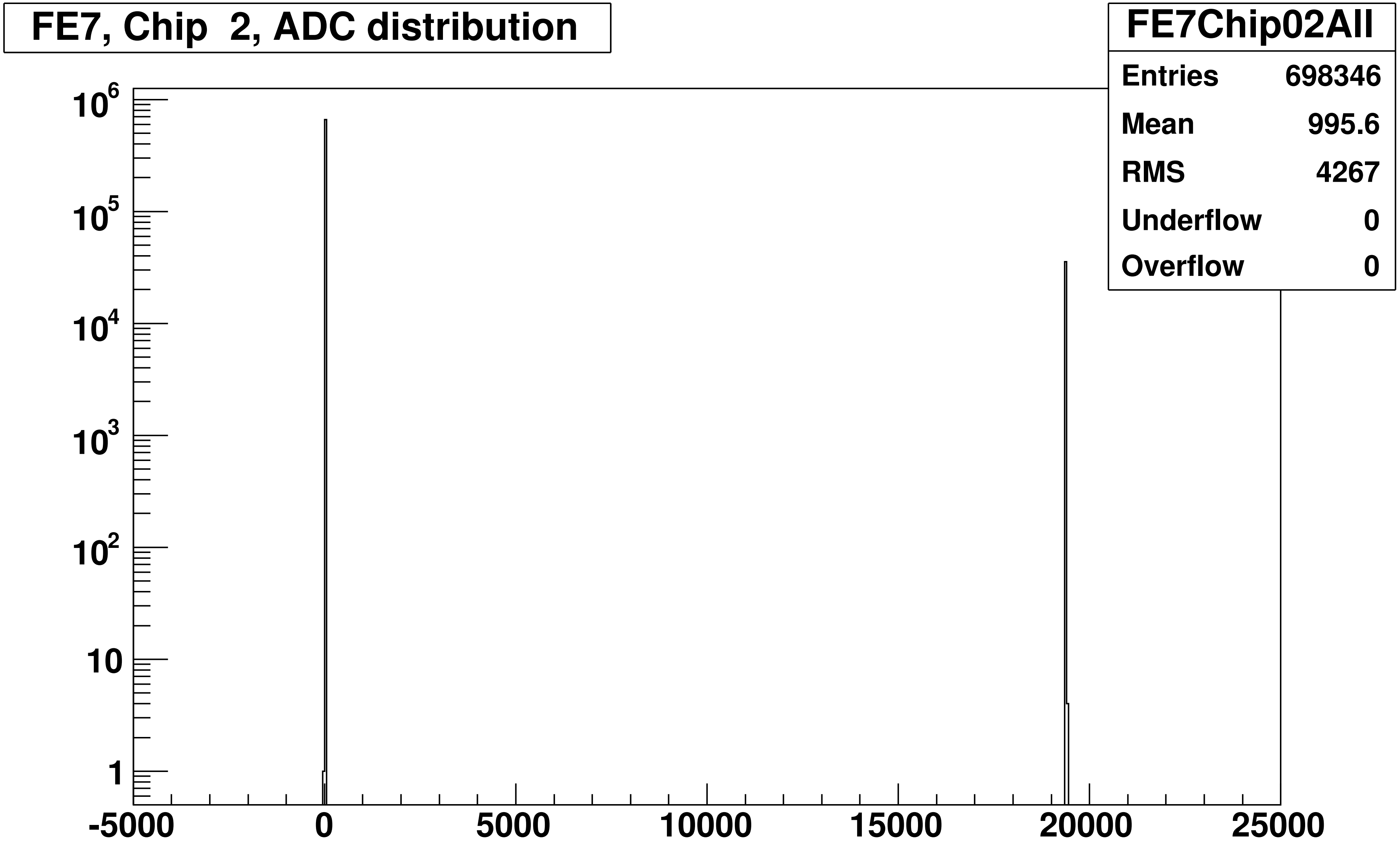


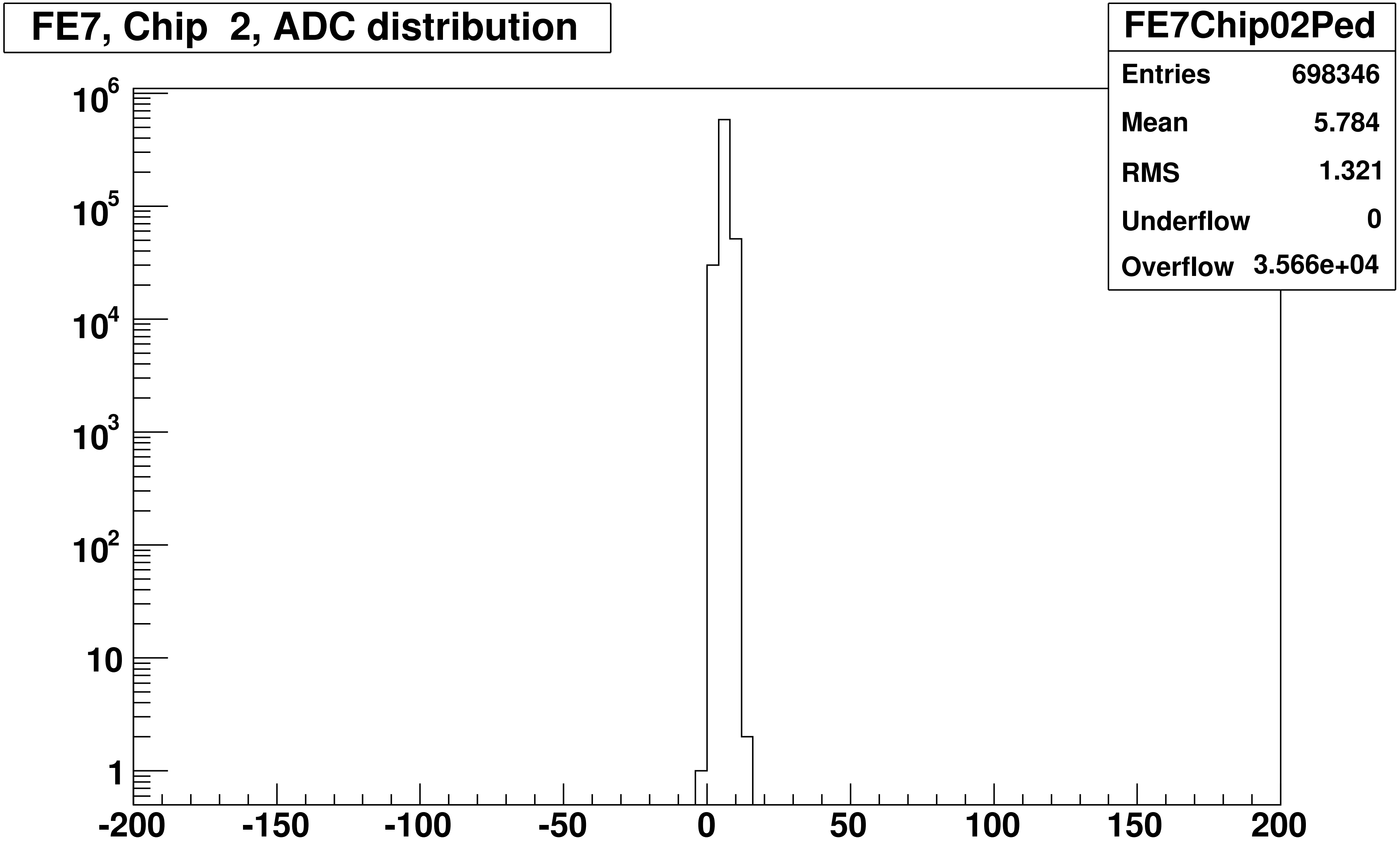
FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 698346
Mean 985.8
RMS 4236
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



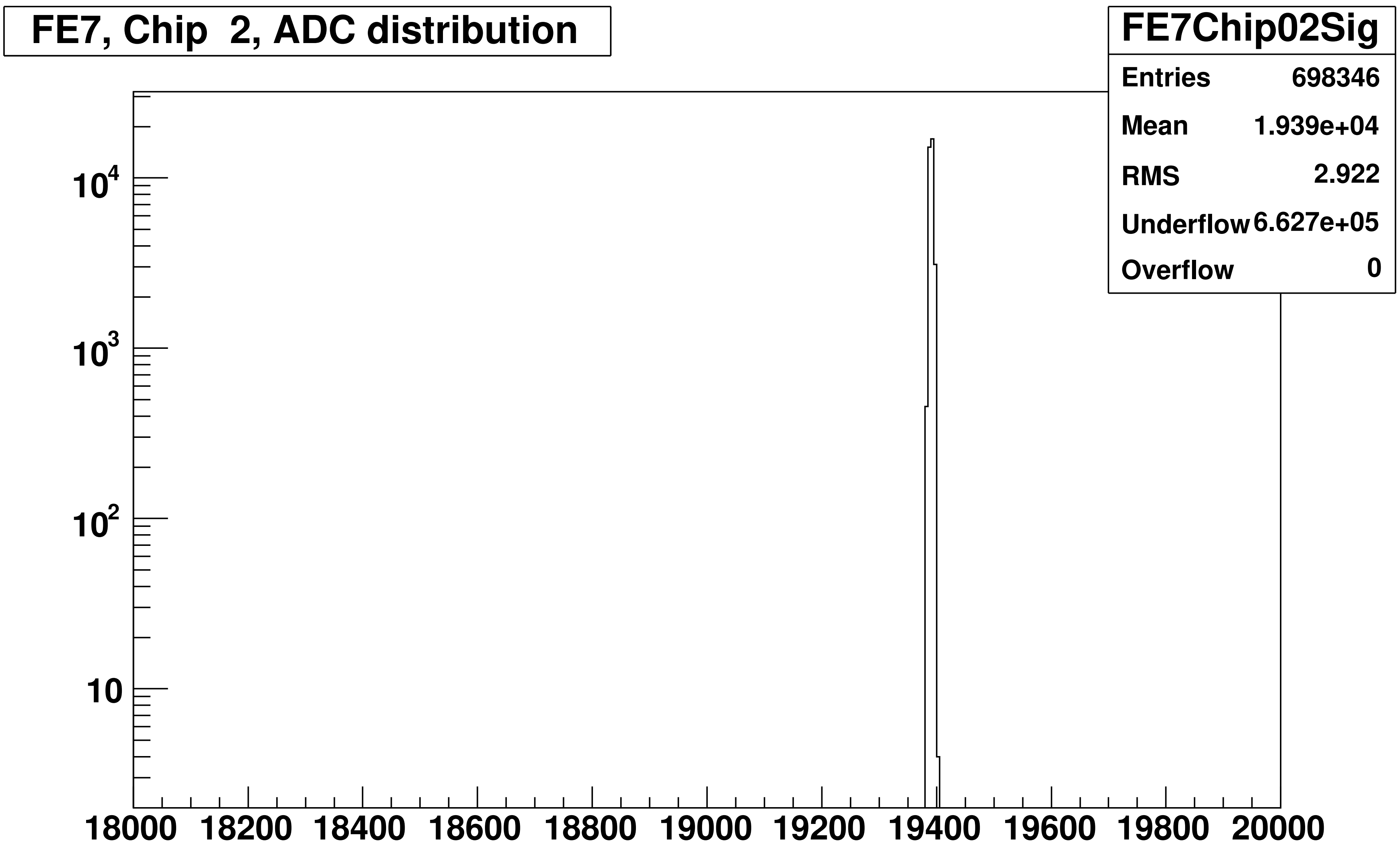
FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 698346
Mean 3.294
RMS 1.379
Underflow 0
Overflow 3.566e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

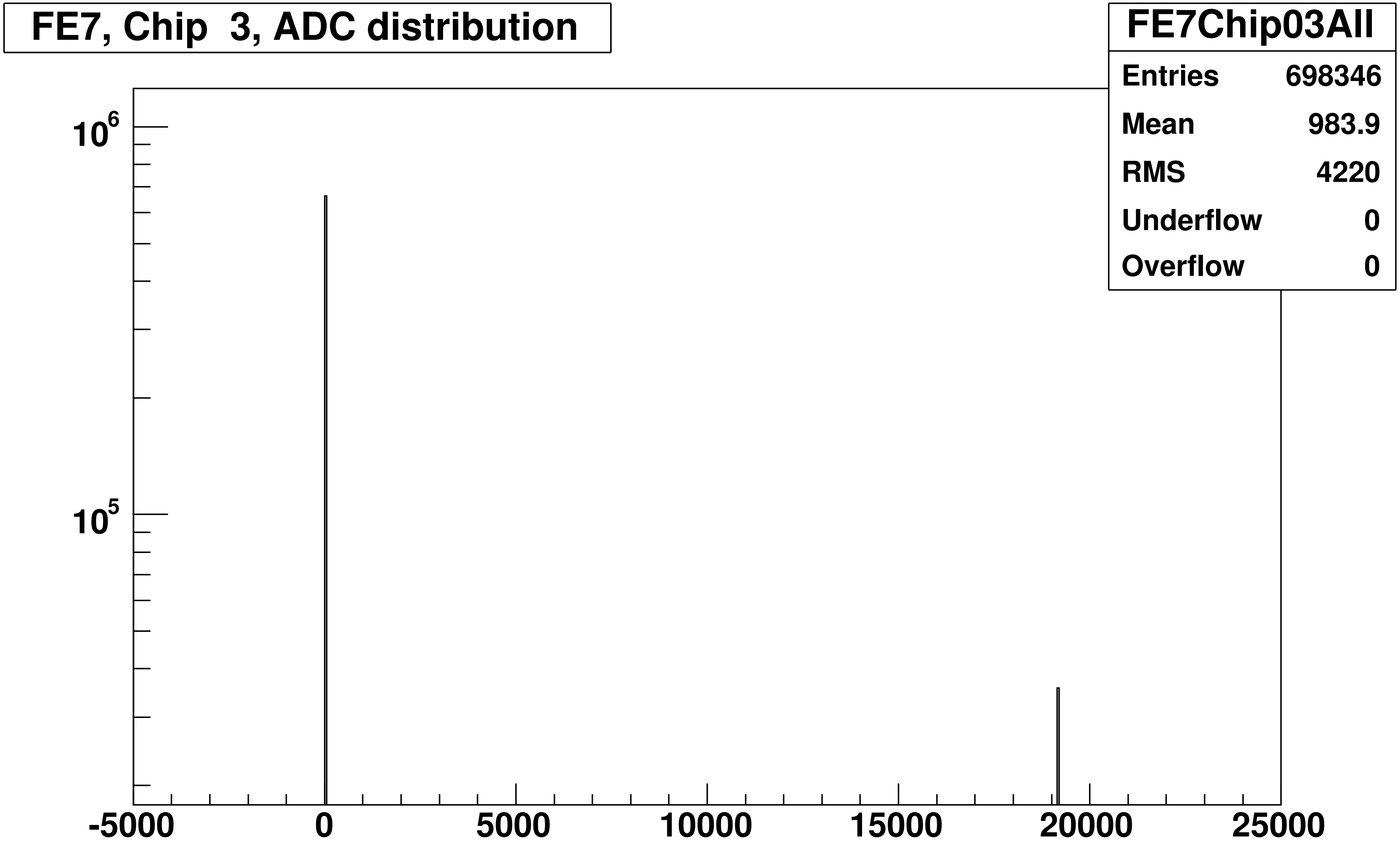

FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 698346
Mean 1.925e+04
RMS 3.221
Underflow 6.627e+05
Overflow 0
10^4
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

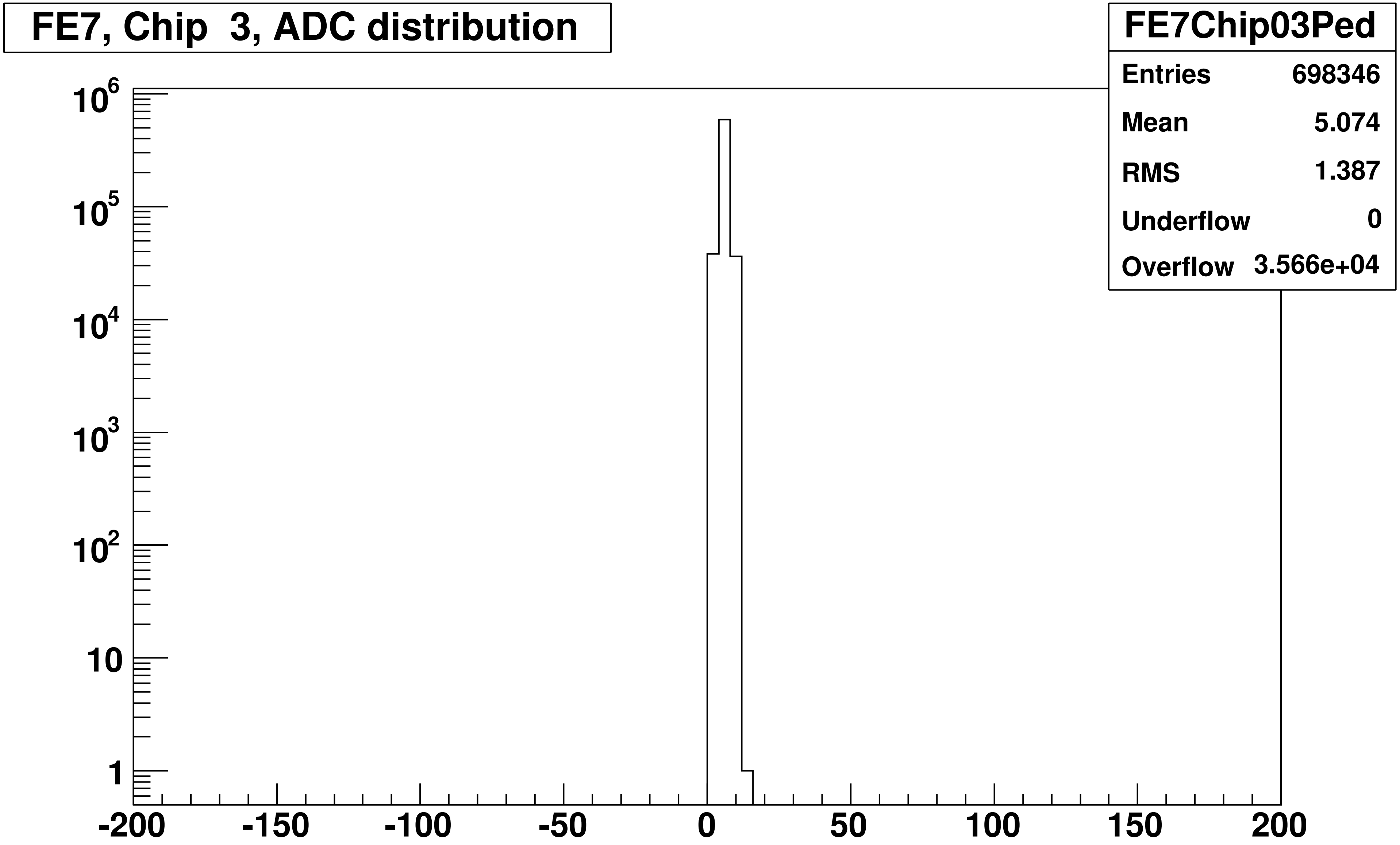

FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 698346
Mean 995.6
RMS 4267
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

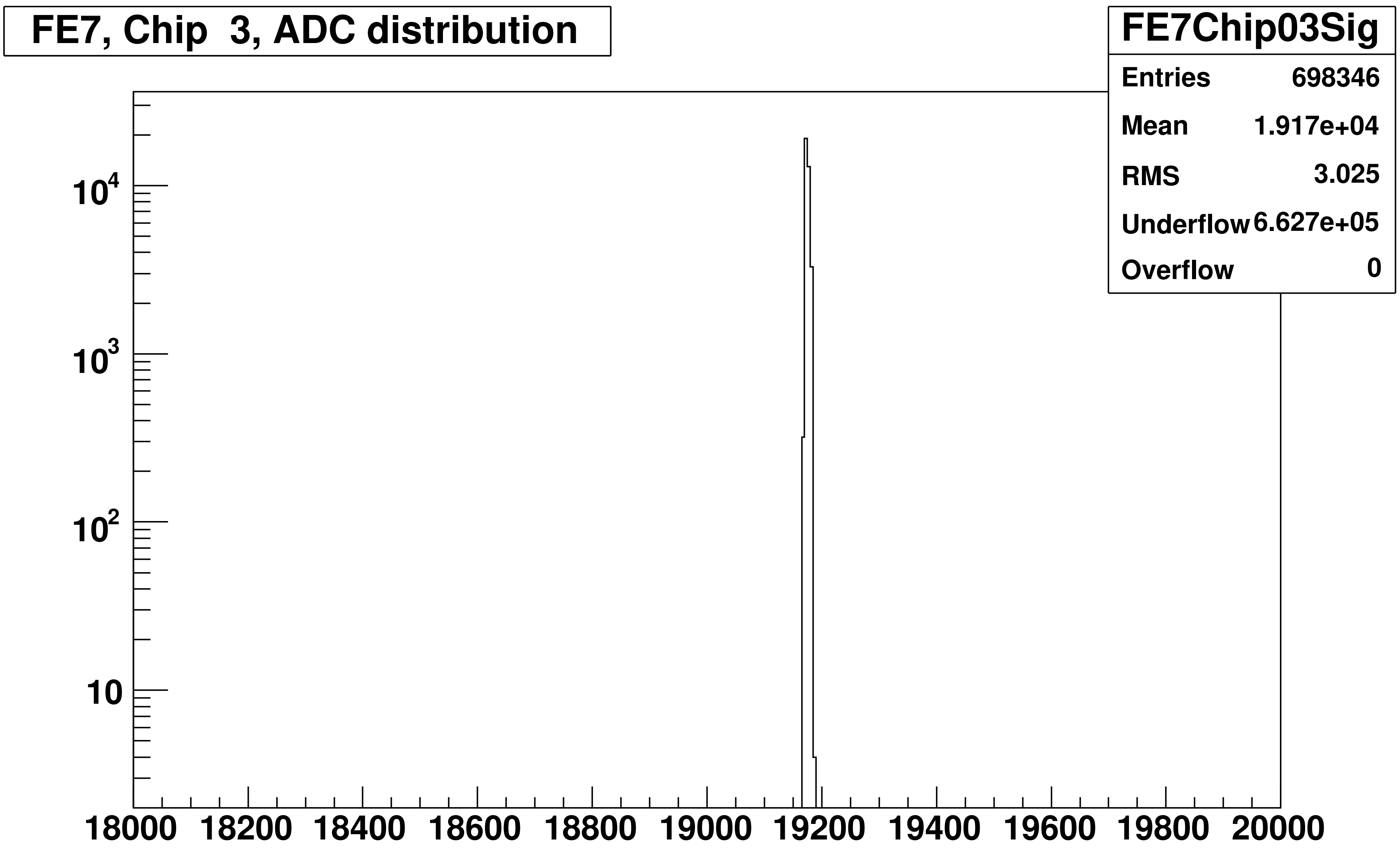


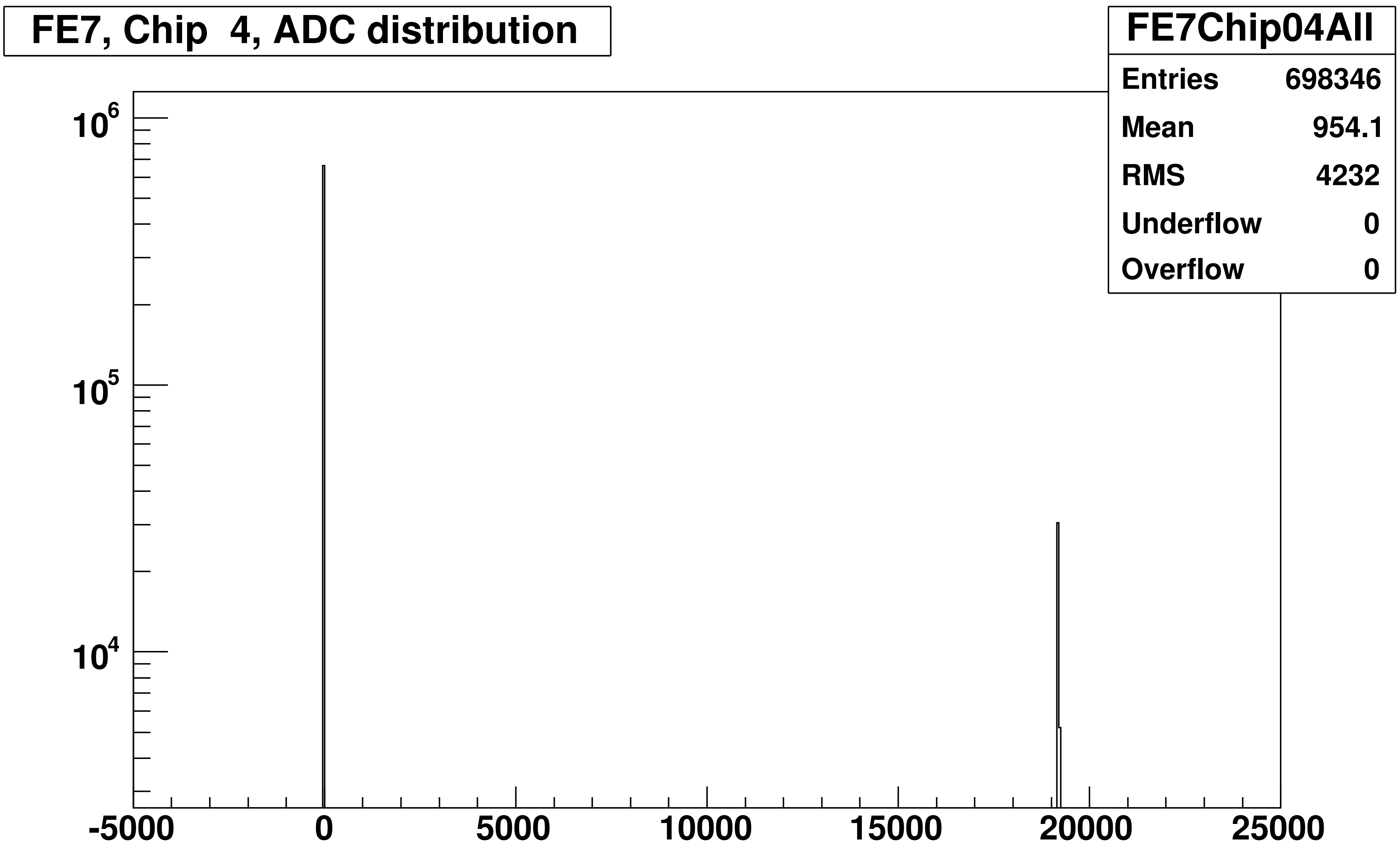
FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 698346
Mean 5.784
RMS 1.321
Underflow 0
Overflow 3.566e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

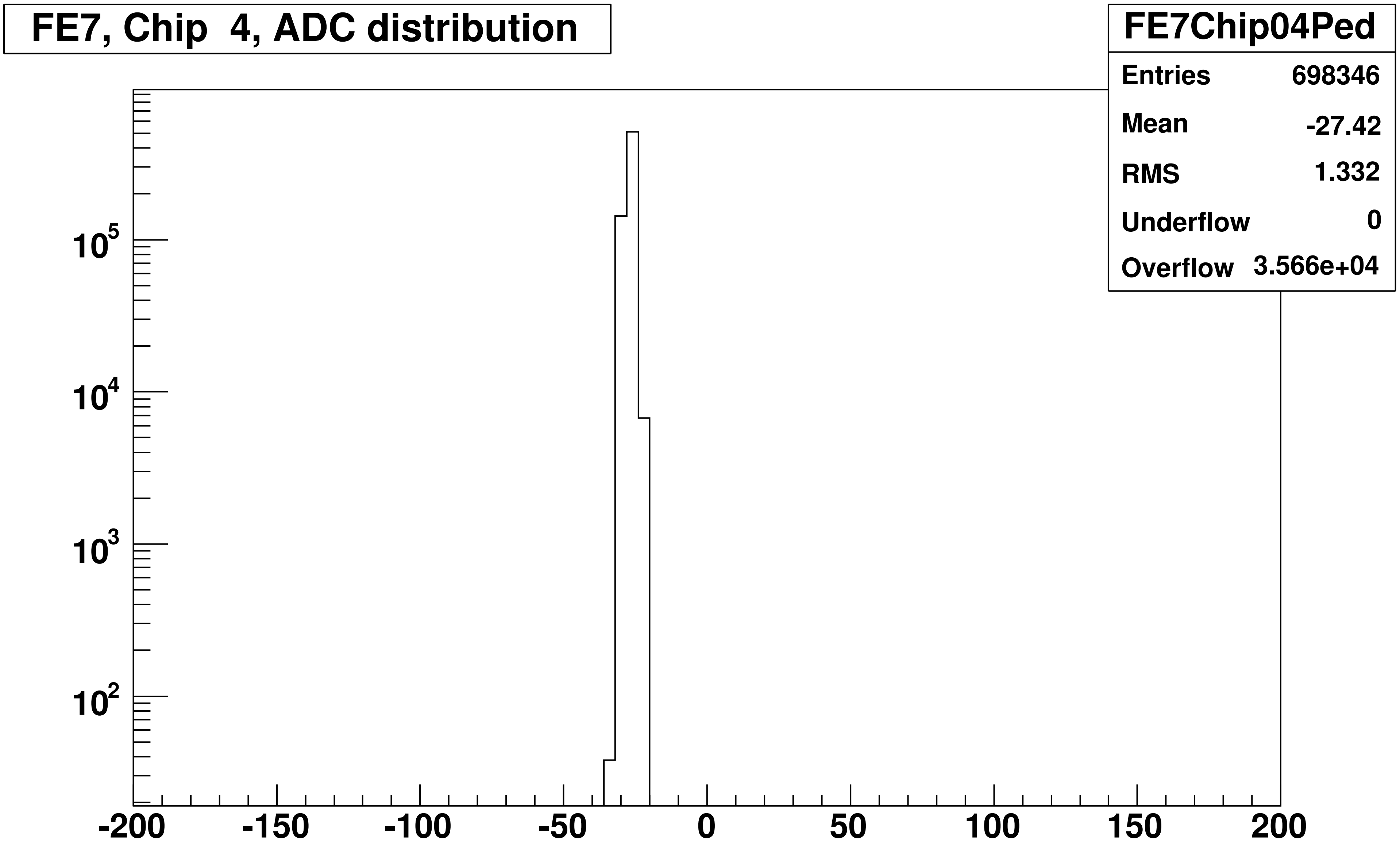

FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 698346
Mean 1.939e+04
RMS 2.922
Underflow 6.627e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

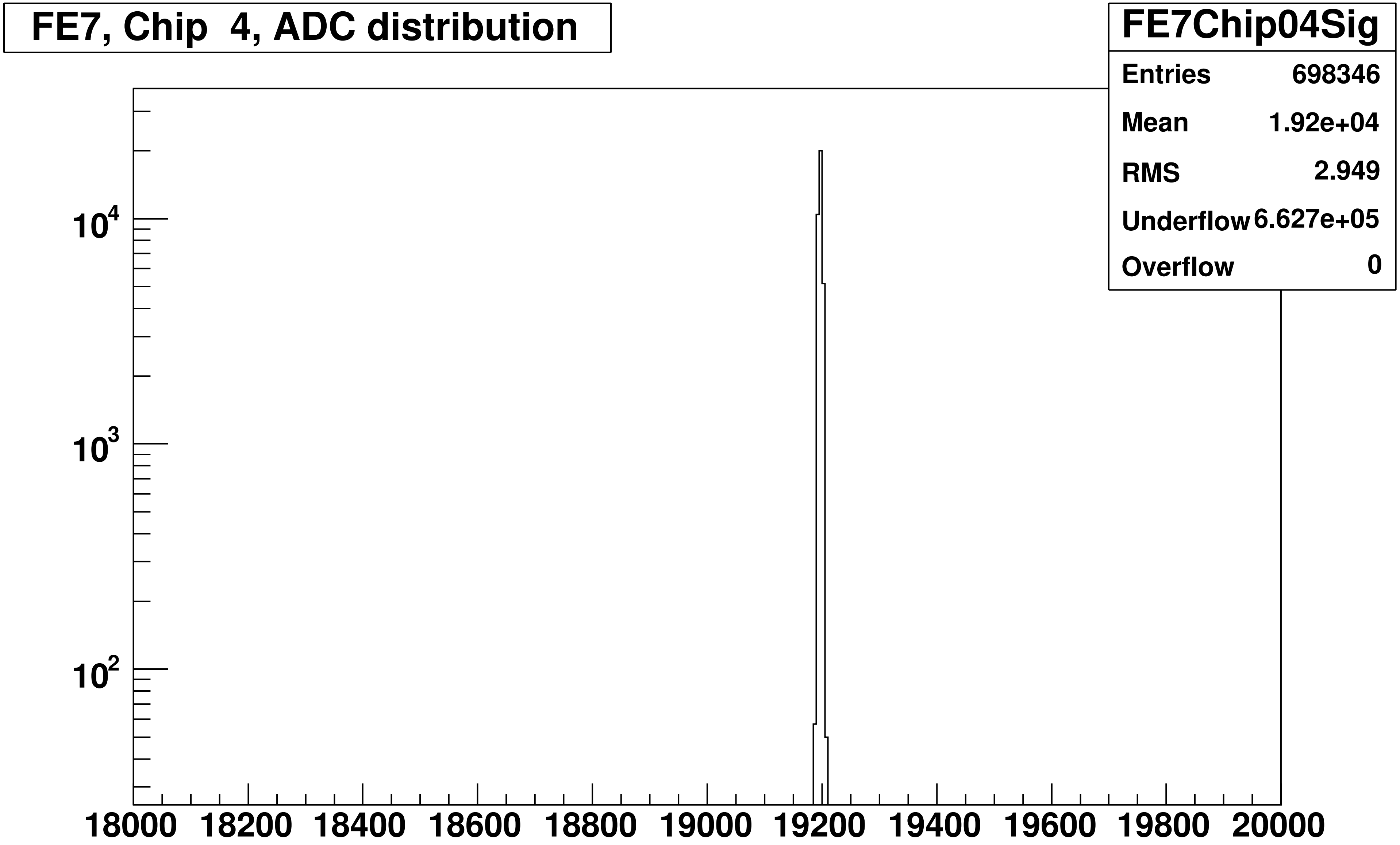

FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 698346
Mean 983.9
RMS 4220
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000

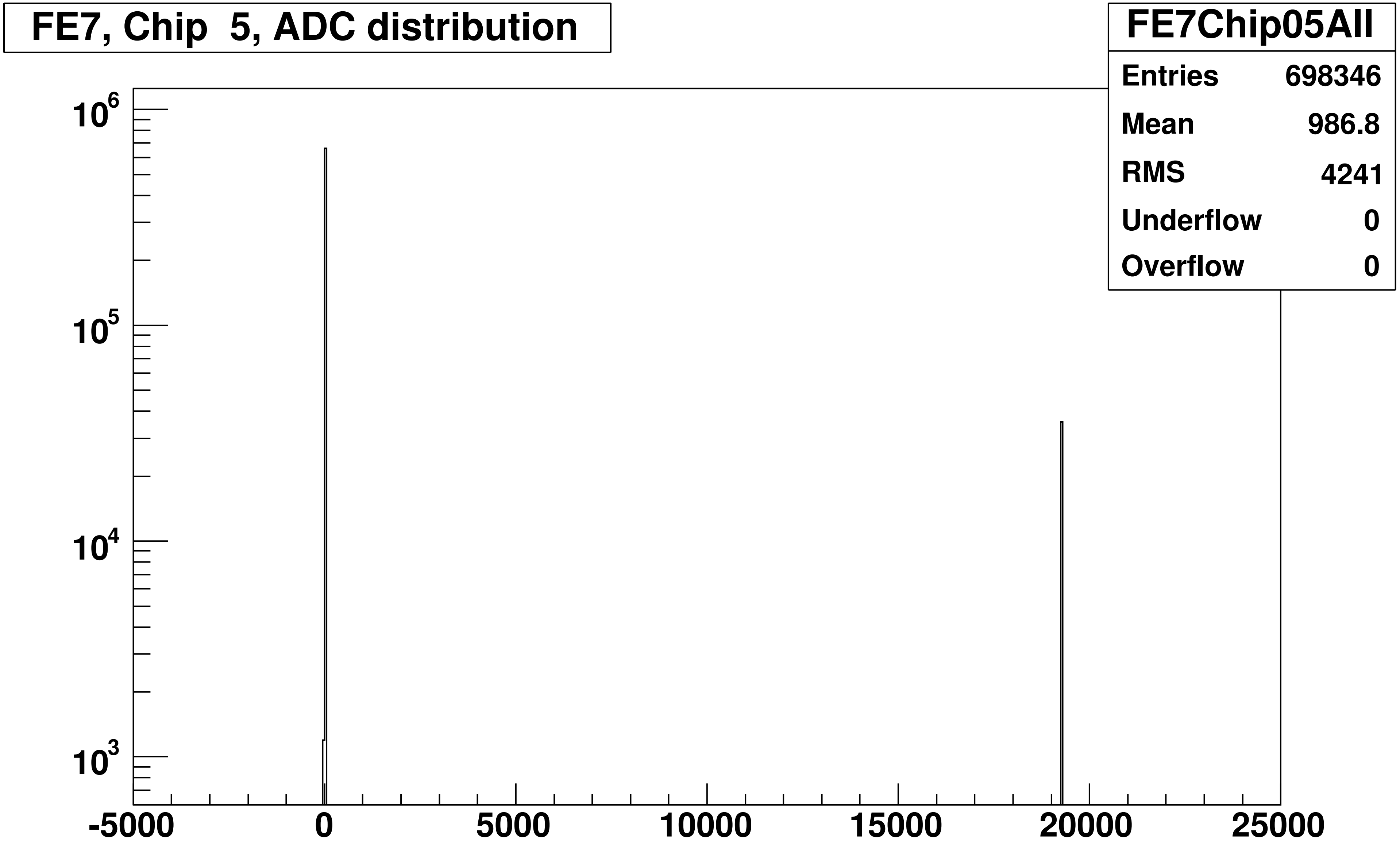

FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 698346
Mean 5.074
RMS 1.387
Underflow 0
Overflow 3.566e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

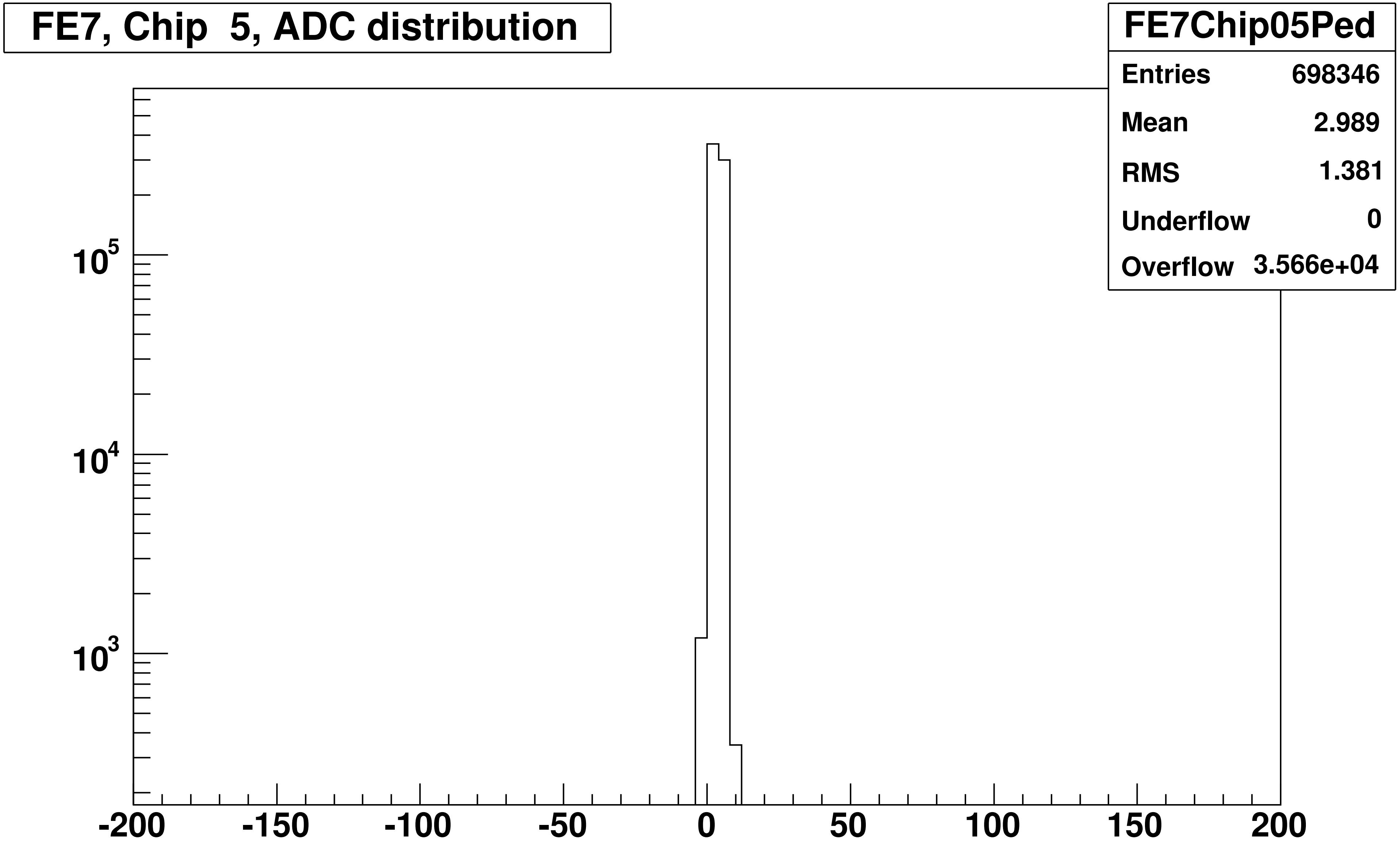

FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 698346
Mean 1.917e+04
RMS 3.025
Underflow 6.627e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

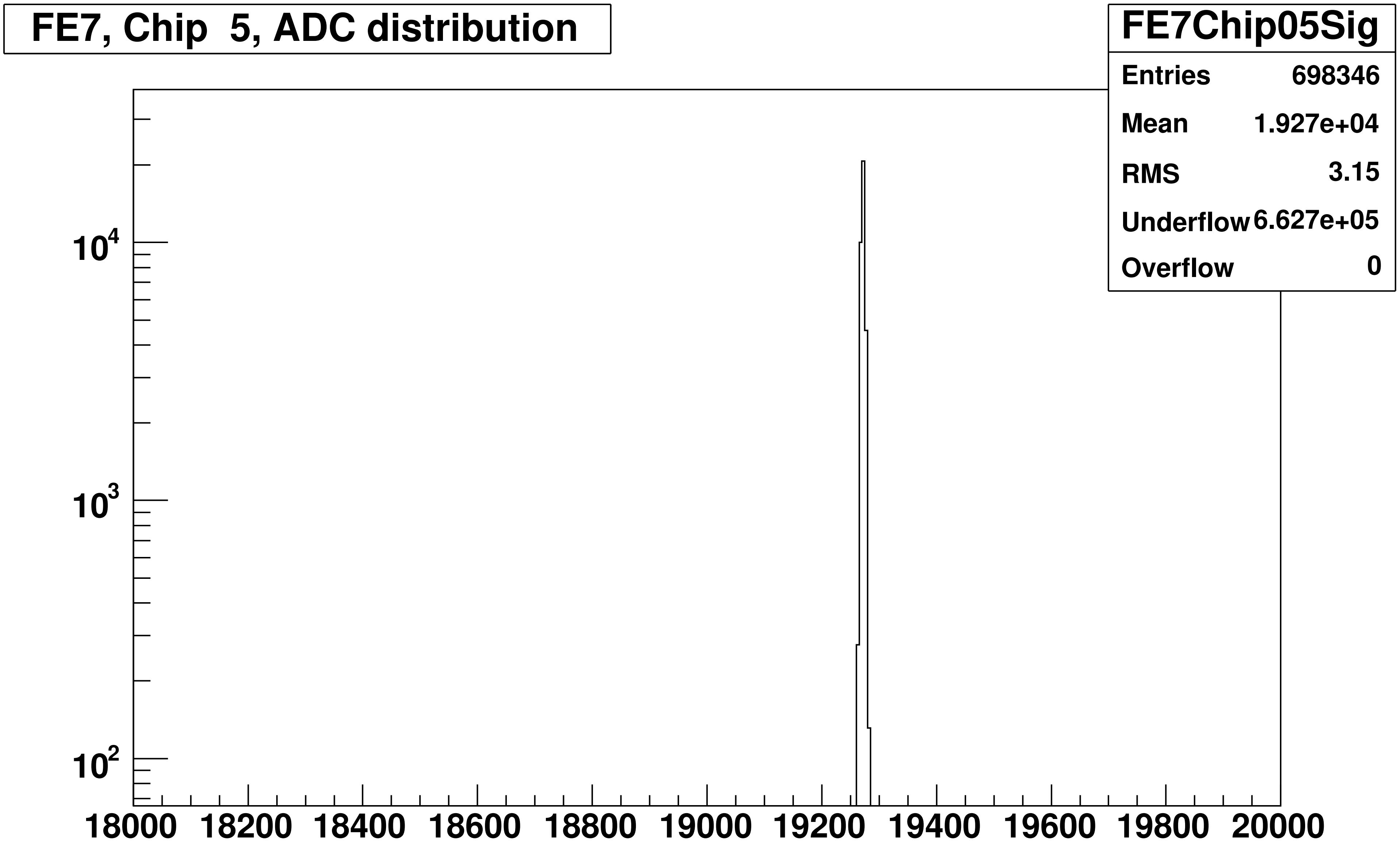

FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 698346
Mean 954.1
RMS 4232
Underflow 0
Overflow 0
10^6
10^5
10^4
-5000
0
5000
10000
15000
20000
25000

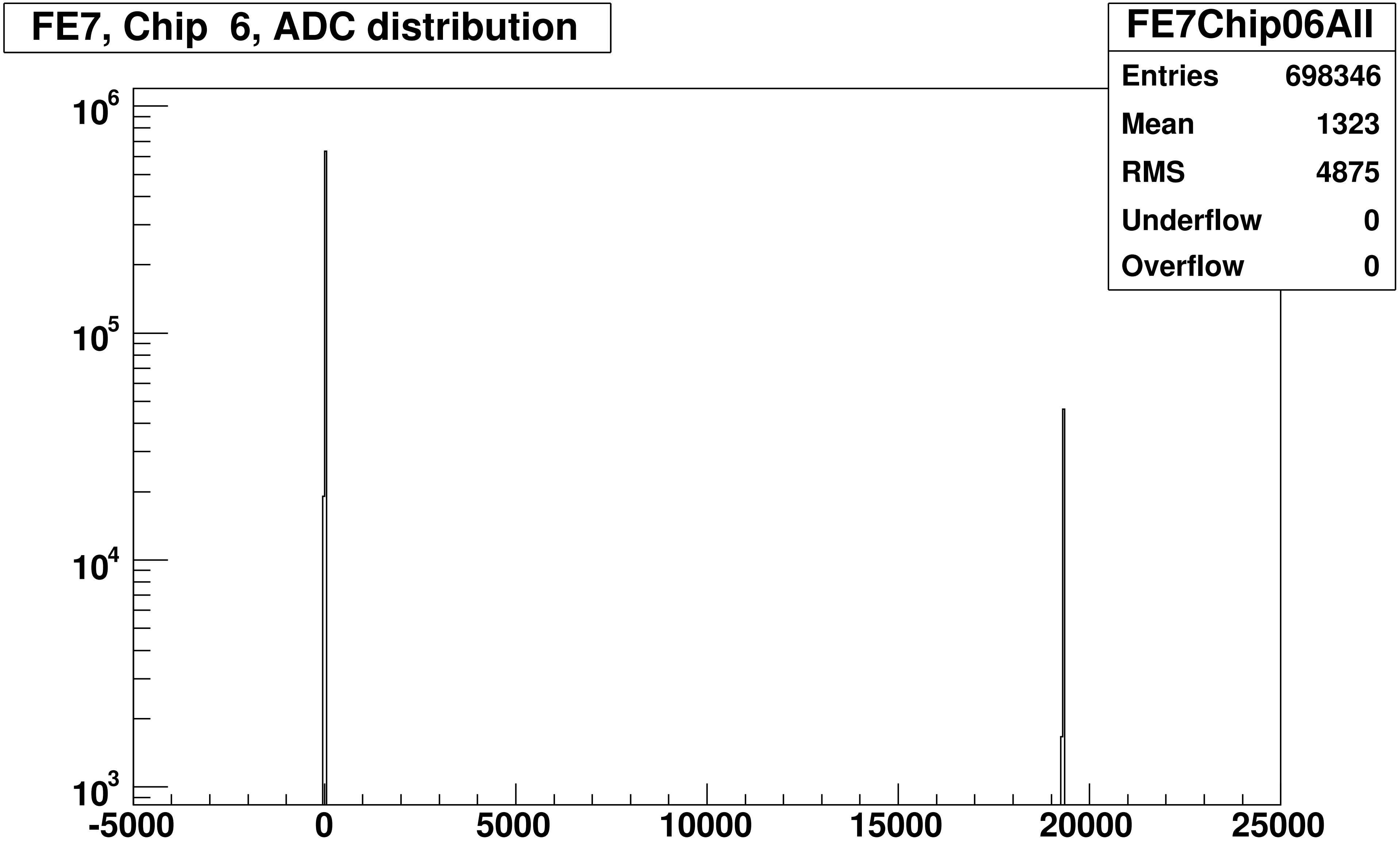

FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 698346
Mean -27.42
RMS 1.332
Underflow 0
Overflow 3.566e+04
10^5
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200

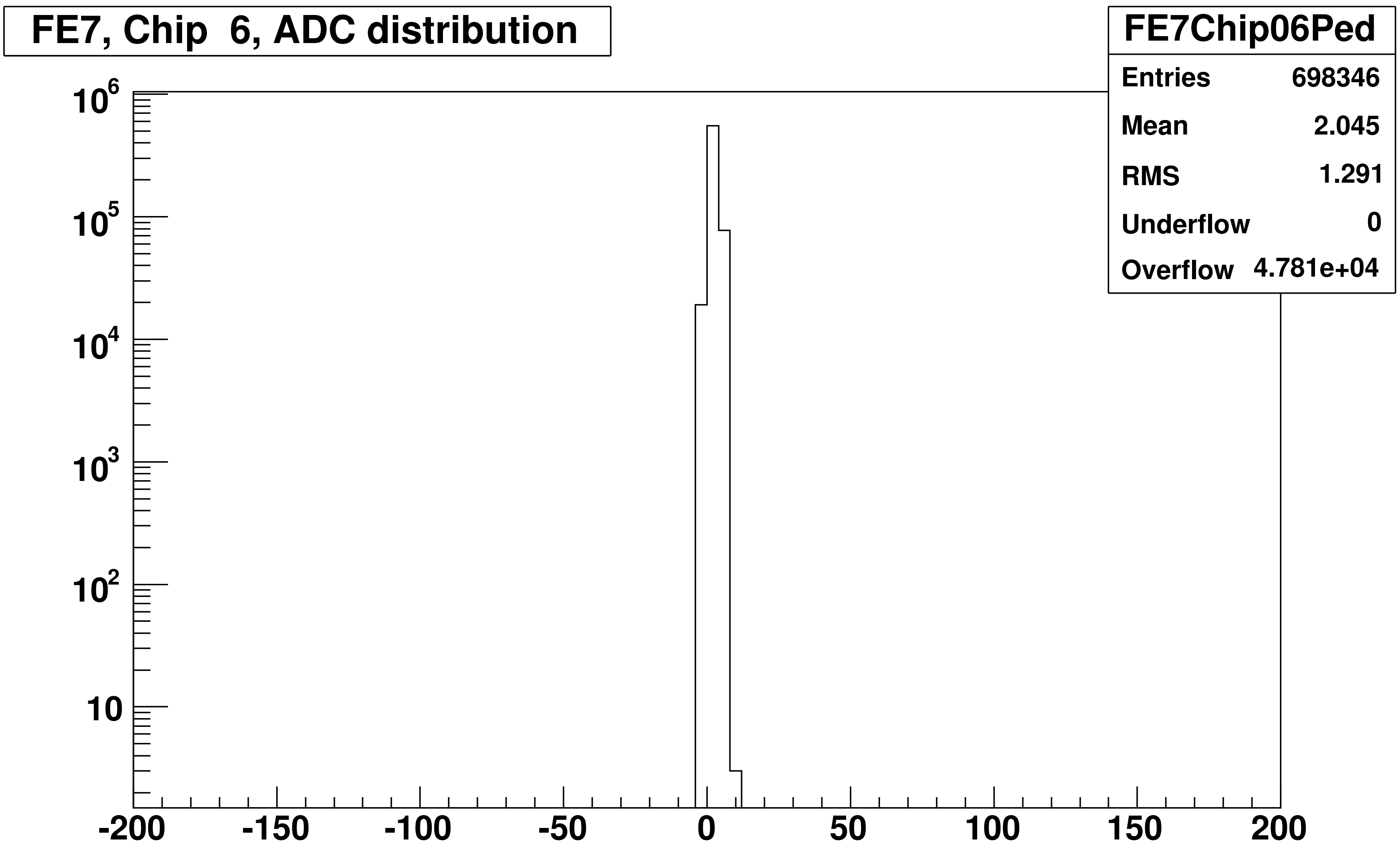

FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 698346
Mean 1.92e+04
RMS 2.949
Underflow 6.627e+05
Overflow 0
10^4
10^3
10^2
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

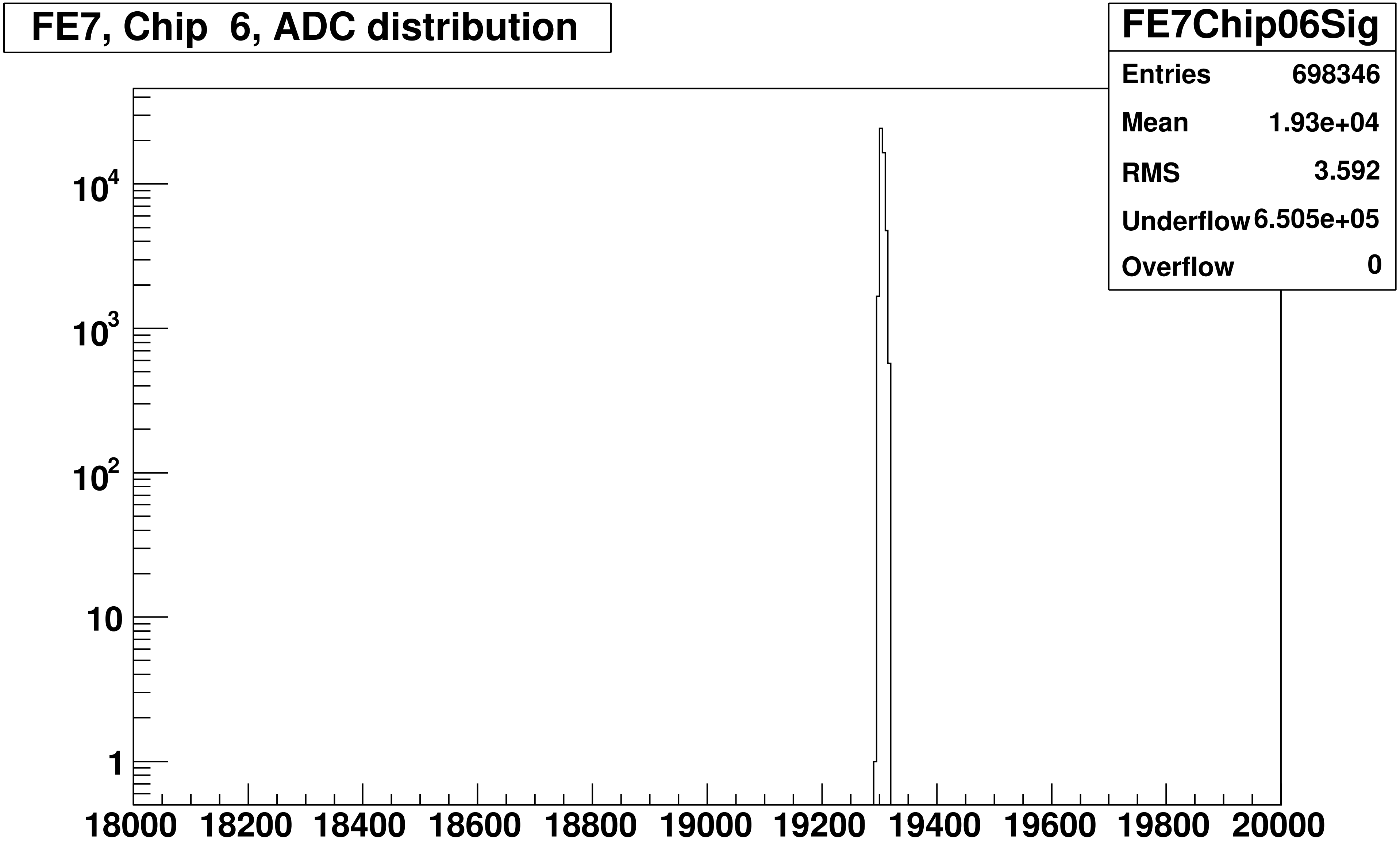

FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 698346
Mean 986.8
RMS 4241
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 698346
Mean 2.989
RMS 1.381
Underflow 0
Overflow 3.566e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

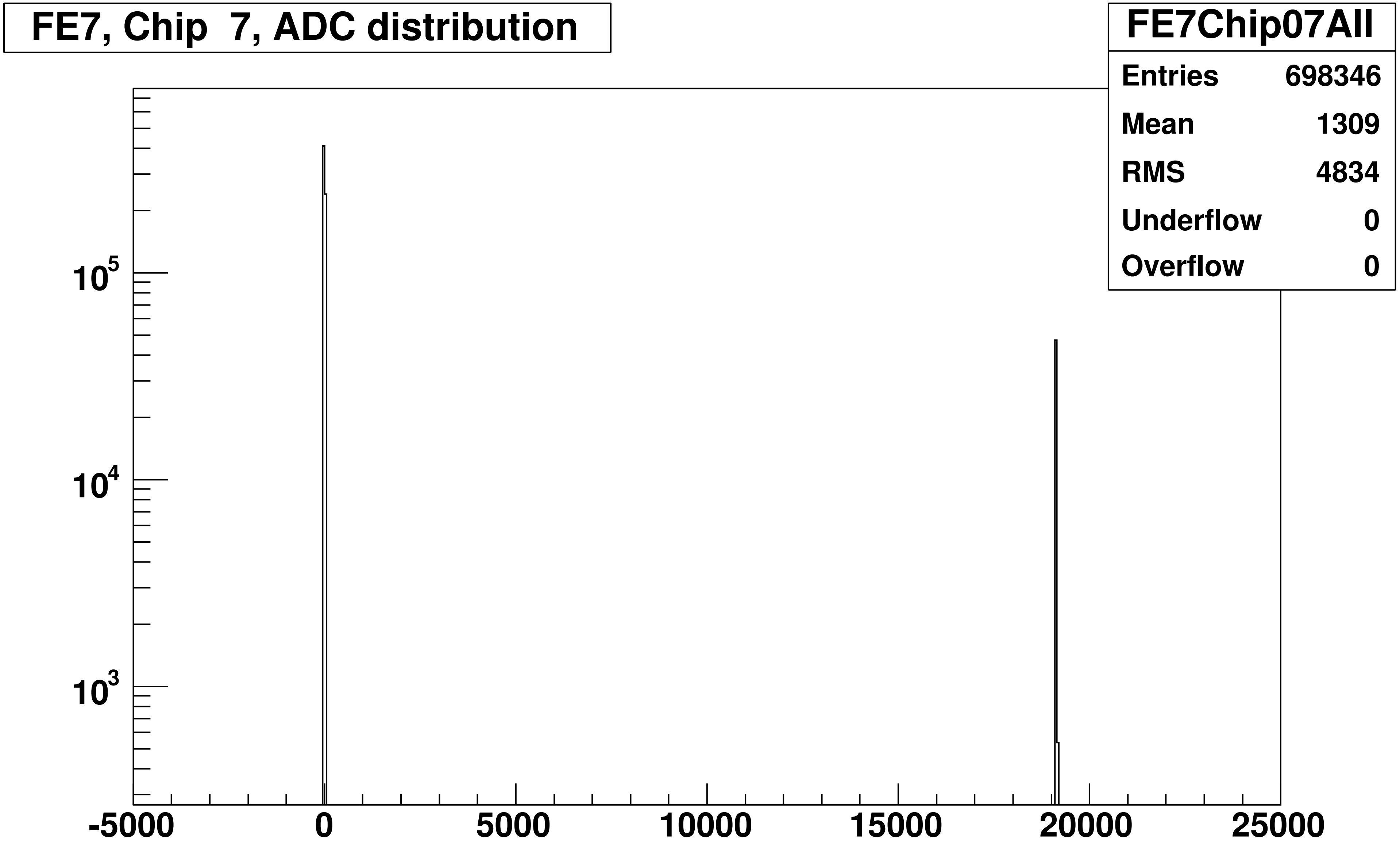

FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 698346
Mean 1.927e+04
RMS 3.15
Underflow 6.627e+05
Overflow 0
10^4
10^3
10^2
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 698346
Mean 1323
RMS 4875
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

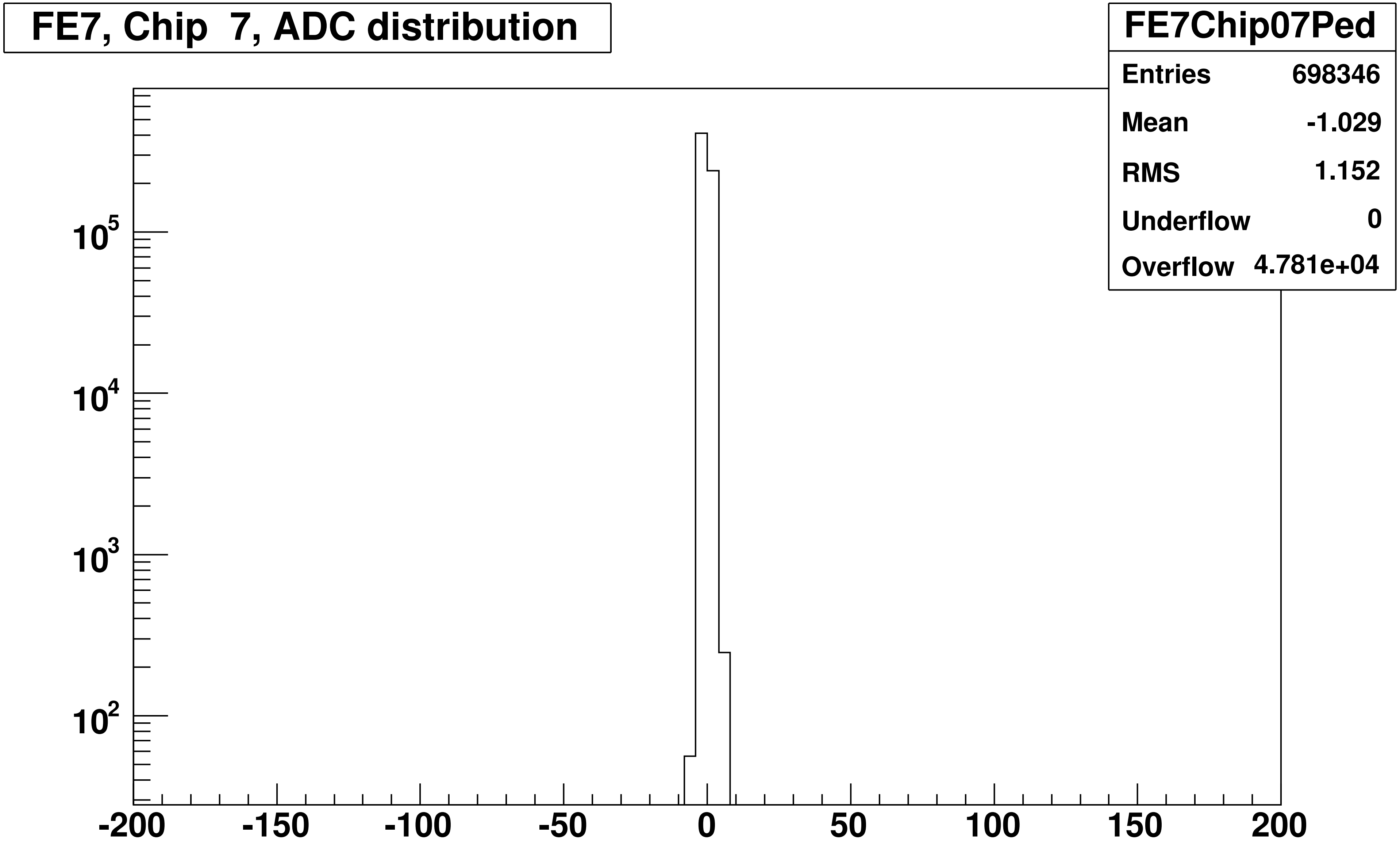

FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 698346
Mean 2.045
RMS 1.291
Underflow 0
Overflow 4.781e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



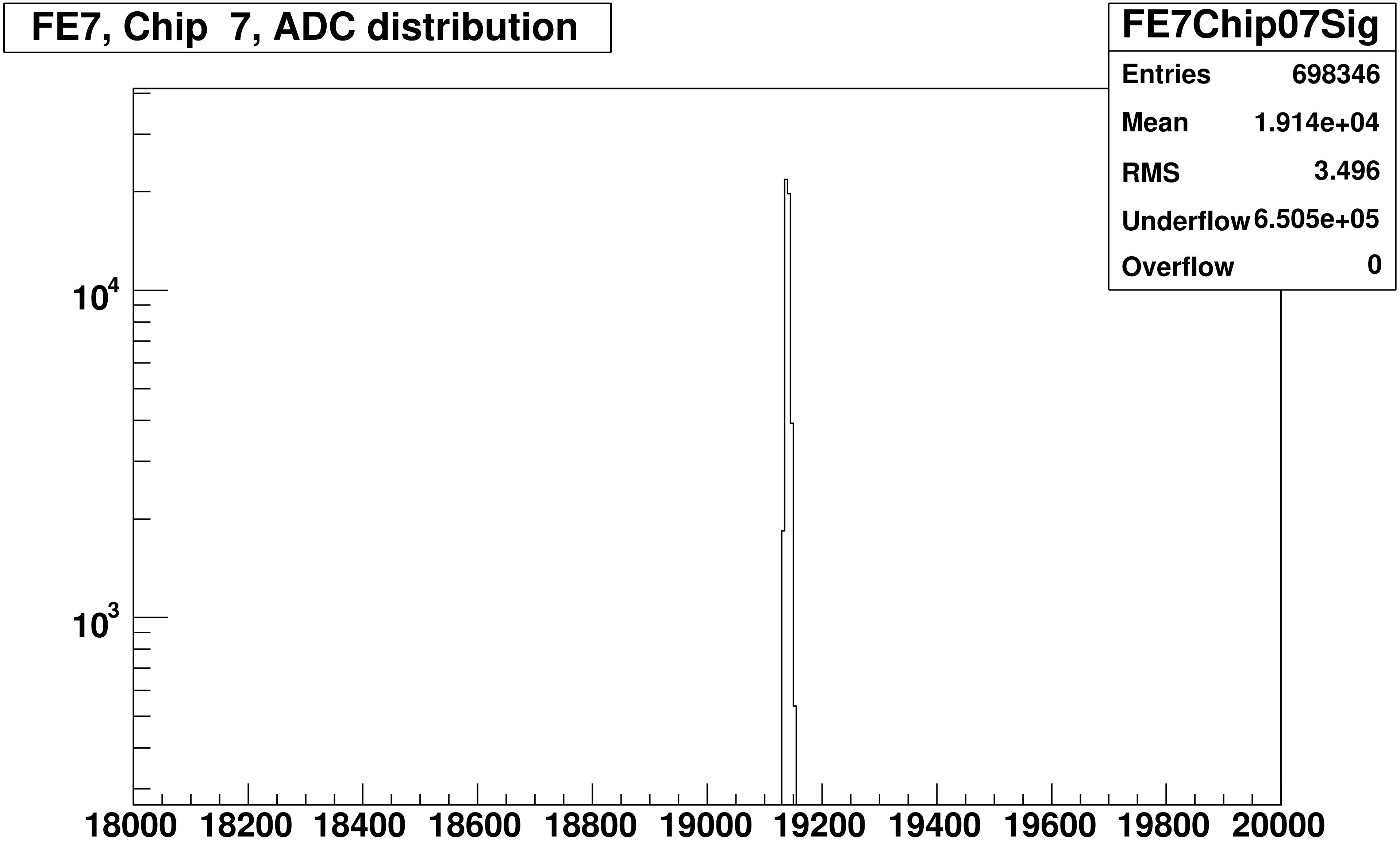
FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 698346
Mean 1.93e+04
RMS 3.592
Underflow 6.505e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

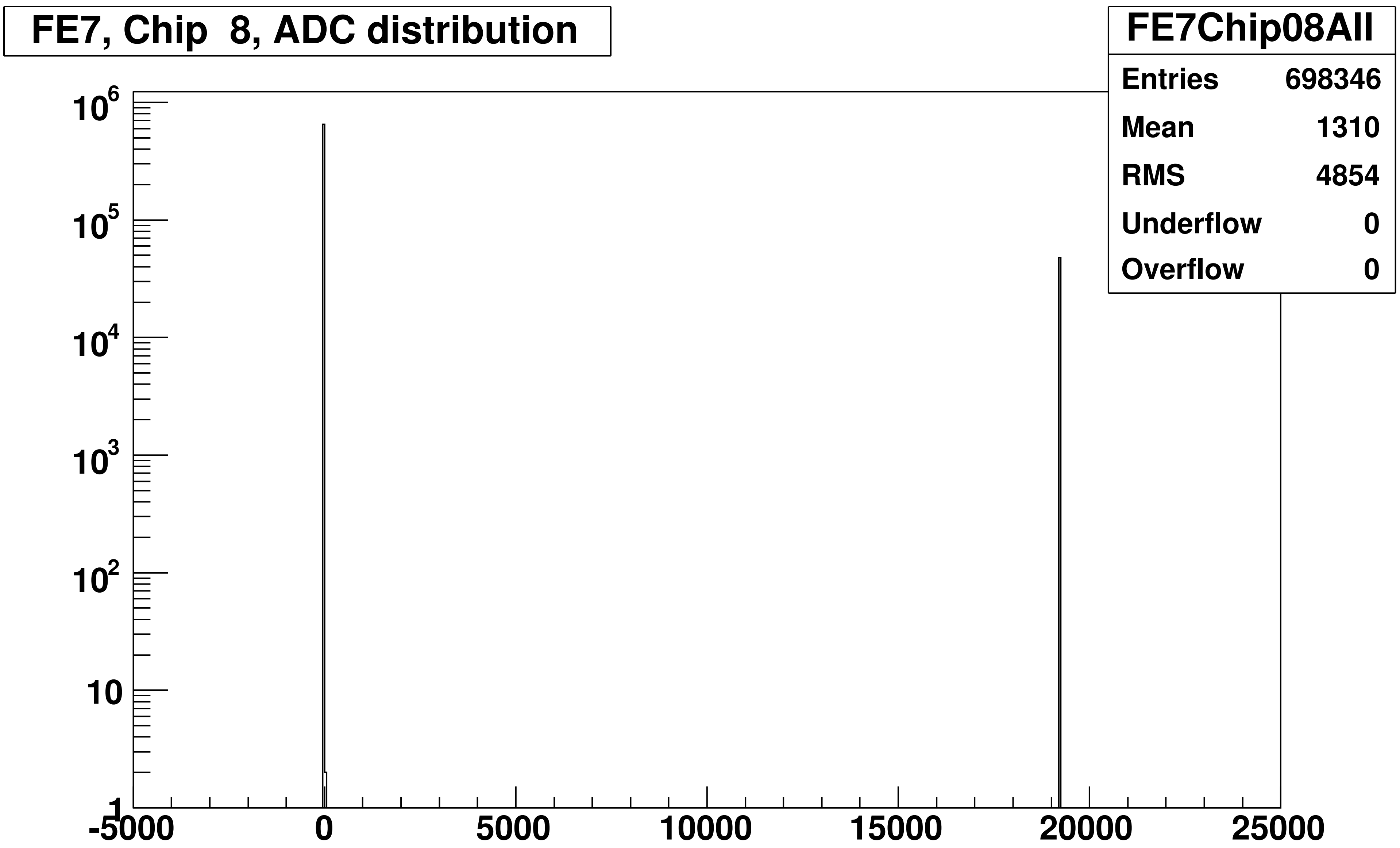


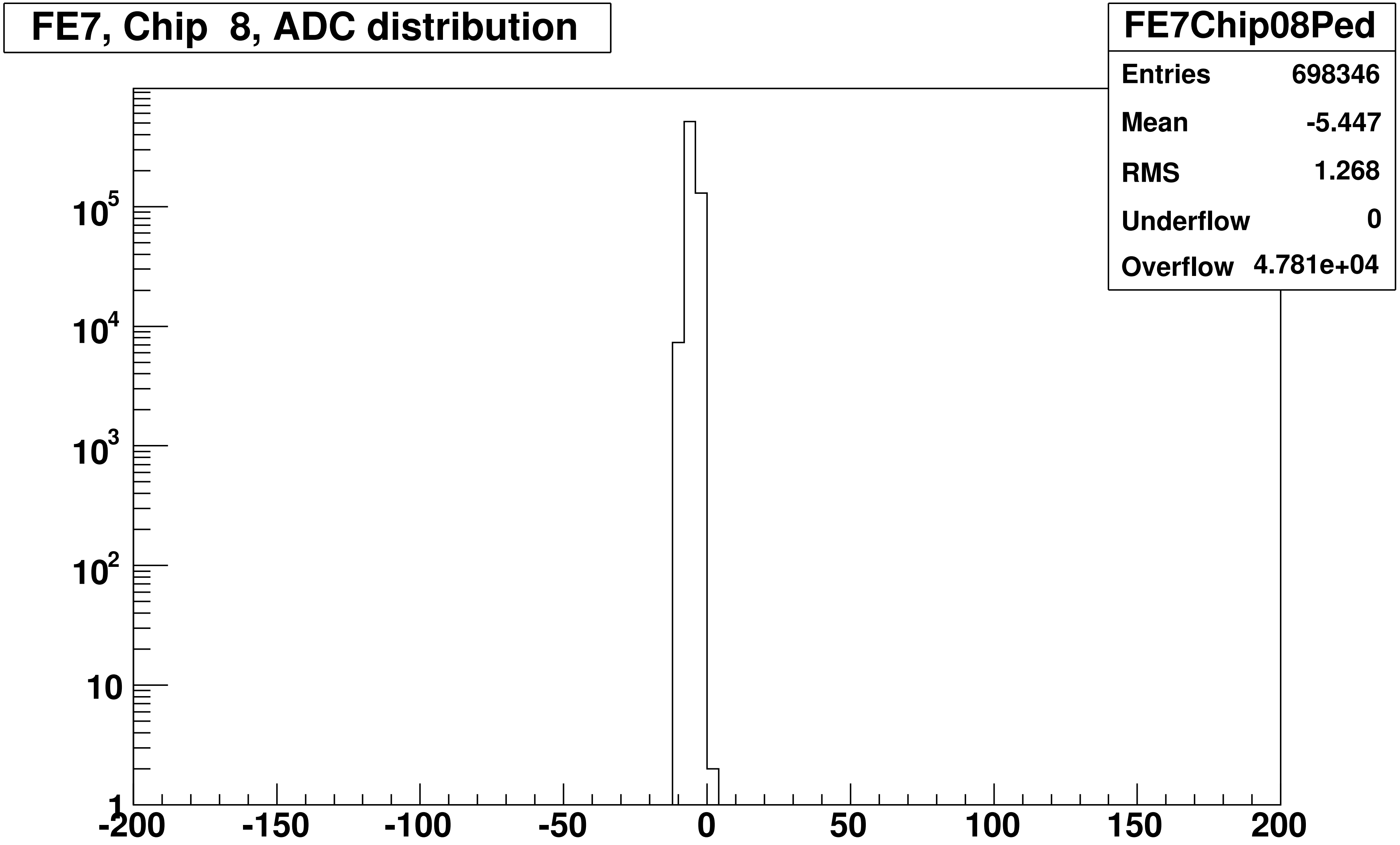
FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 698346
Mean 1309
RMS 4834
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



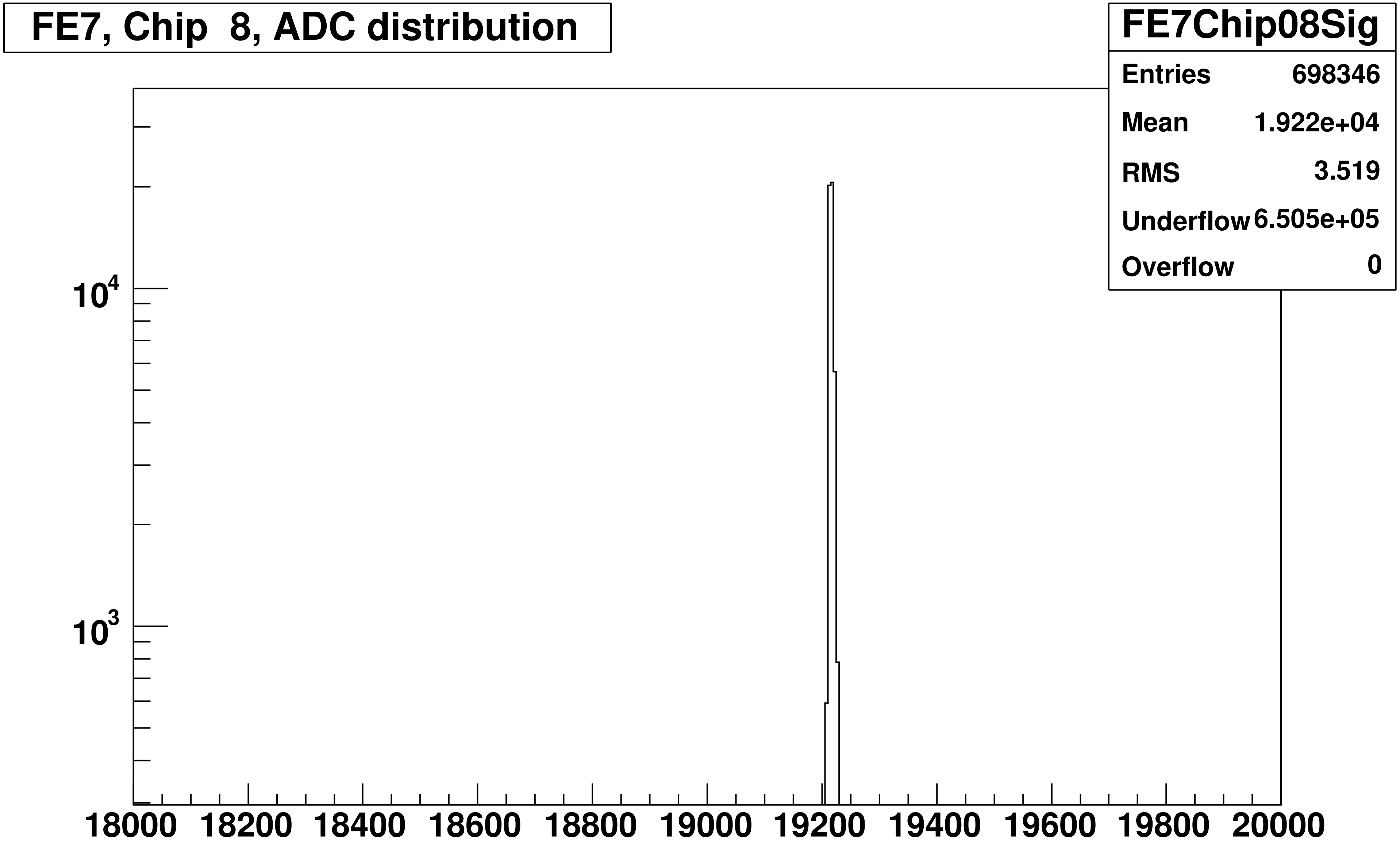
FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 698346
Mean -1.029
RMS 1.152
Underflow 0
Overflow 4.781e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

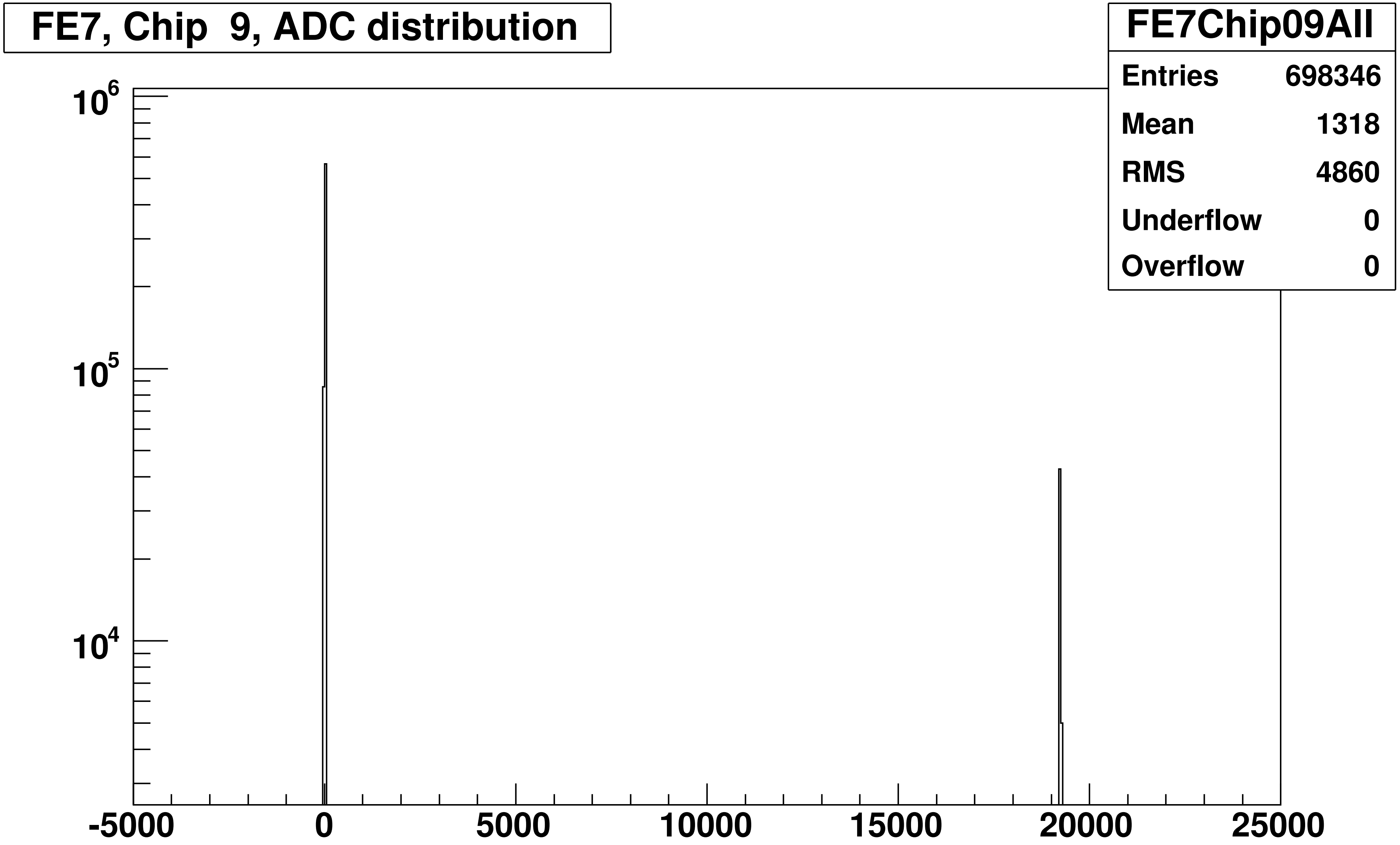

FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 698346
Mean 1.914e+04
RMS 3.496
Underflow 6.505e+05
Overflow 0
10^4
10^3
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

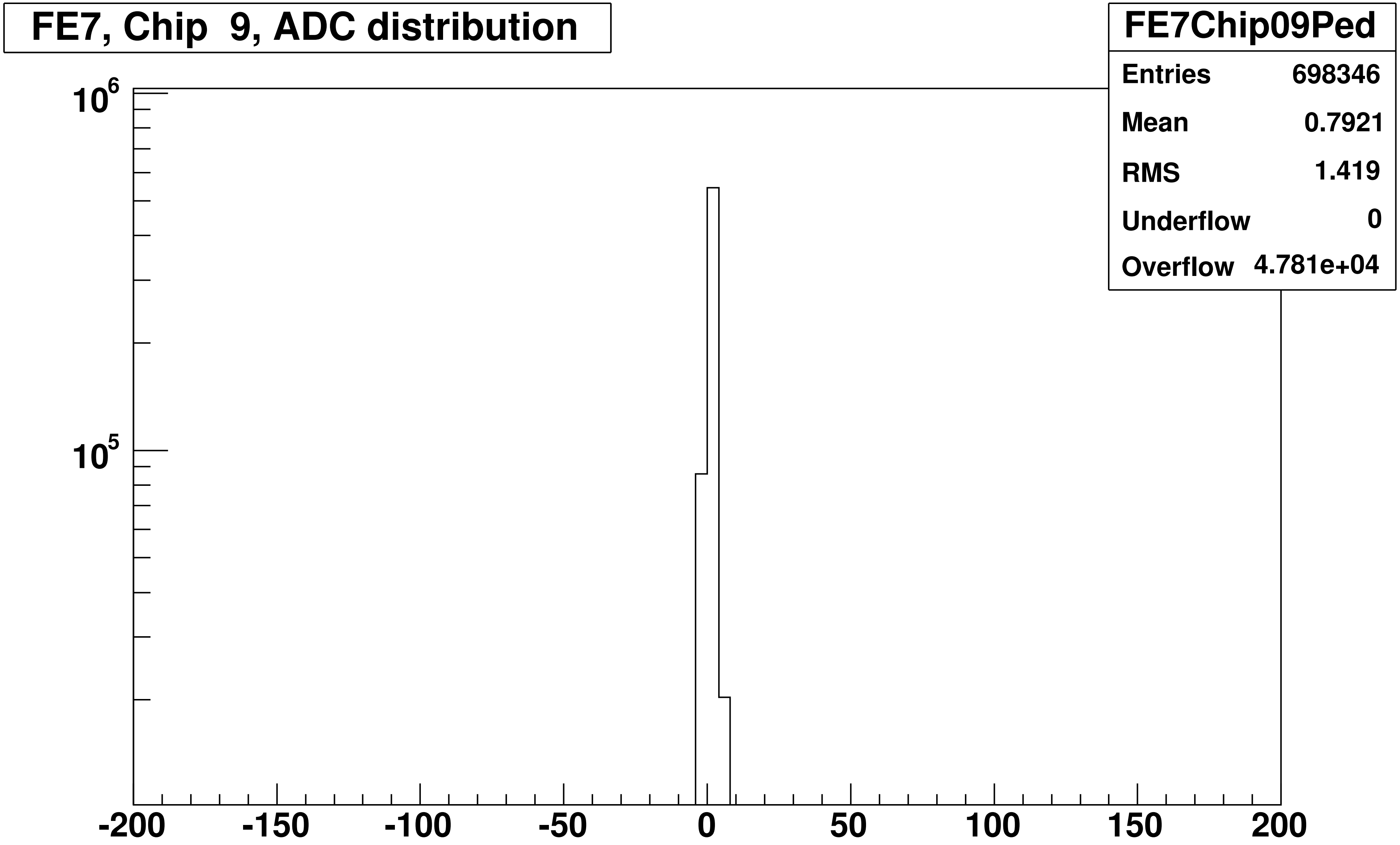

FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 698346
Mean 1310
RMS 4854
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

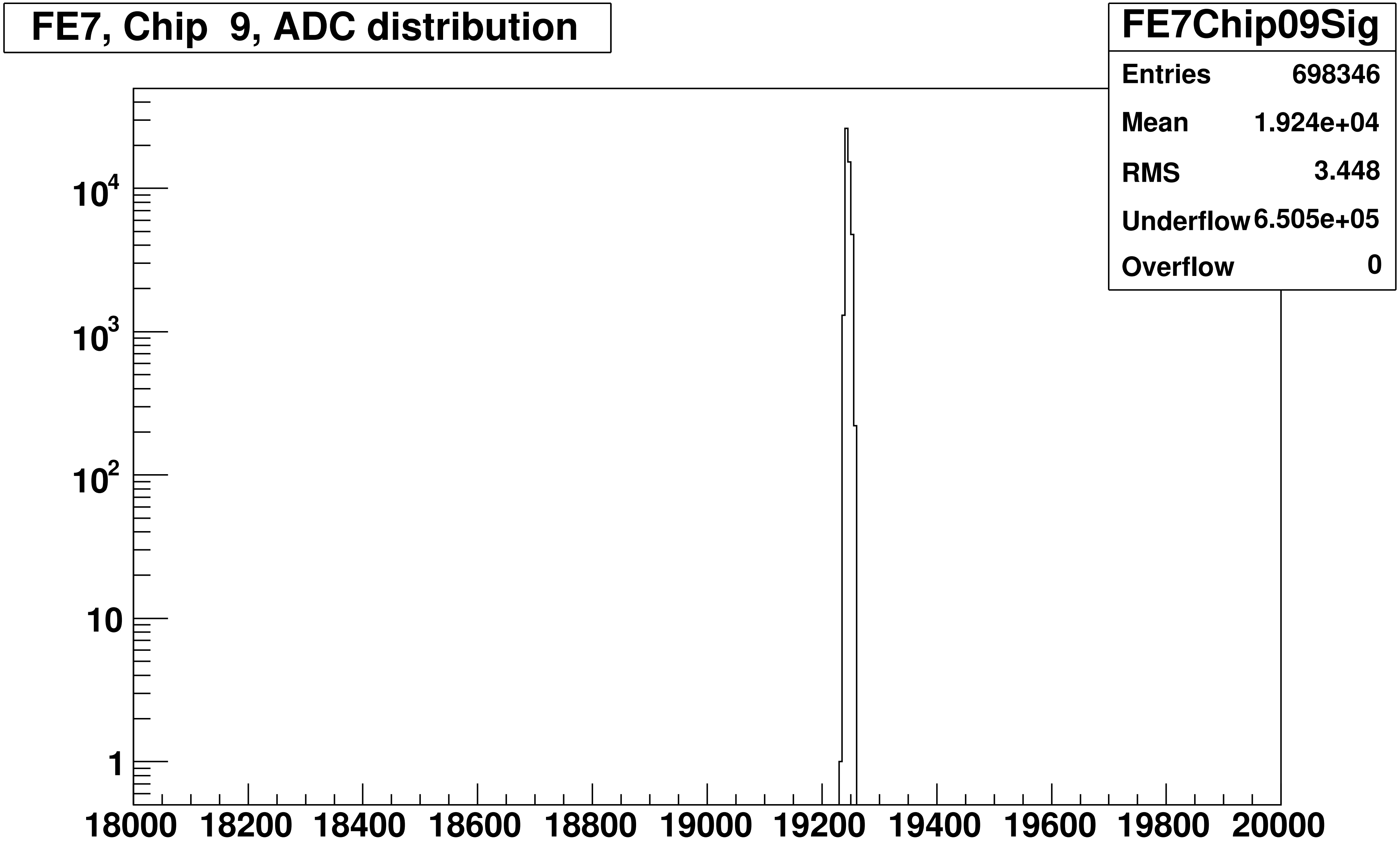


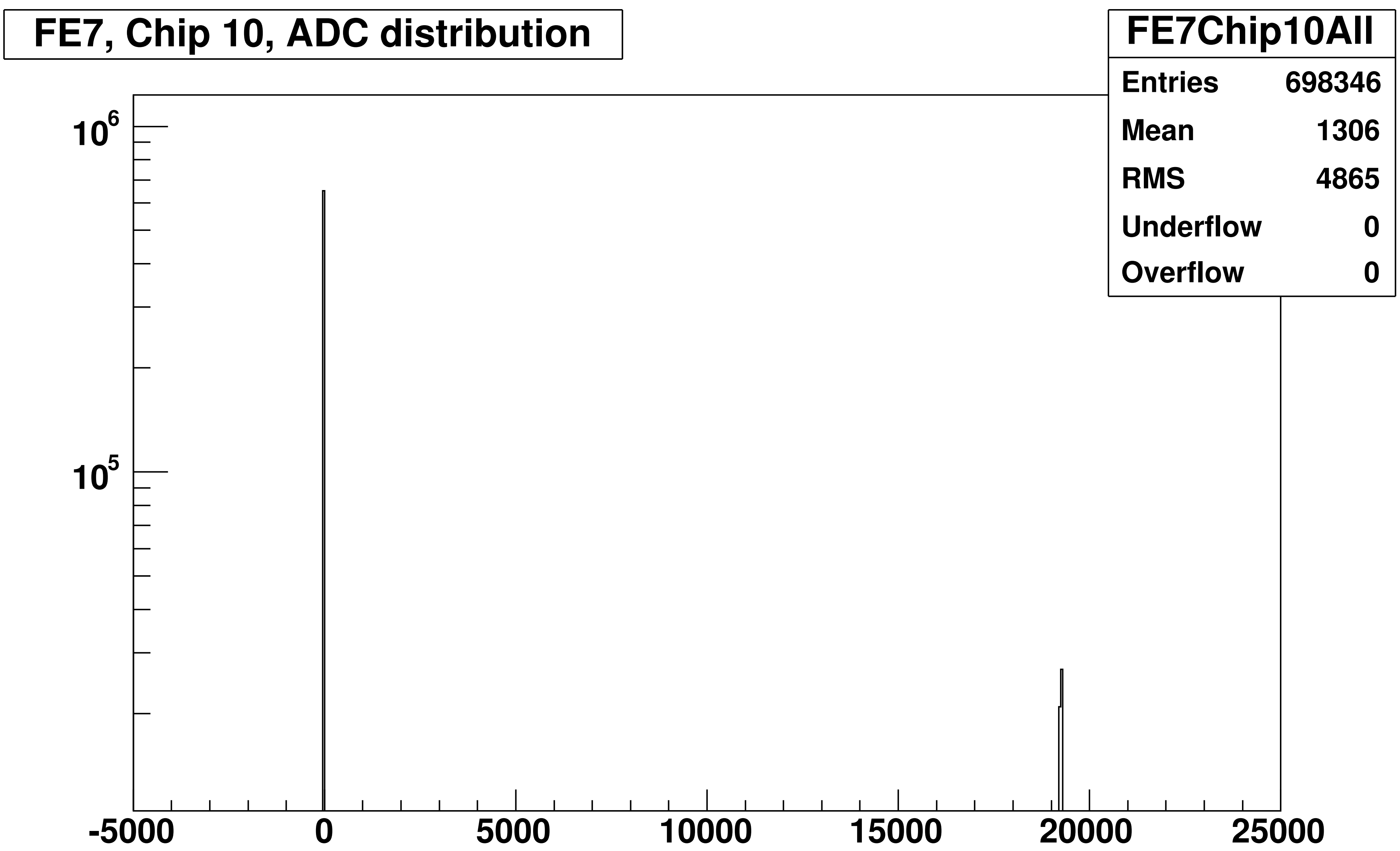
FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 698346
Mean -5.447
RMS 1.268
Underflow 0
Overflow 4.781e+04
1
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200

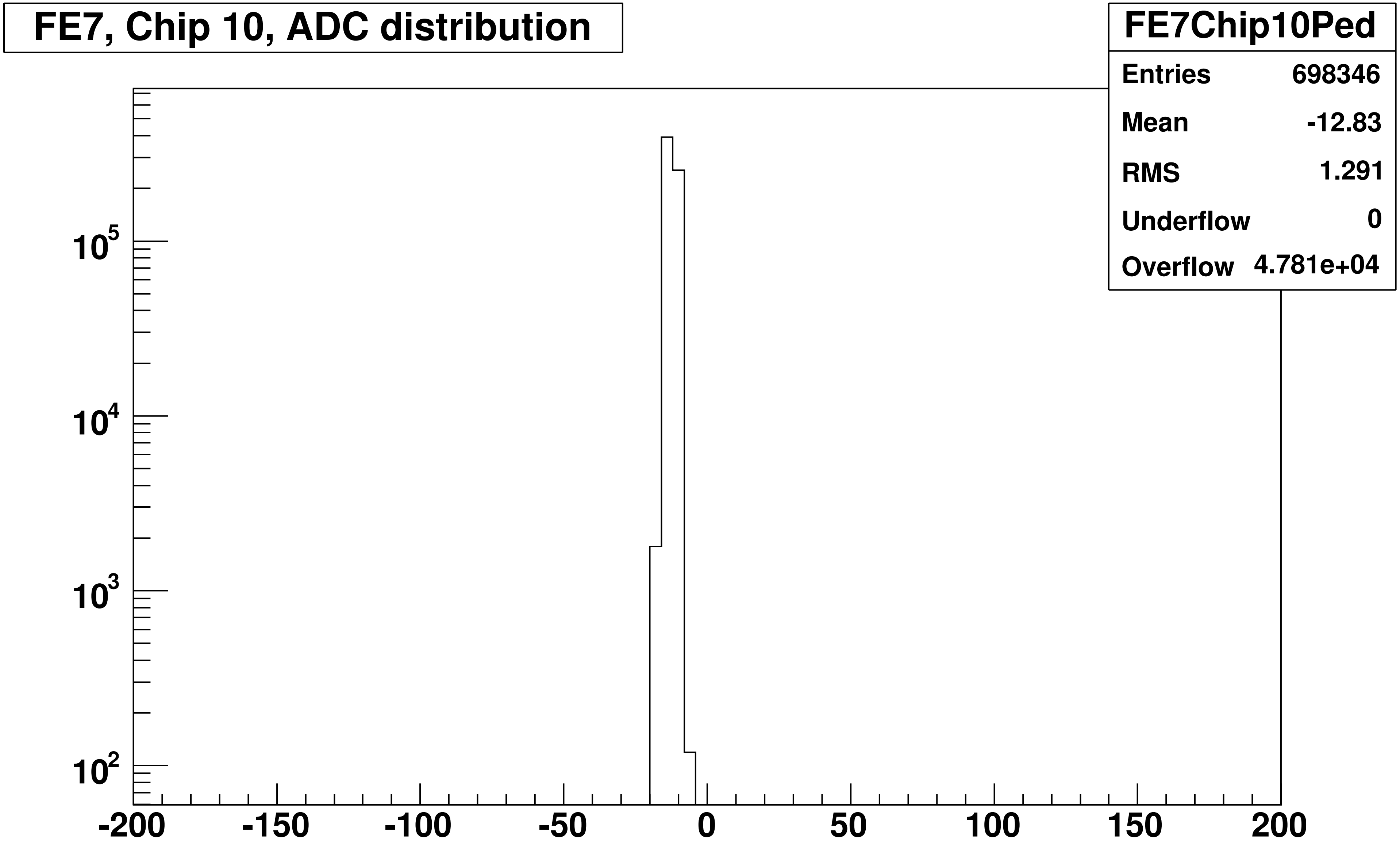

FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 698346
Mean 1.922e+04
RMS 3.519
Underflow 6.505e+05
Overflow 0
10^4
10^3
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 698346
Mean 1318
RMS 4860
Underflow 0
Overflow 0
10^6
10^5
10^4
-5000
0
5000
10000
15000
20000
25000

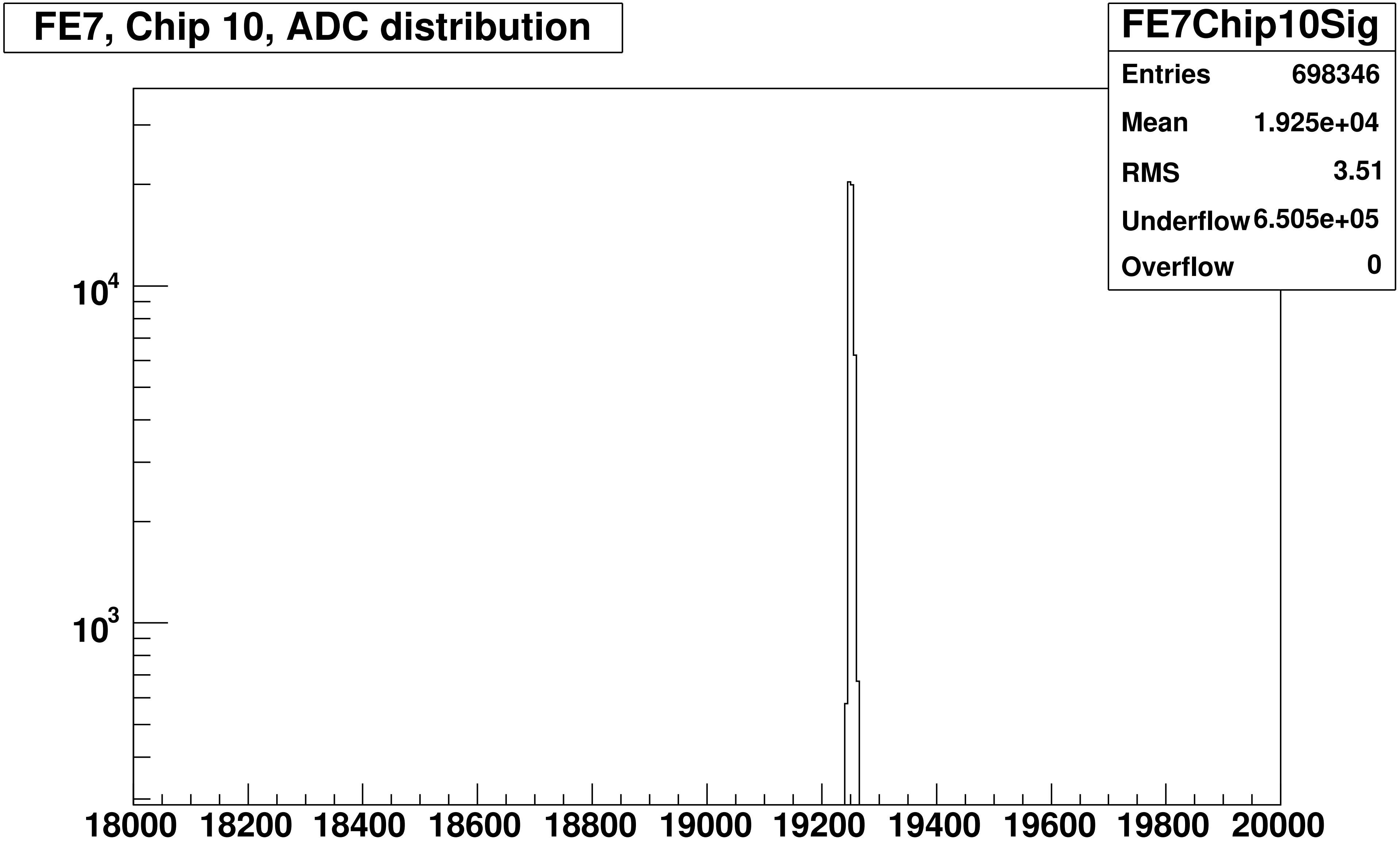

FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 698346
Mean 0.7921
RMS 1.419
Underflow 0
Overflow 4.781e+04
10^6
10^5
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 698346
Mean 1.924e+04
RMS 3.448
Underflow 6.505e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

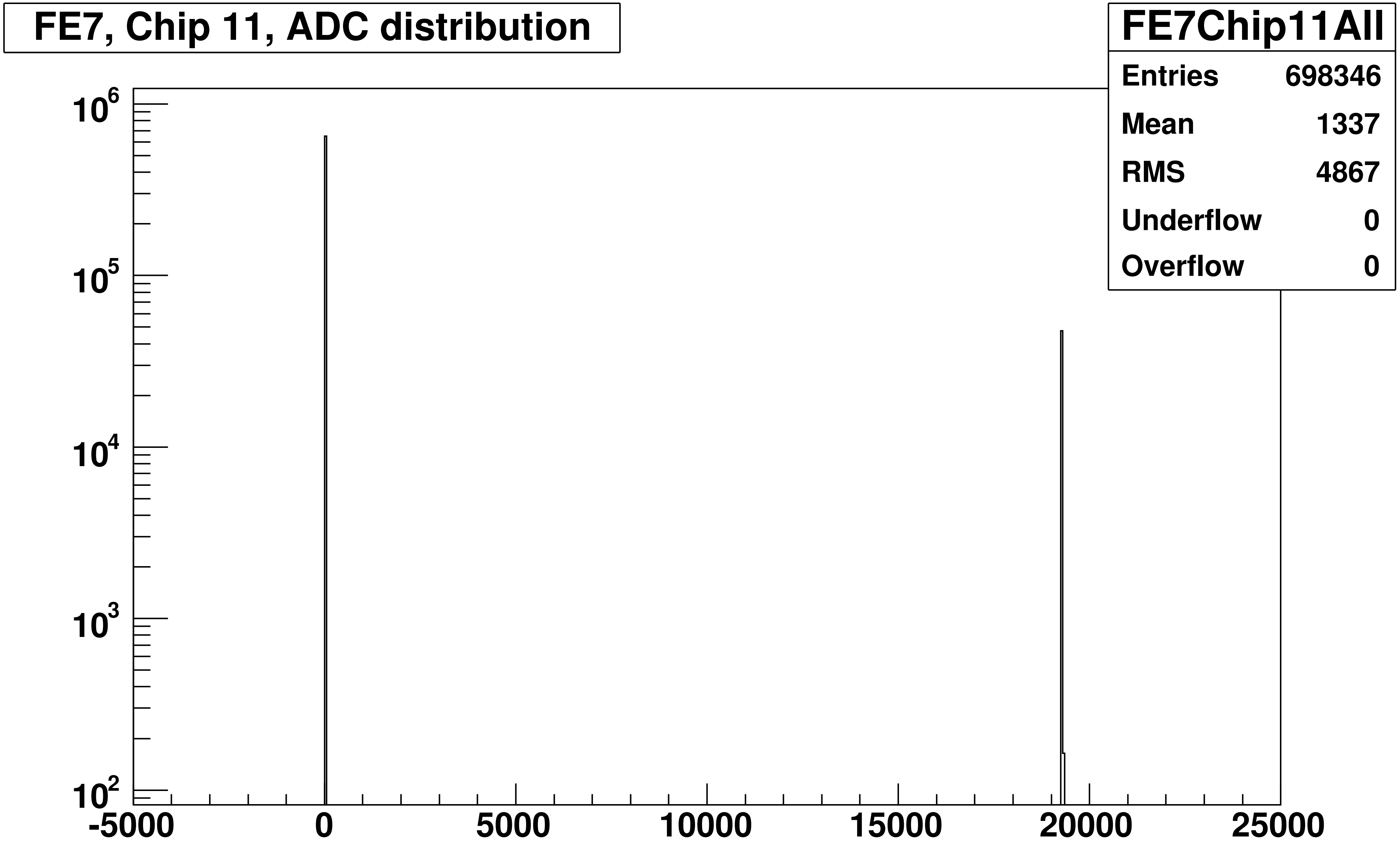

FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 698346
Mean 1306
RMS 4865
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000

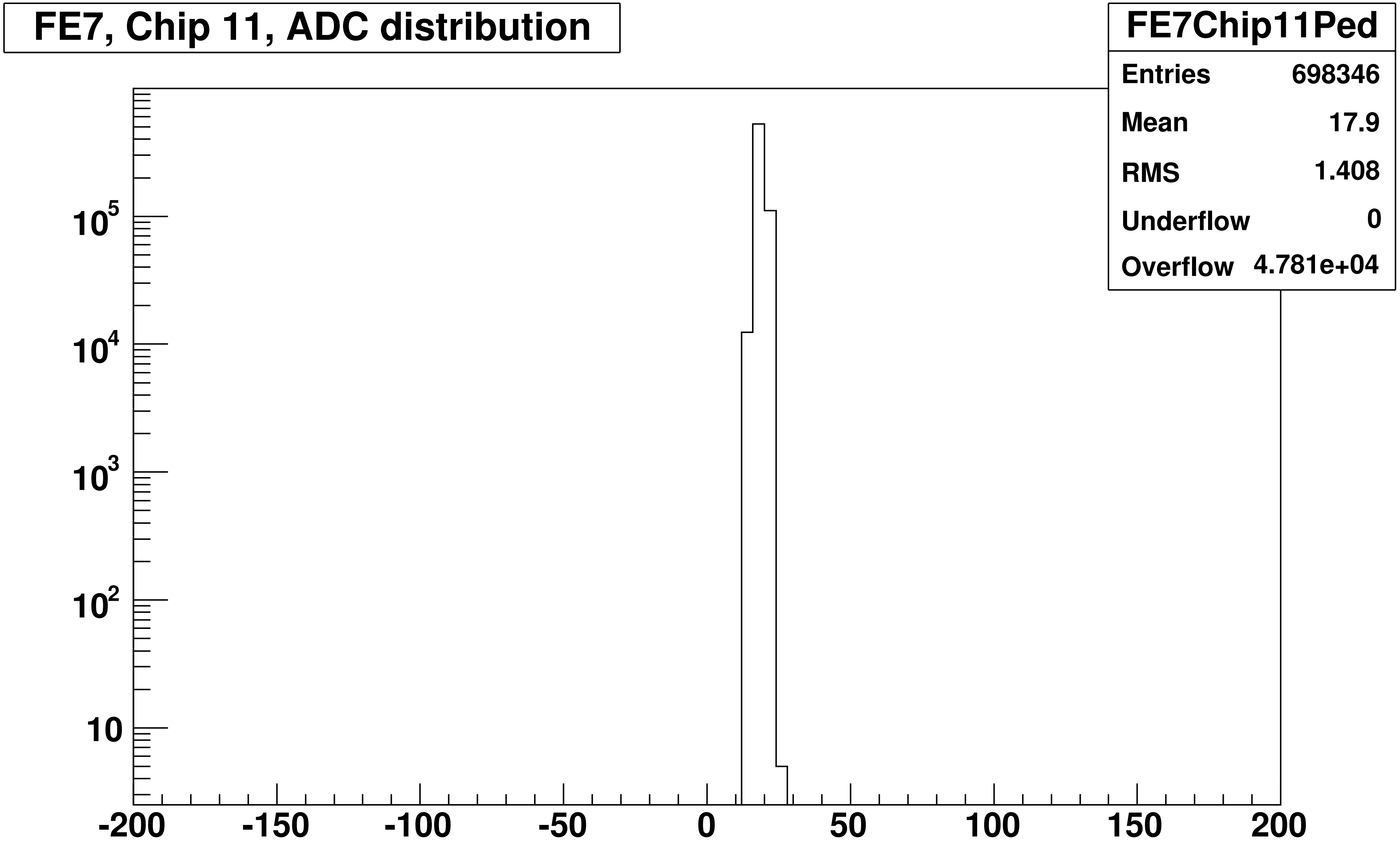


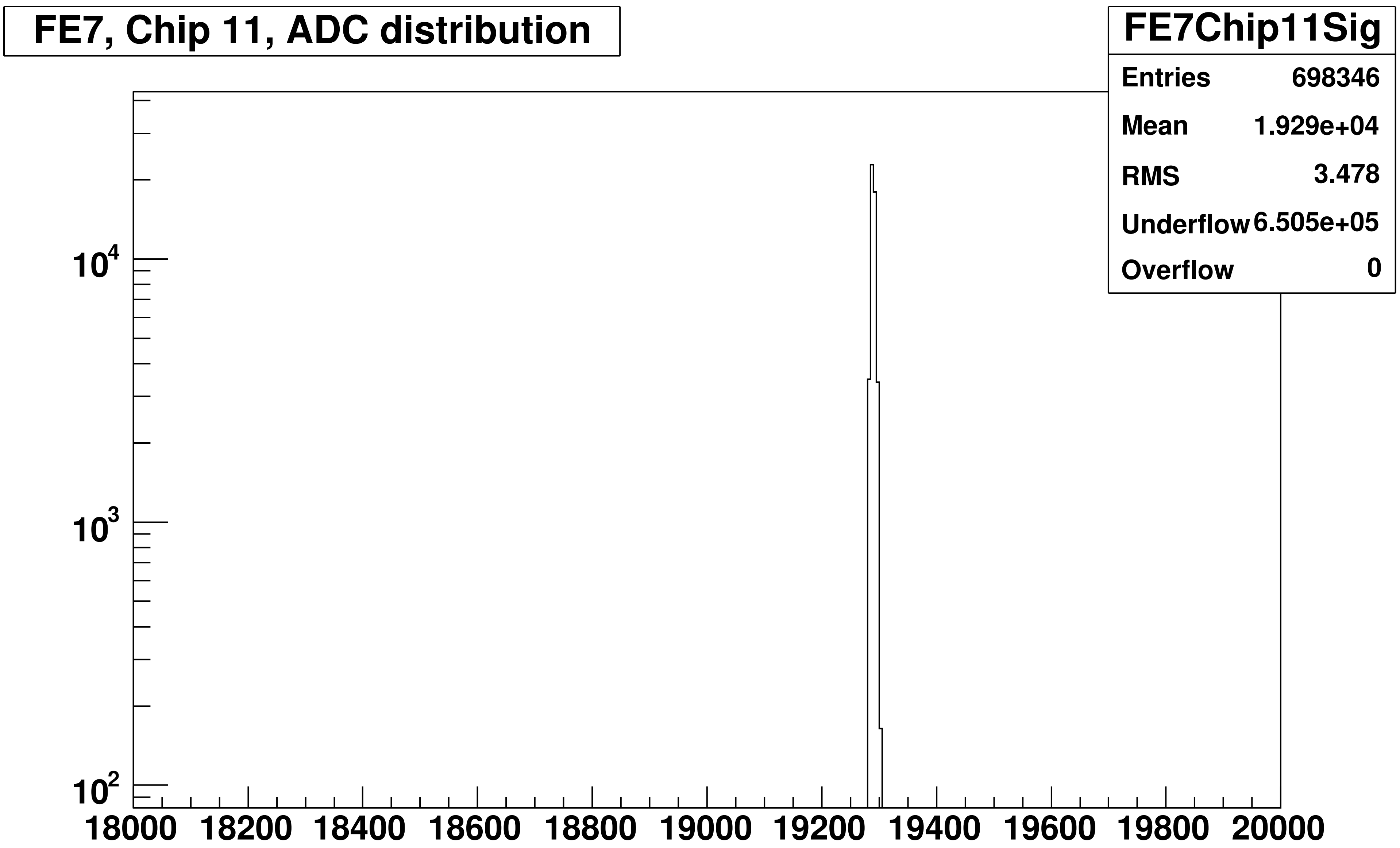
FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 698346
Mean -12.83
RMS 1.291
Underflow 0
Overflow 4.781e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

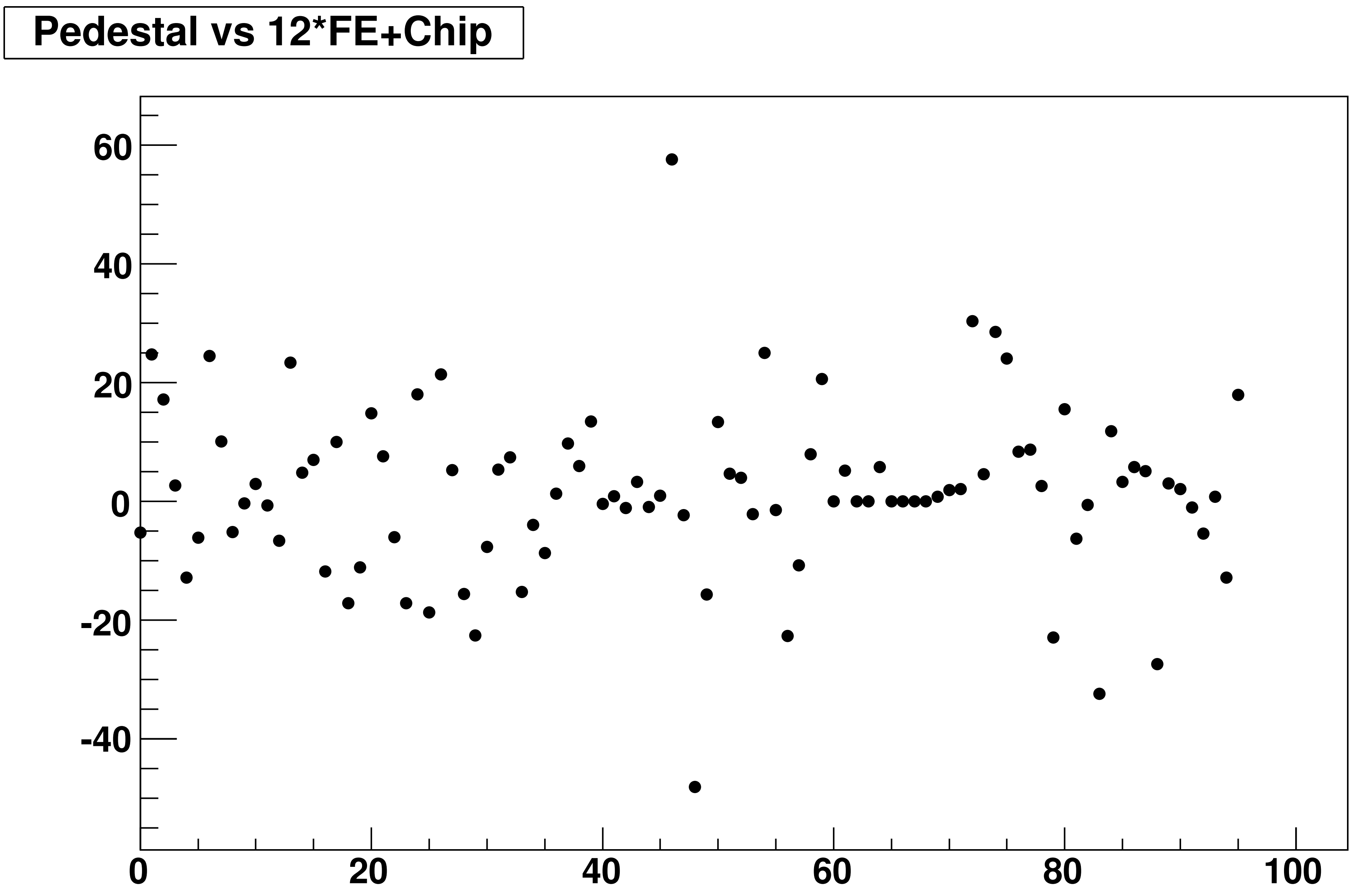


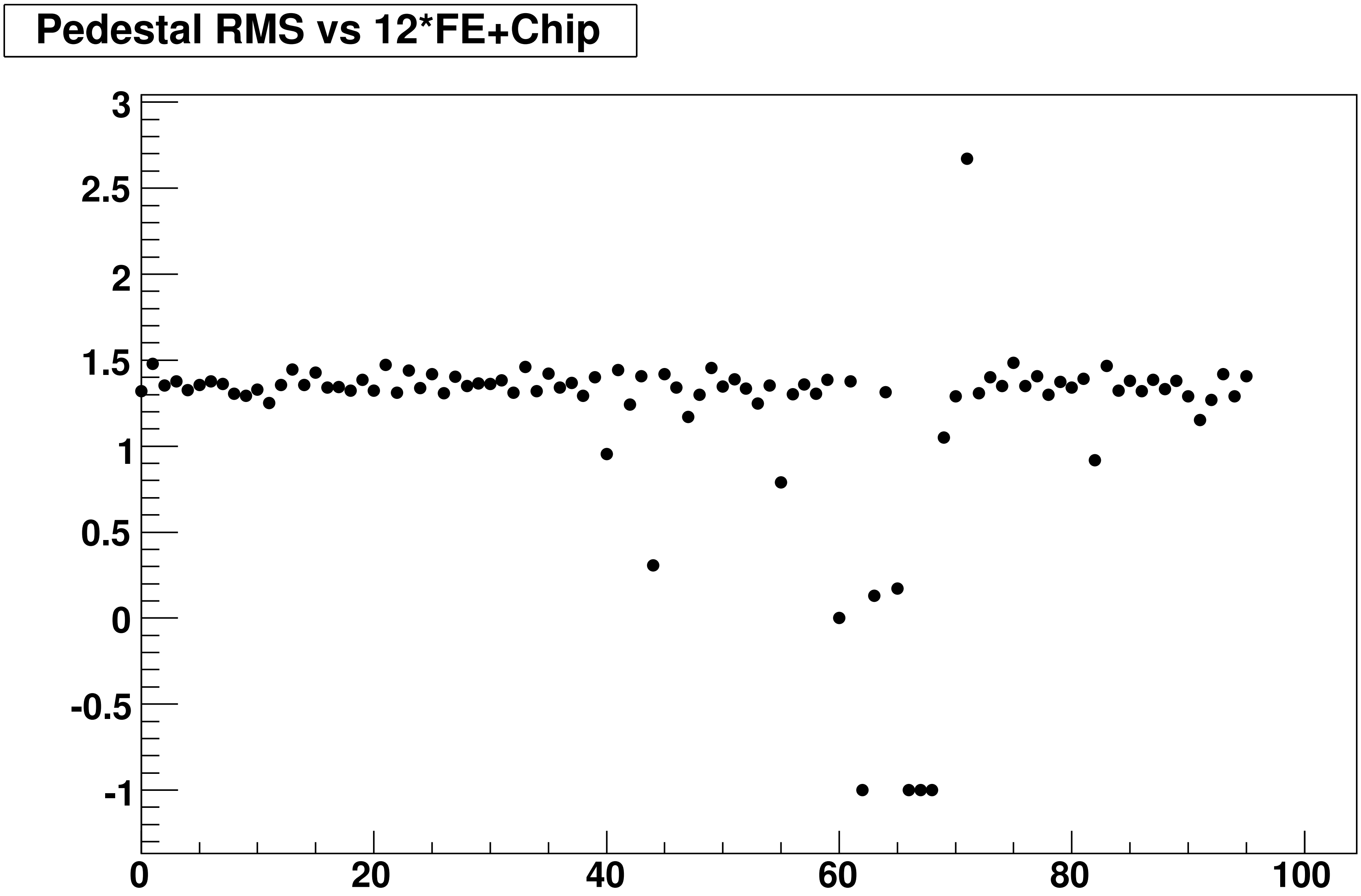
FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 698346
Mean 1.925e+04
RMS 3.51
Underflow 6.505e+05
Overflow 0
10^4
10^3
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

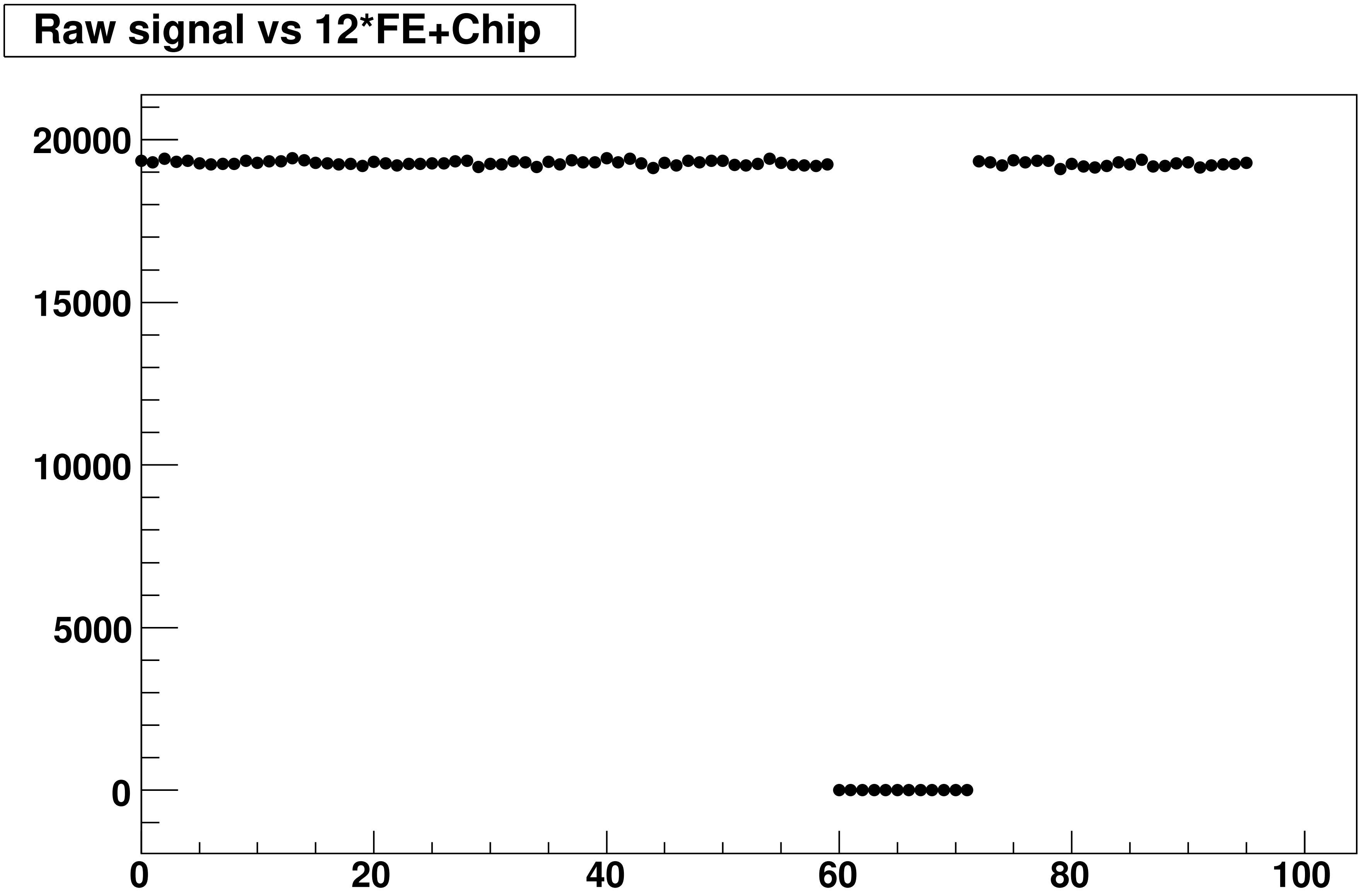

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 698346
Mean 1337
RMS 4867
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

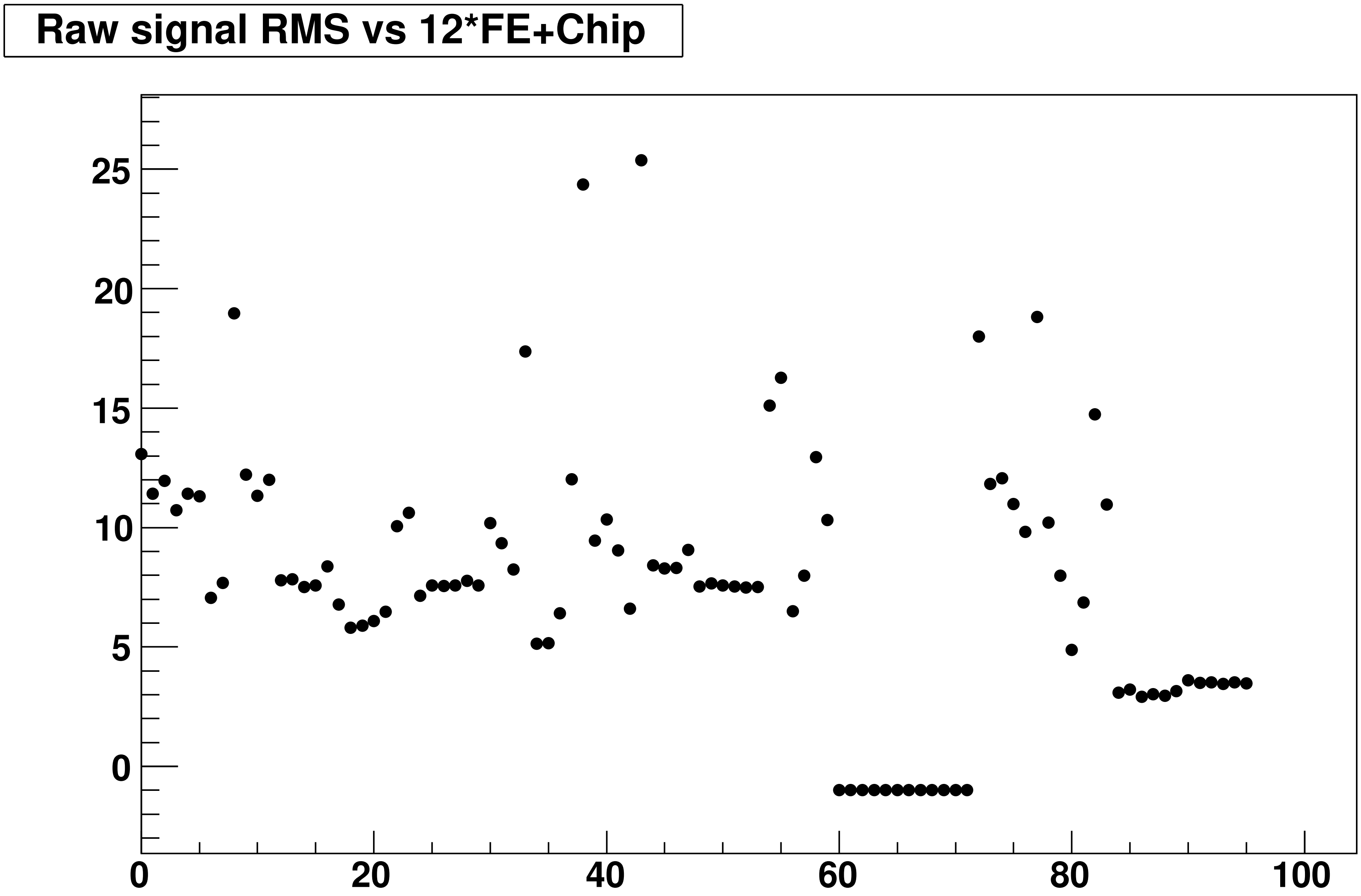

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 698346
Mean 17.9
RMS 1.408
Underflow 0
Overflow 4.781e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 698346
Mean 1.929e+04
RMS 3.478
Underflow 6.505e+05
Overflow 0
10^4
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

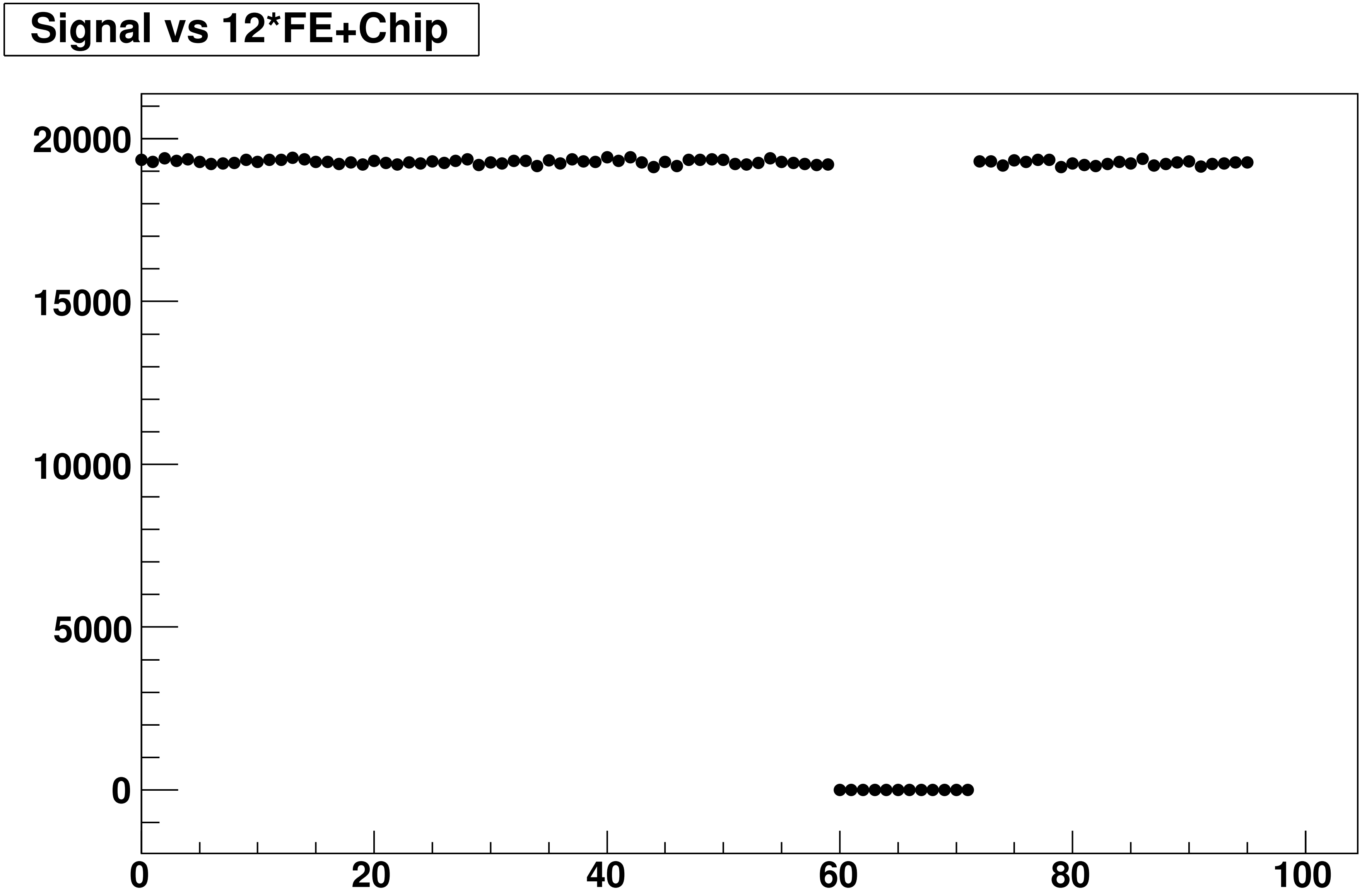

Pedestal vs 12*FE+Chip
60
40
20
0
-20
-40
0
20
40
60
80
100

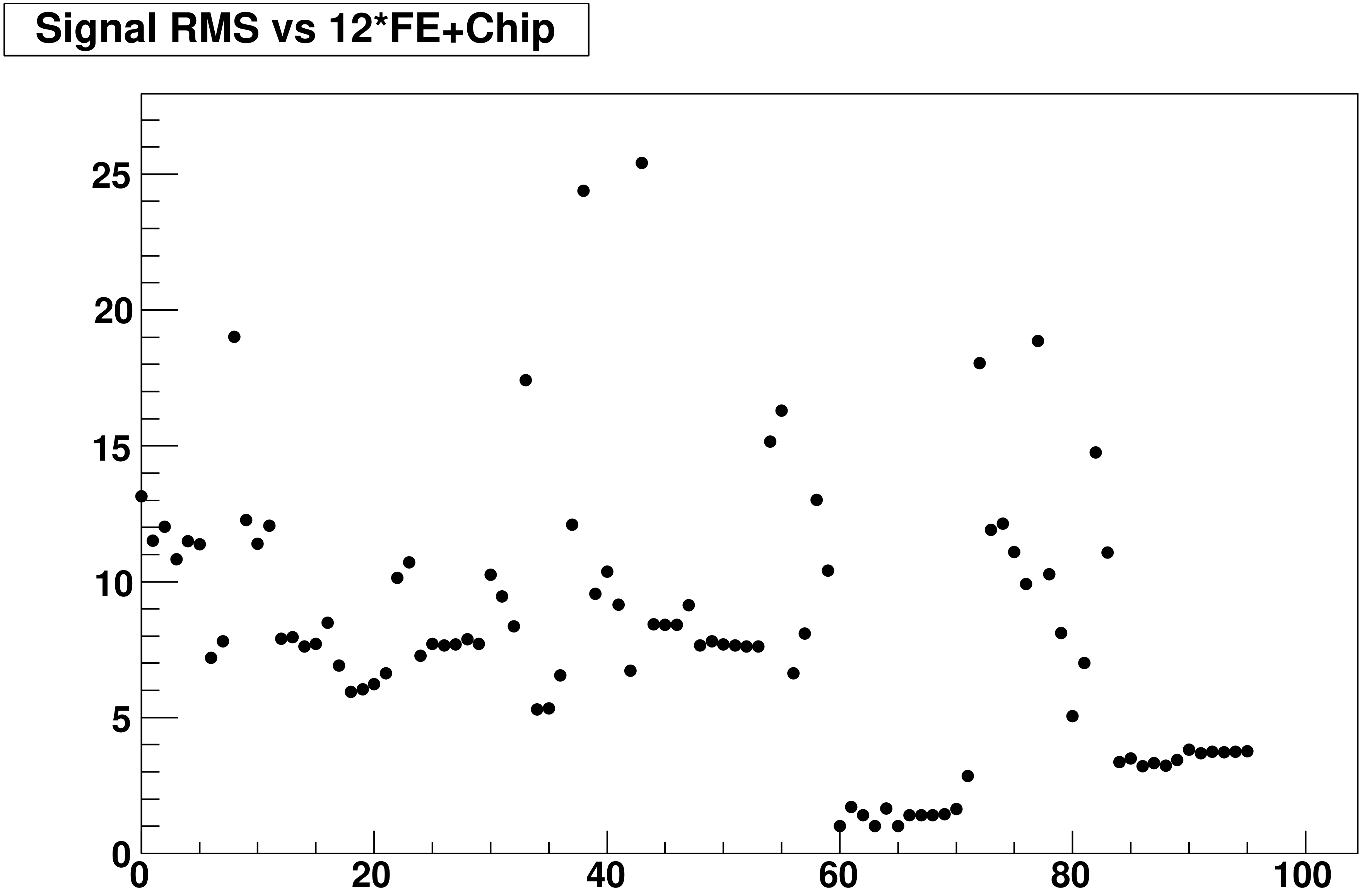

Pedestal RMS vs 12*FE+Chip
3
2.5
2
1.5
1
0.5
0
-0.5
-1
0
20
40
60
80
100


Raw signal vs 12*FE+Chip
20000
15000
10000
5000
0
0
20
40
60
80
100



Raw signal RMS vs 12*FE+Chip
0
5
10
15
20
25
0
20
40
60
80
100


Signal vs 12*FE+Chip
20000
15000
10000
5000
0
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
25
20
15
10
5
0
0
20
40
60
80
100