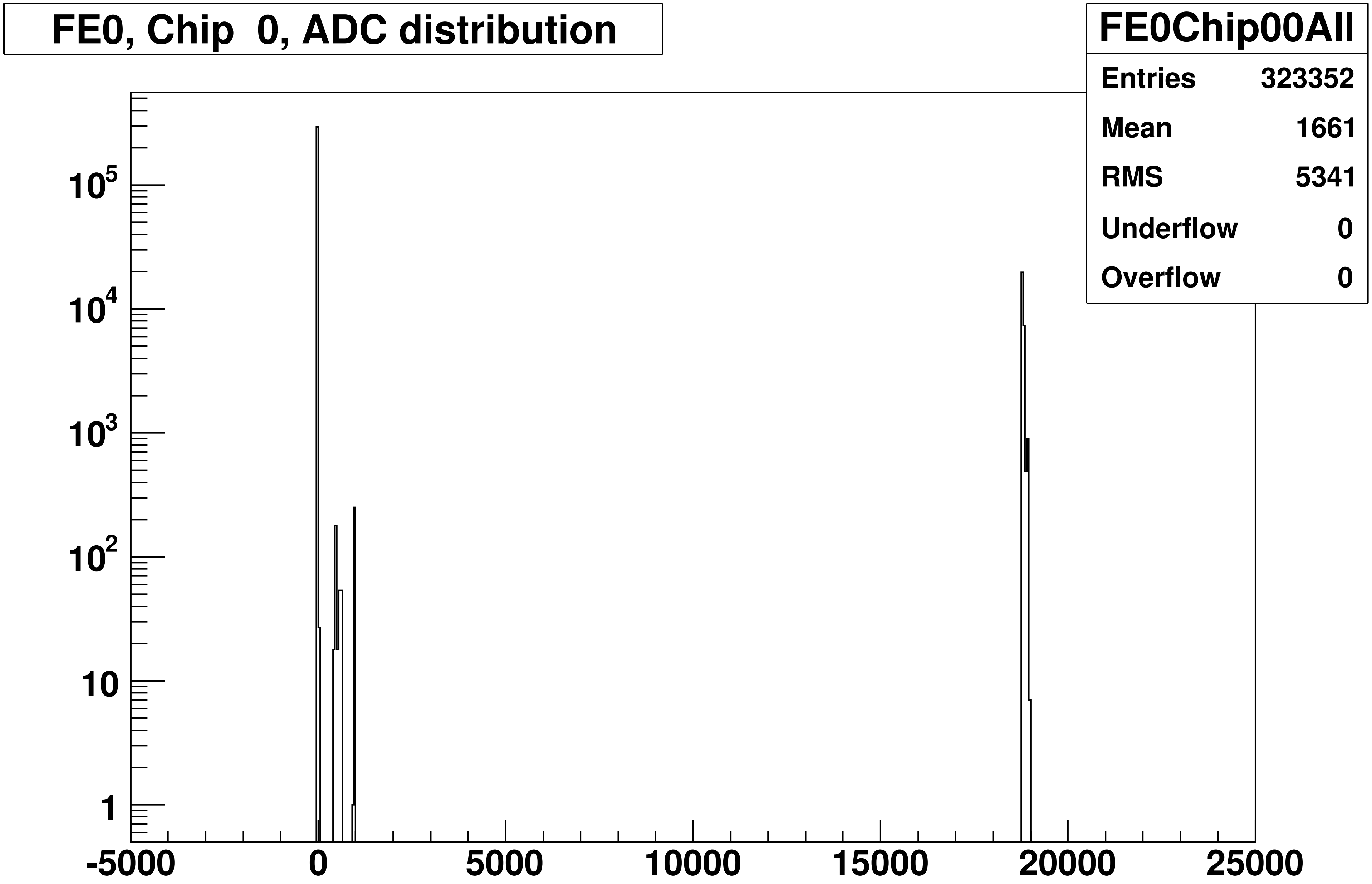
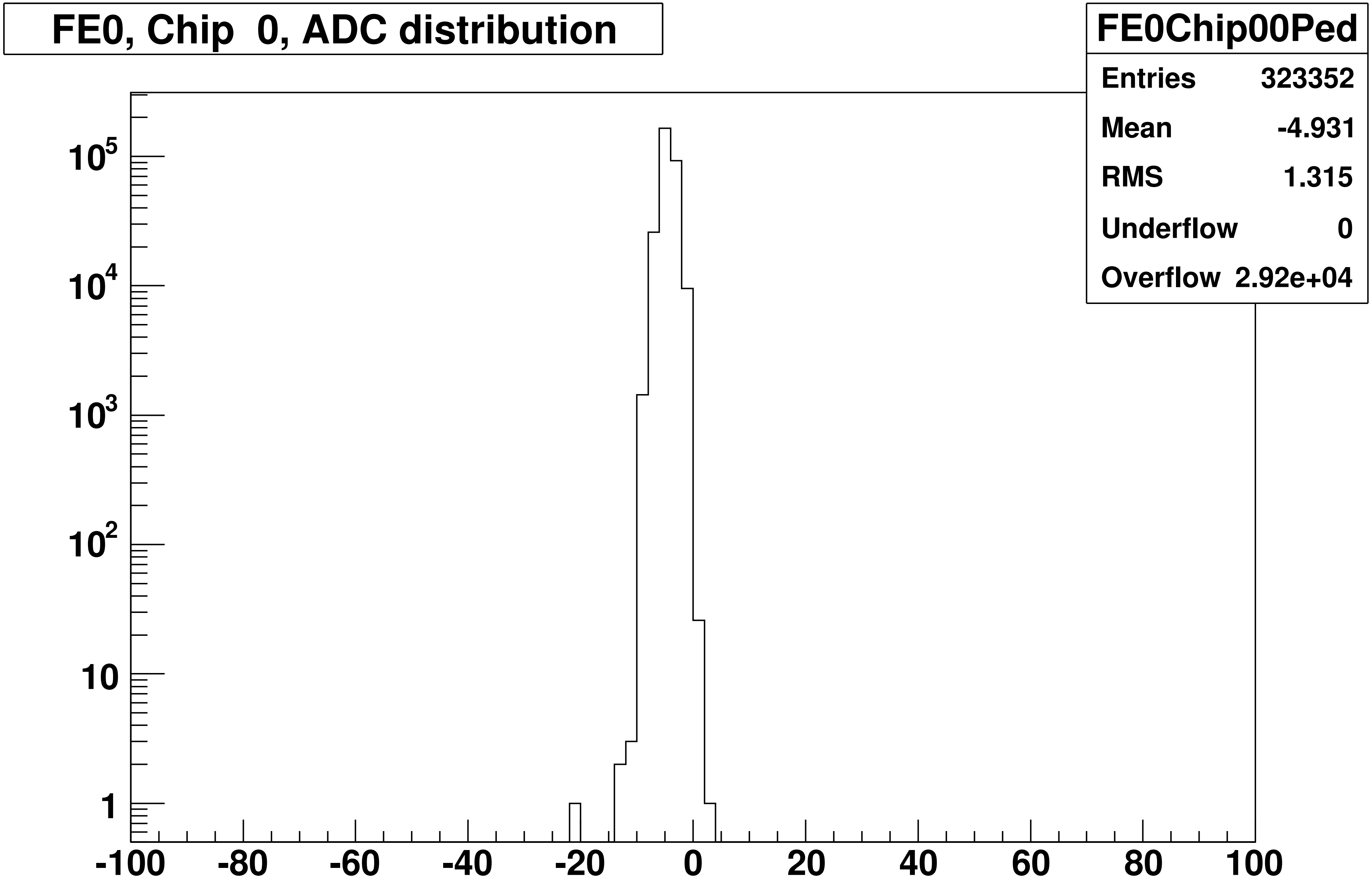
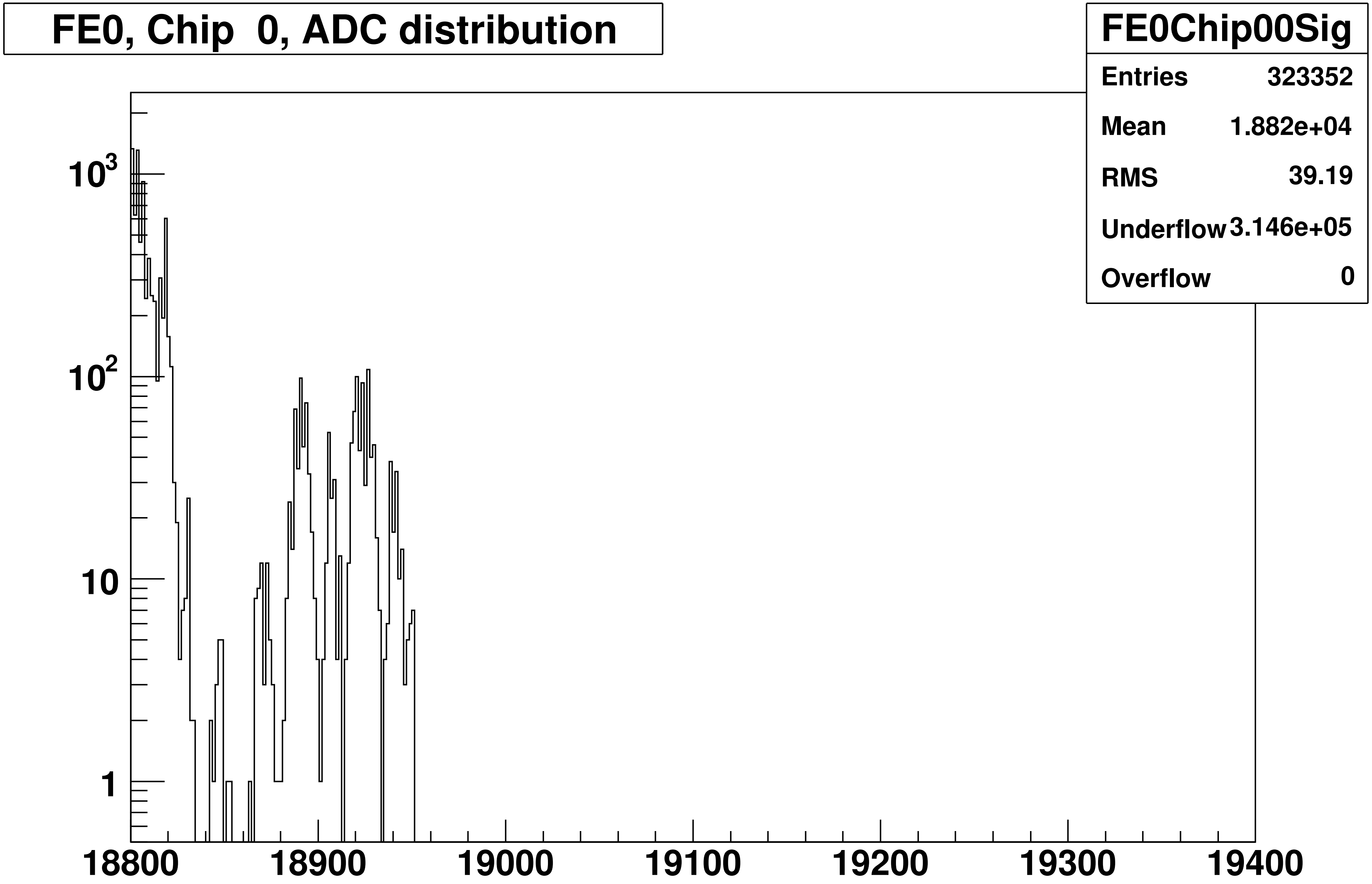
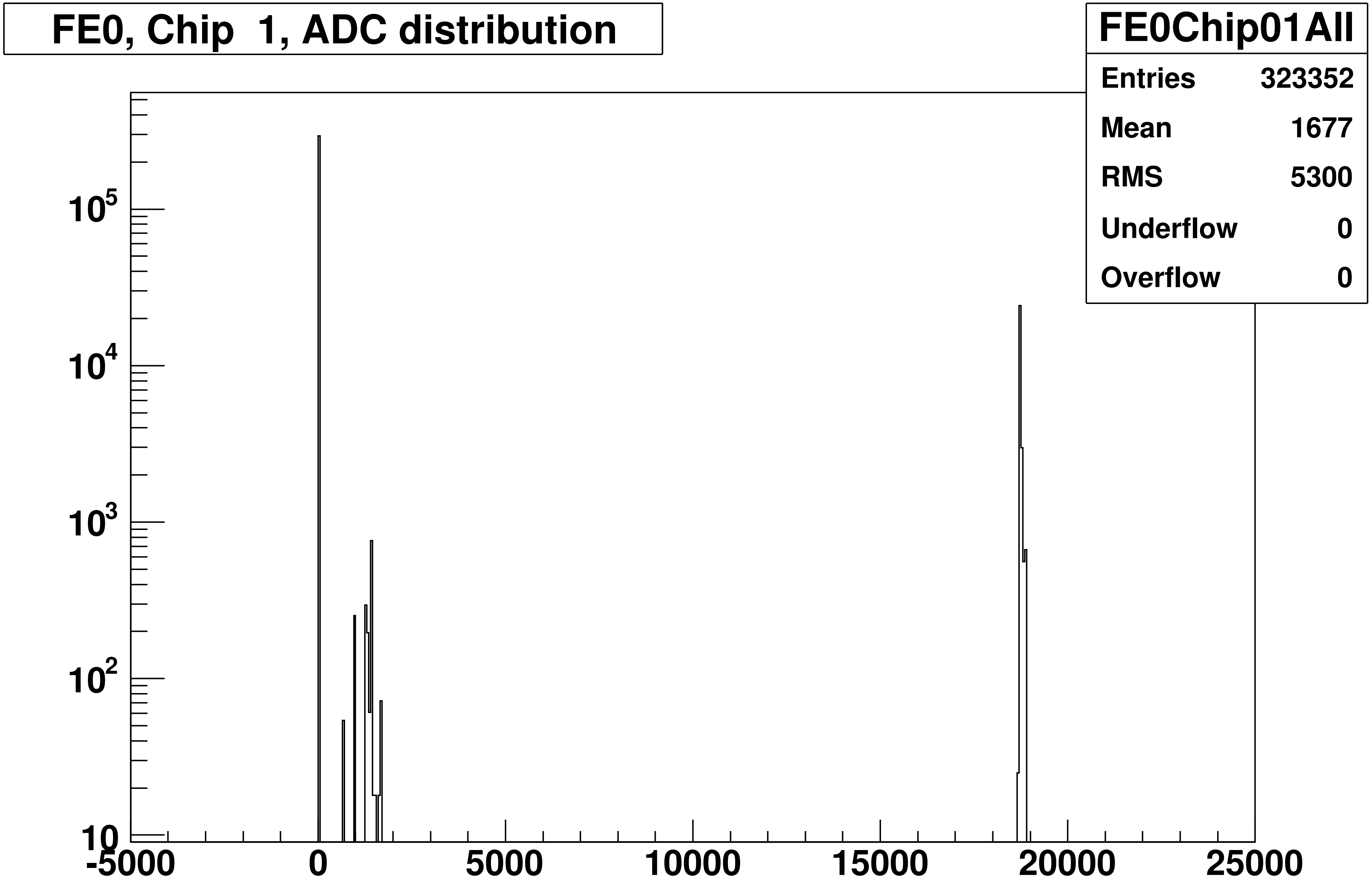
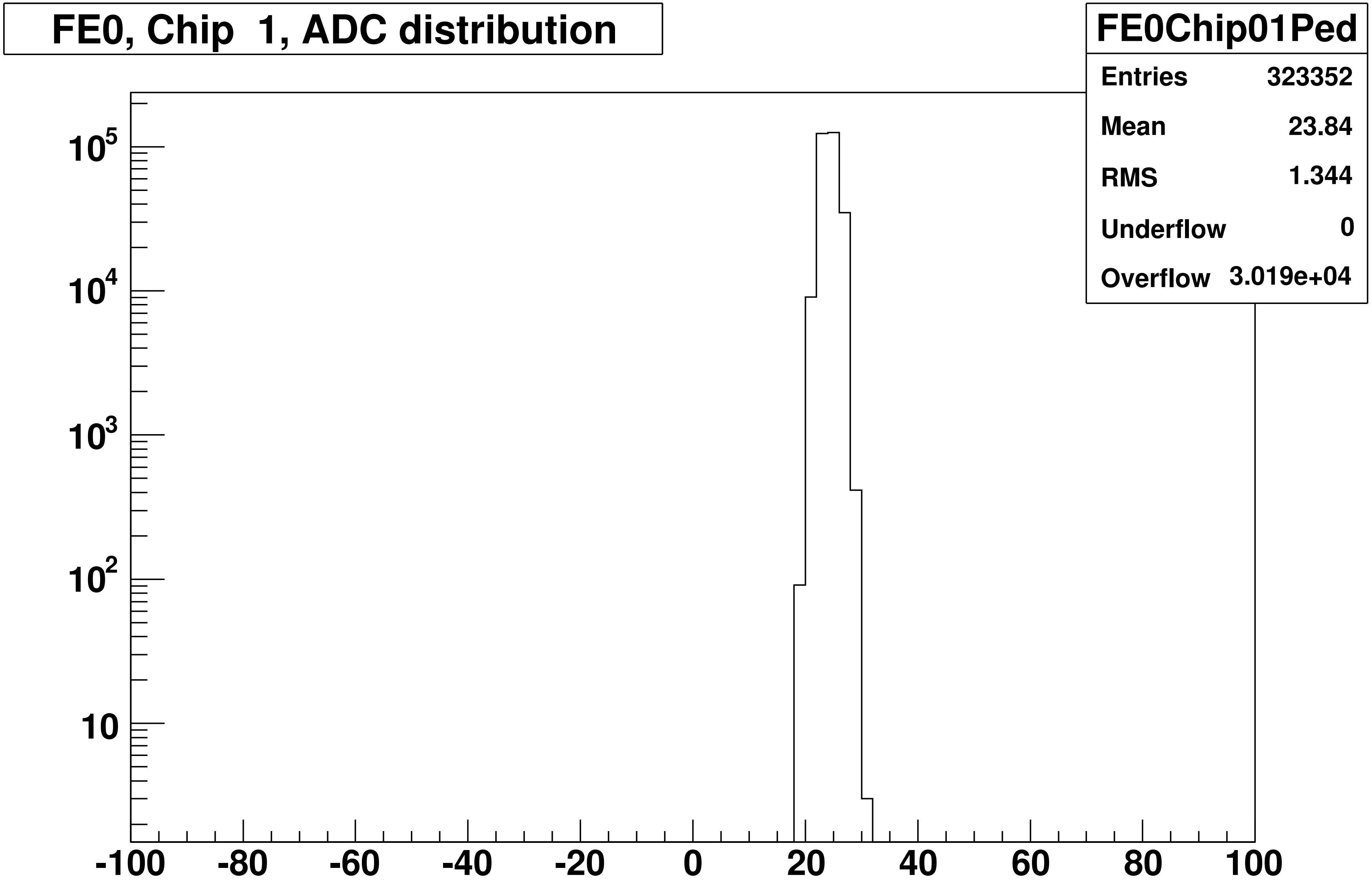
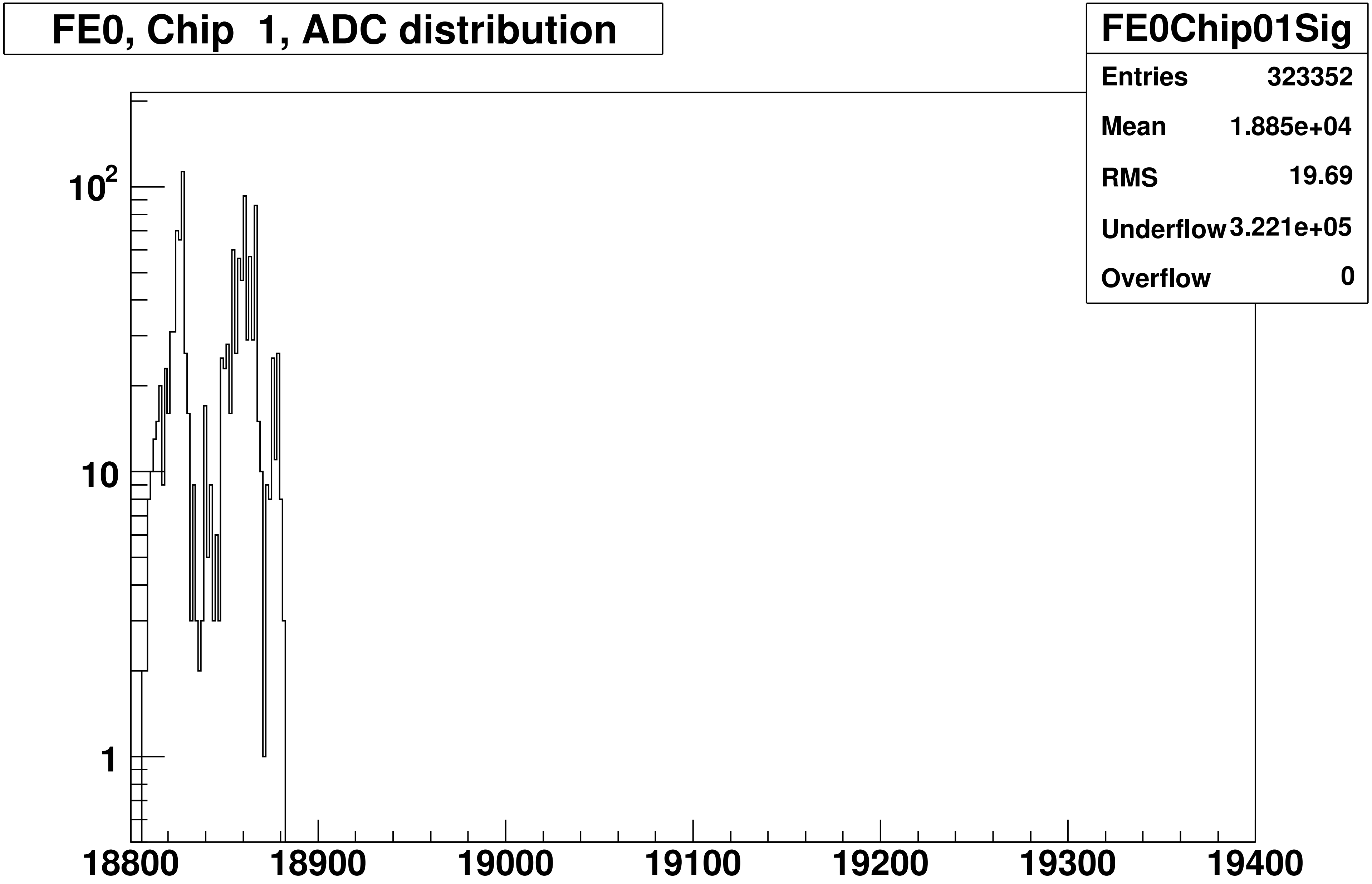

FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 323352
Mean 1661
RMS 5341
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

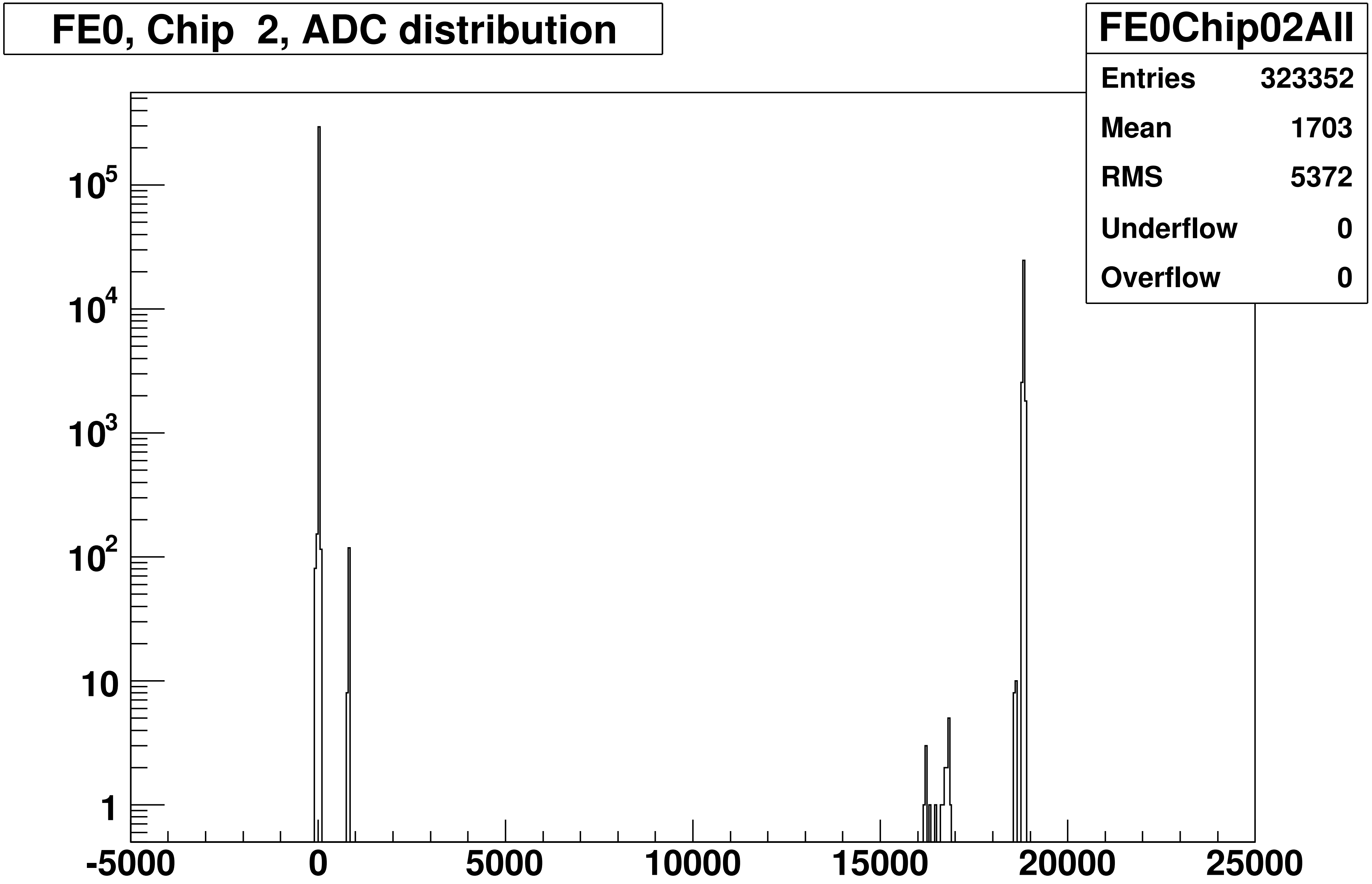

FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 323352
Mean -4.931
RMS 1.315
Underflow 0
Overflow 2.92e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

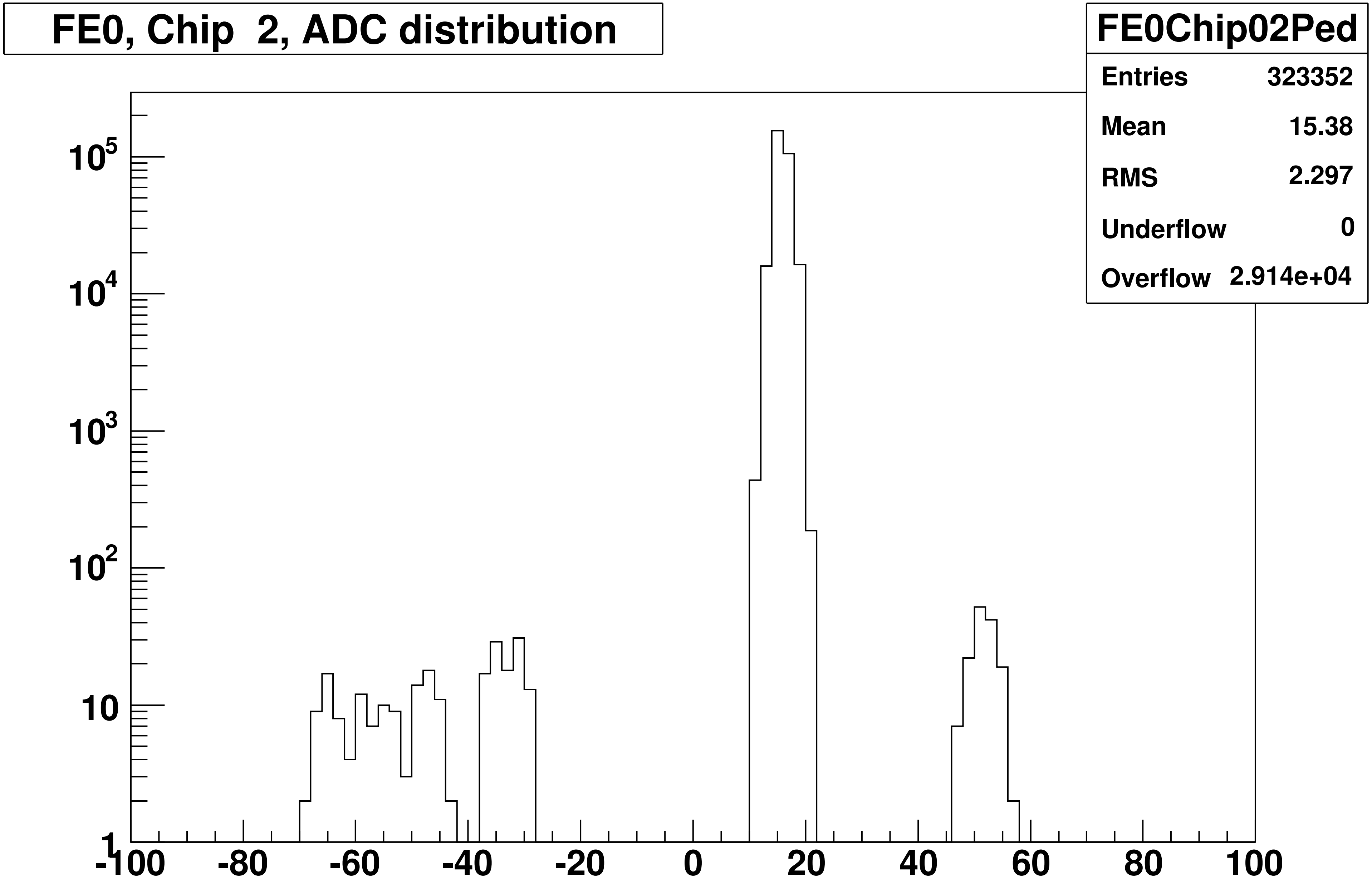

FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 323352
Mean 1.882e+04
RMS 39.19
Underflow 3.146e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

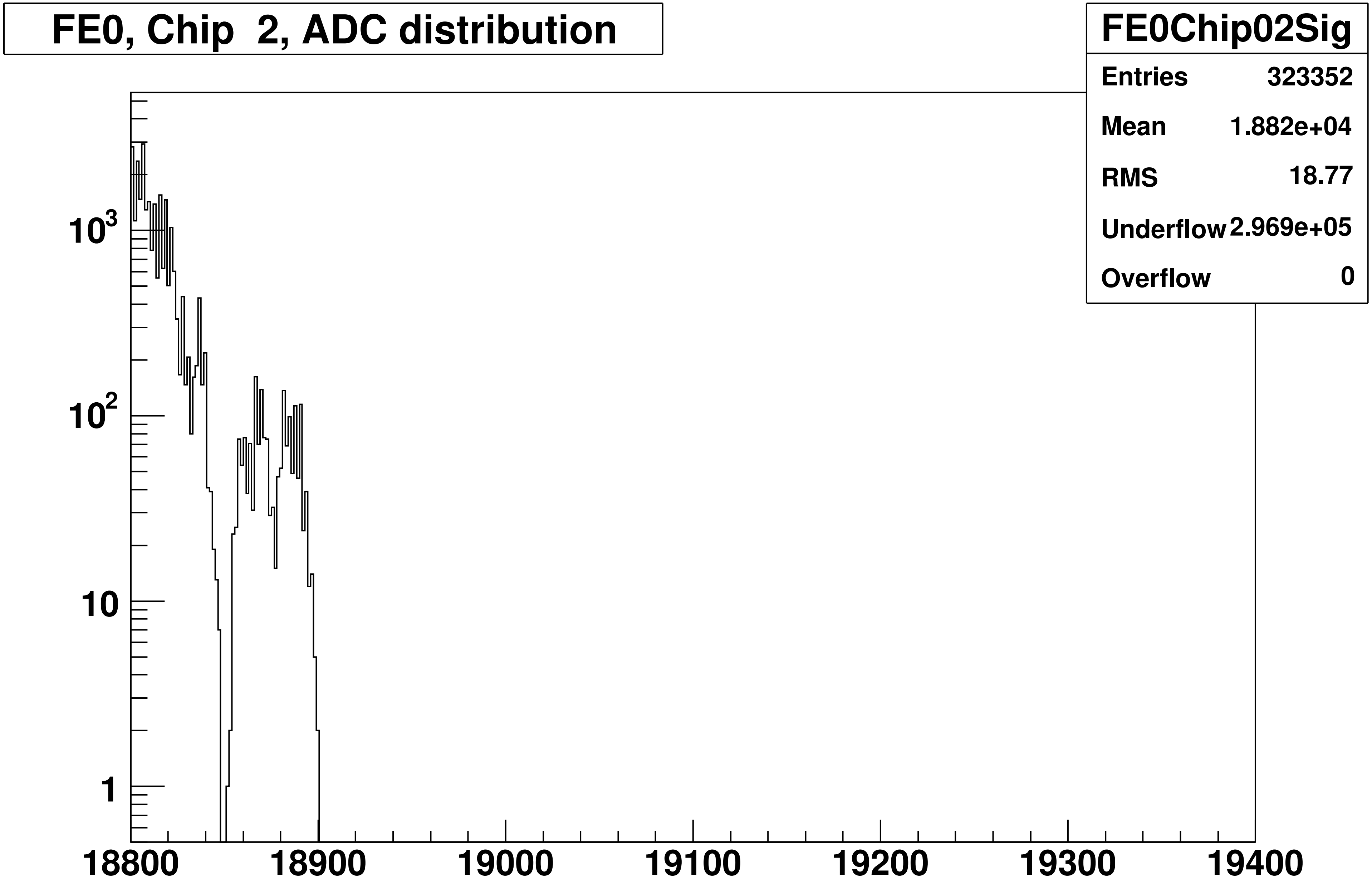

FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 323352
Mean 1677
RMS 5300
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

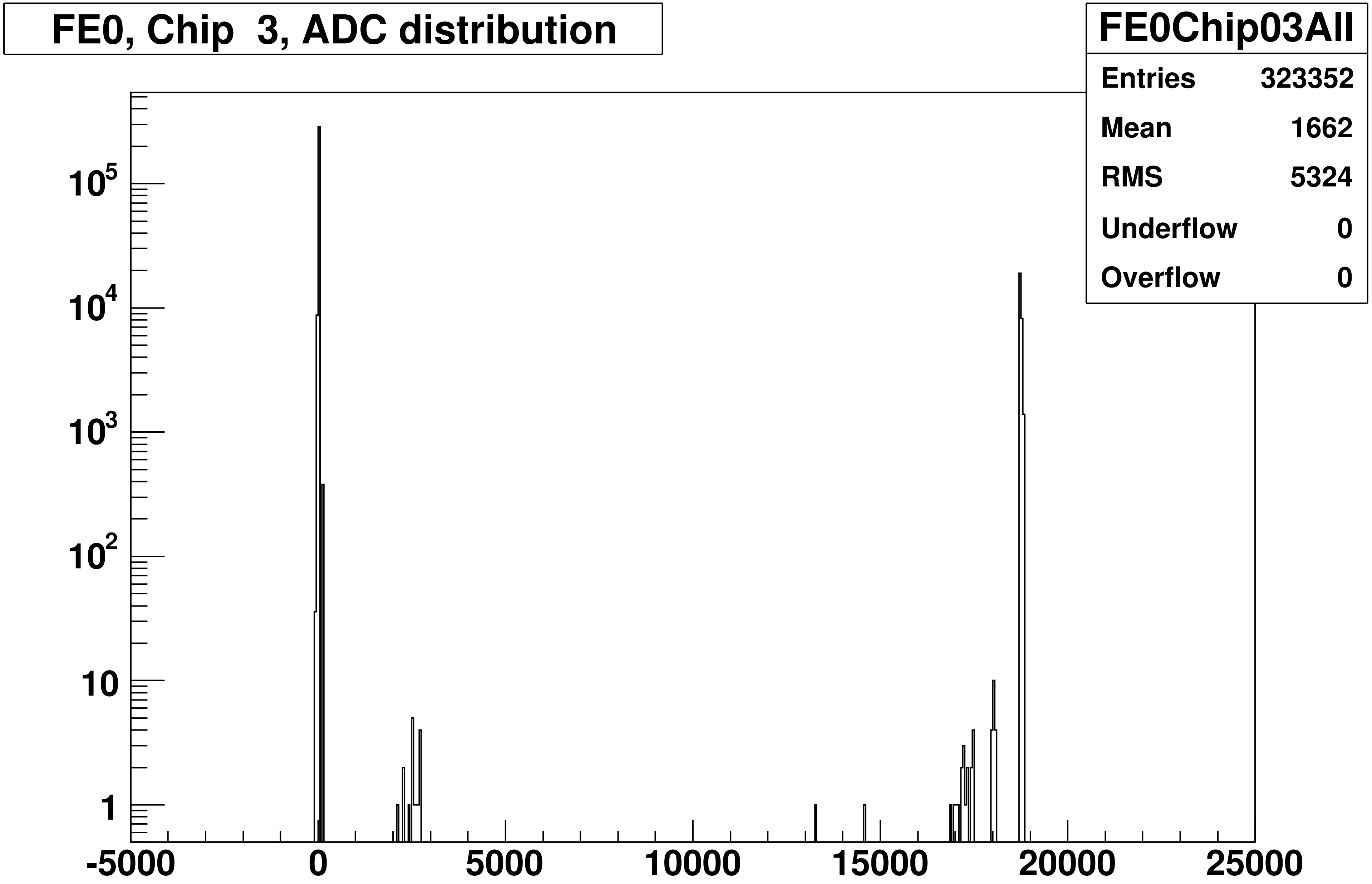

FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 323352
Mean 23.84
RMS 1.344
Underflow 0
Overflow 3.019e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

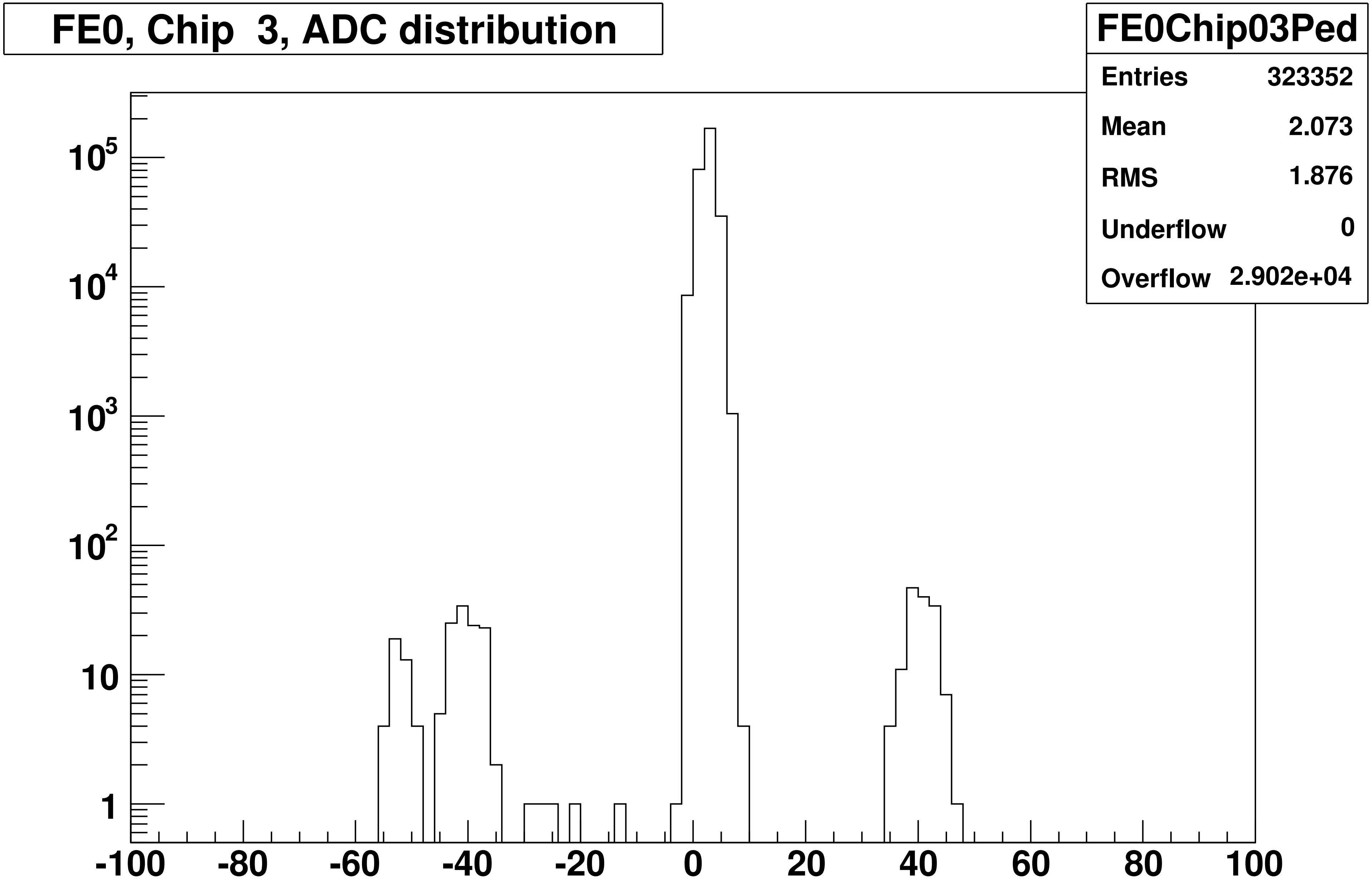

FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 323352
Mean 1.885e+04
RMS 19.69
Underflow 3.221e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

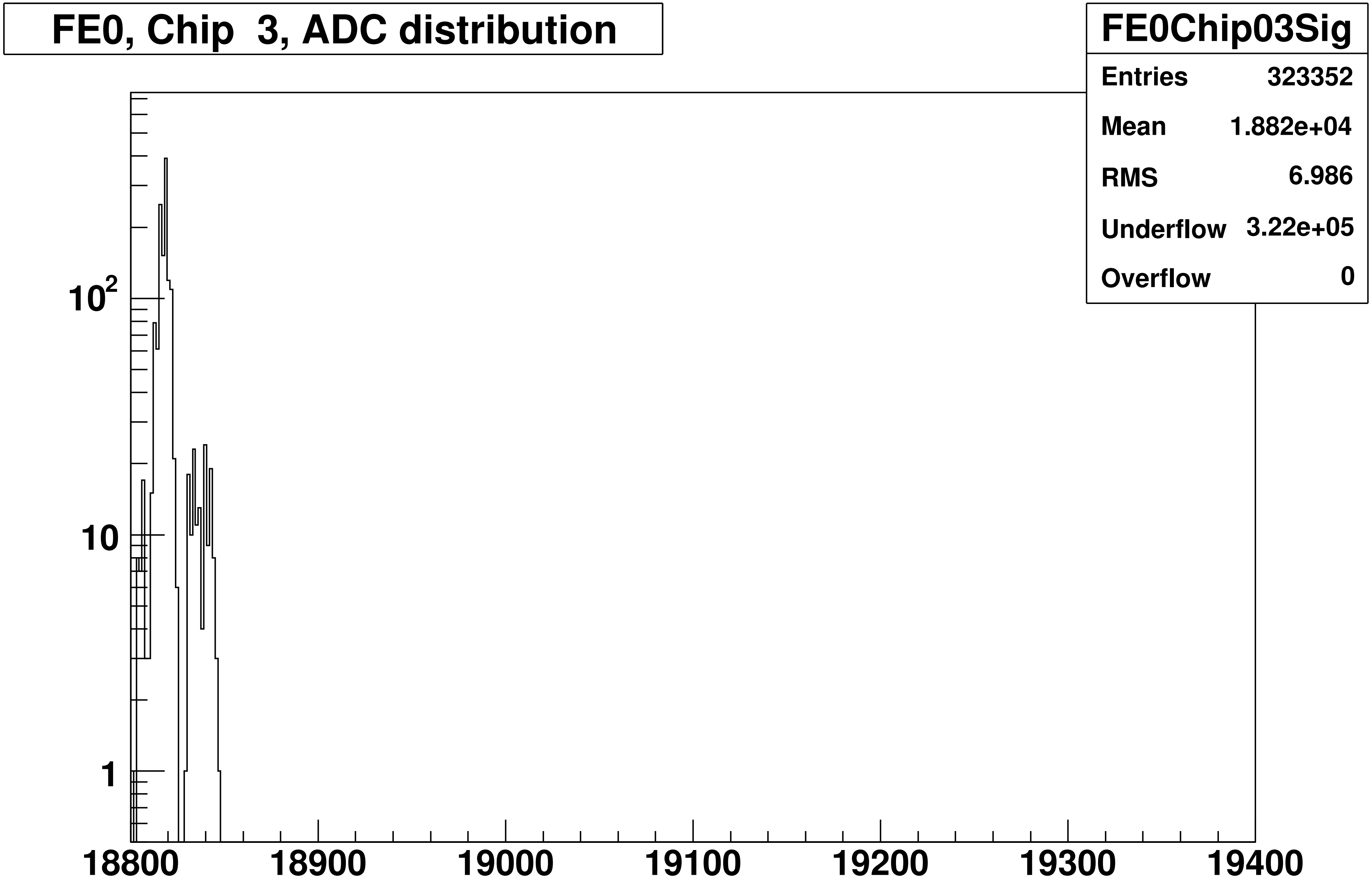

FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 323352
Mean 1703
RMS 5372
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 323352
Mean 15.38
RMS 2.297
Underflow 0
Overflow 2.914e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

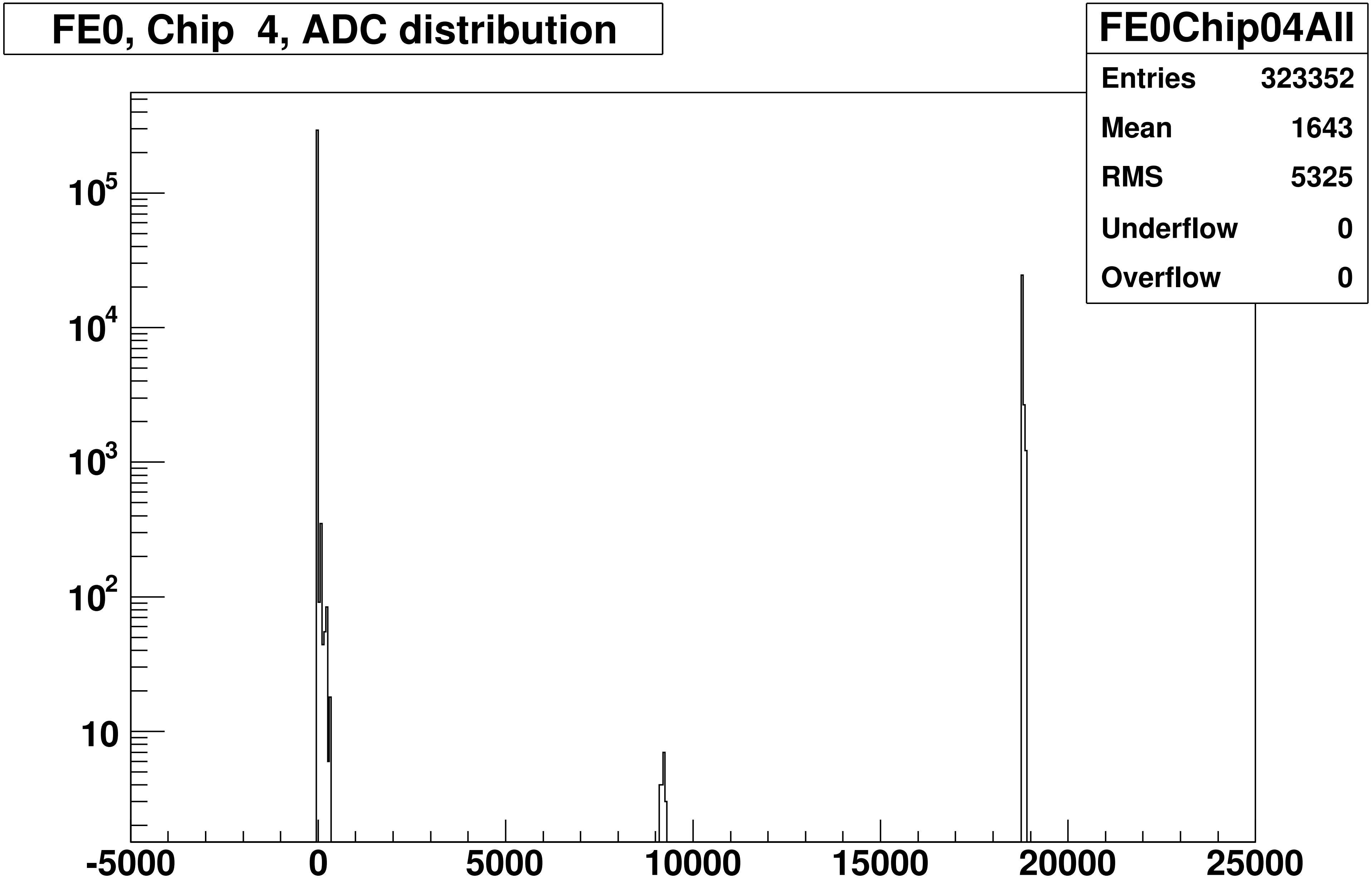

FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 323352
Mean 1.882e+04
RMS 18.77
Underflow 2.969e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

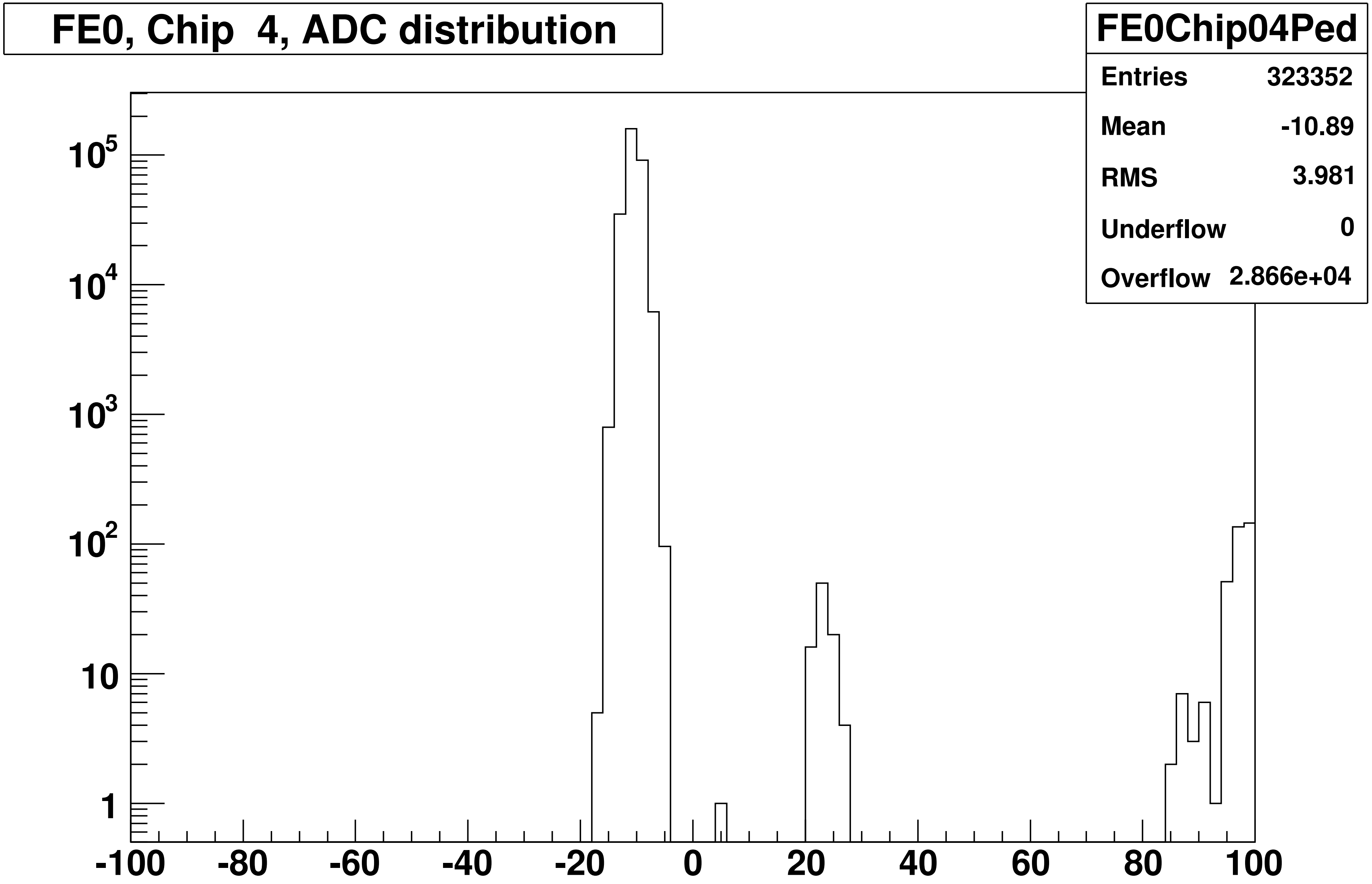

FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 323352
Mean 1662
RMS 5324
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

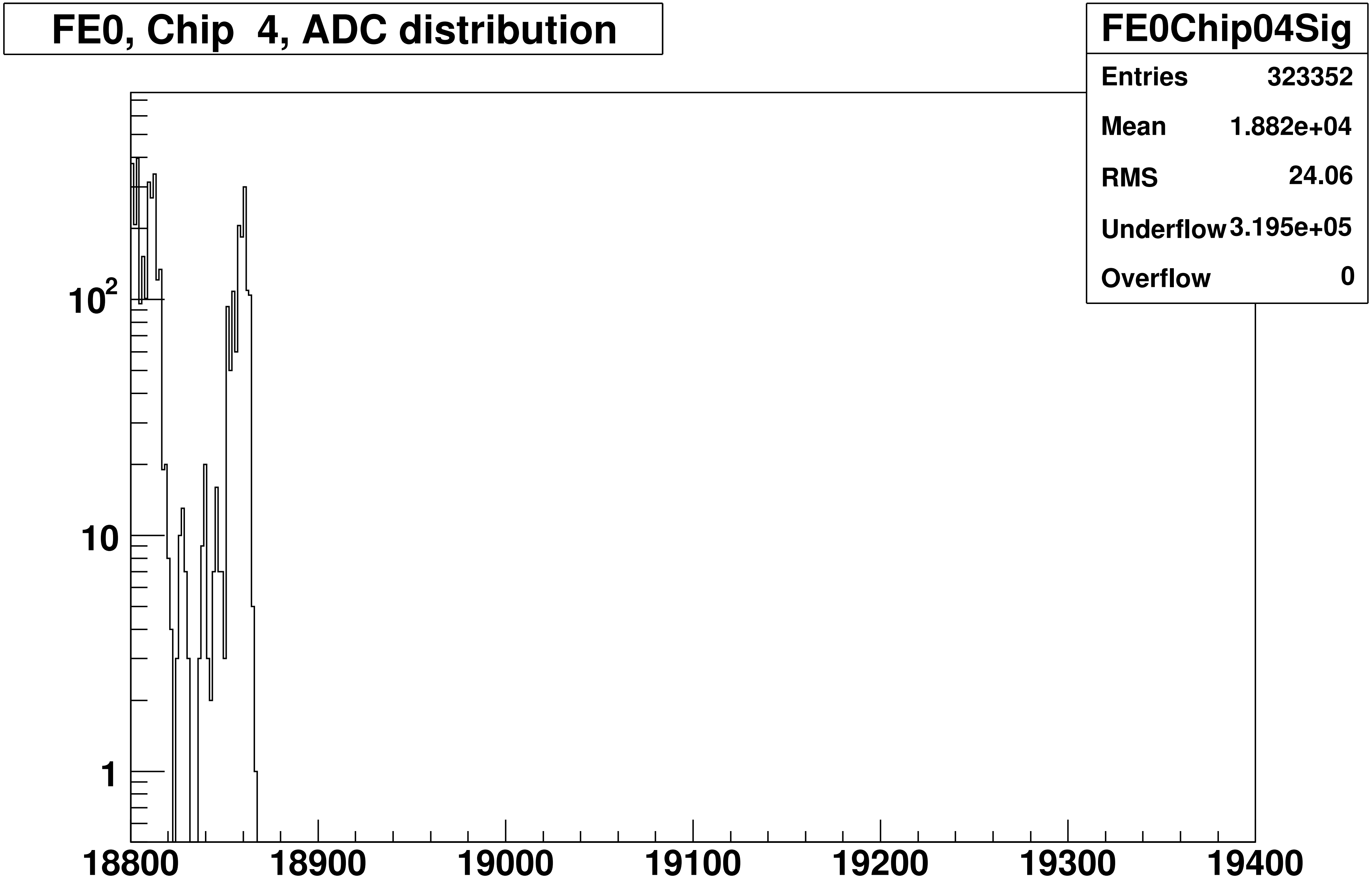

FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 323352
Mean 2.073
RMS 1.876
Underflow 0
Overflow 2.902e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



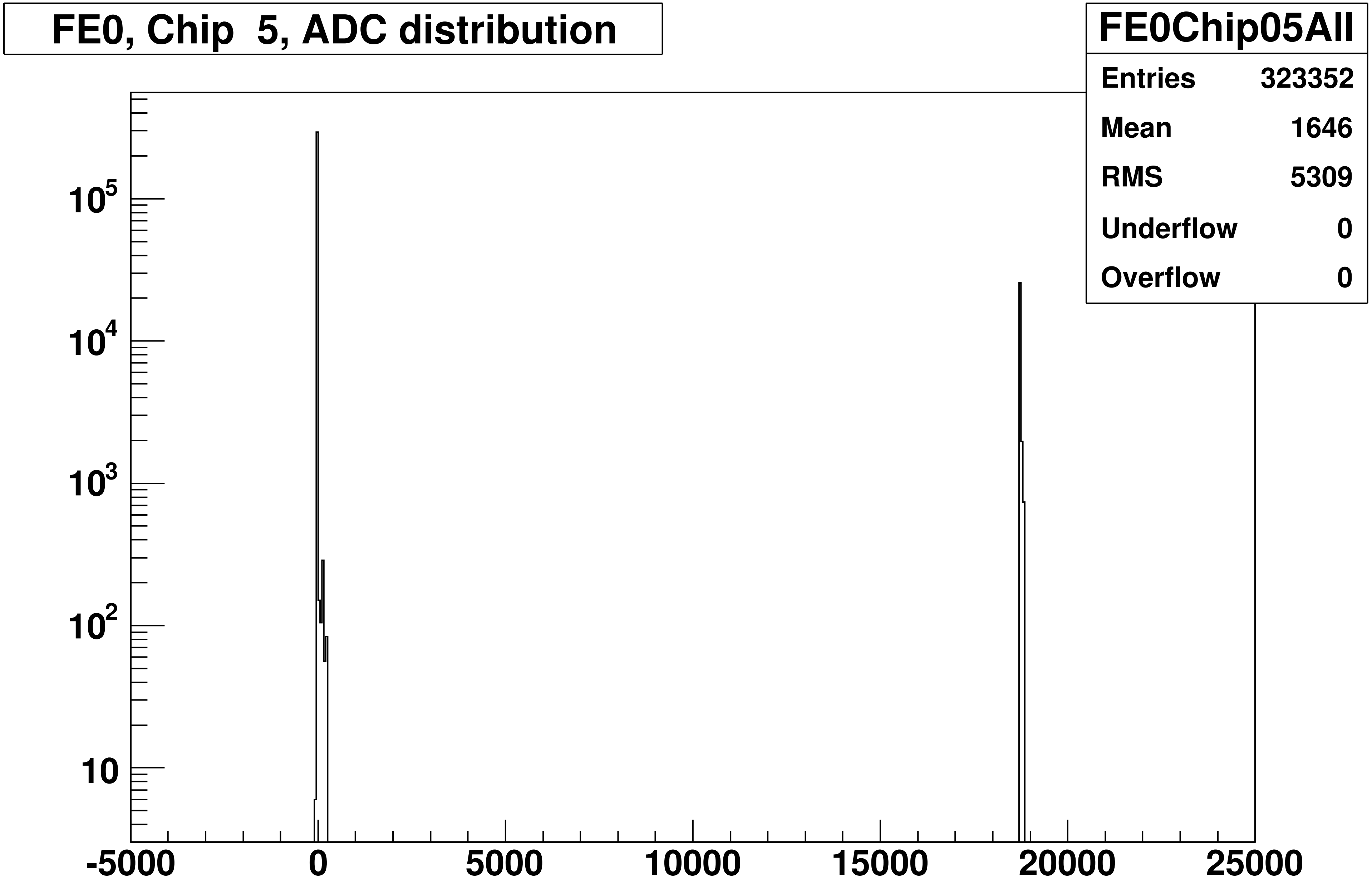
FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 323352
Mean 1.882e+04
RMS 6.986
Underflow 3.22e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 323352
Mean 1643
RMS 5325
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

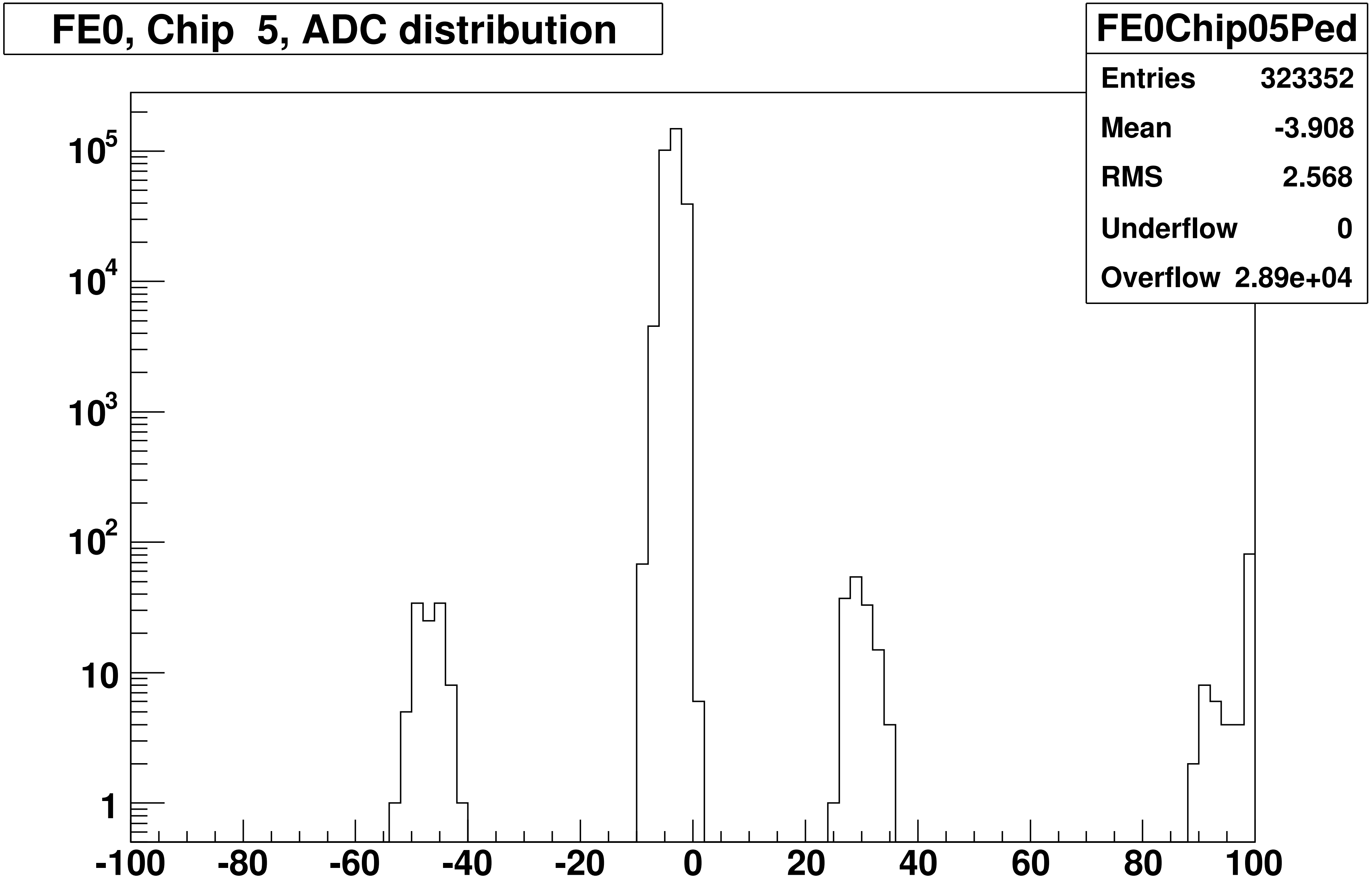

FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 323352
Mean -10.89
RMS 3.981
Underflow 0
Overflow 2.866e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

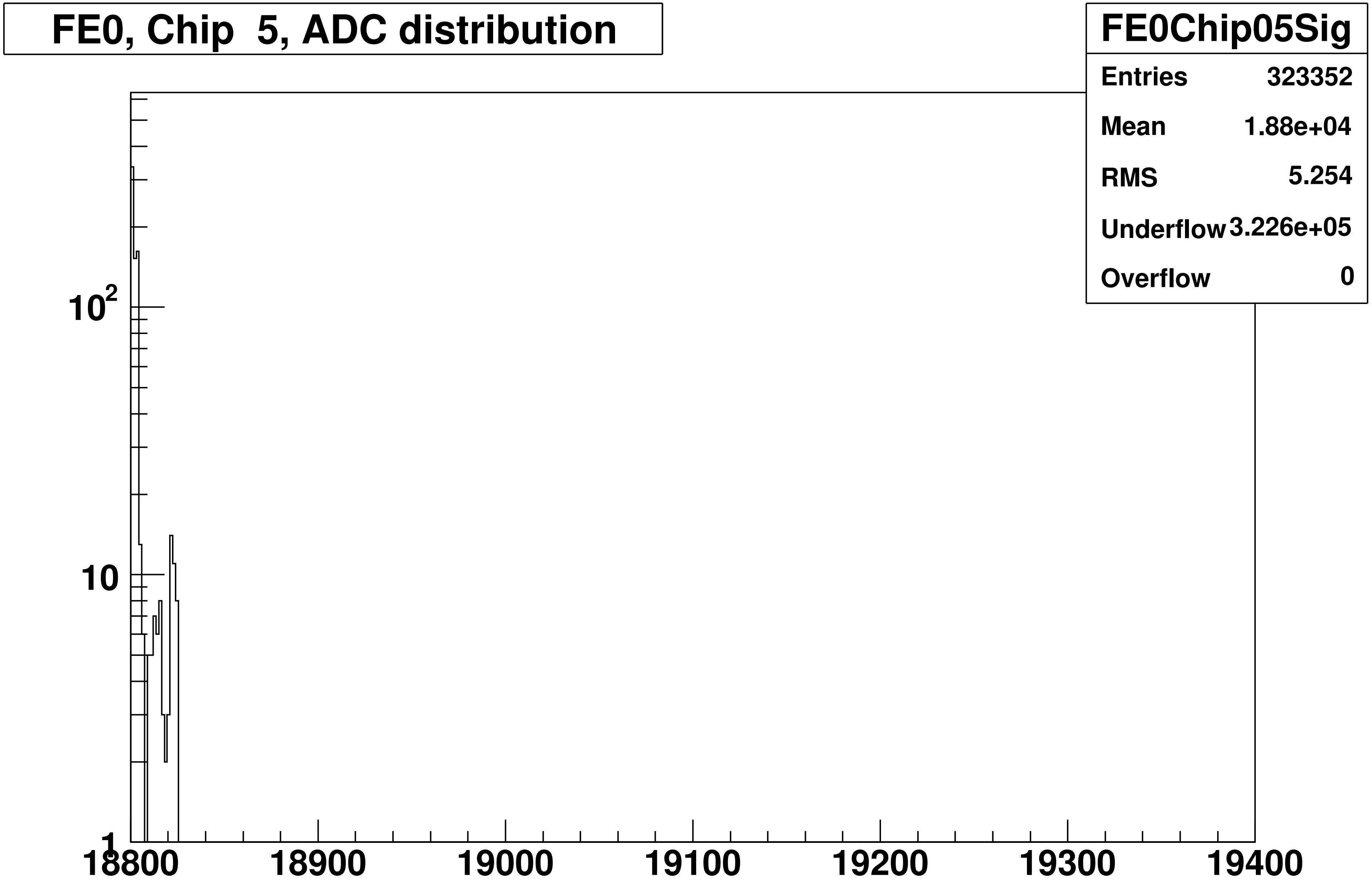


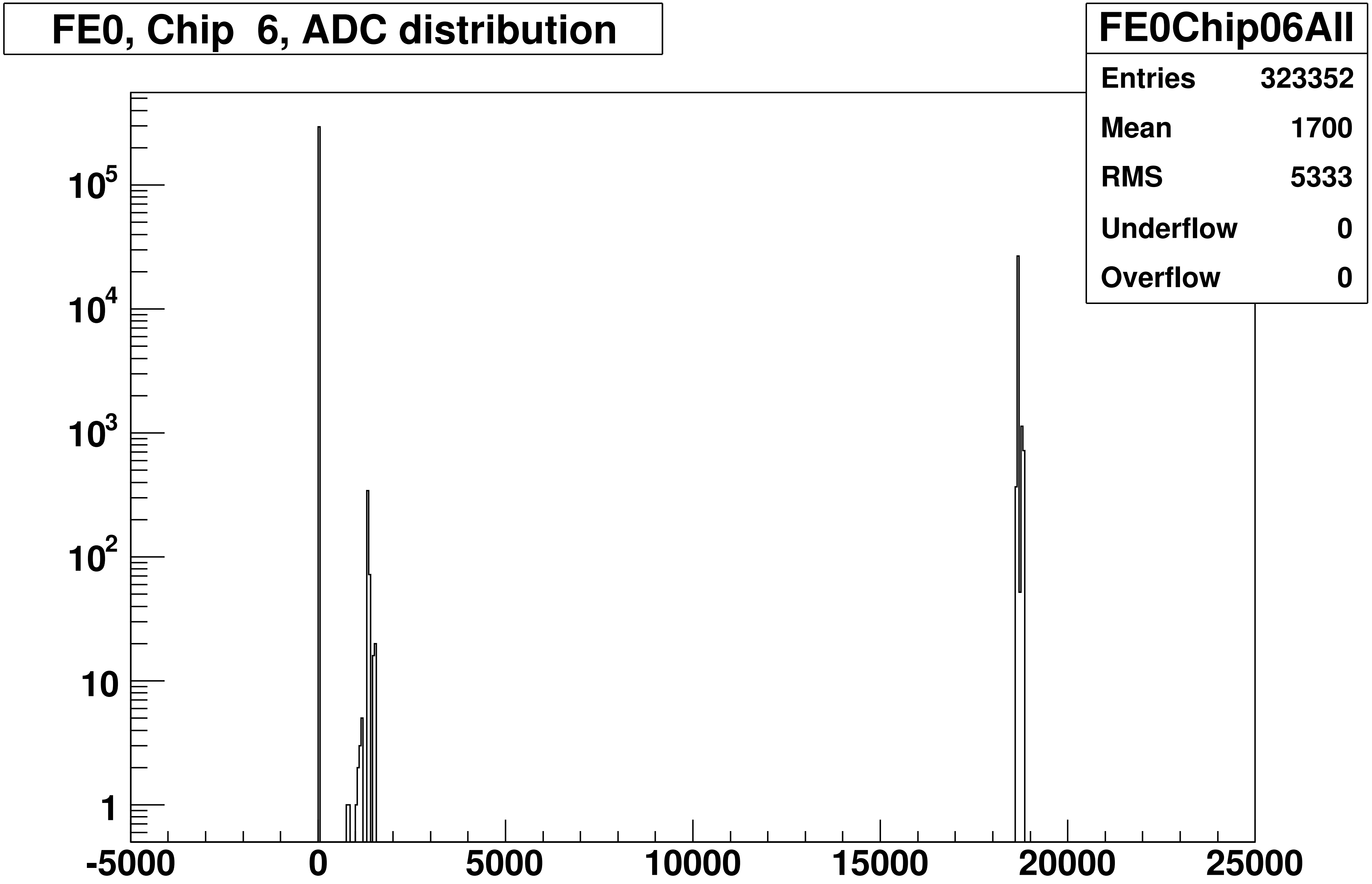
FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 323352
Mean 1.882e+04
RMS 24.06
Underflow 3.195e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

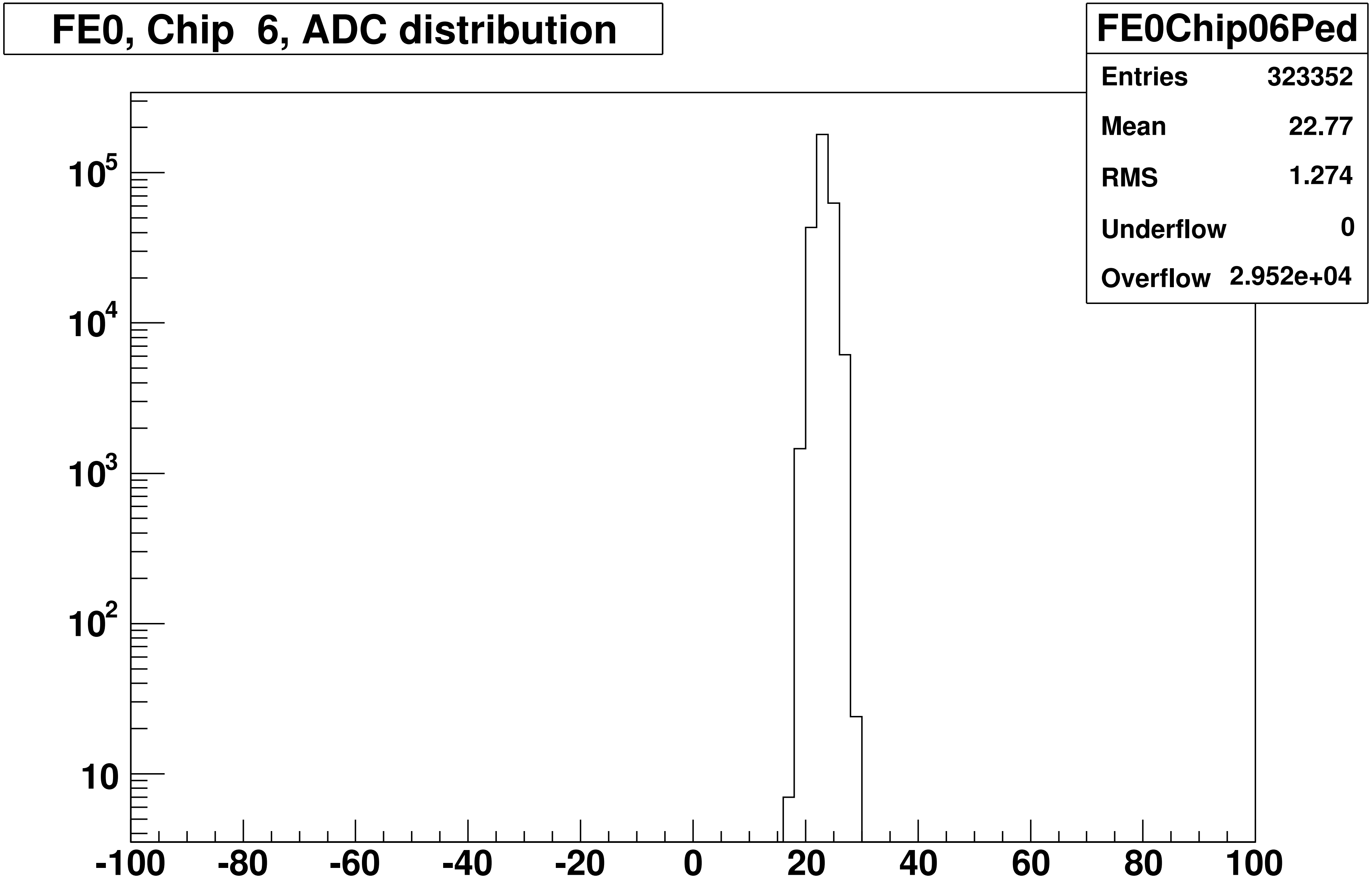


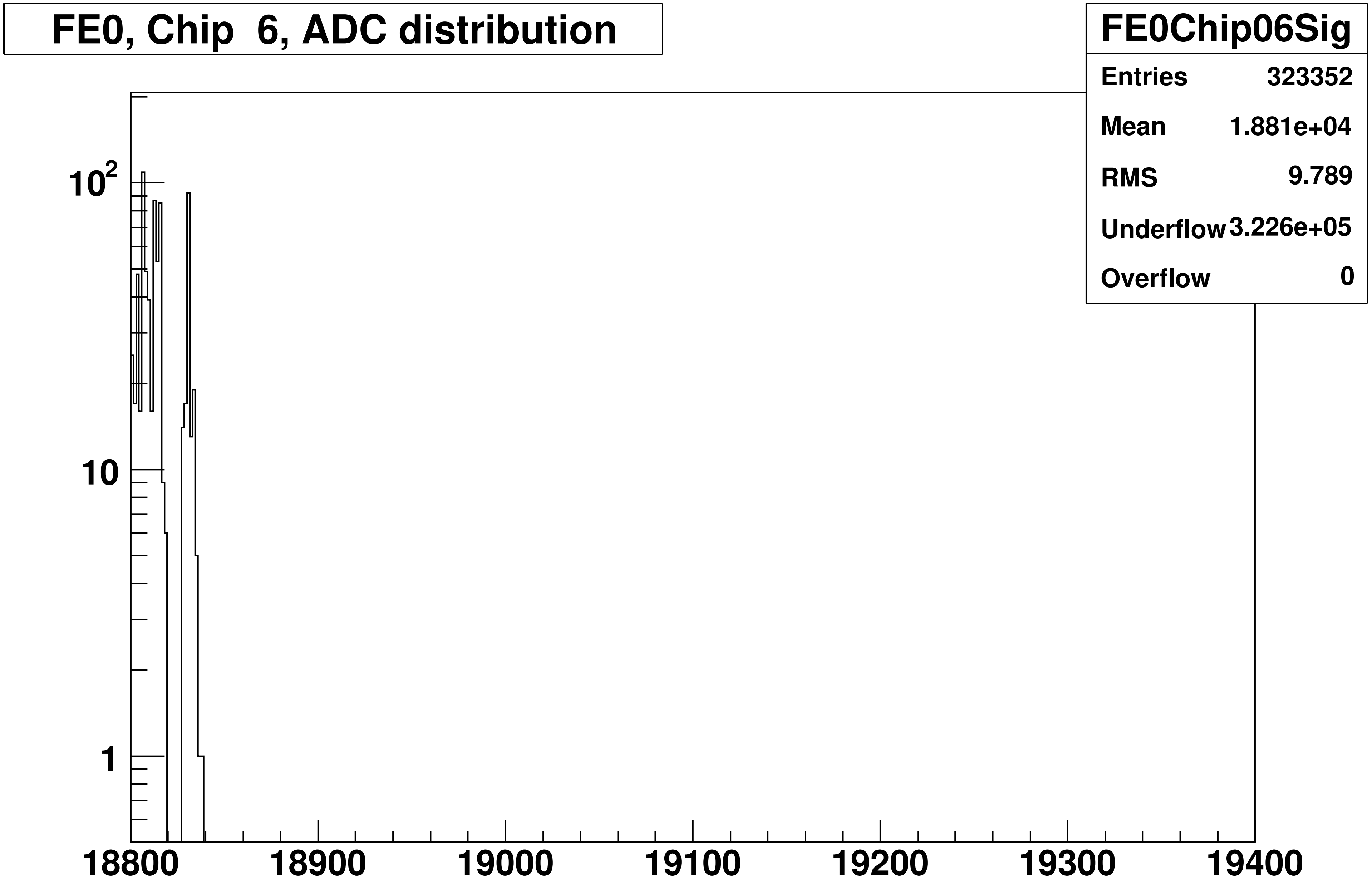
FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 323352
Mean 1646
RMS 5309
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

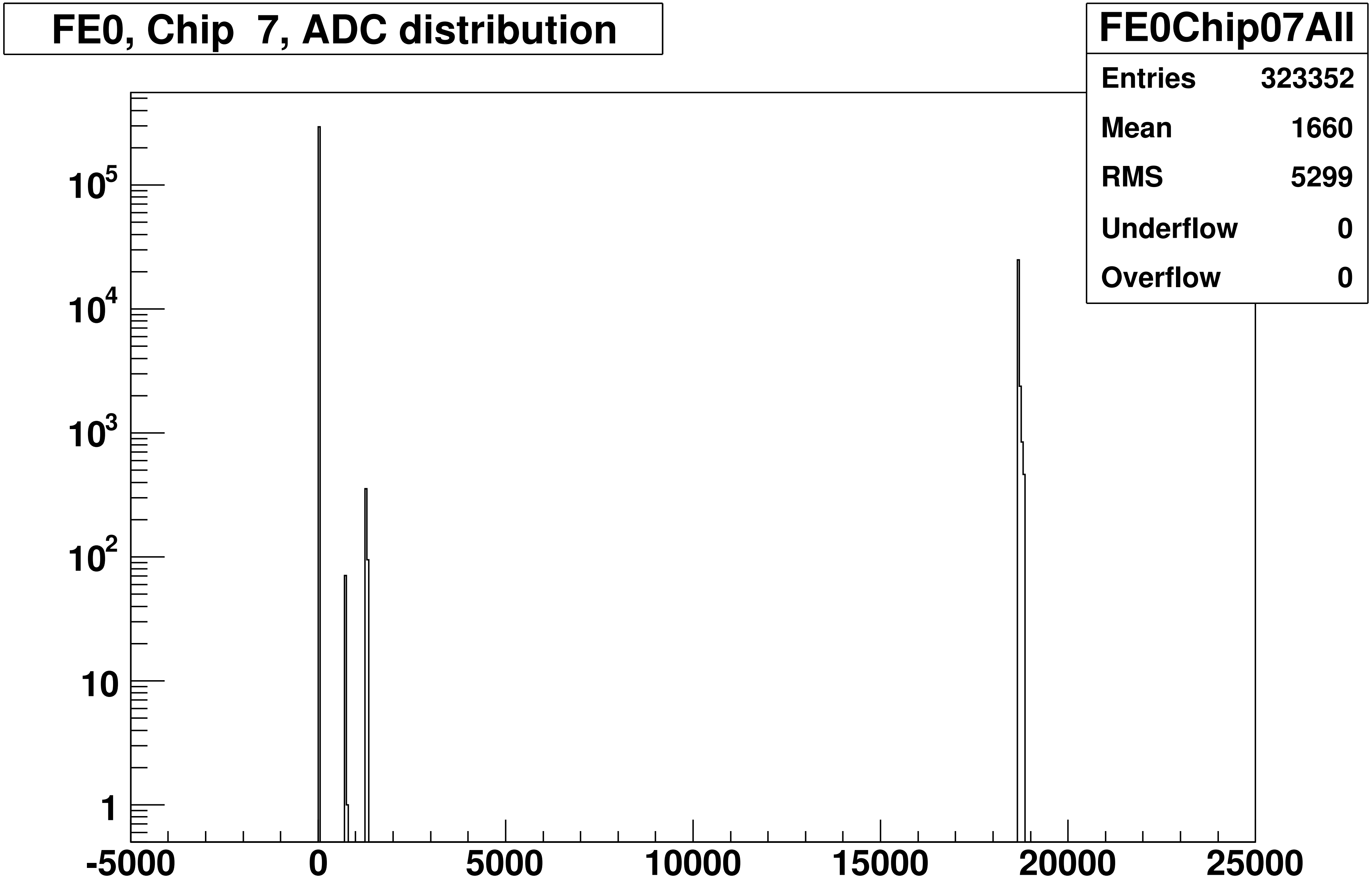

FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 323352
Mean -3.908
RMS 2.568
Underflow 0
Overflow 2.89e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

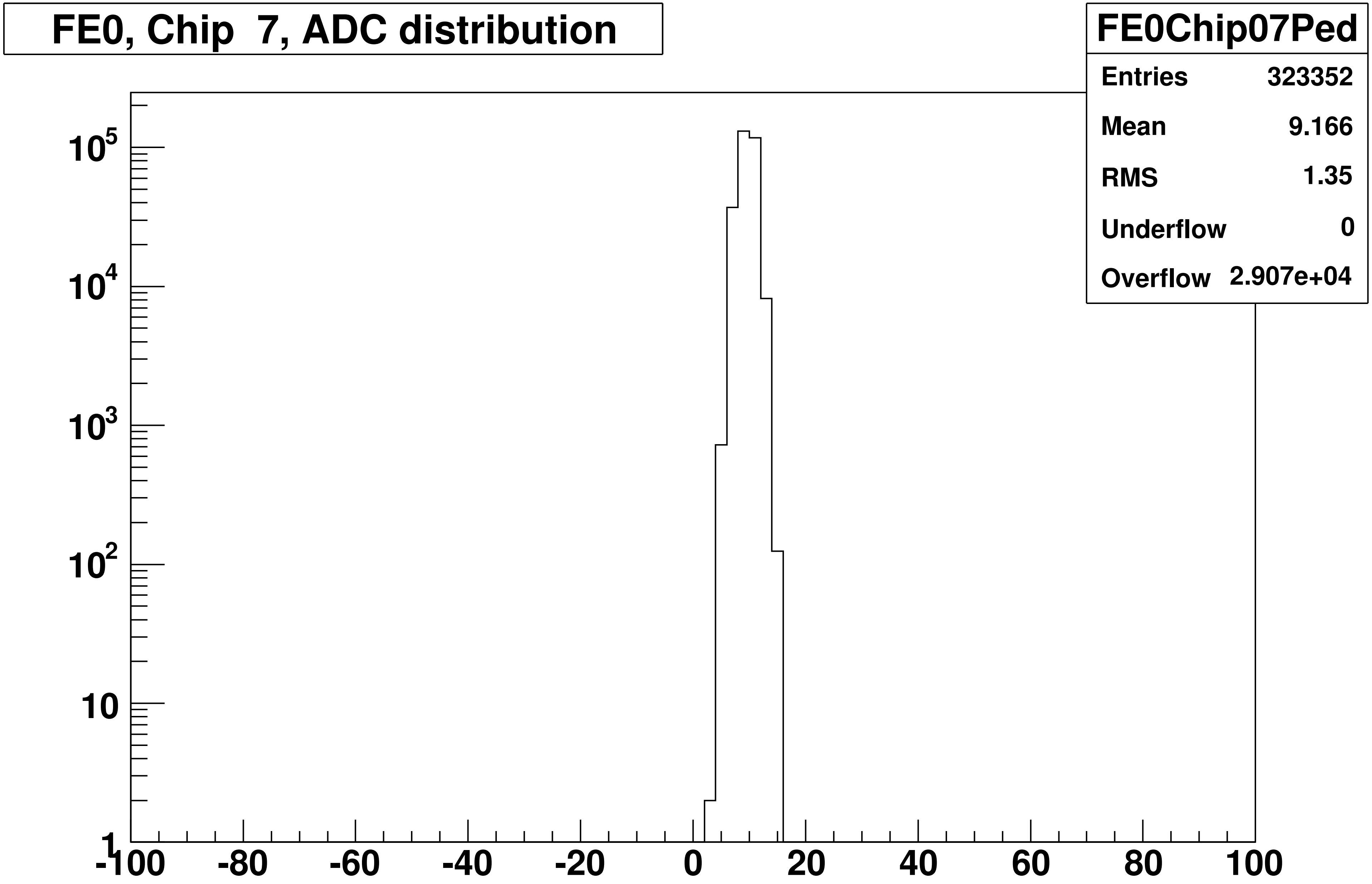

FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 323352
Mean 1.88e+04
RMS 5.254
Underflow 3.226e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

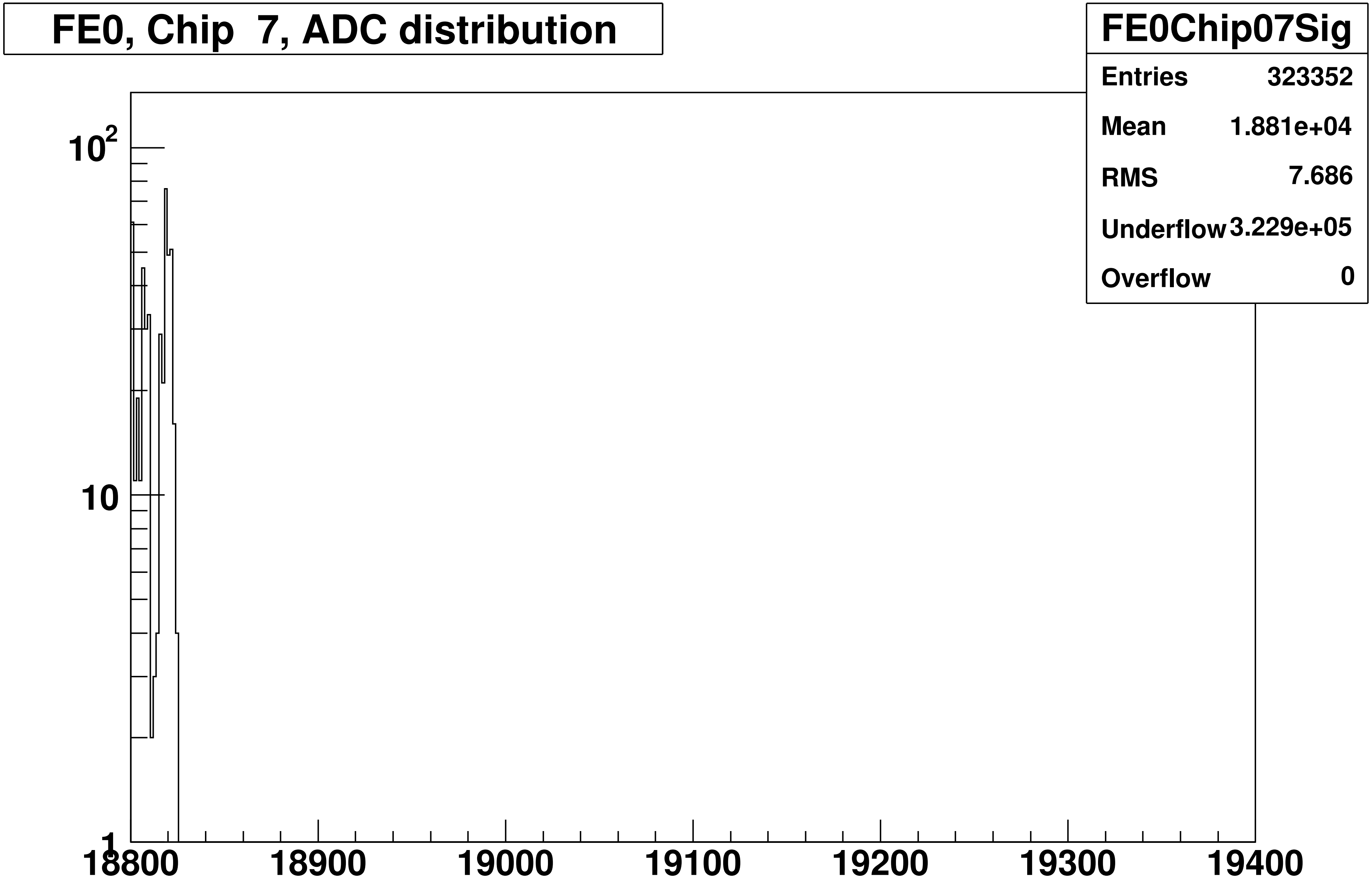

FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 323352
Mean 1700
RMS 5333
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

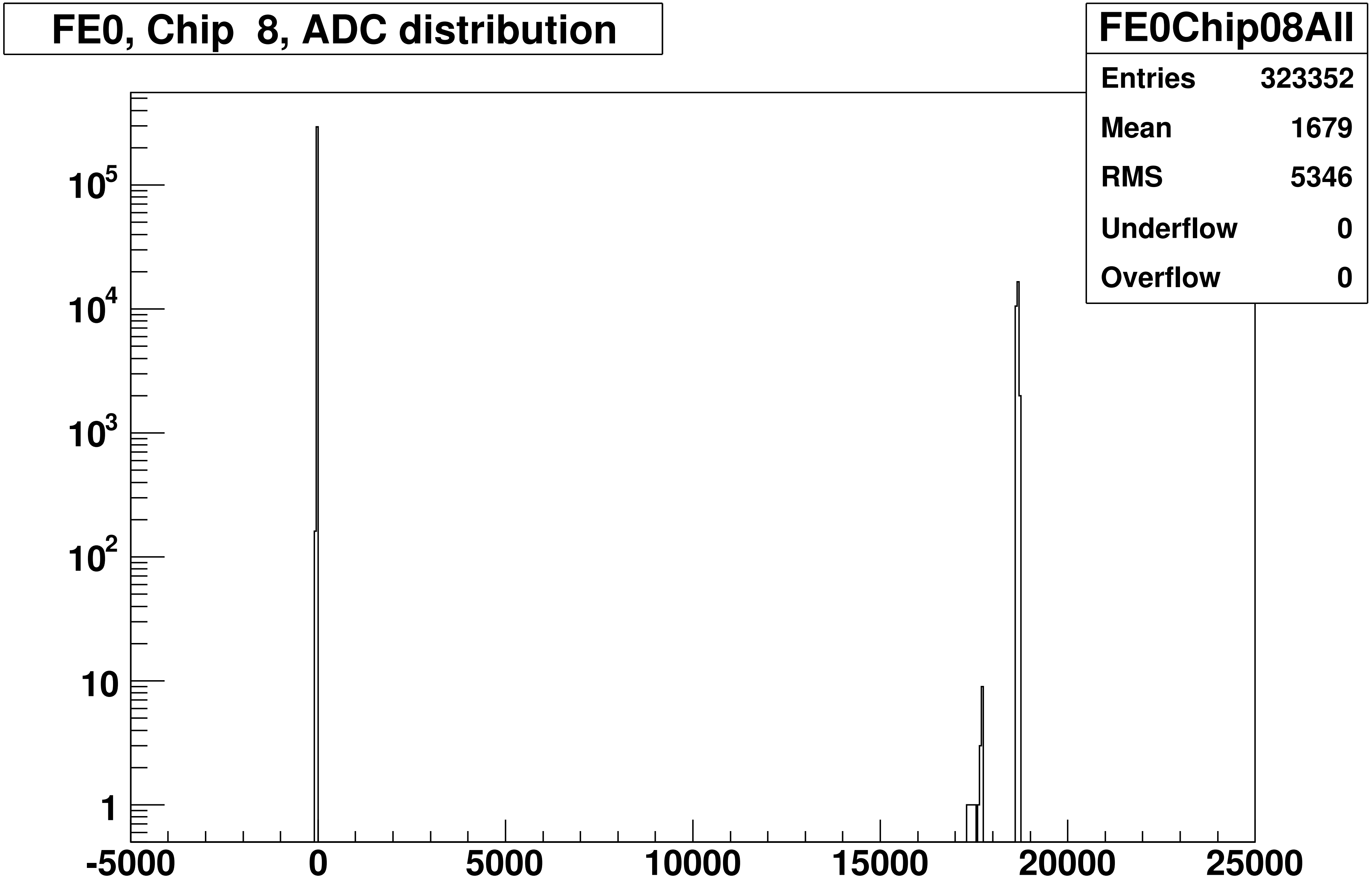

FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 323352
Mean 22.77
RMS 1.274
Underflow 0
Overflow 2.952e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

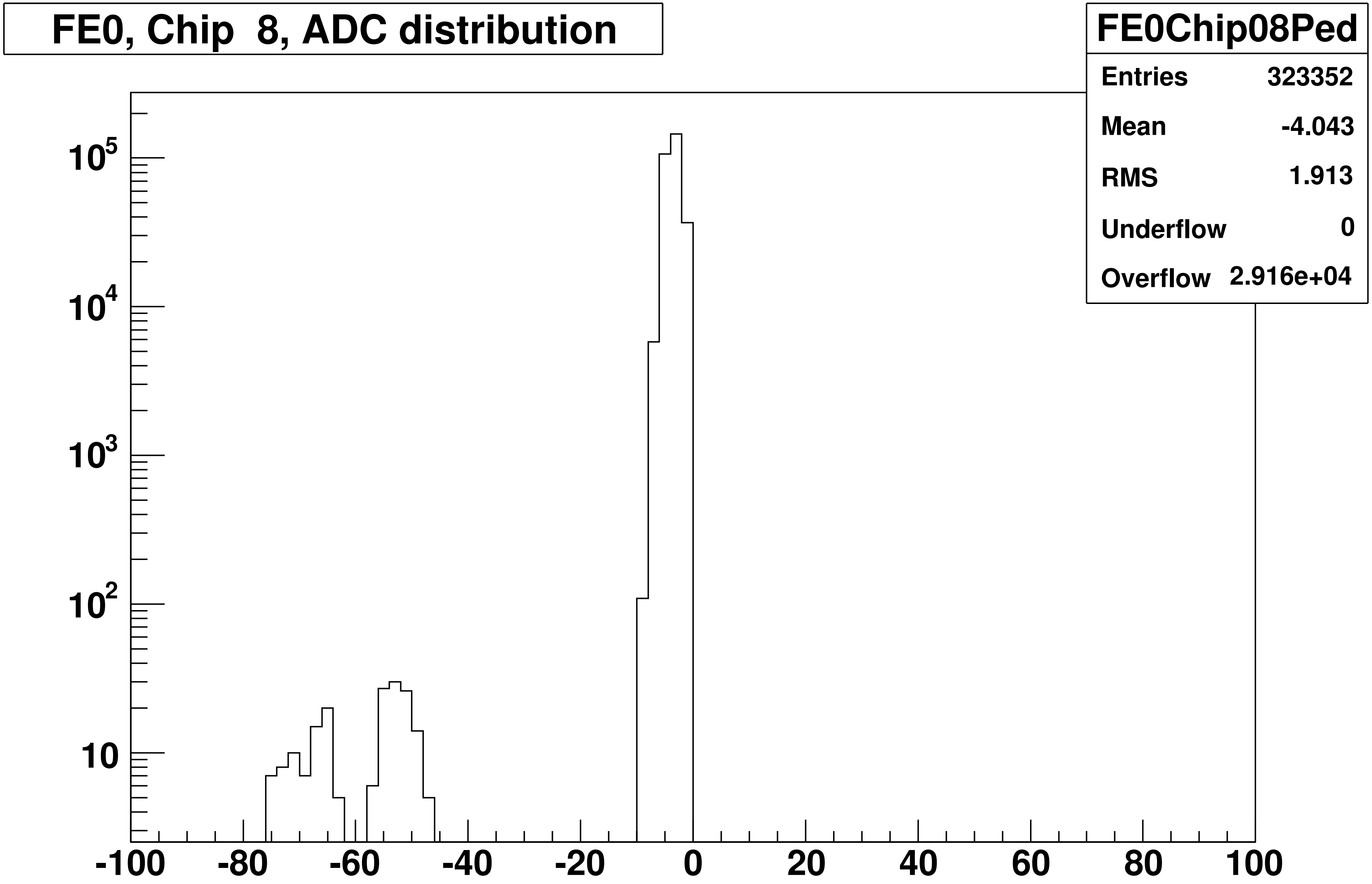

FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 323352
Mean 1.881e+04
RMS 9.789
Underflow 3.226e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

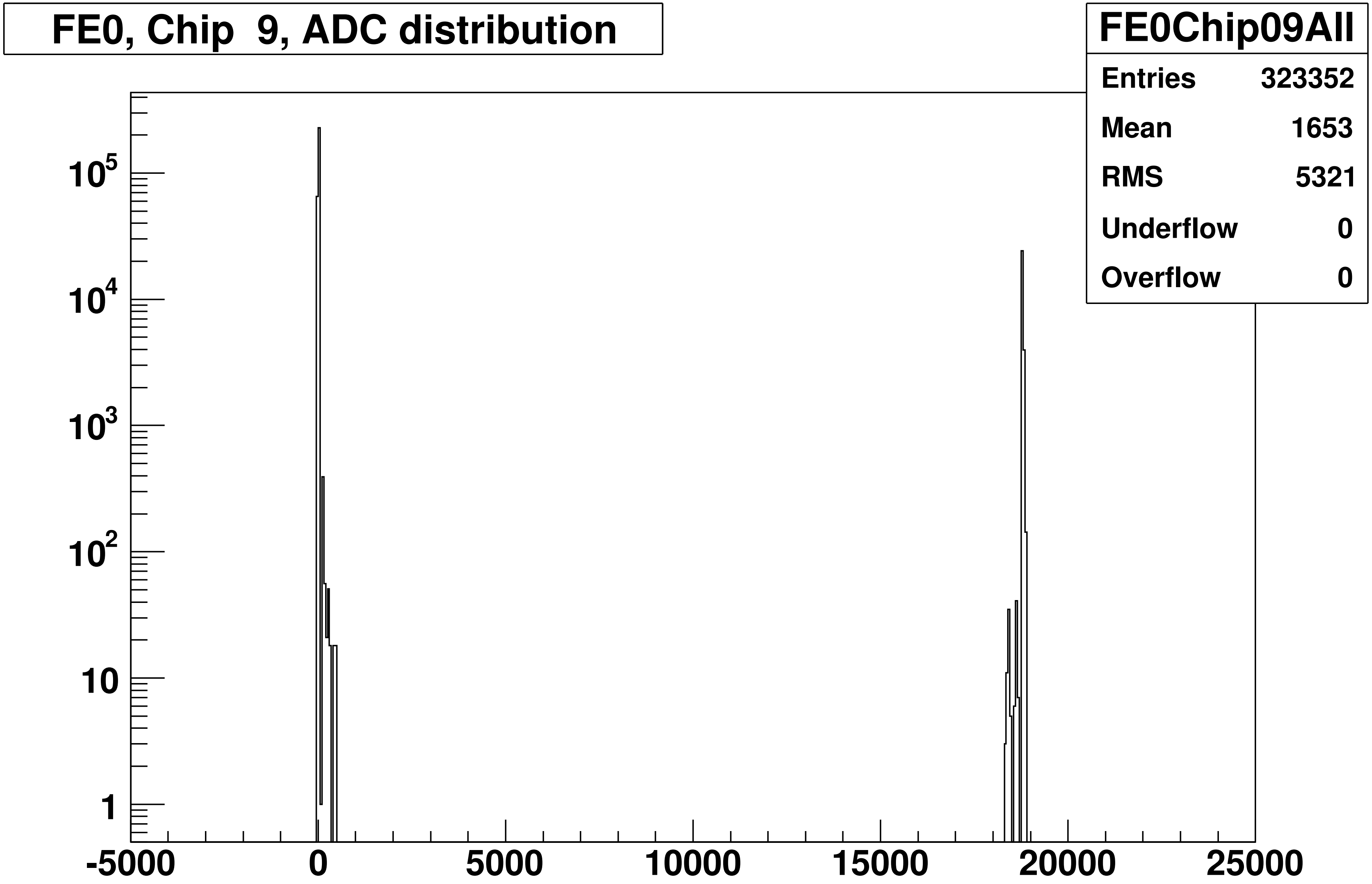

FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 323352
Mean 1660
RMS 5299
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

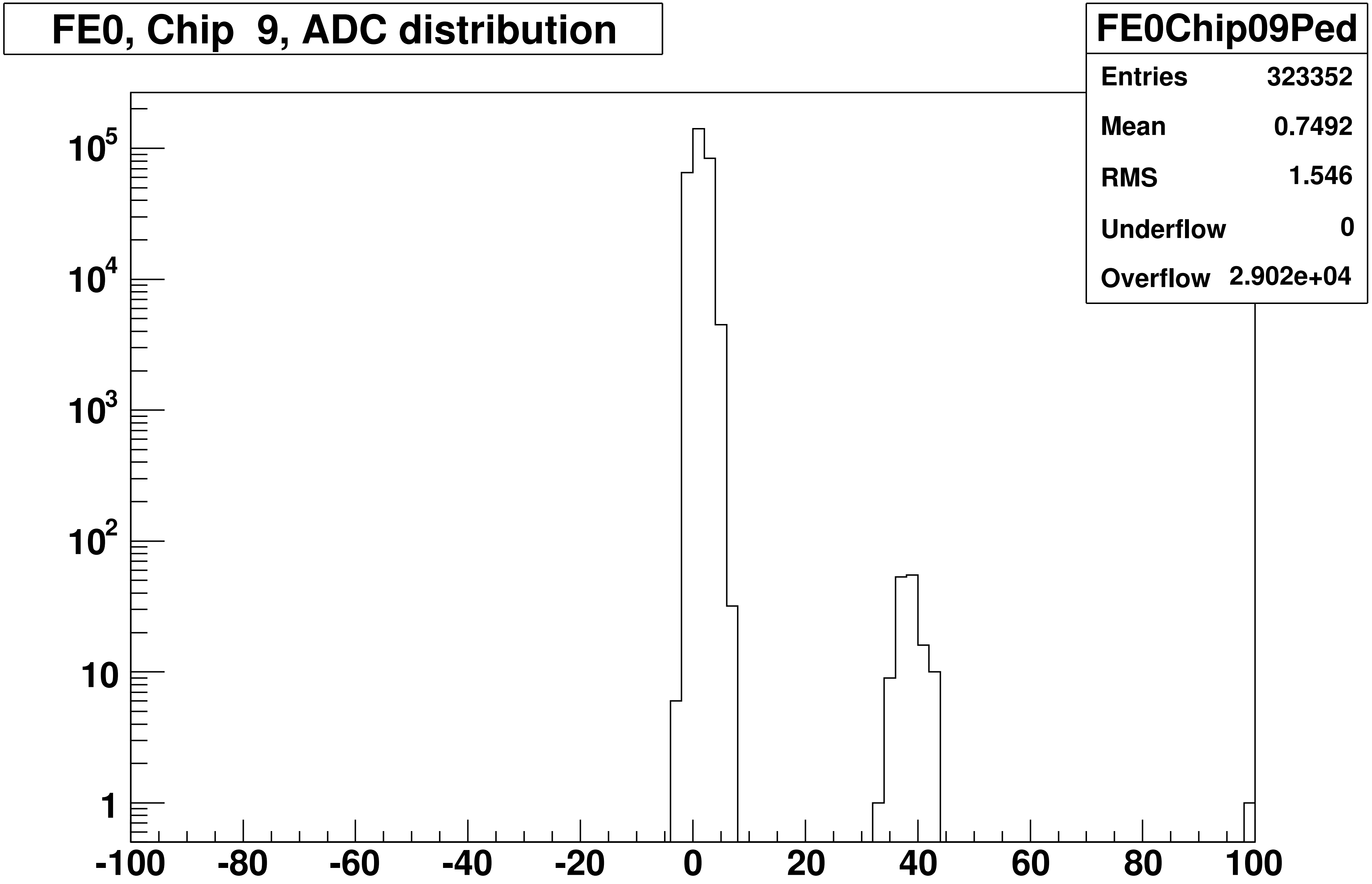

FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 323352
Mean 9.166
RMS 1.35
Underflow 0
Overflow 2.907e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 323352
Mean 1.881e+04
RMS 7.686
Underflow 3.229e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

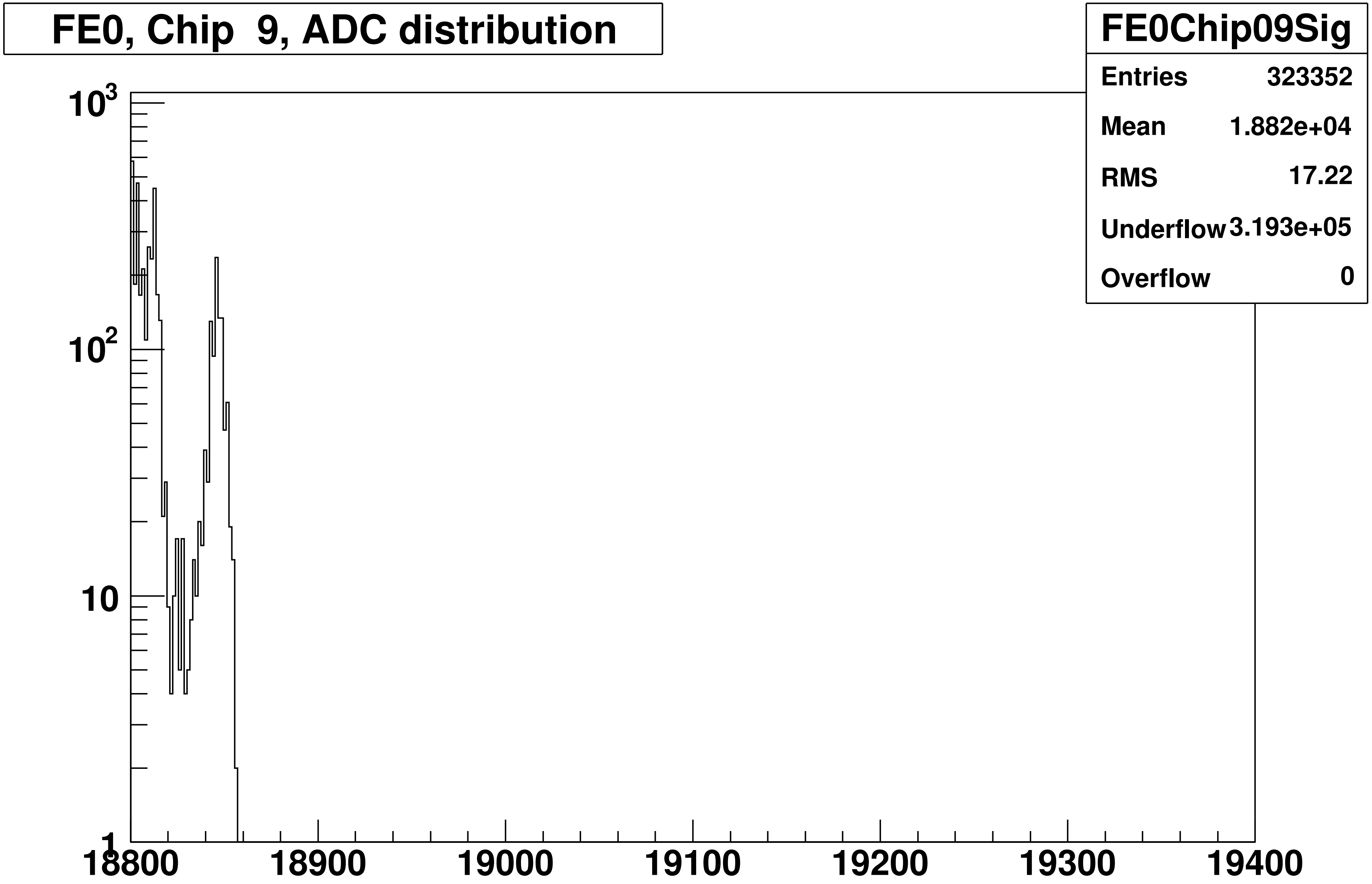

FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 323352
Mean 1679
RMS 5346
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

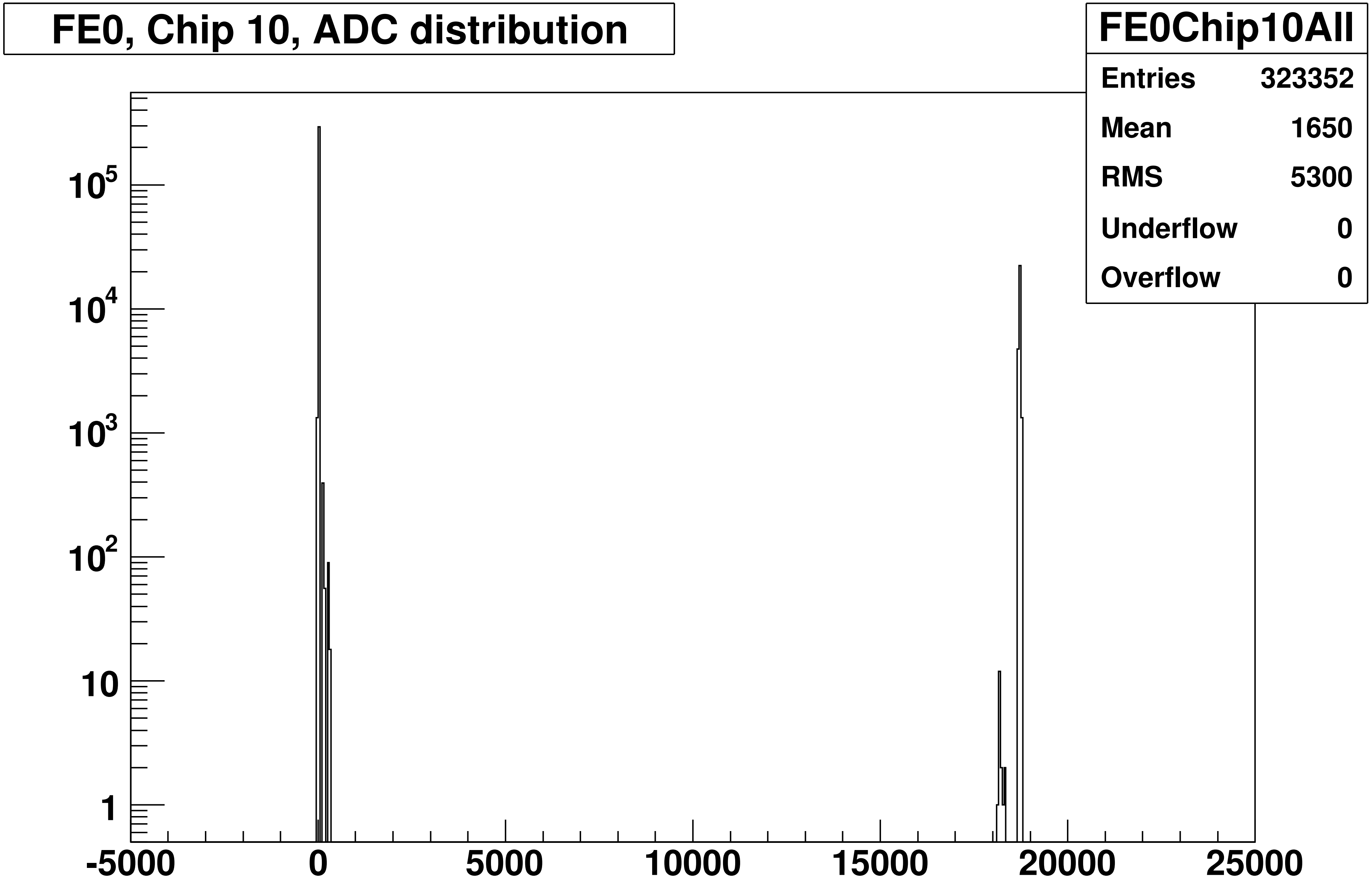

FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 323352
Mean -4.043
RMS 1.913
Underflow 0
Overflow 2.916e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

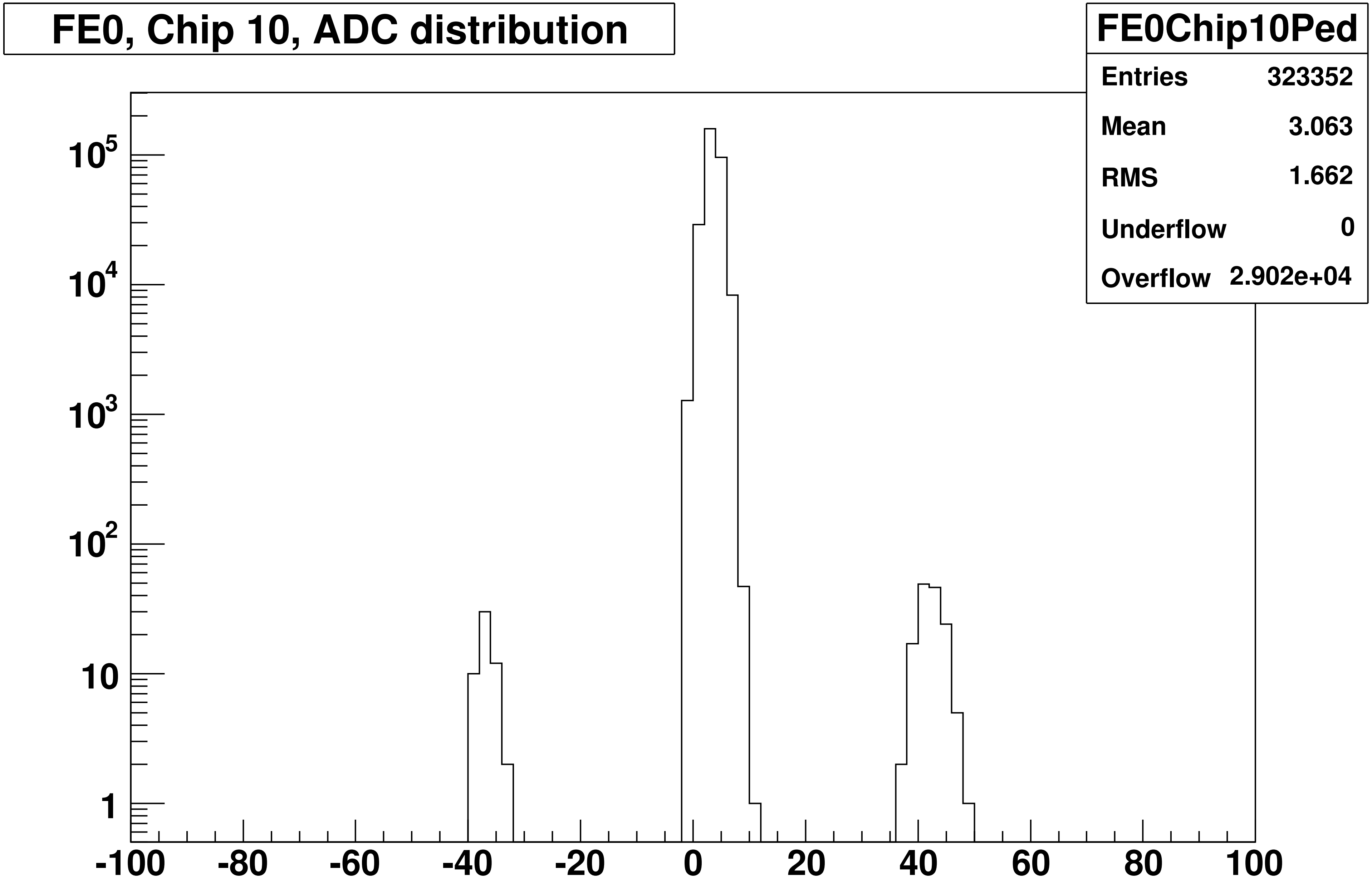

FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 323352
Mean 1653
RMS 5321
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

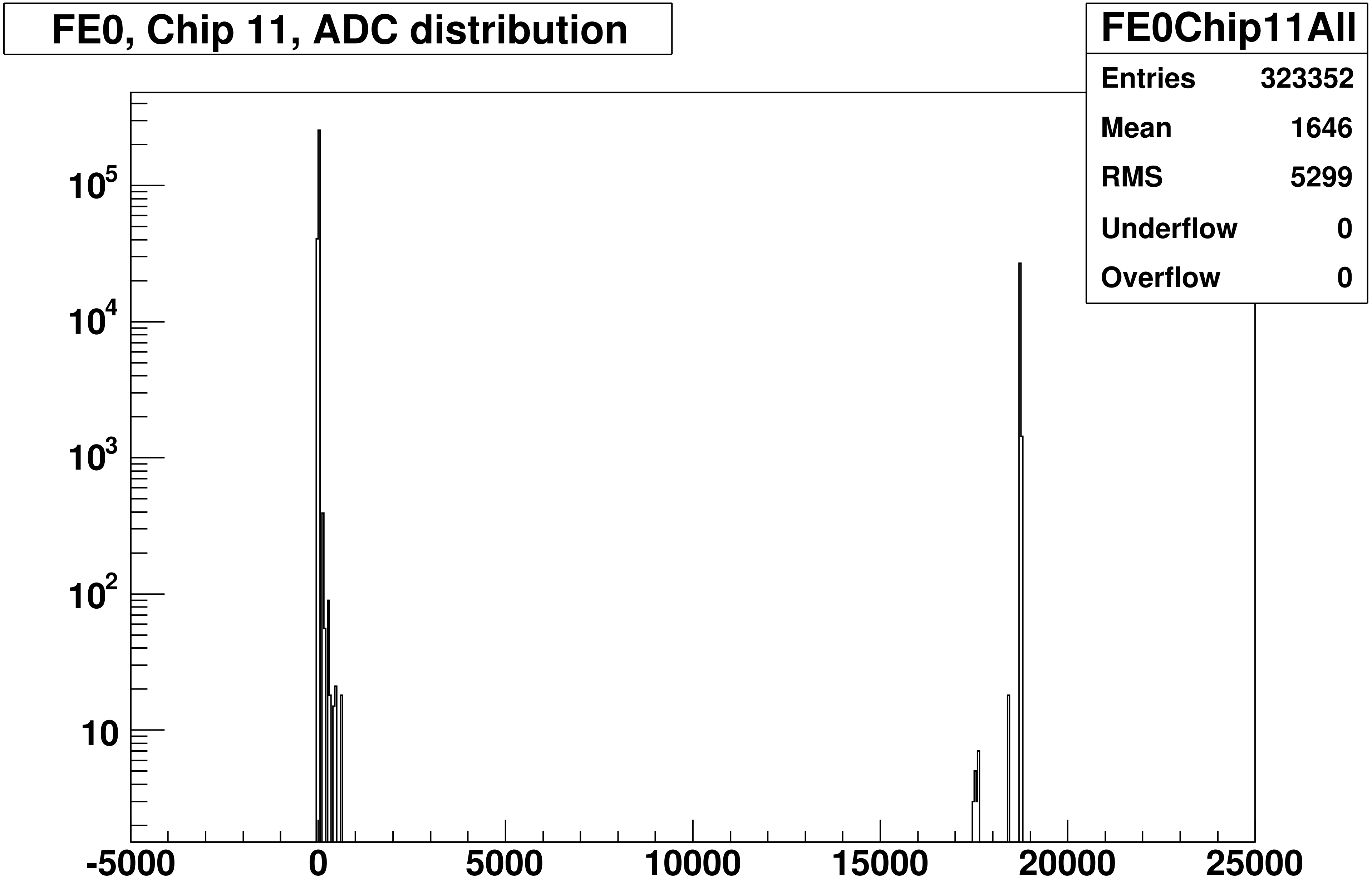


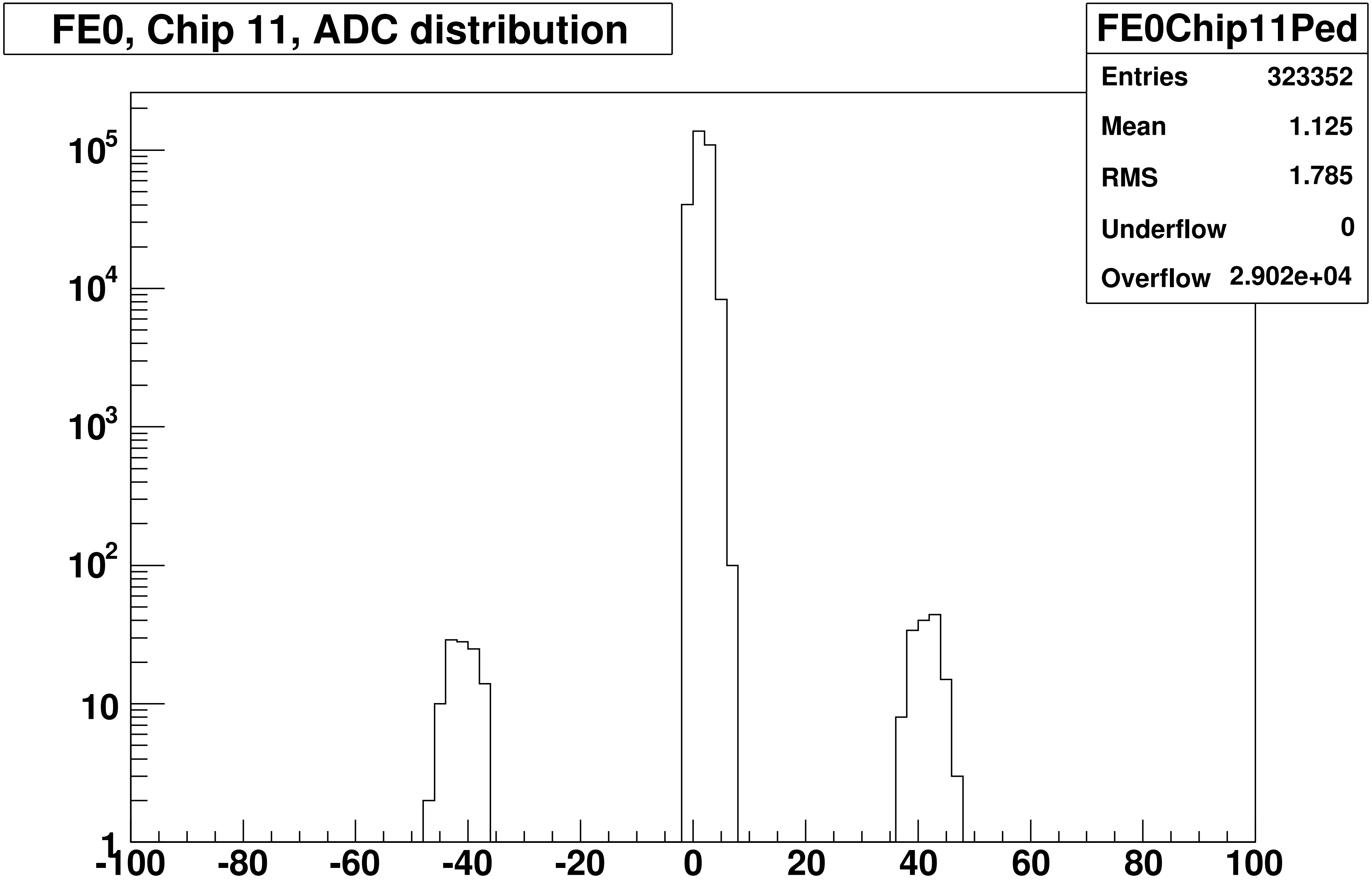
FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 323352
Mean 0.7492
RMS 1.546
Underflow 0
Overflow 2.902e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 323352
Mean 1.882e+04
RMS 17.22
Underflow 3.193e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

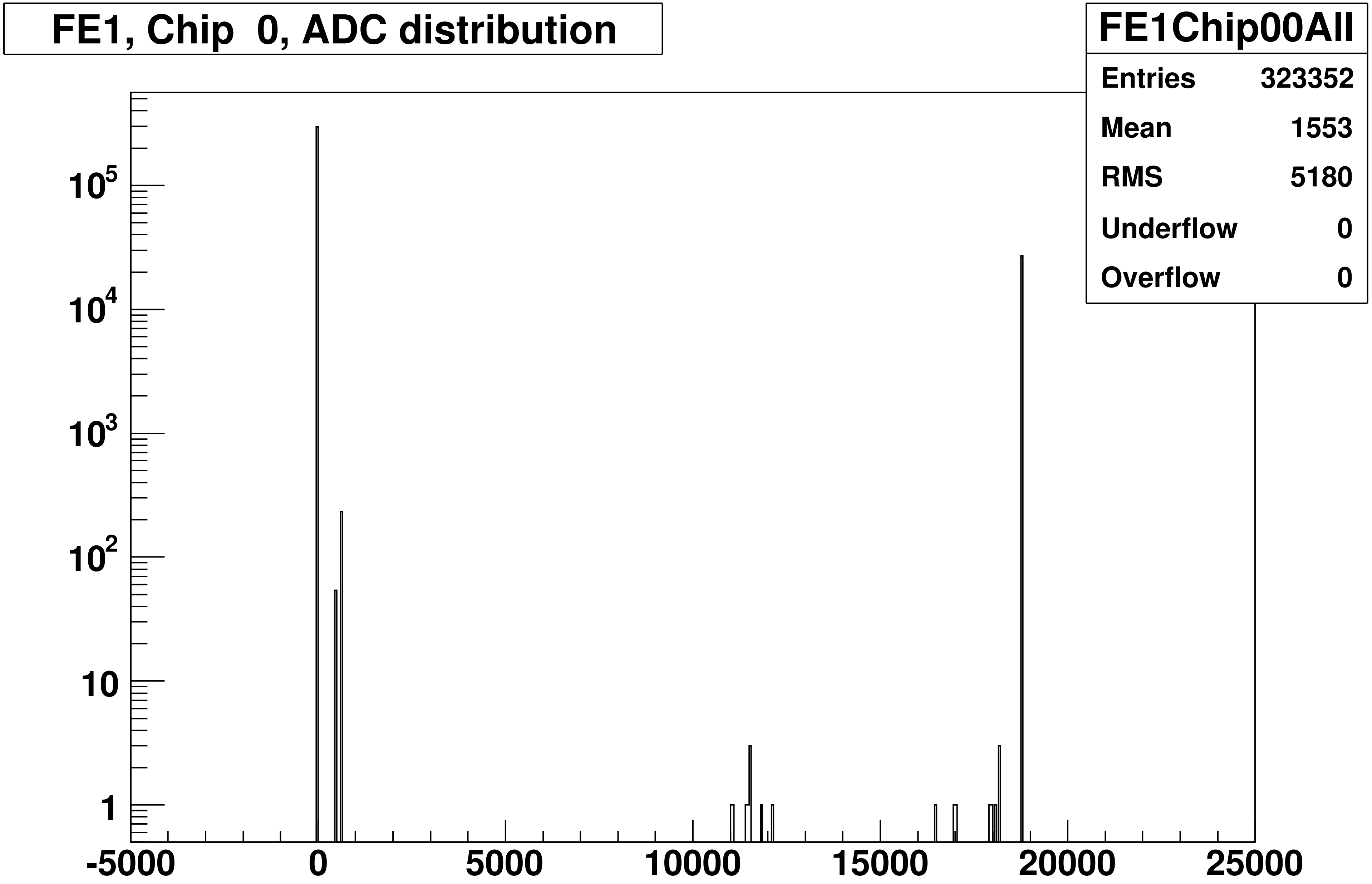

FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 323352
Mean 1650
RMS 5300
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

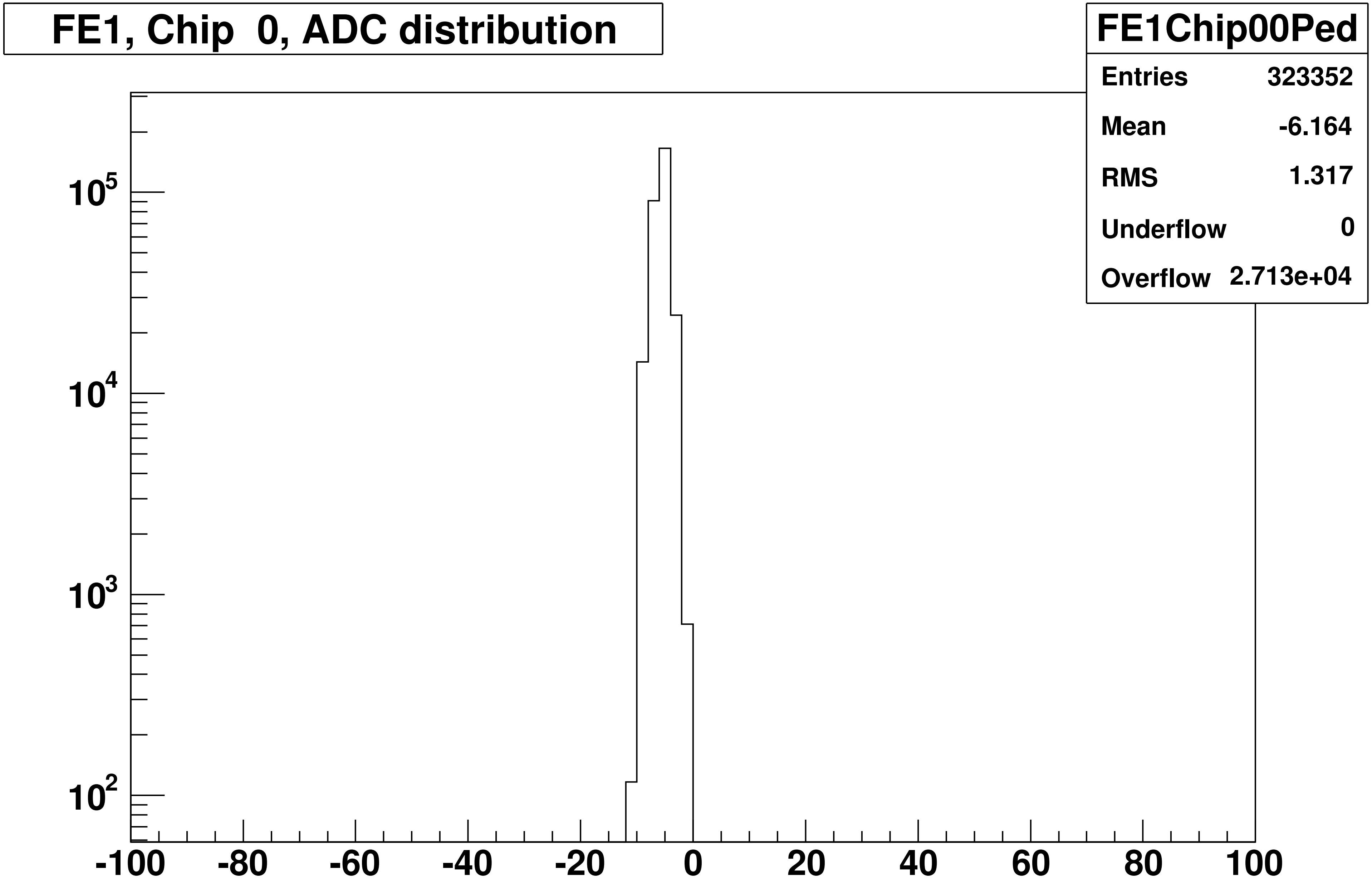

FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 323352
Mean 3.063
RMS 1.662
Underflow 0
Overflow 2.902e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

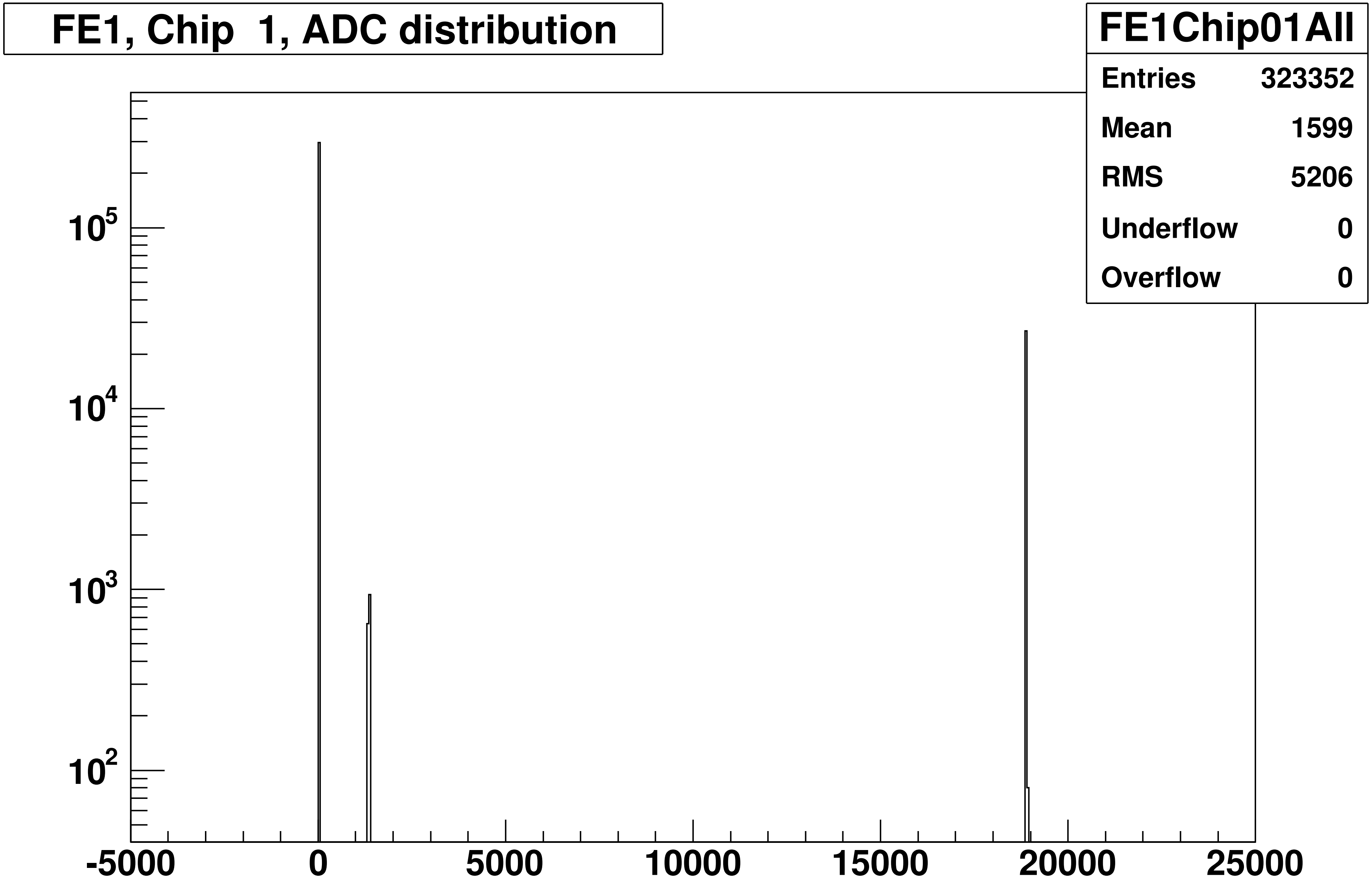

FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 323352
Mean 1646
RMS 5299
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

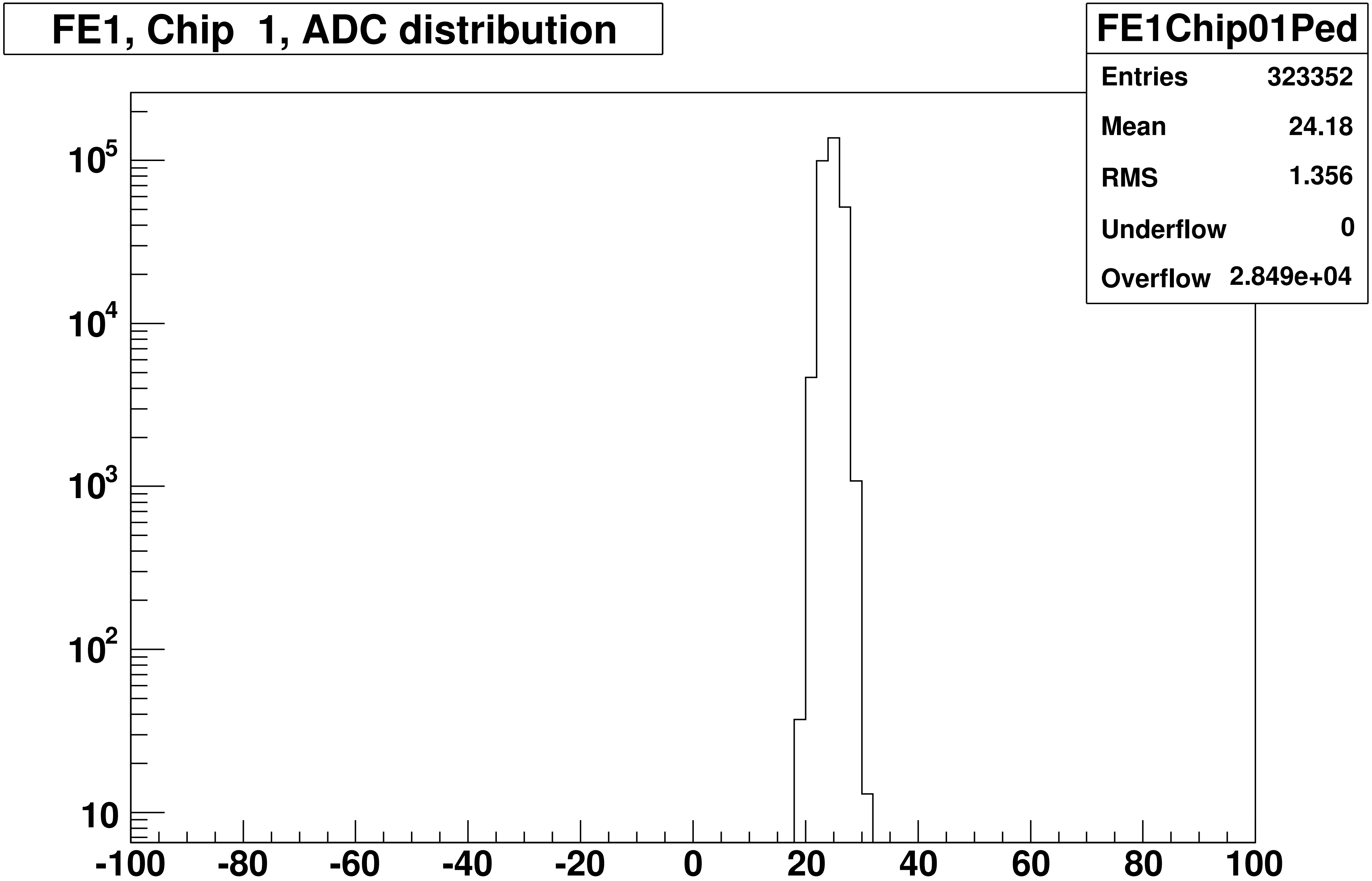


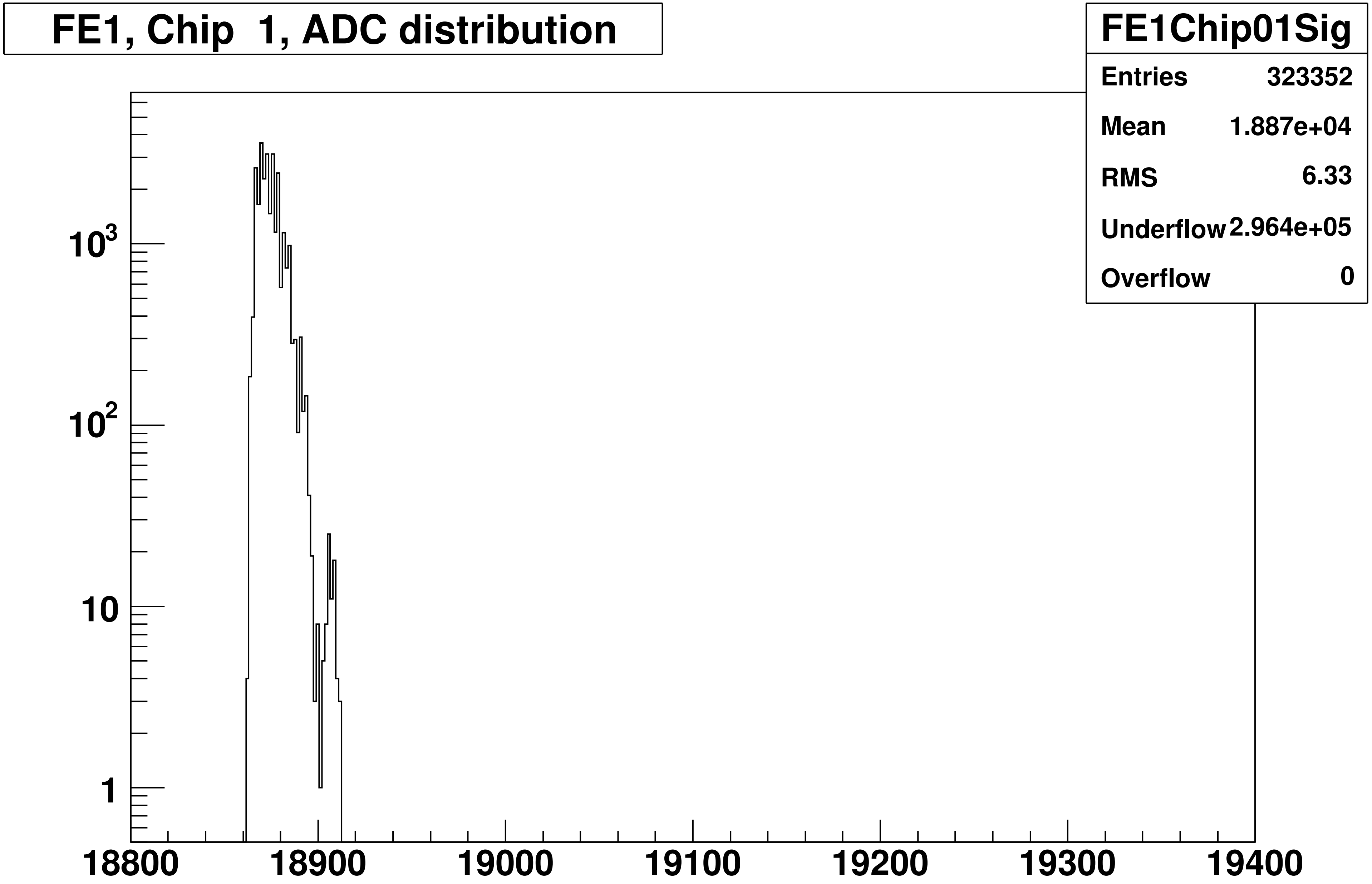
FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 323352
Mean 1.125
RMS 1.785
Underflow 0
Overflow 2.902e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 323352
Mean 1553
RMS 5180
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

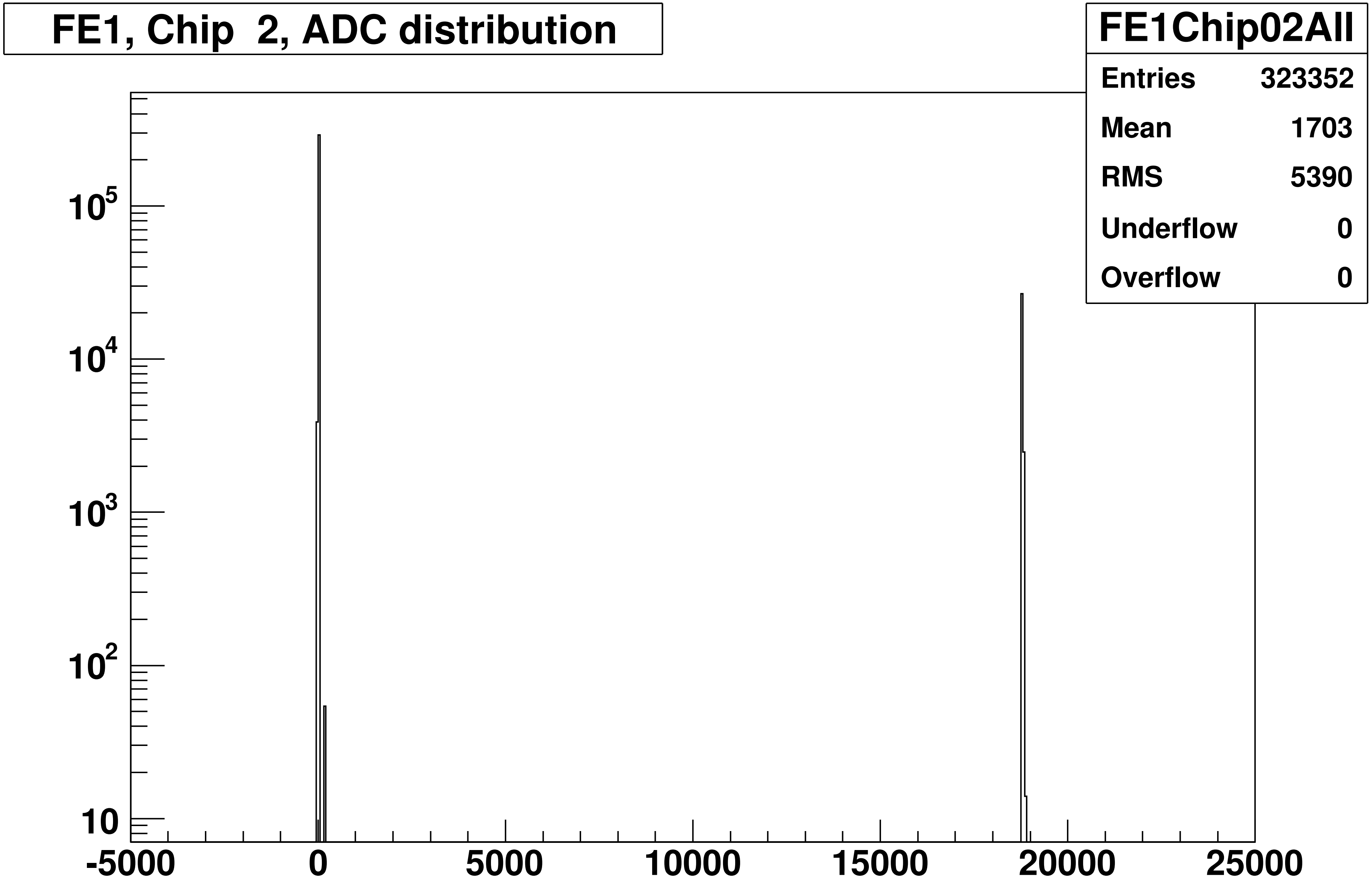

FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 323352
Mean -6.164
RMS 1.317
Underflow 0
Overflow 2.713e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

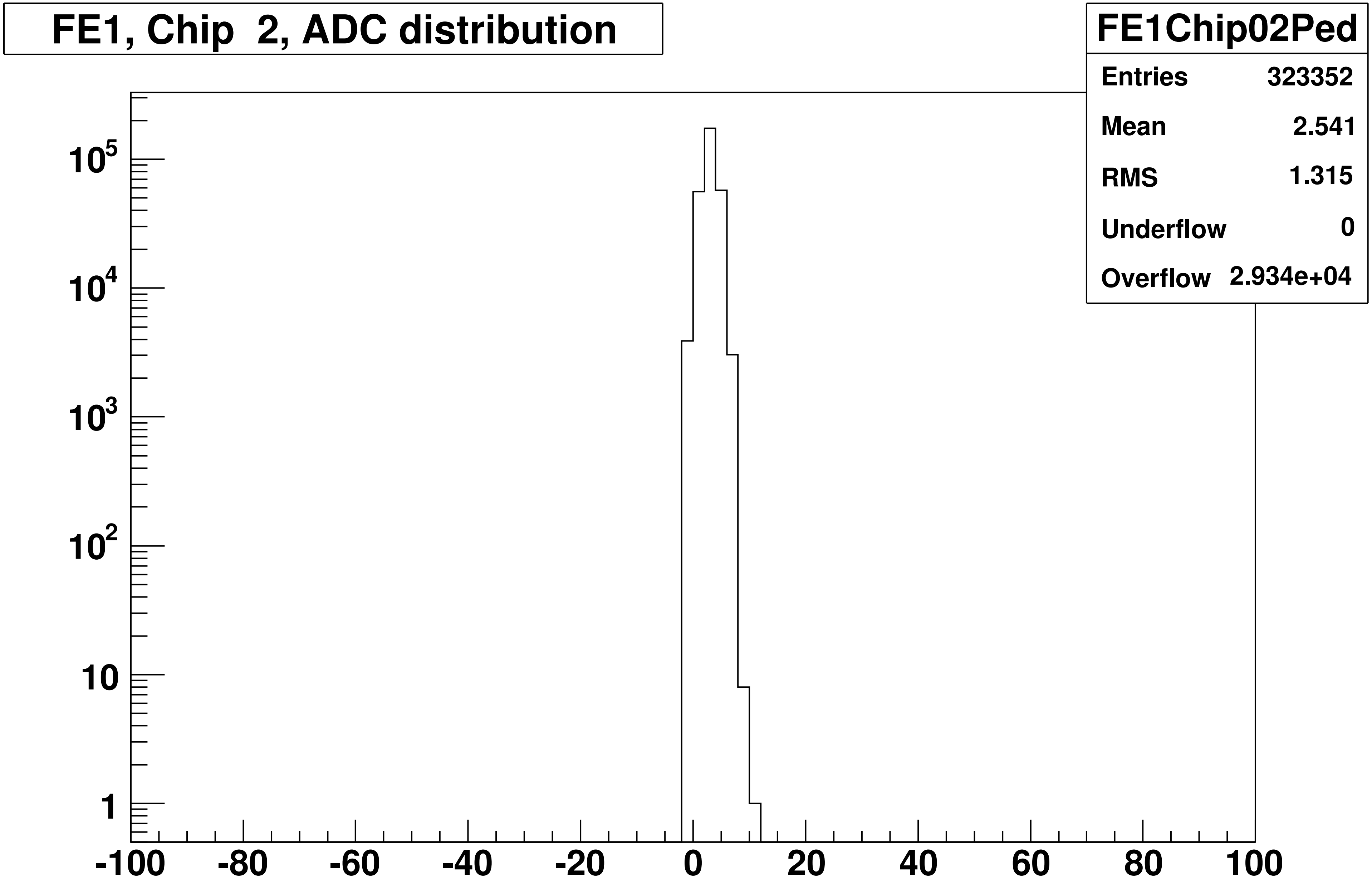

FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 323352
Mean 1599
RMS 5206
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

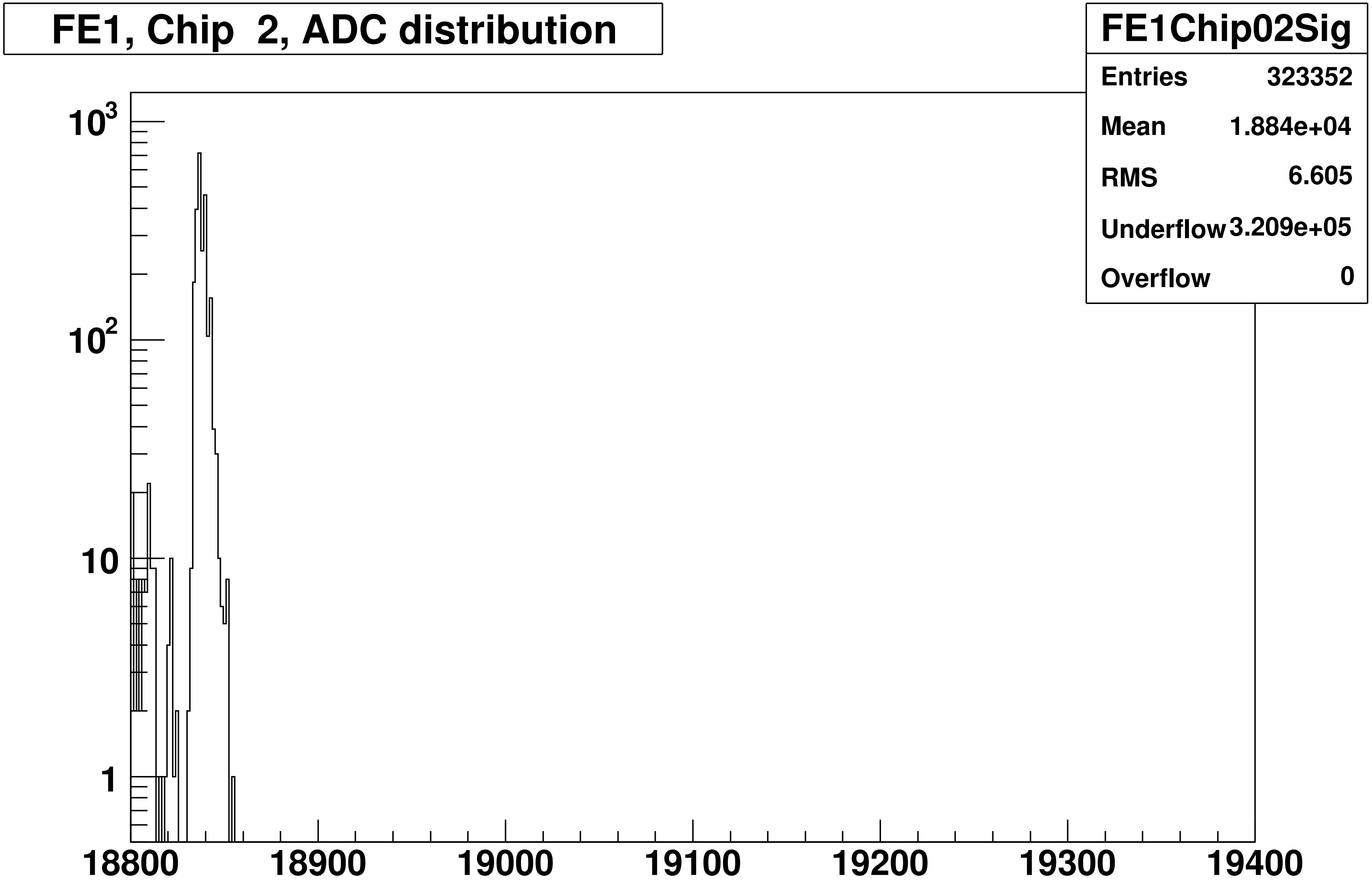

FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 323352
Mean 24.18
RMS 1.356
Underflow 0
Overflow 2.849e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

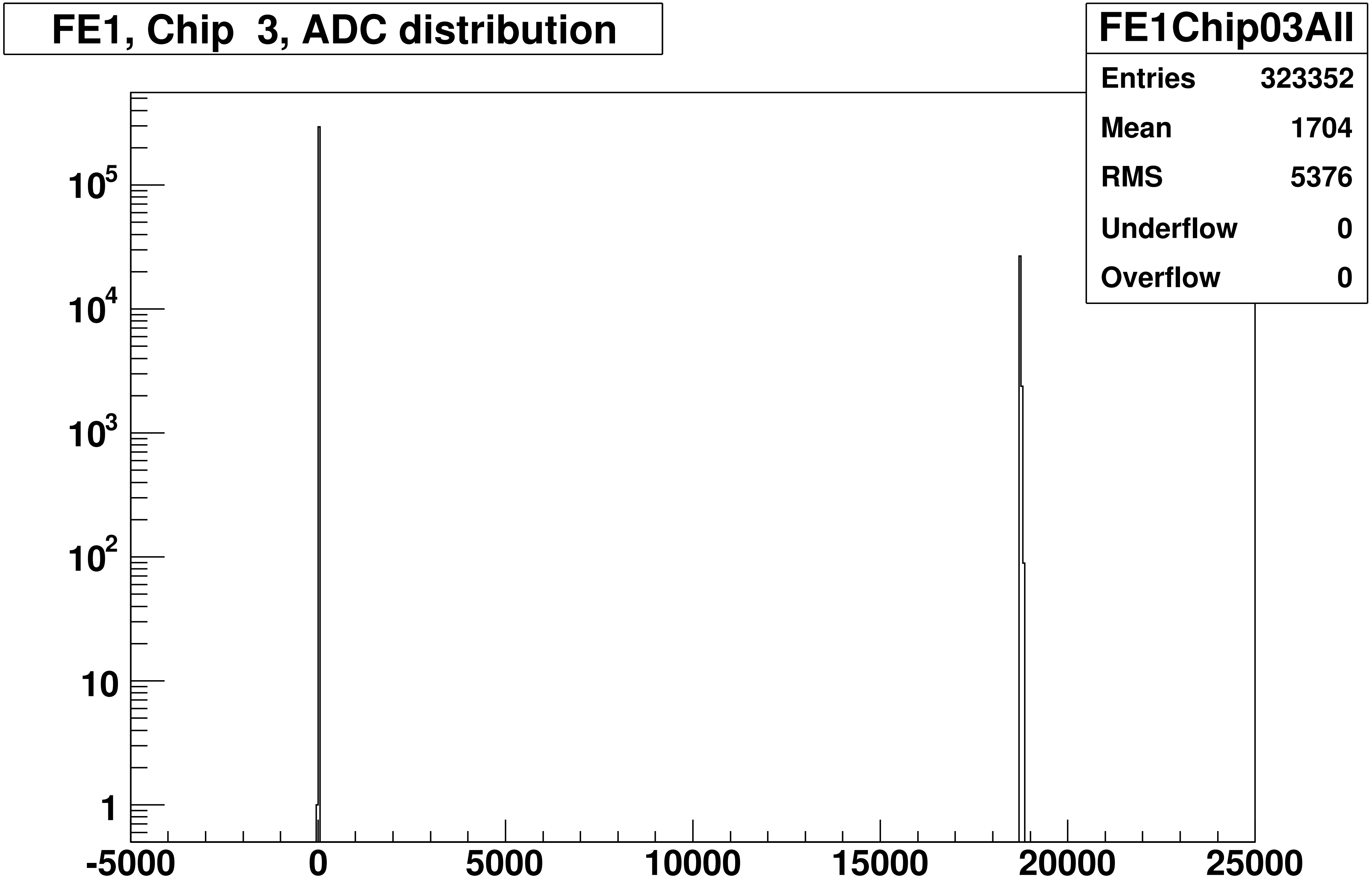


FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 323352
Mean 1.887e+04
RMS 6.33
Underflow 2.964e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

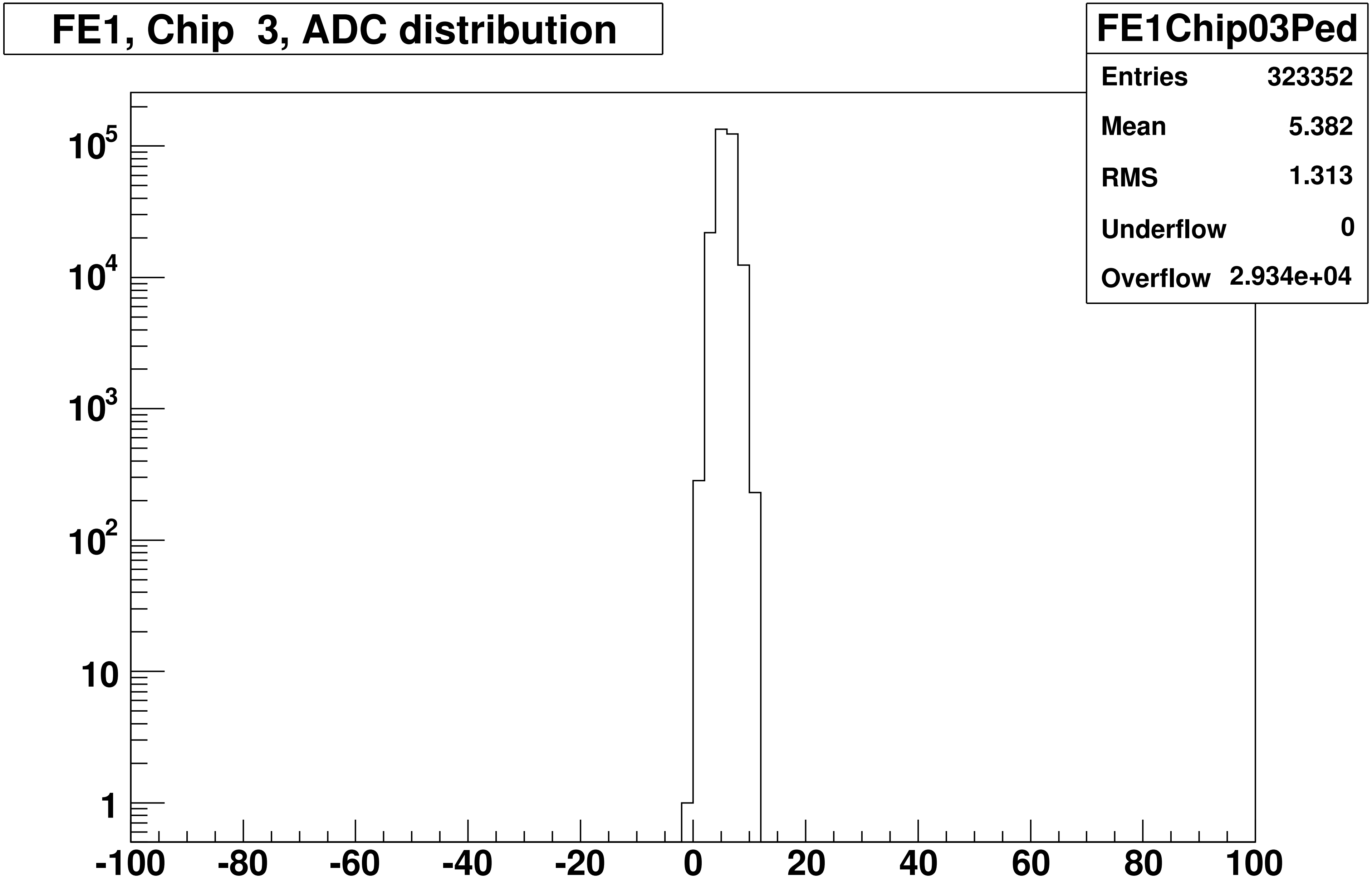

FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 323352
Mean 1703
RMS 5390
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

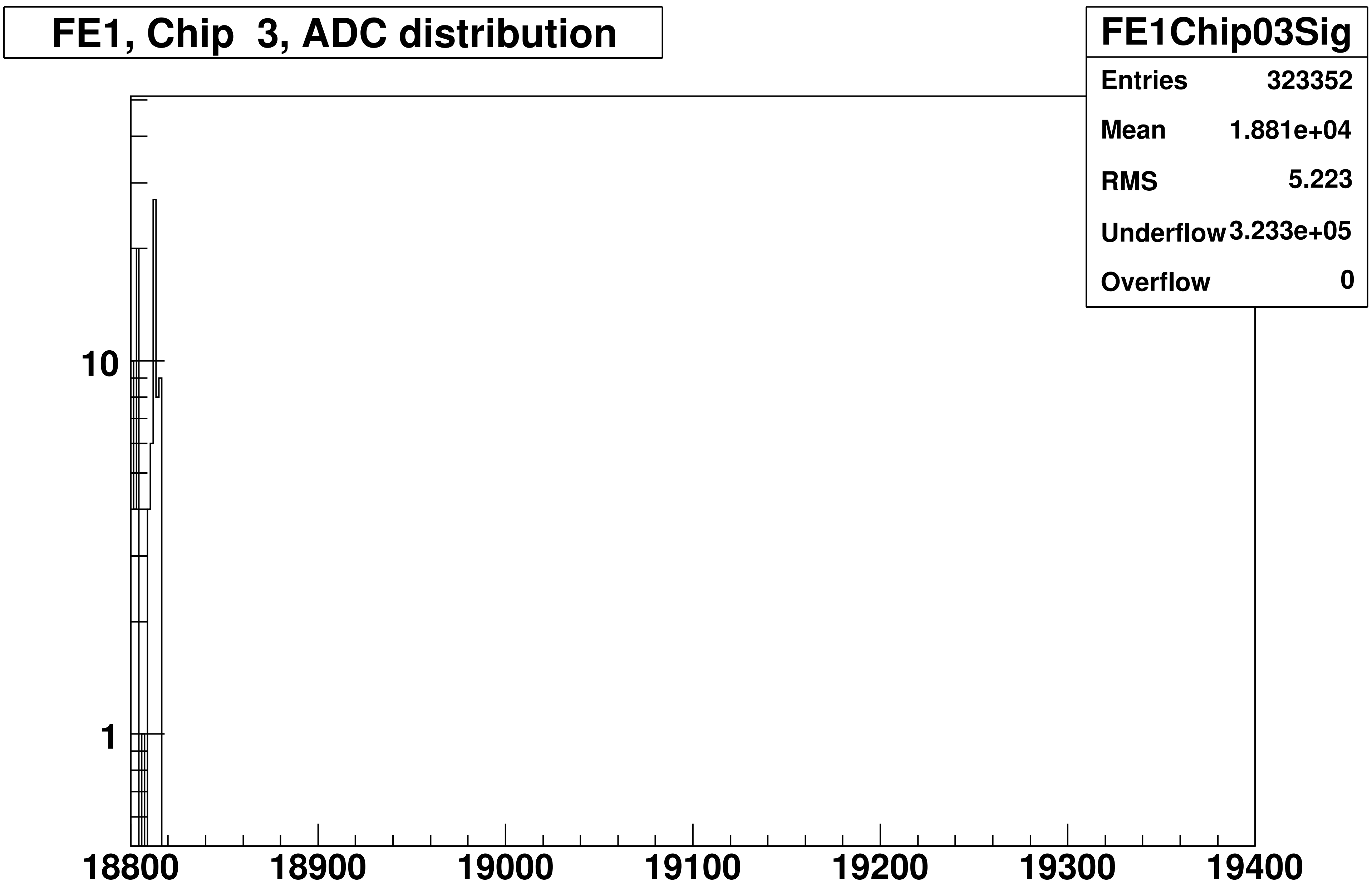

FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 323352
Mean 2.541
RMS 1.315
Underflow 0
Overflow 2.934e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 323352
Mean 1.884e+04
RMS 6.605
Underflow 3.209e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

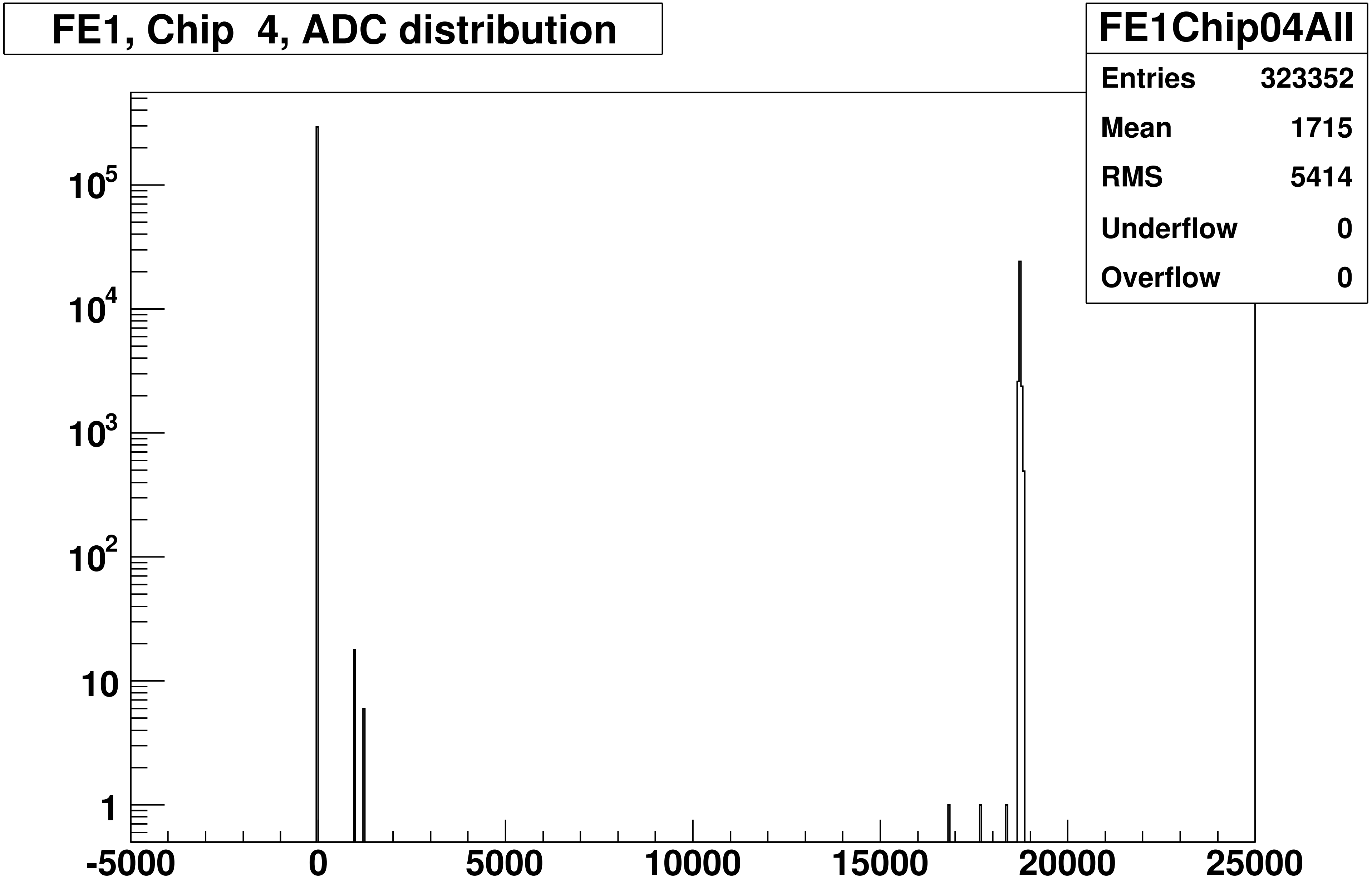


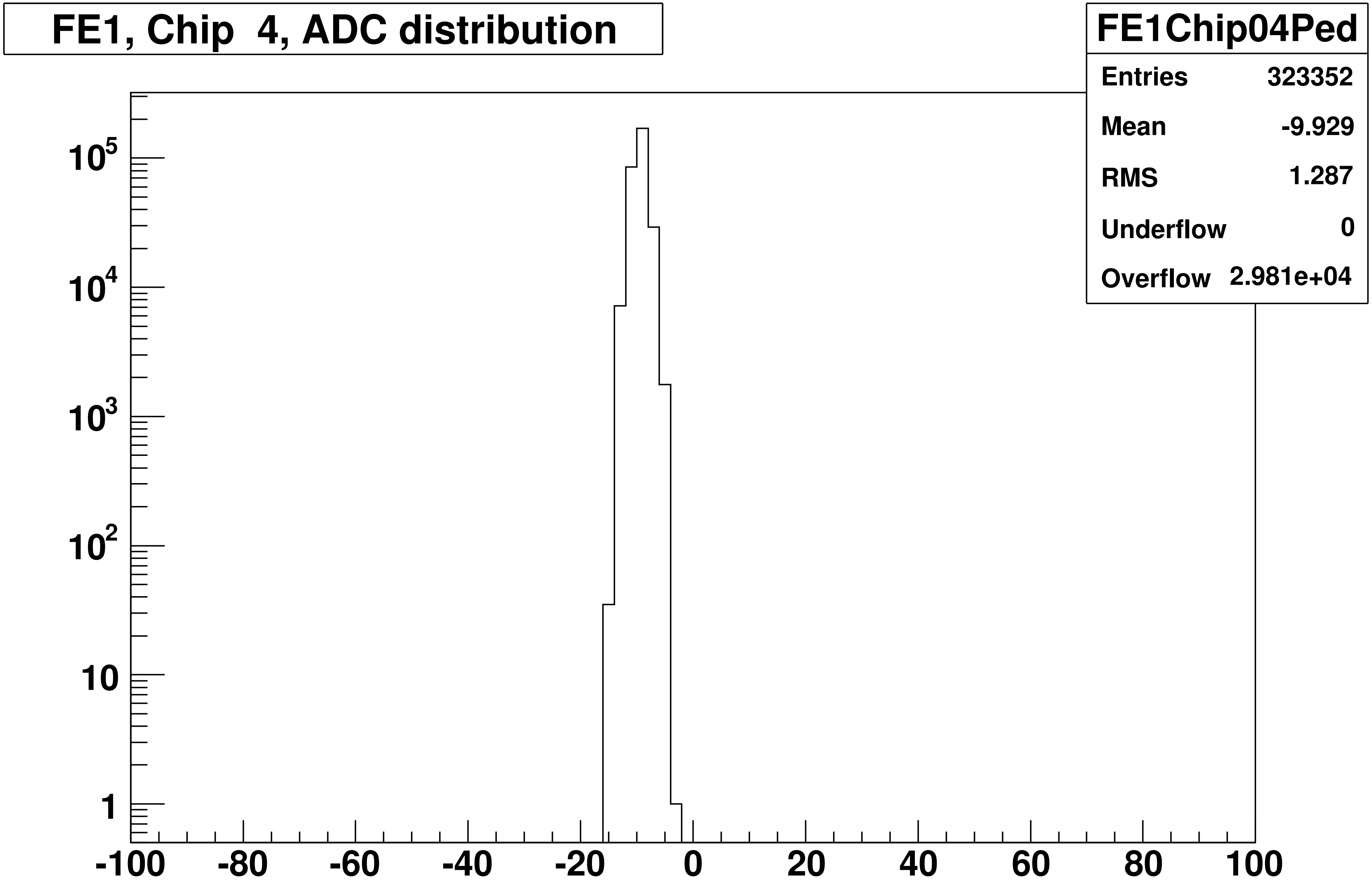
FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 323352
Mean 1704
RMS 5376
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

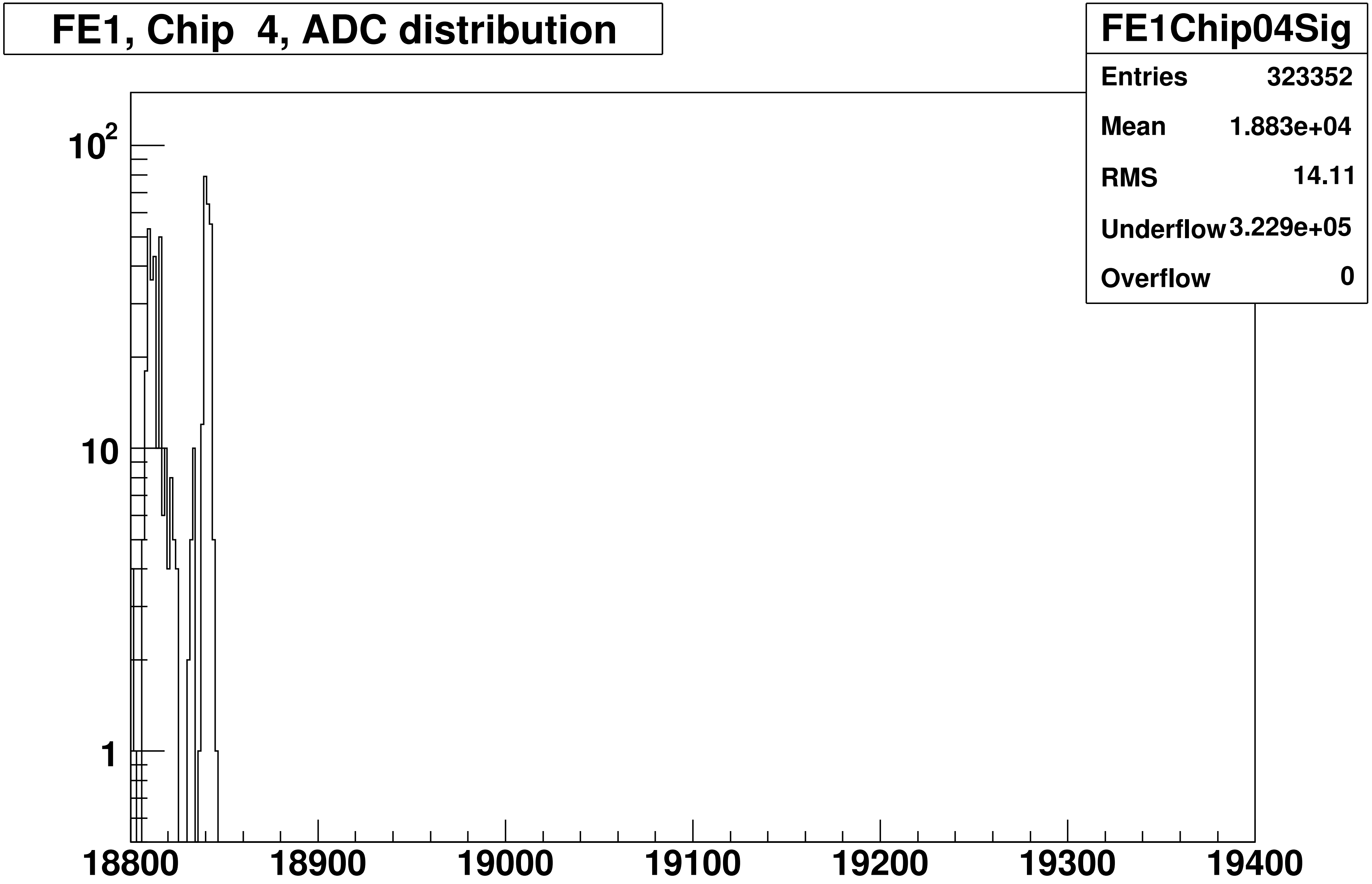

FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 323352
Mean 5.382
RMS 1.313
Underflow 0
Overflow 2.934e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

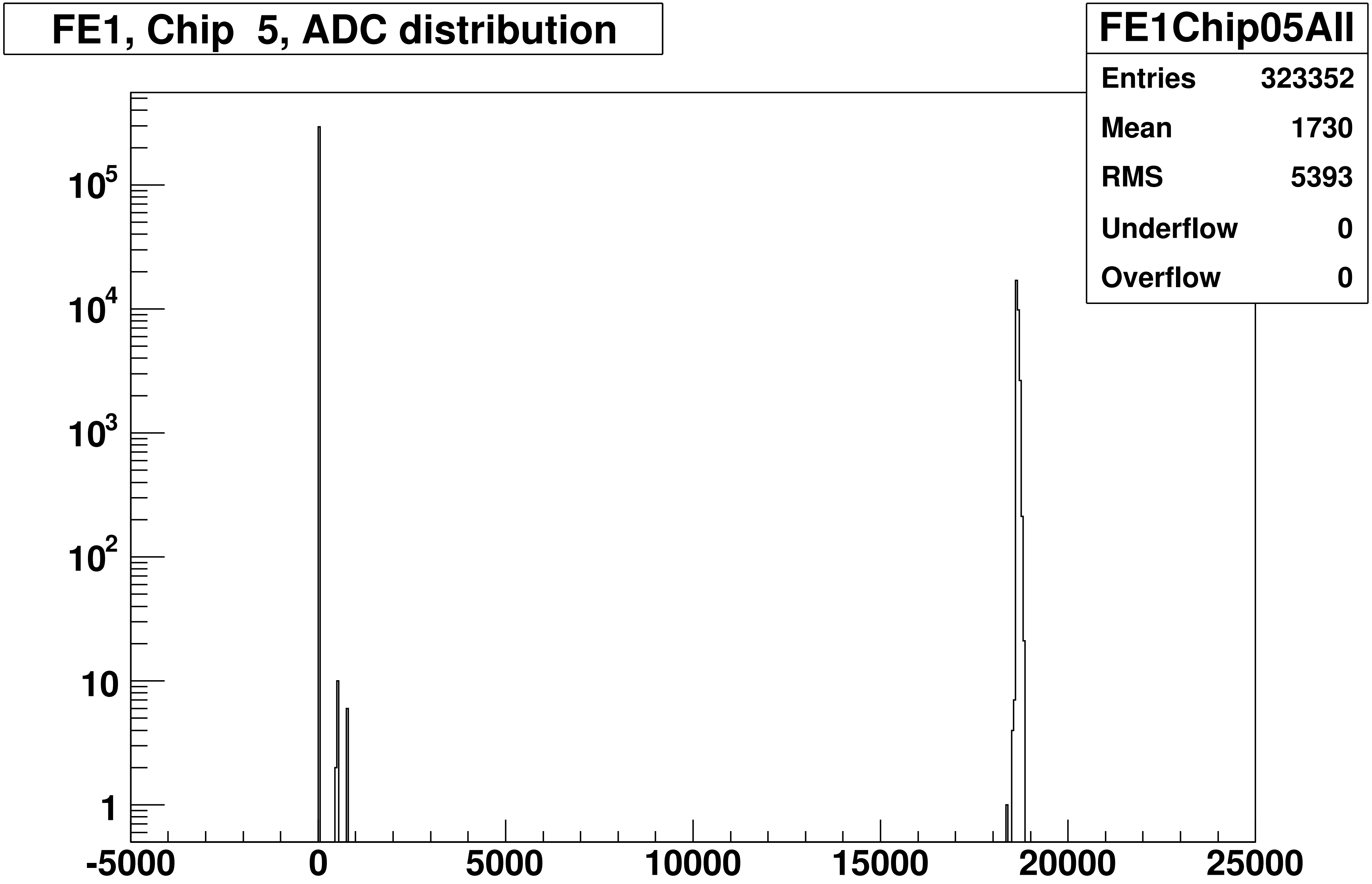


FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 323352
Mean 1.881e+04
RMS 5.223
Underflow 3.233e+05
Overflow 0
10
1
18800
18900
19000
19100
19200
19300
19400

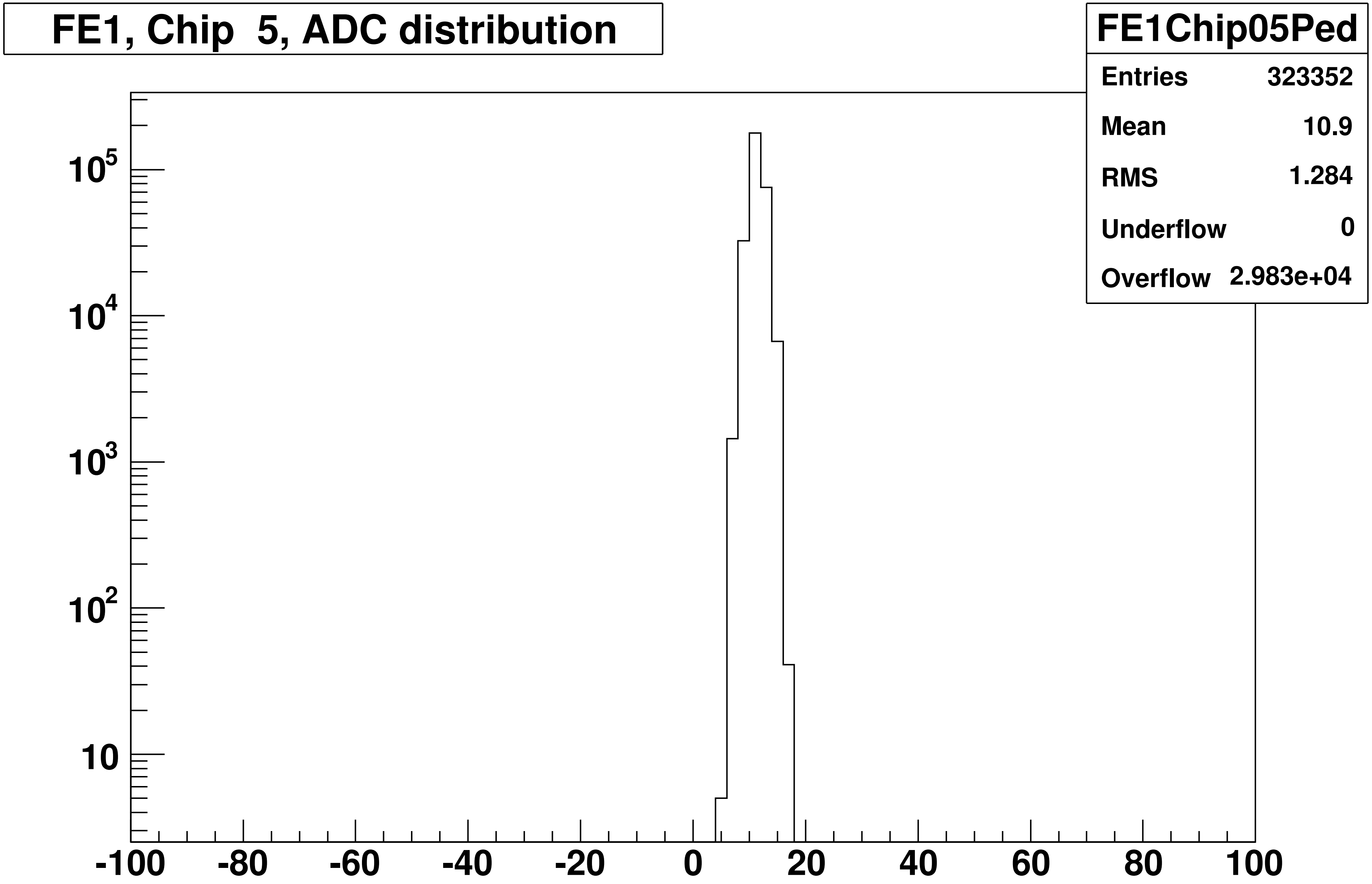

FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 323352
Mean 1715
RMS 5414
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

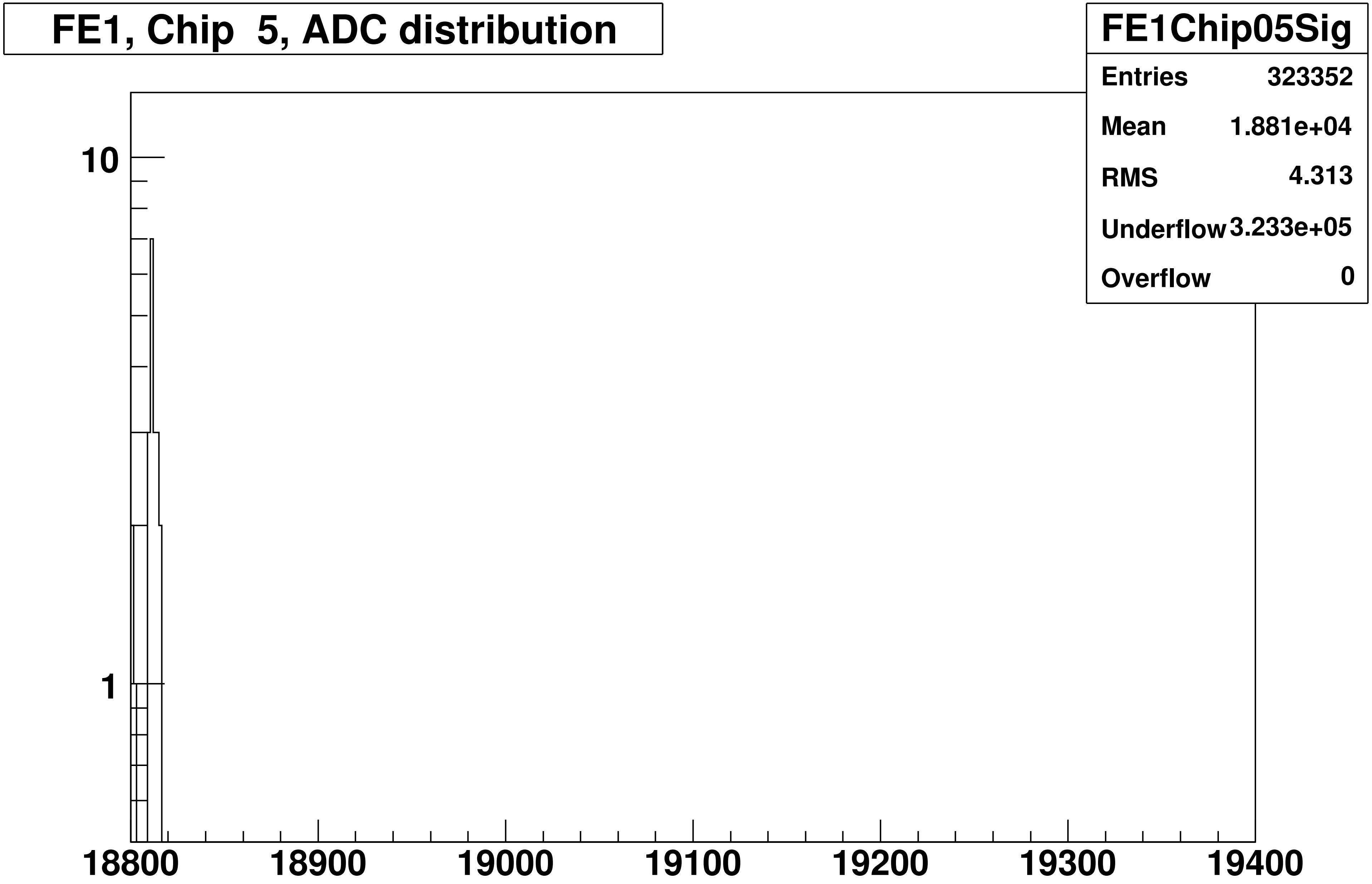

FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 323352
Mean -9.929
RMS 1.287
Underflow 0
Overflow 2.981e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

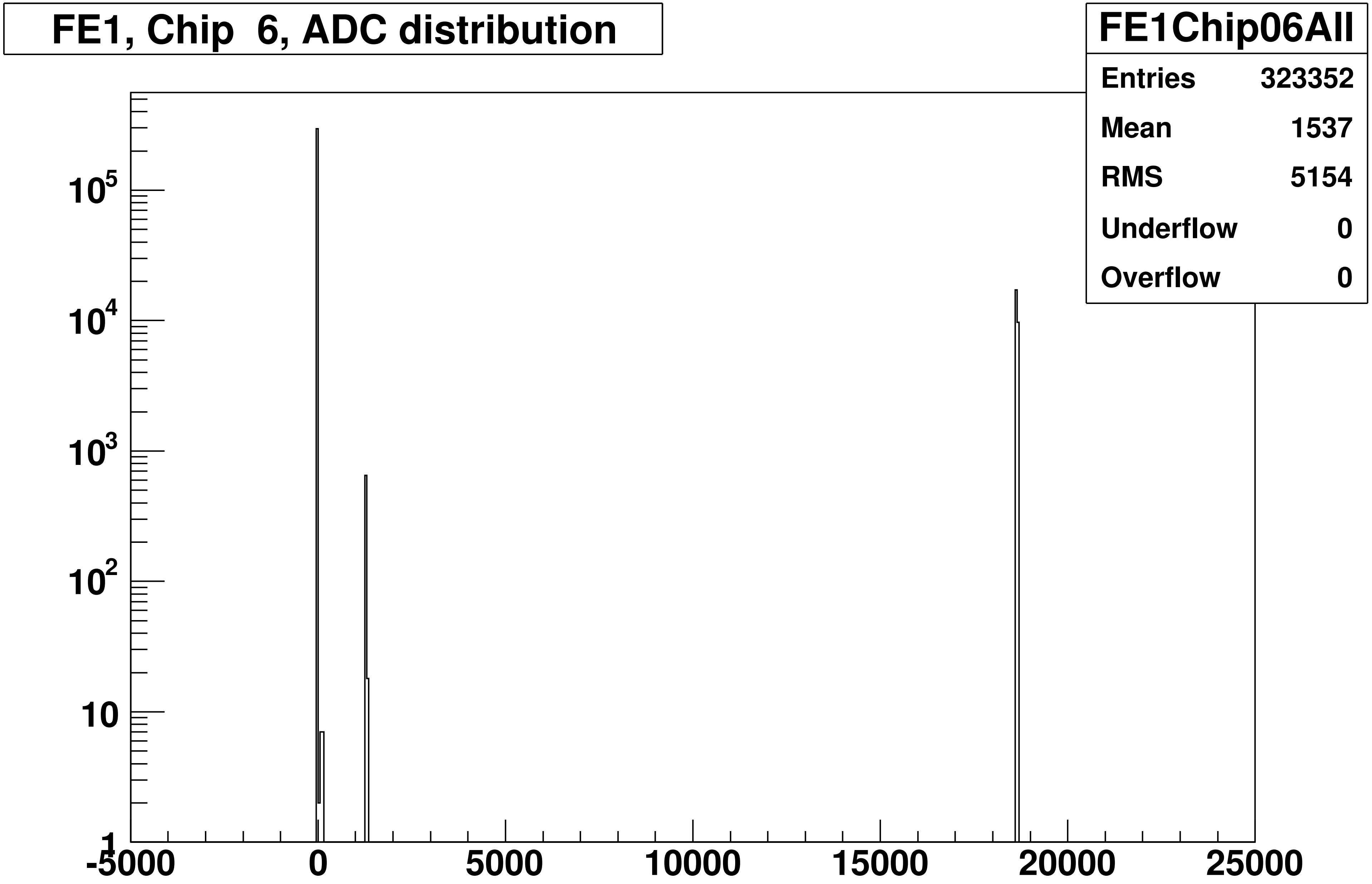

FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 323352
Mean 1.883e+04
RMS 14.11
Underflow 3.229e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

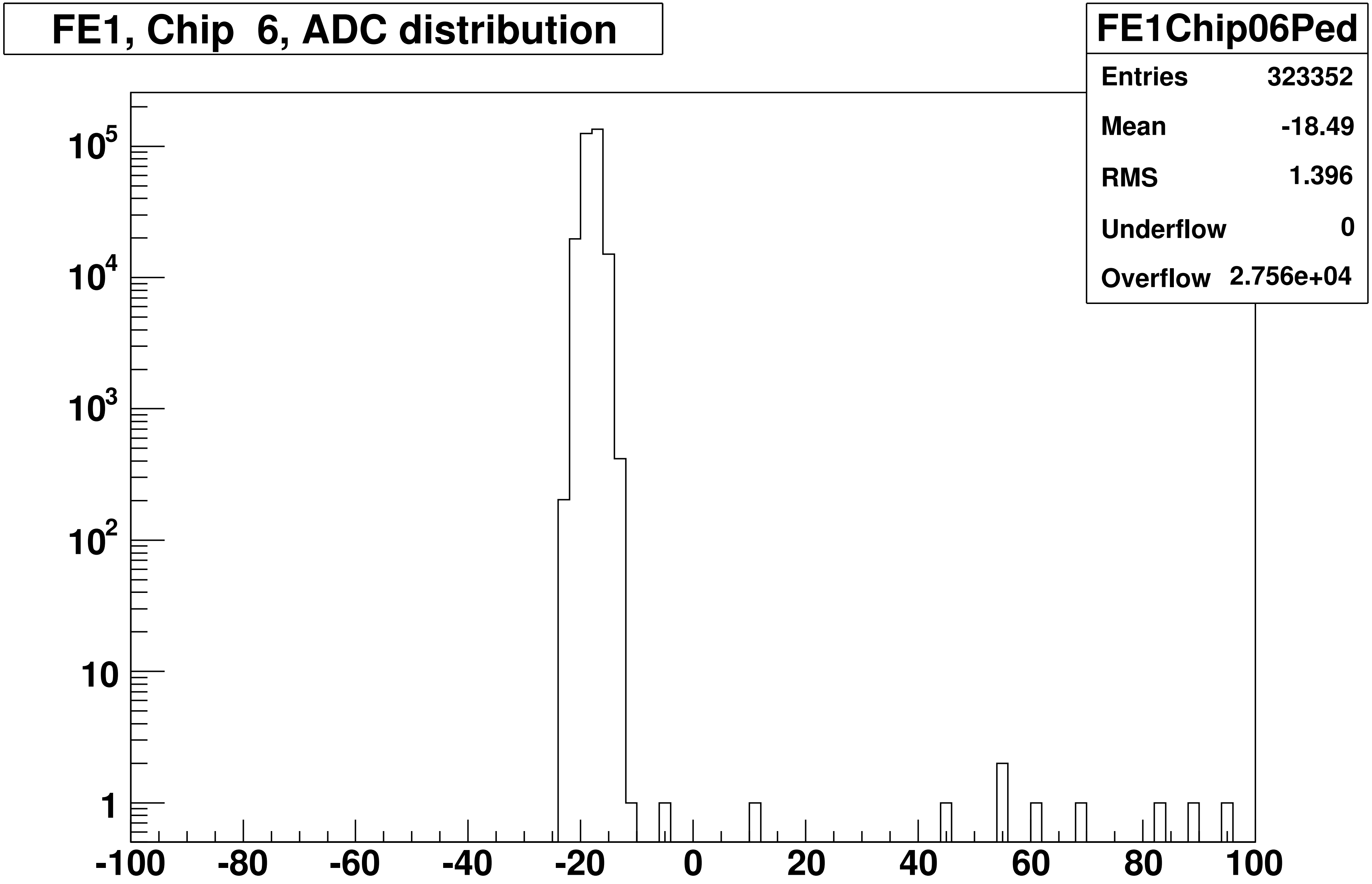


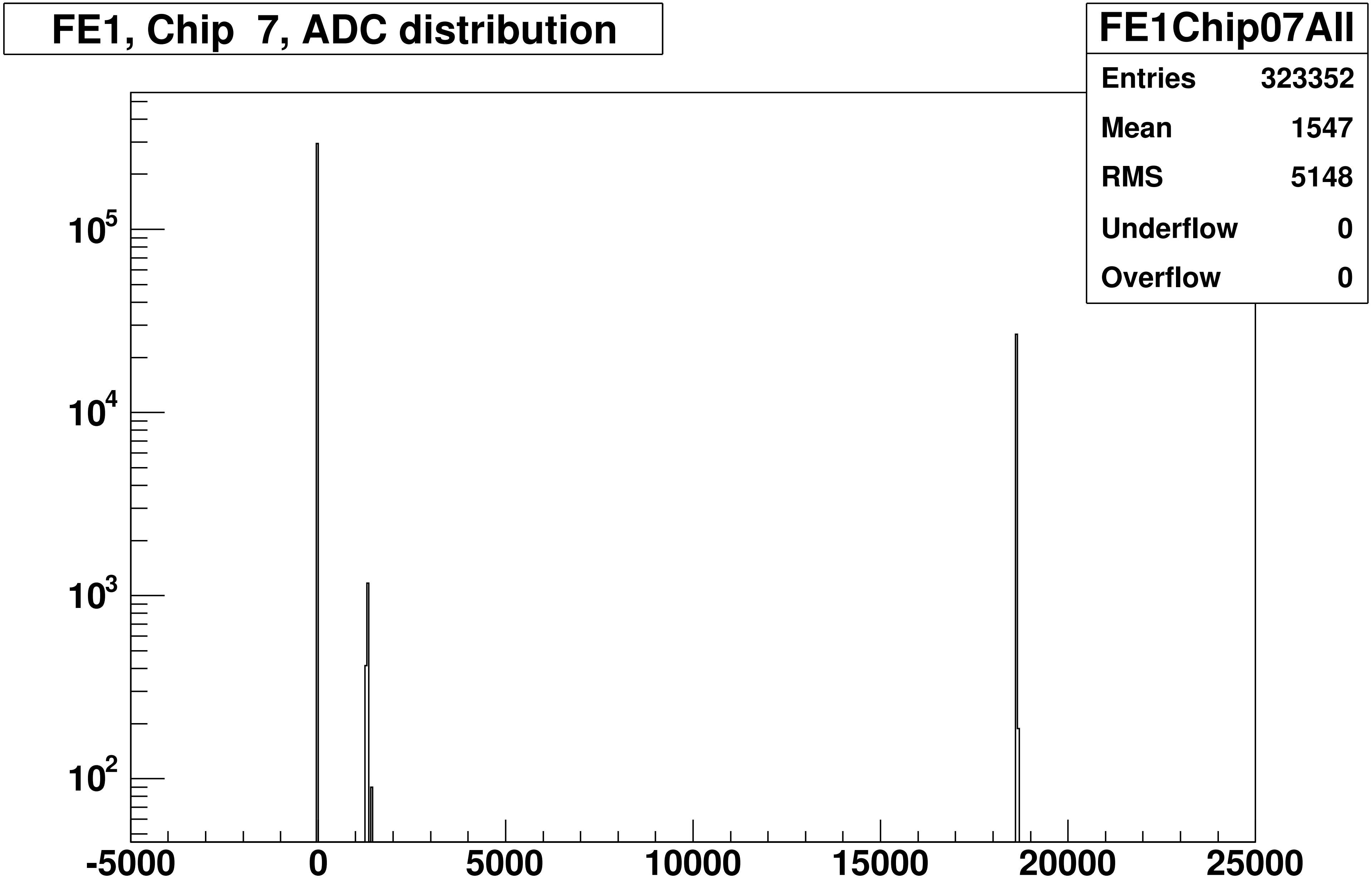
FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 323352
Mean 1730
RMS 5393
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

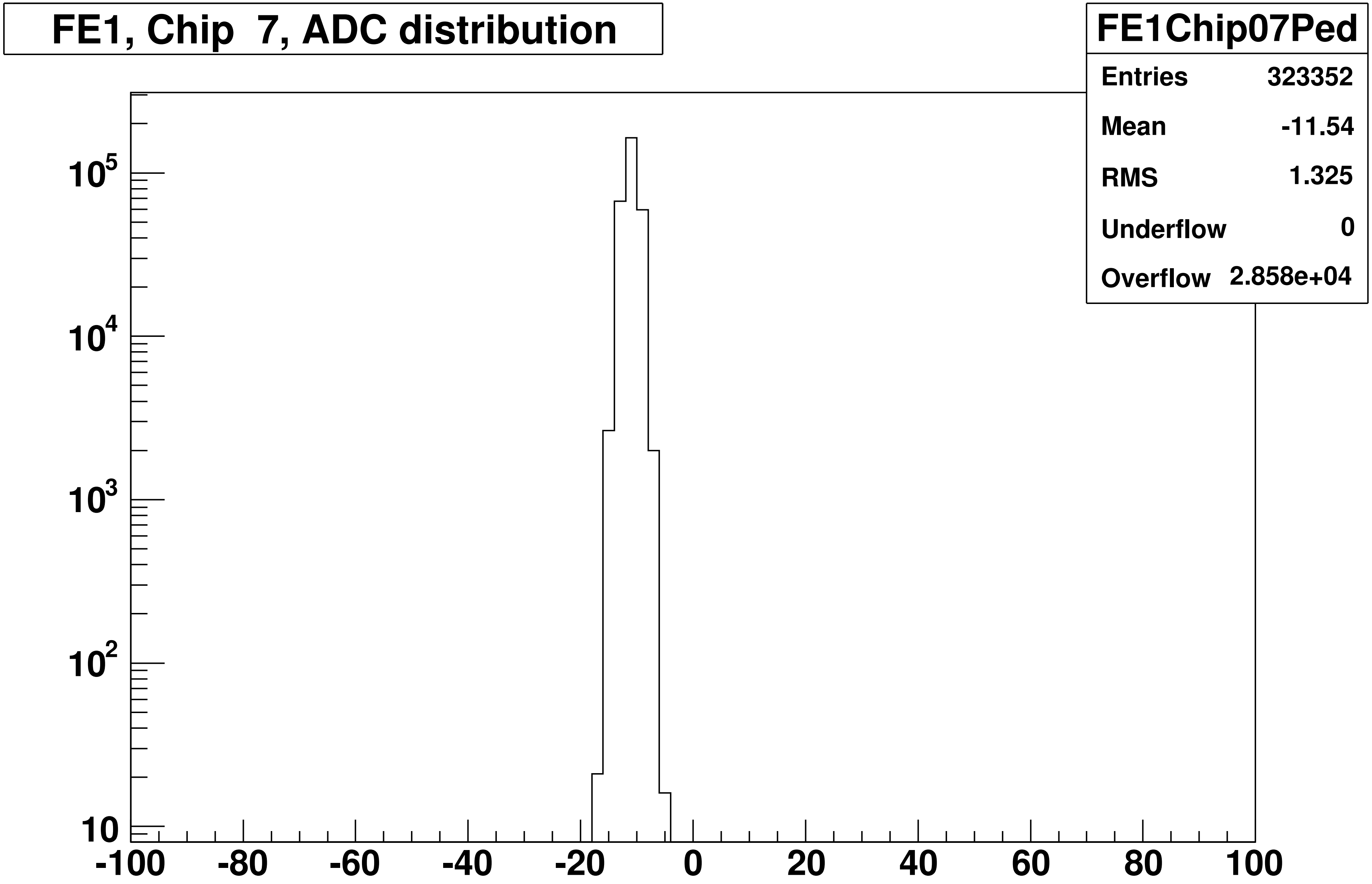

FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 323352
Mean 10.9
RMS 1.284
Underflow 0
Overflow 2.983e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 323352
Mean 1.881e+04
RMS 4.313
Underflow 3.233e+05
Overflow 0
10
1
18800
18900
19000
19100
19200
19300
19400

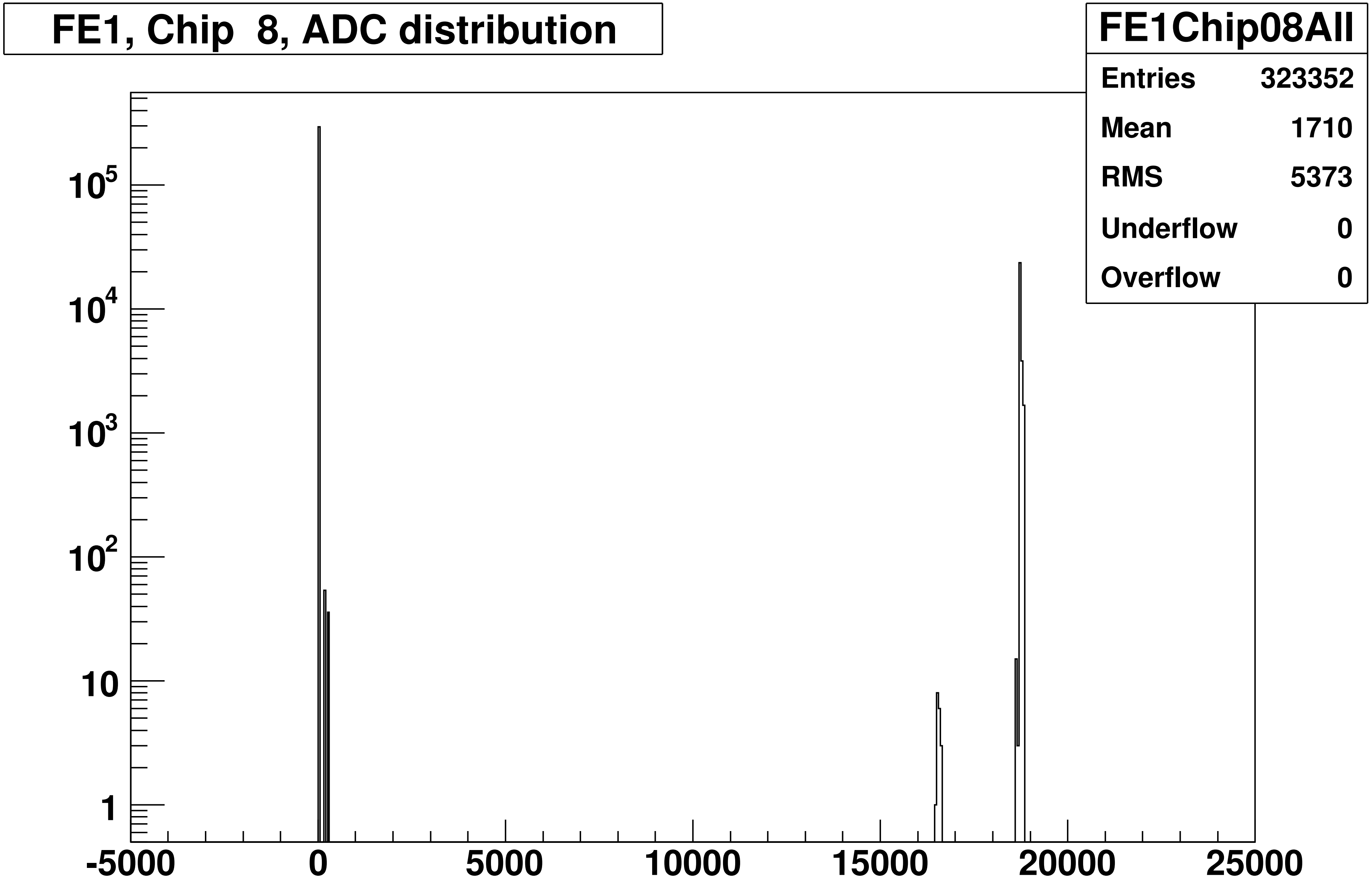

FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 323352
Mean 1537
RMS 5154
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 323352
Mean -18.49
RMS 1.396
Underflow 0
Overflow 2.756e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

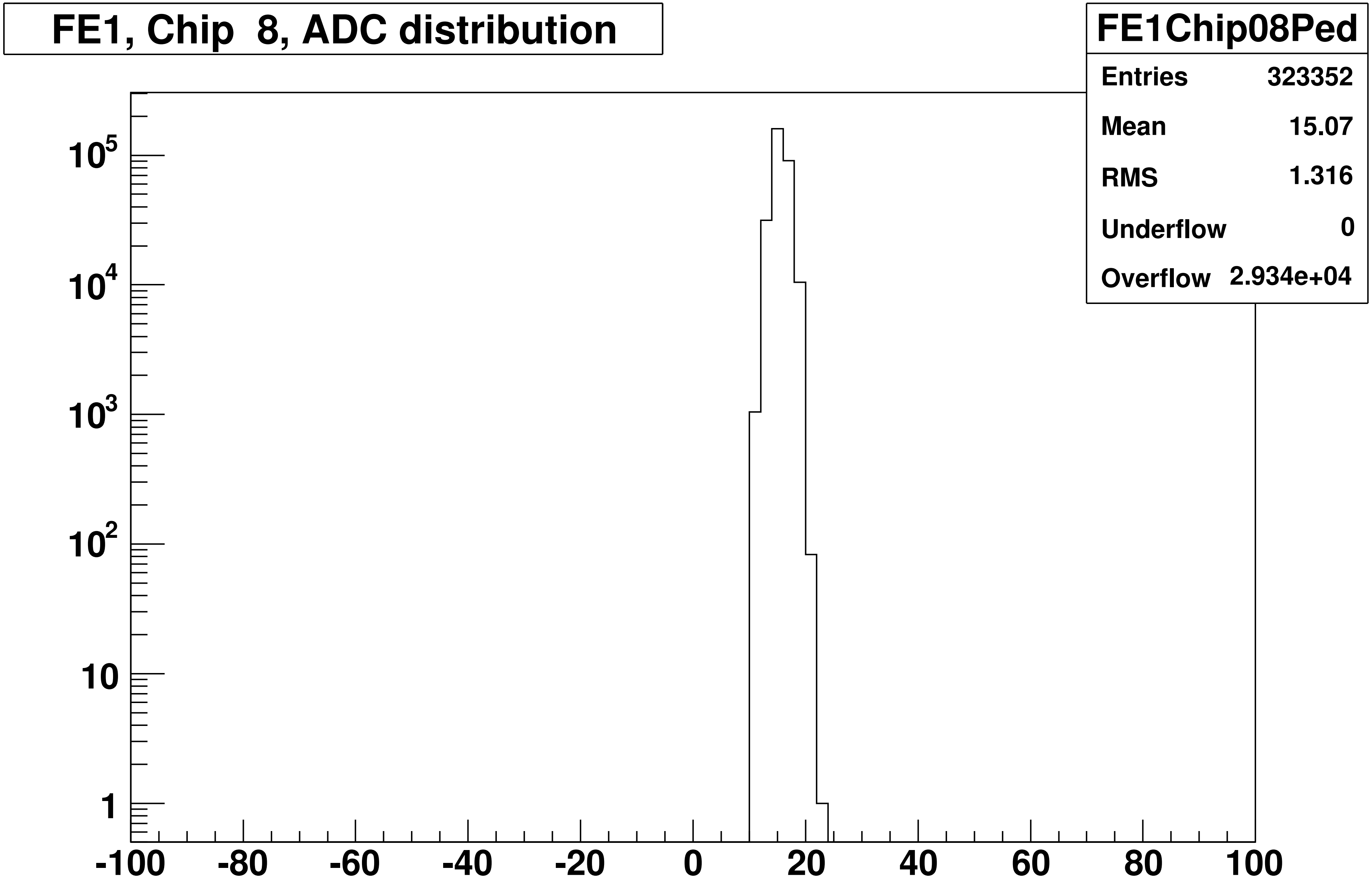

FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 323352
Mean 1547
RMS 5148
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

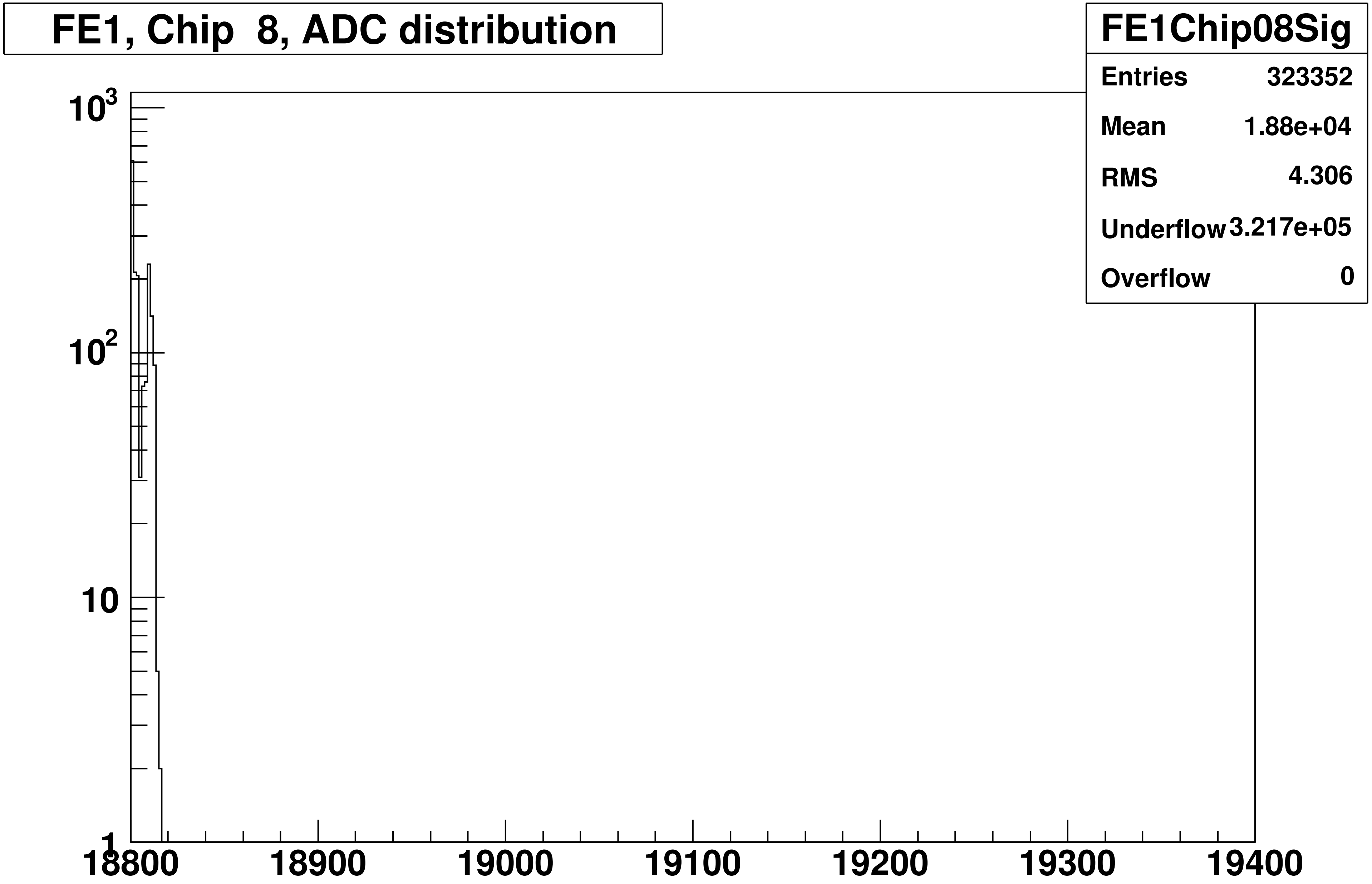


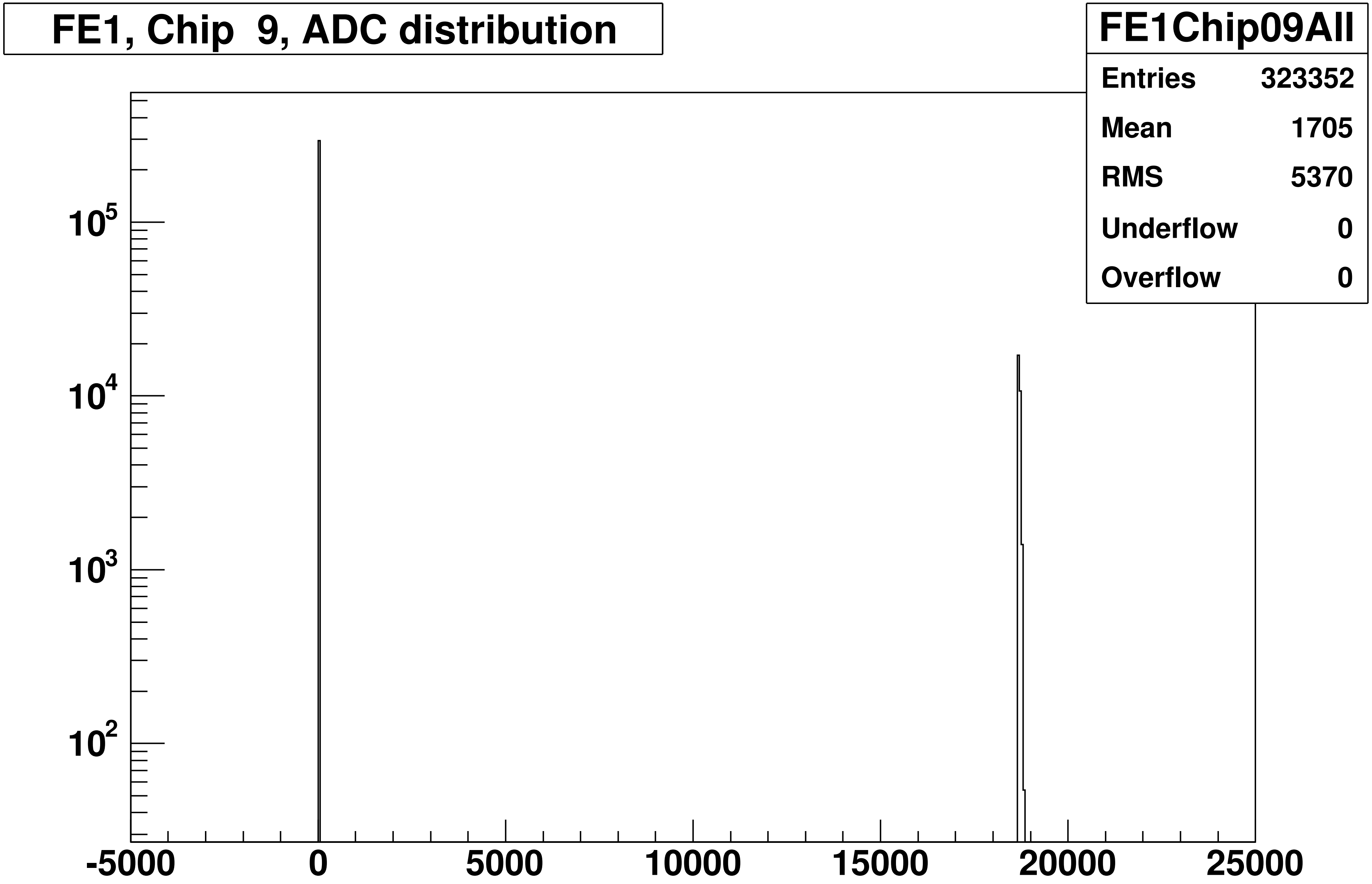
FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 323352
Mean -11.54
RMS 1.325
Underflow 0
Overflow 2.858e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



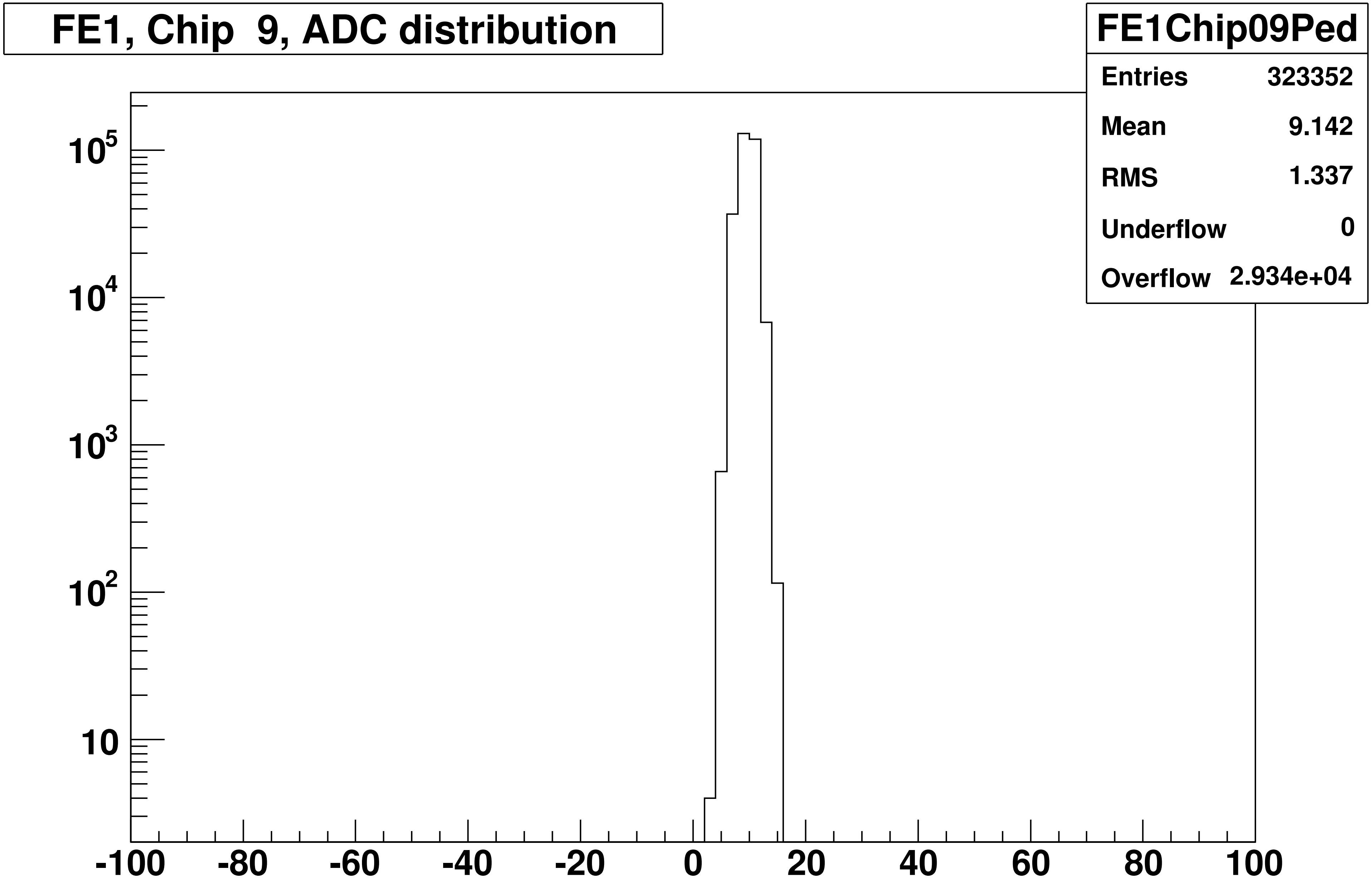
FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 323352
Mean 1710
RMS 5373
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

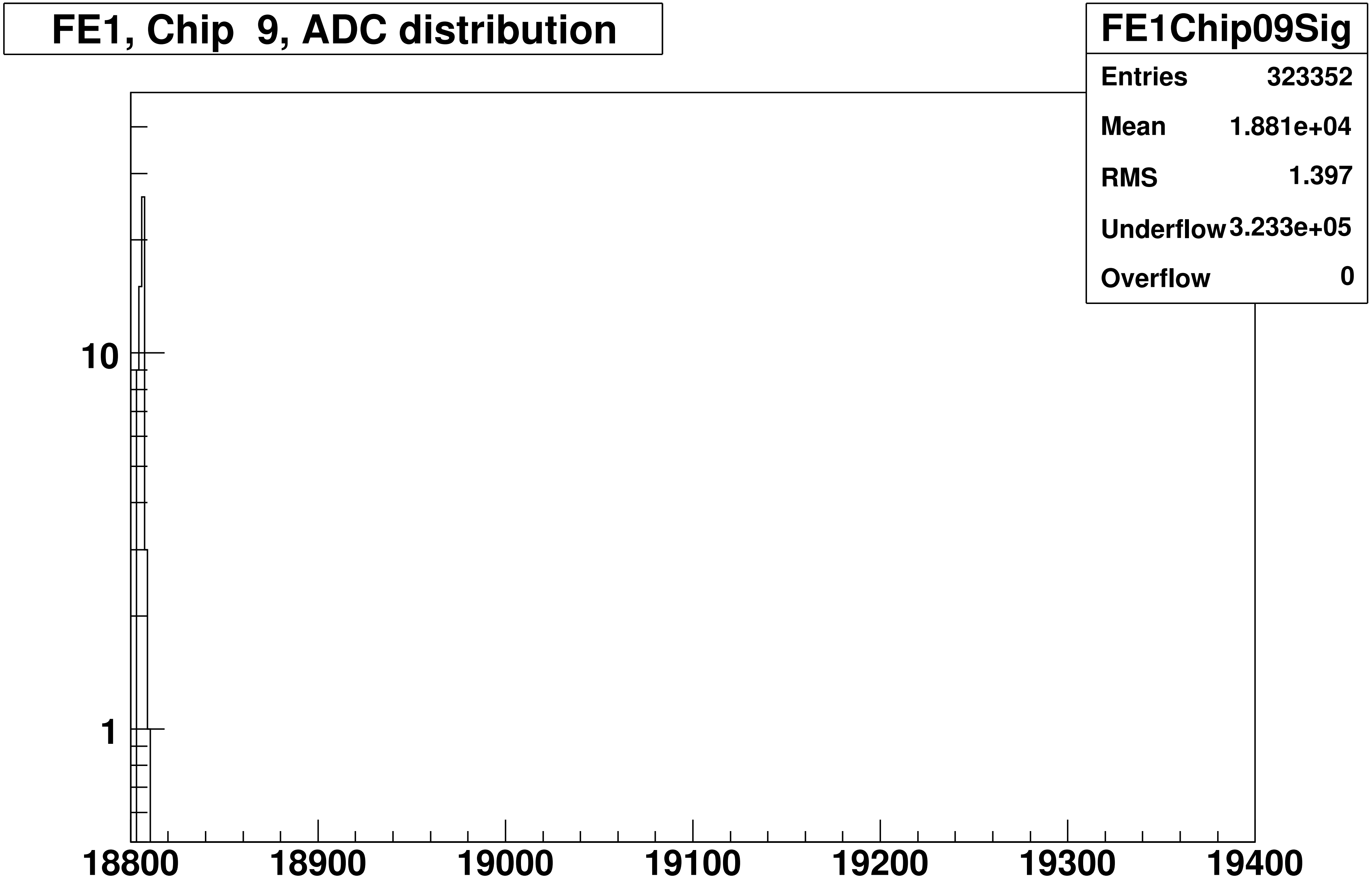

FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 323352
Mean 15.07
RMS 1.316
Underflow 0
Overflow 2.934e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

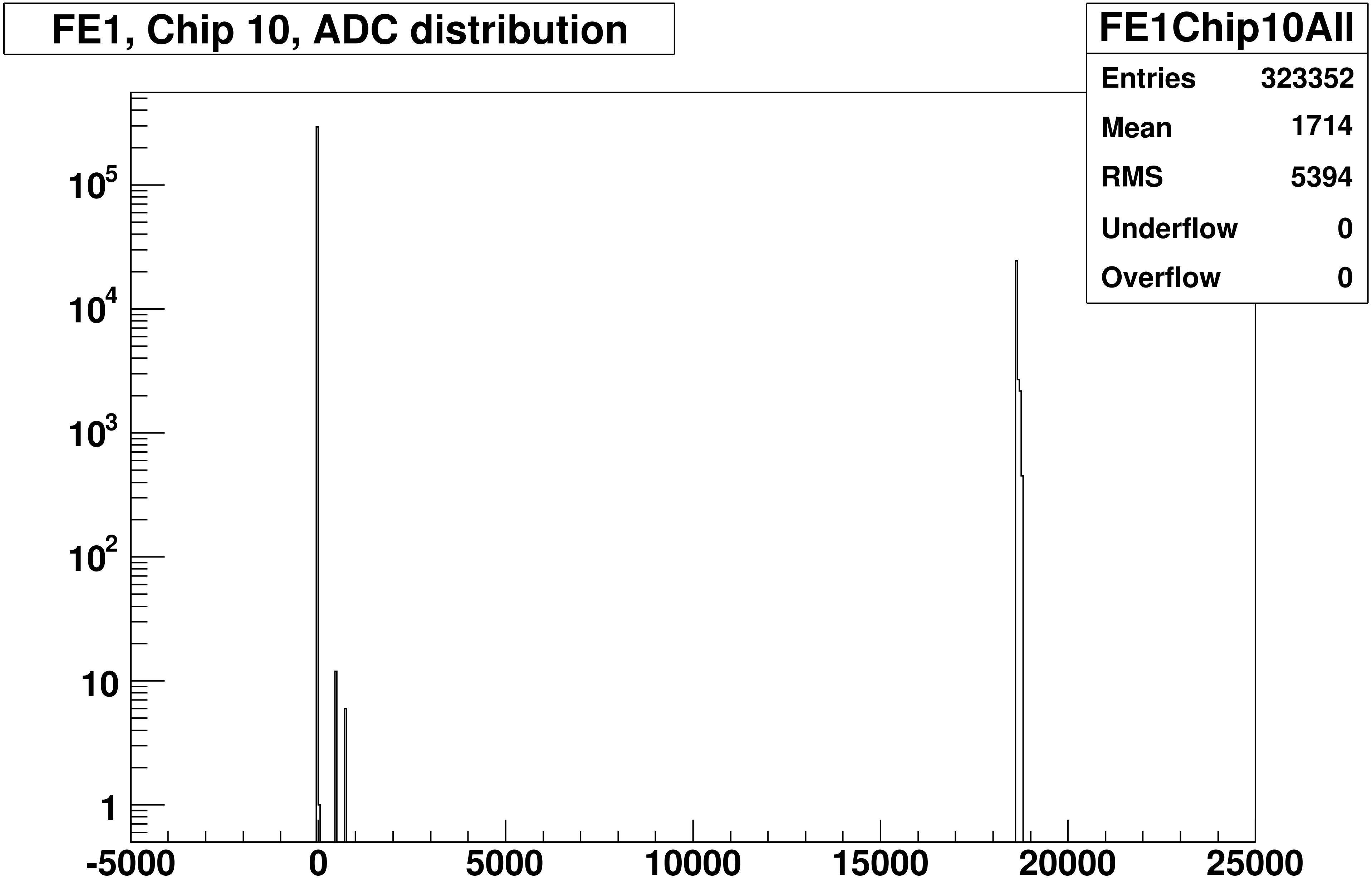

FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 323352
Mean 1.88e+04
RMS 4.306
Underflow 3.217e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

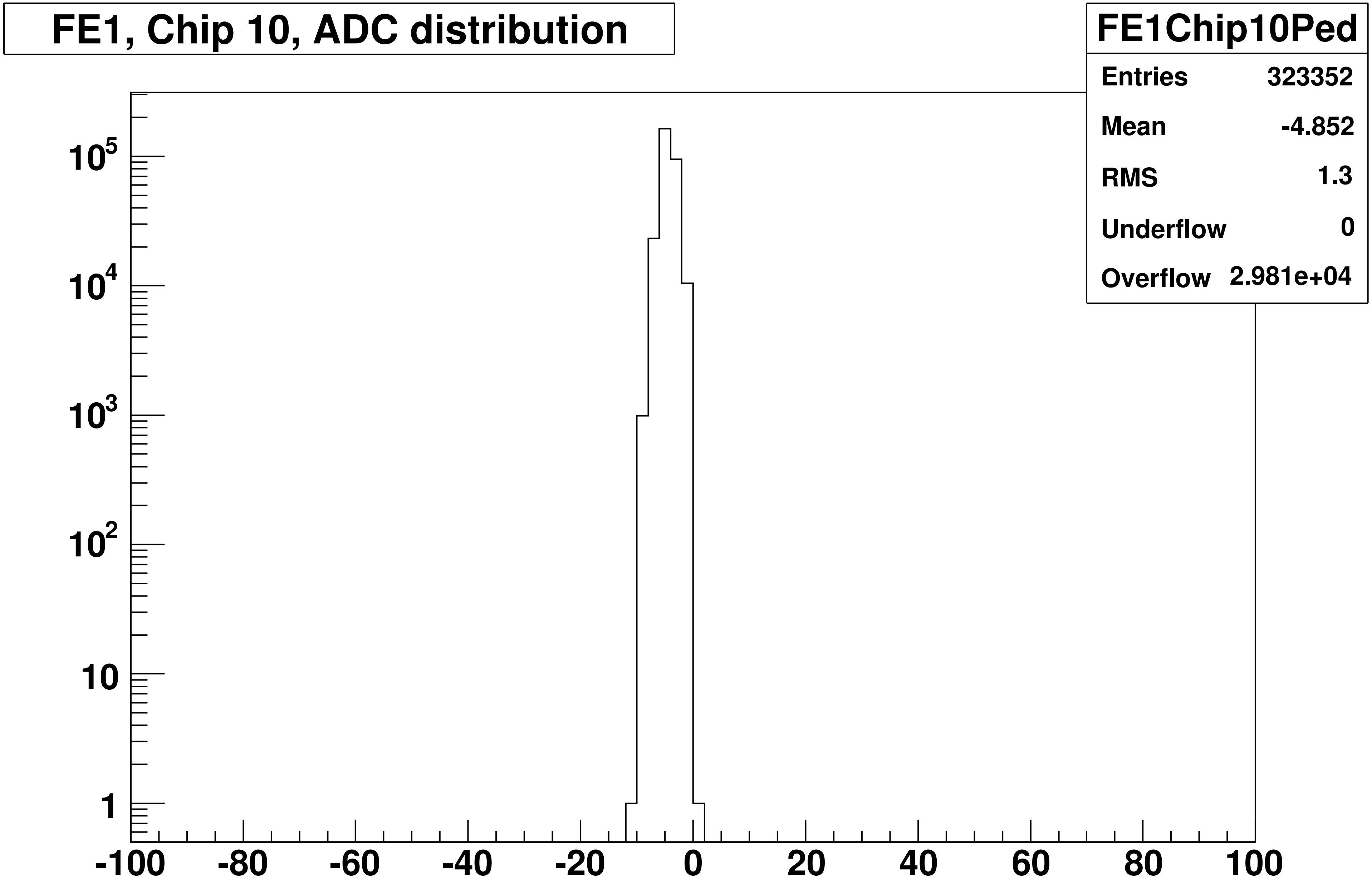


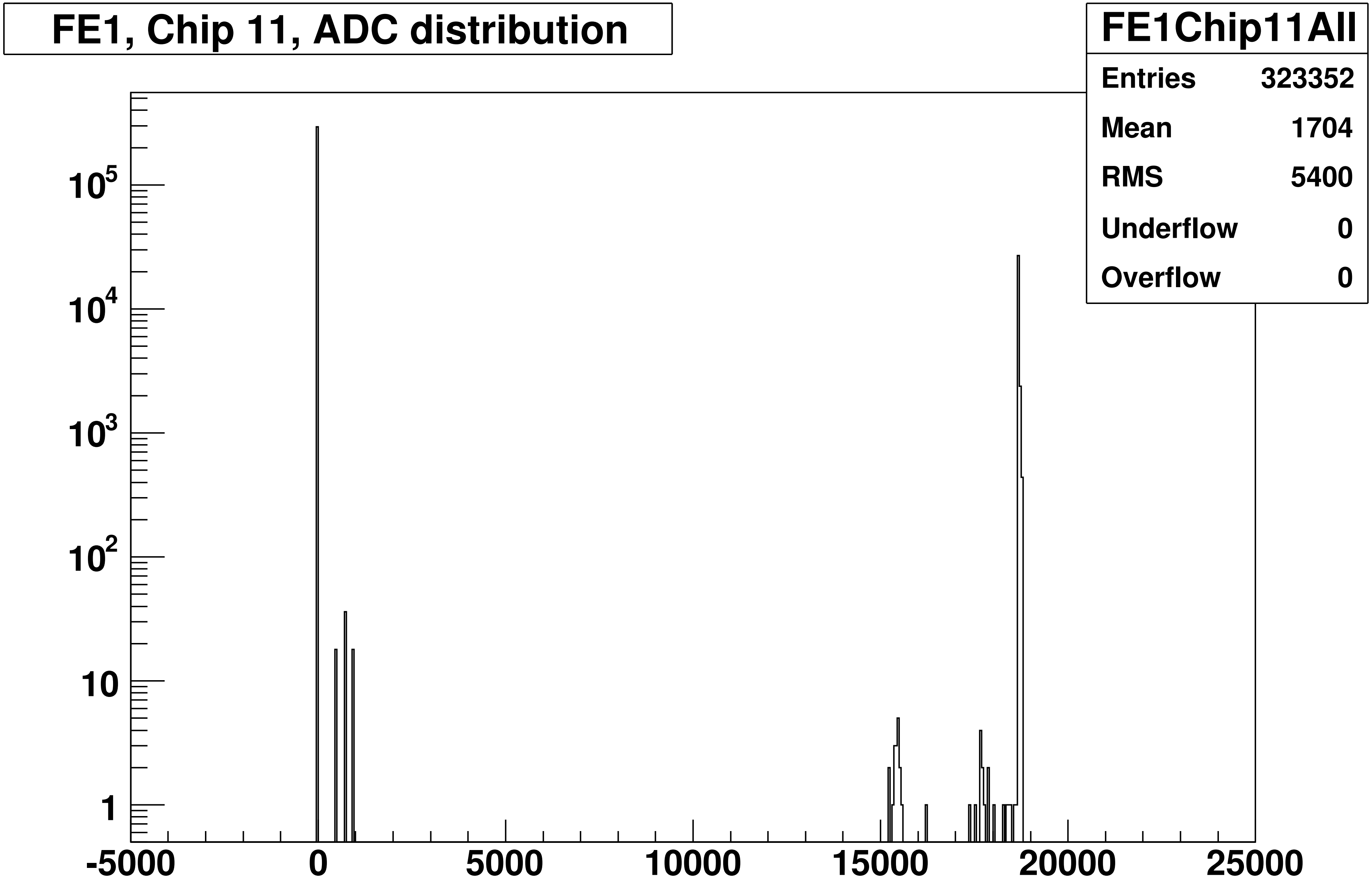
FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 323352
Mean 1705
RMS 5370
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 323352
Mean 9.142
RMS 1.337
Underflow 0
Overflow 2.934e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

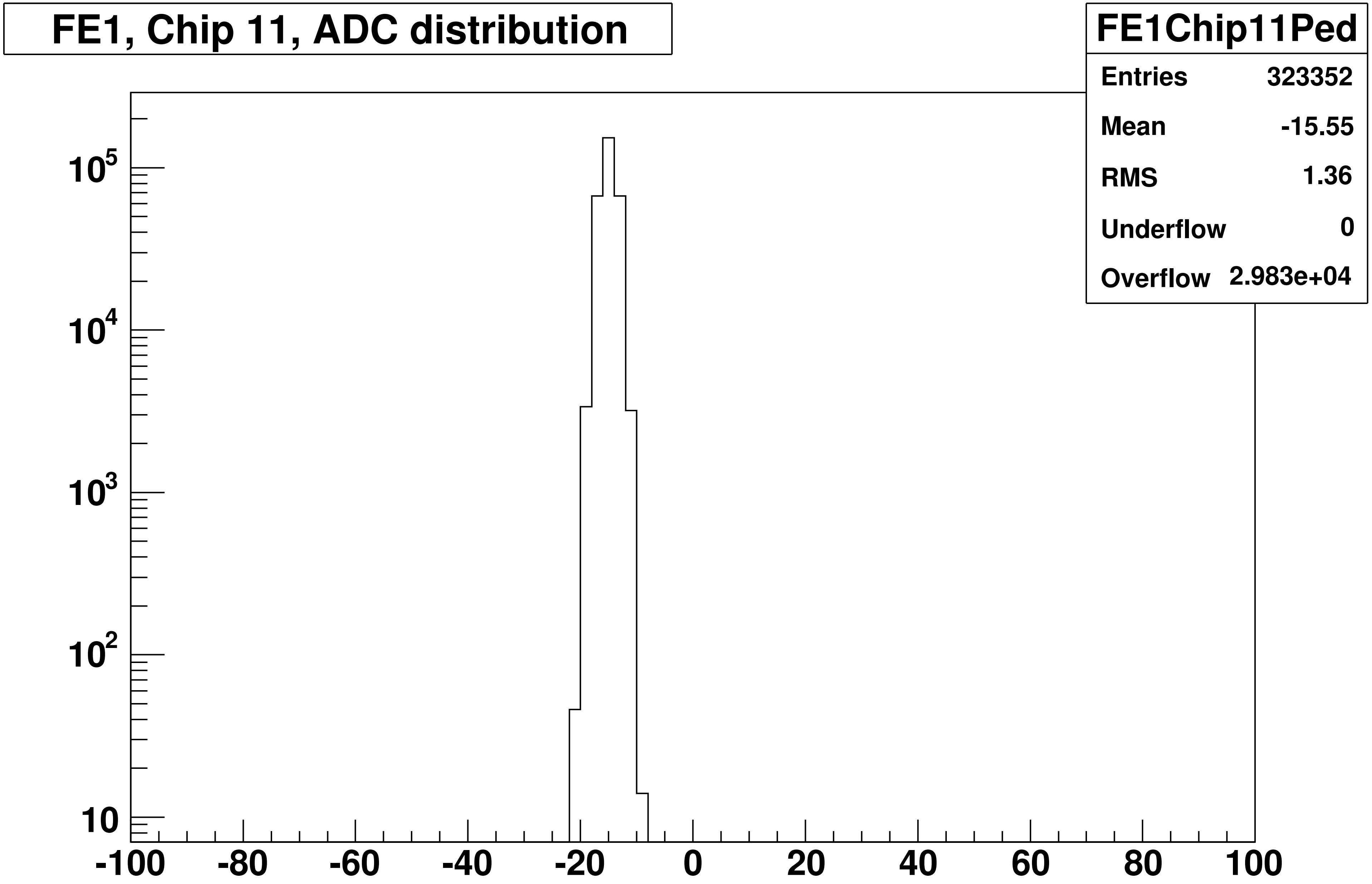

FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 323352
Mean 1.881e+04
RMS 1.397
Underflow 3.233e+05
Overflow 0
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 323352
Mean 1714
RMS 5394
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

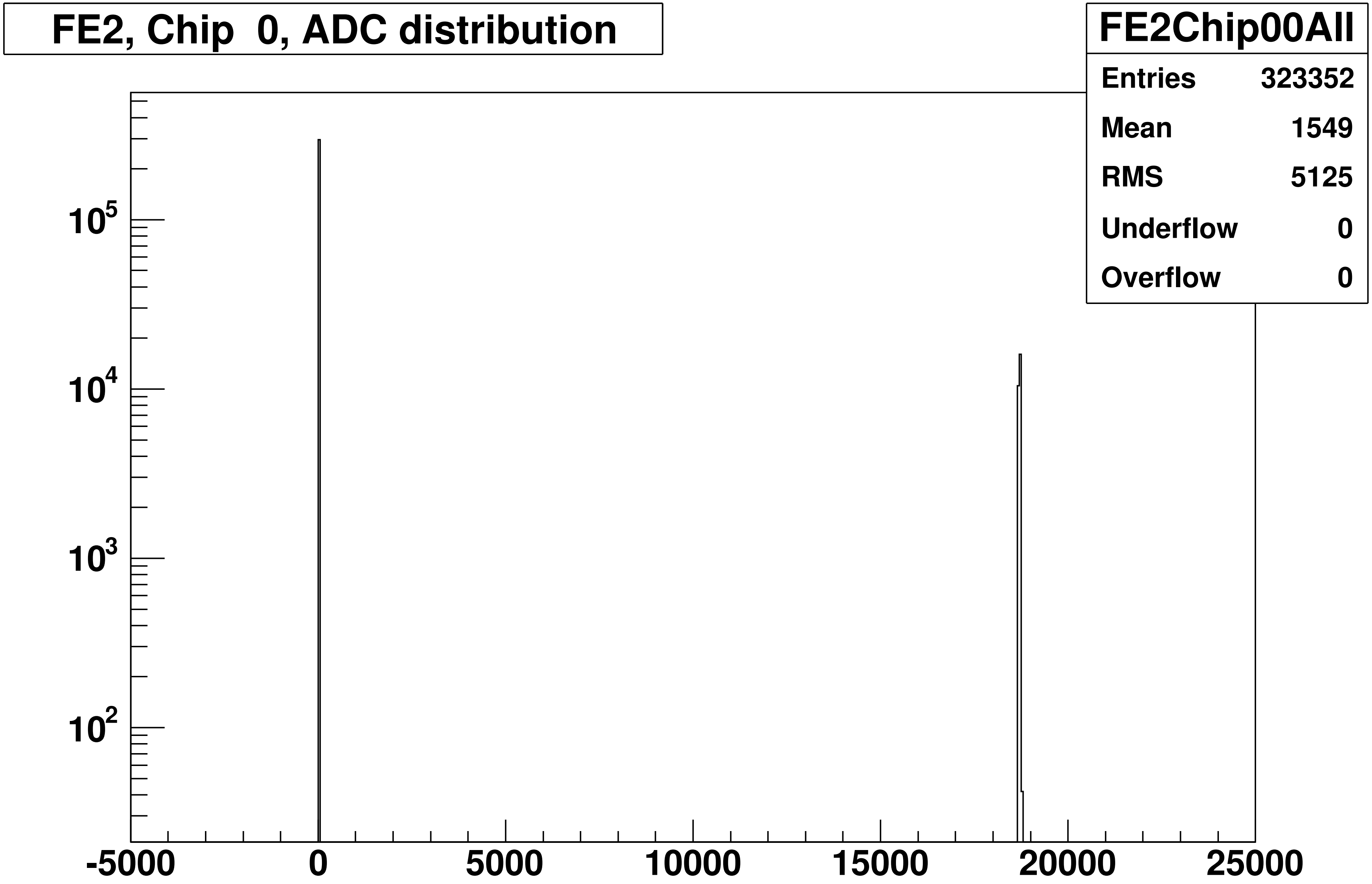

FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 323352
Mean -4.852
RMS 1.3
Underflow 0
Overflow 2.981e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



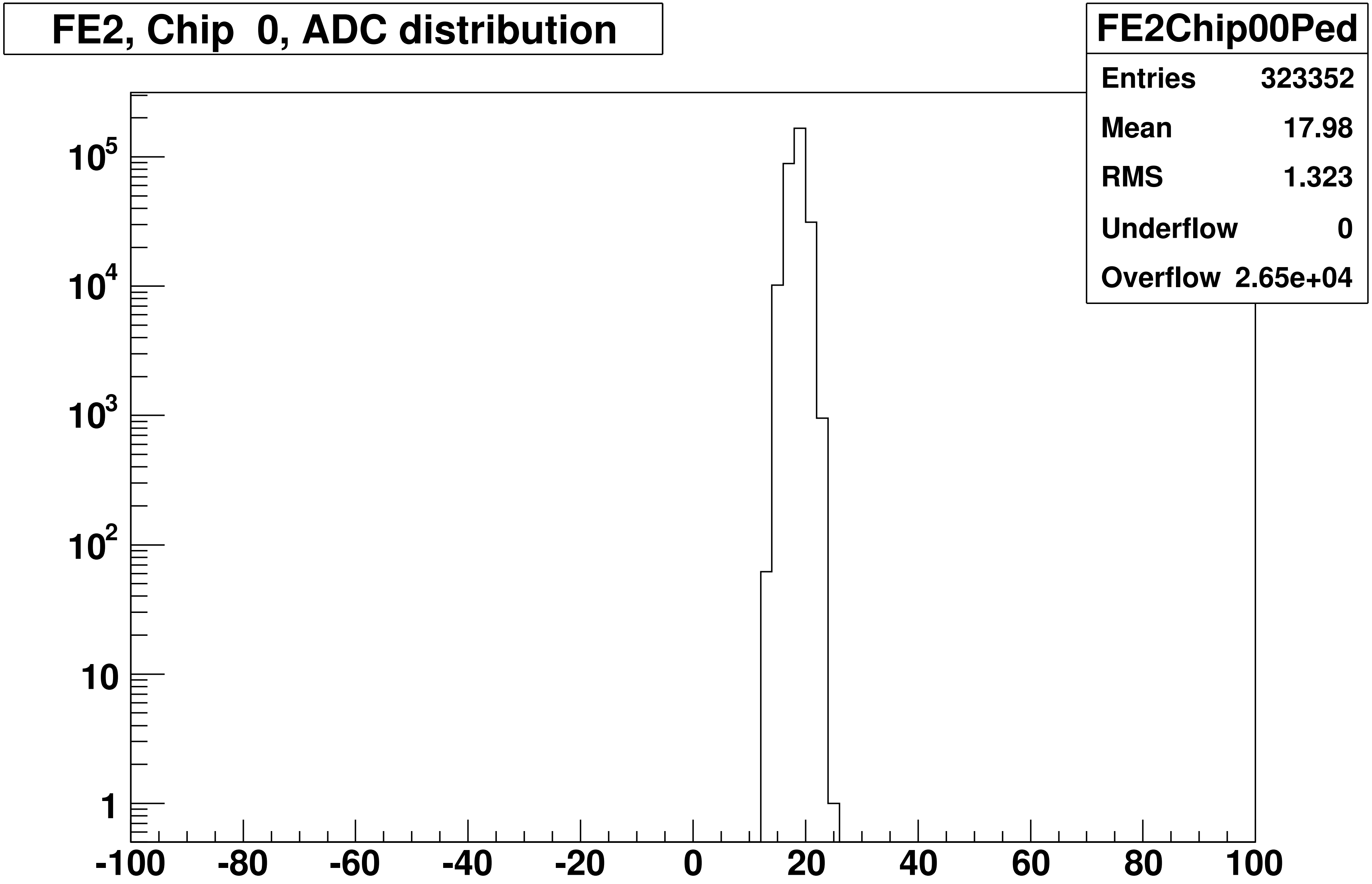
FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 323352
Mean 1704
RMS 5400
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

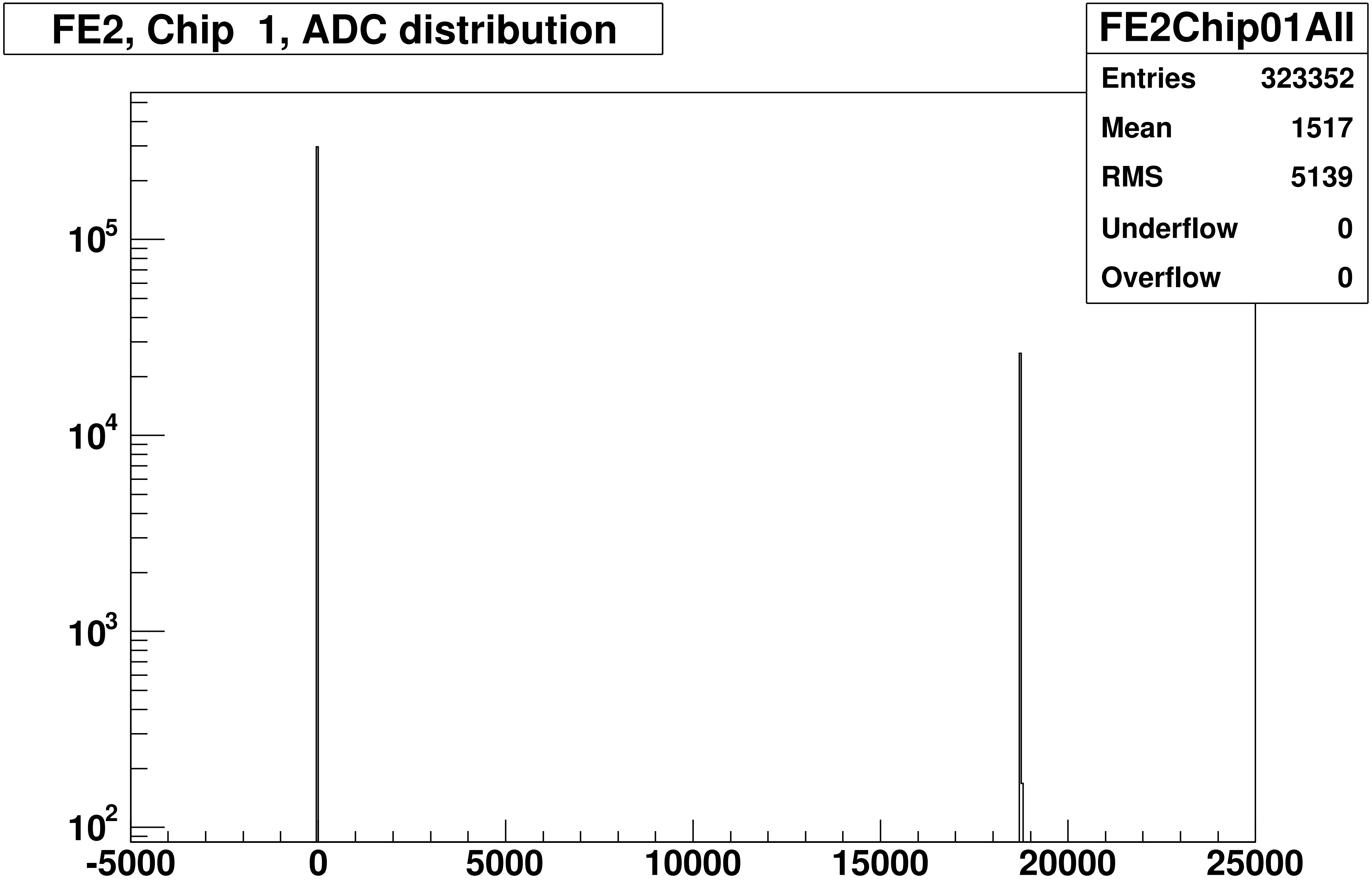


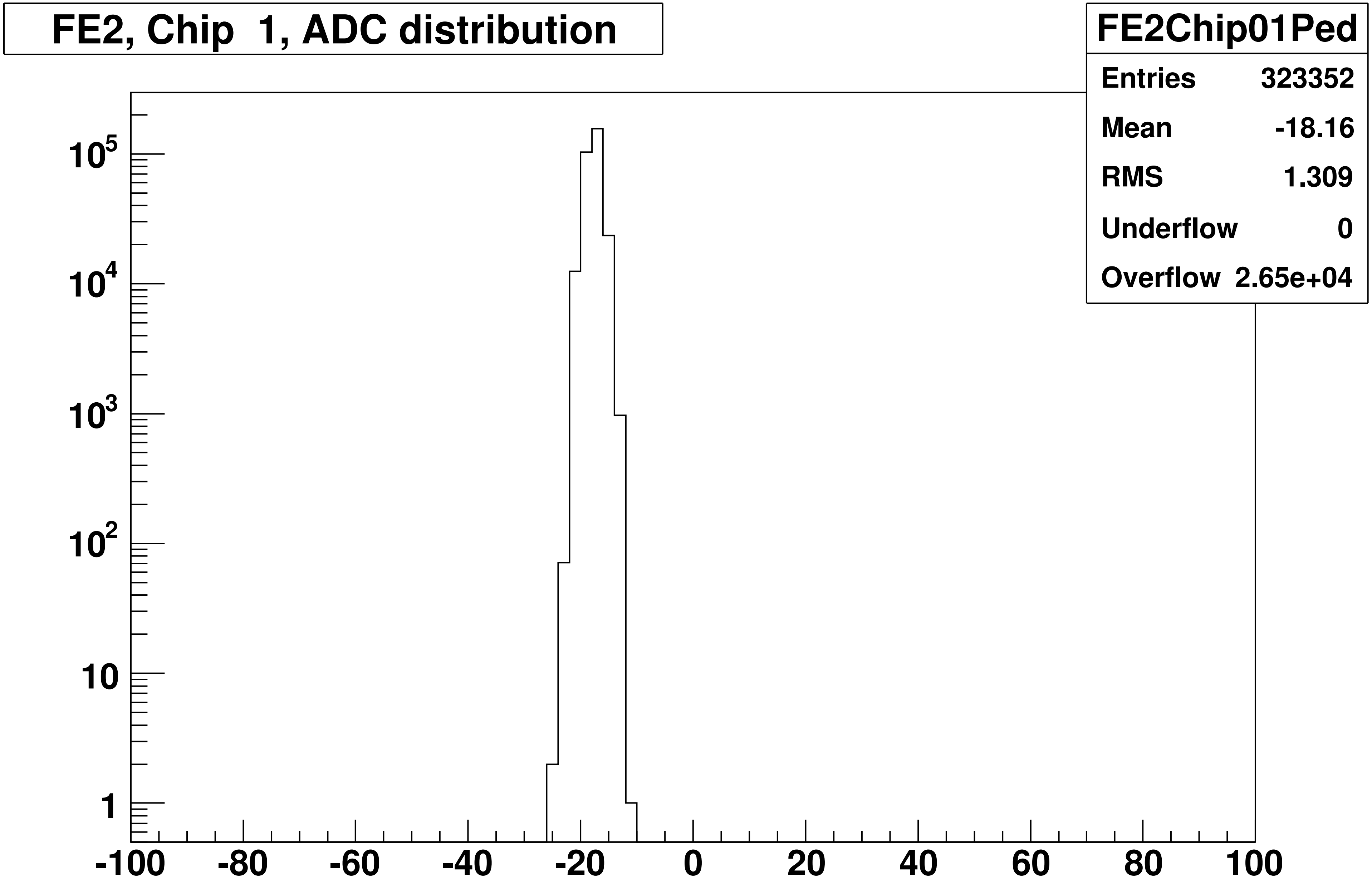
FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 323352
Mean -15.55
RMS 1.36
Underflow 0
Overflow 2.983e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

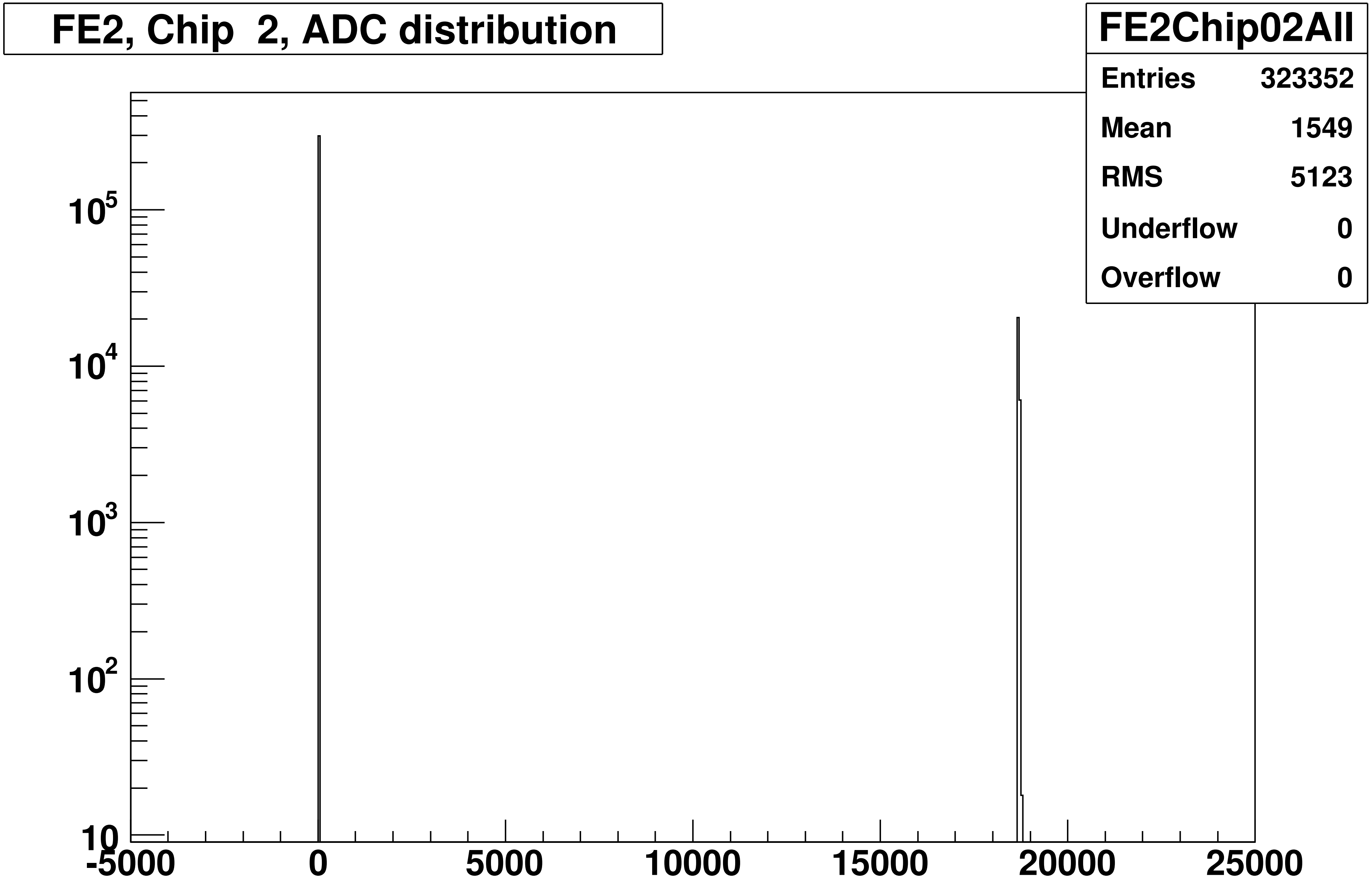


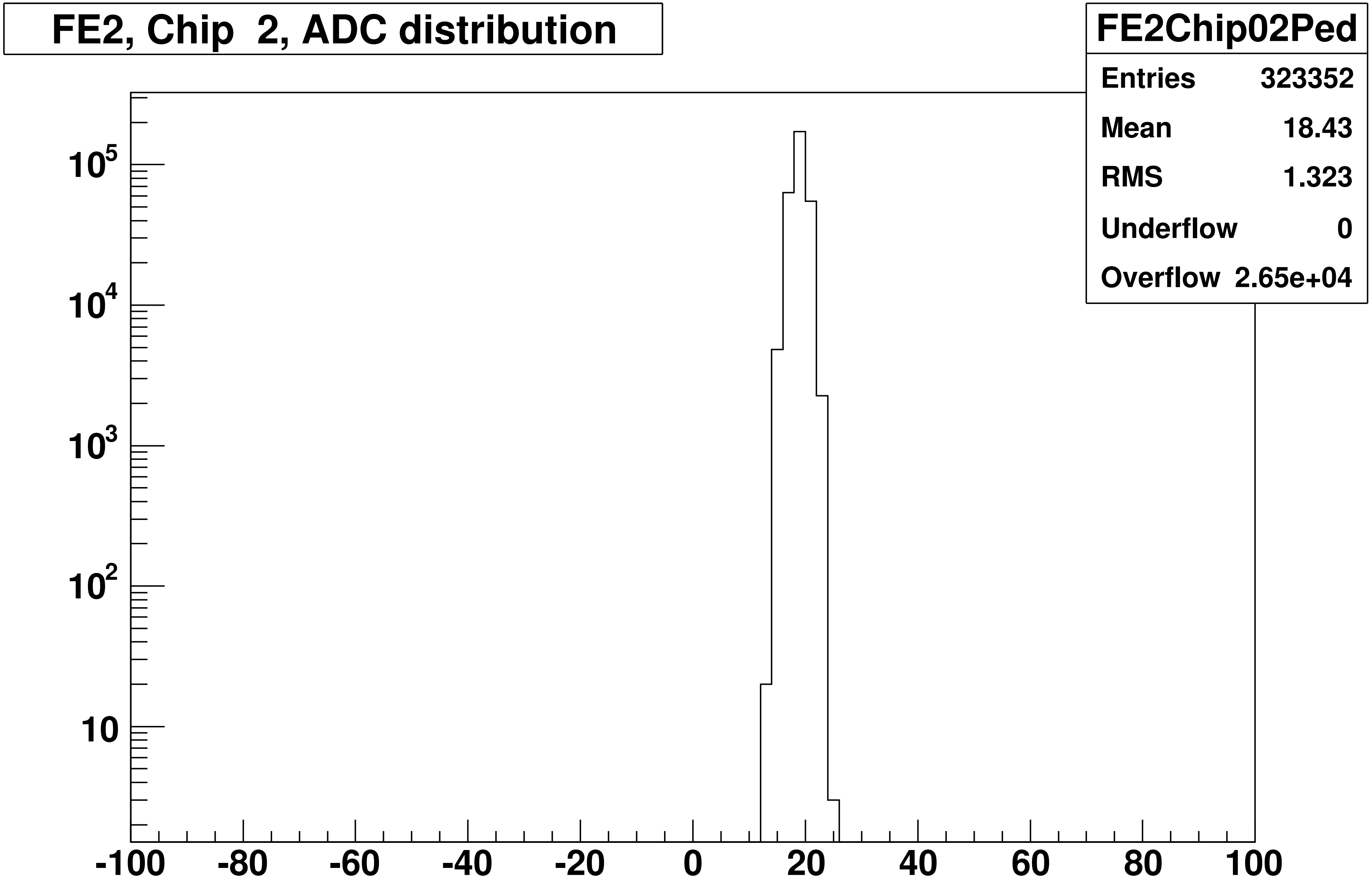
FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 323352
Mean 1549
RMS 5125
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

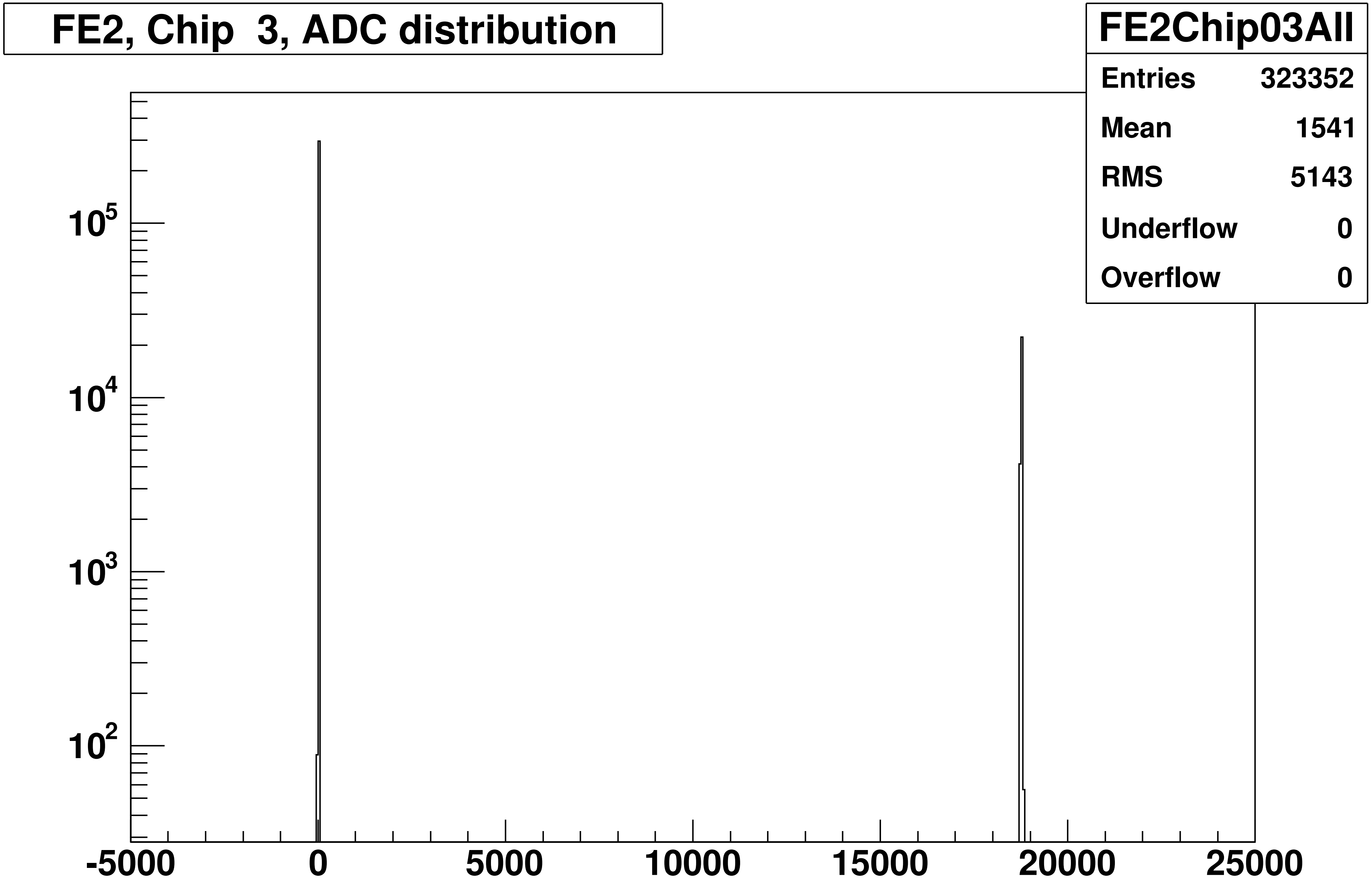

FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 323352
Mean 17.98
RMS 1.323
Underflow 0
Overflow 2.65e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

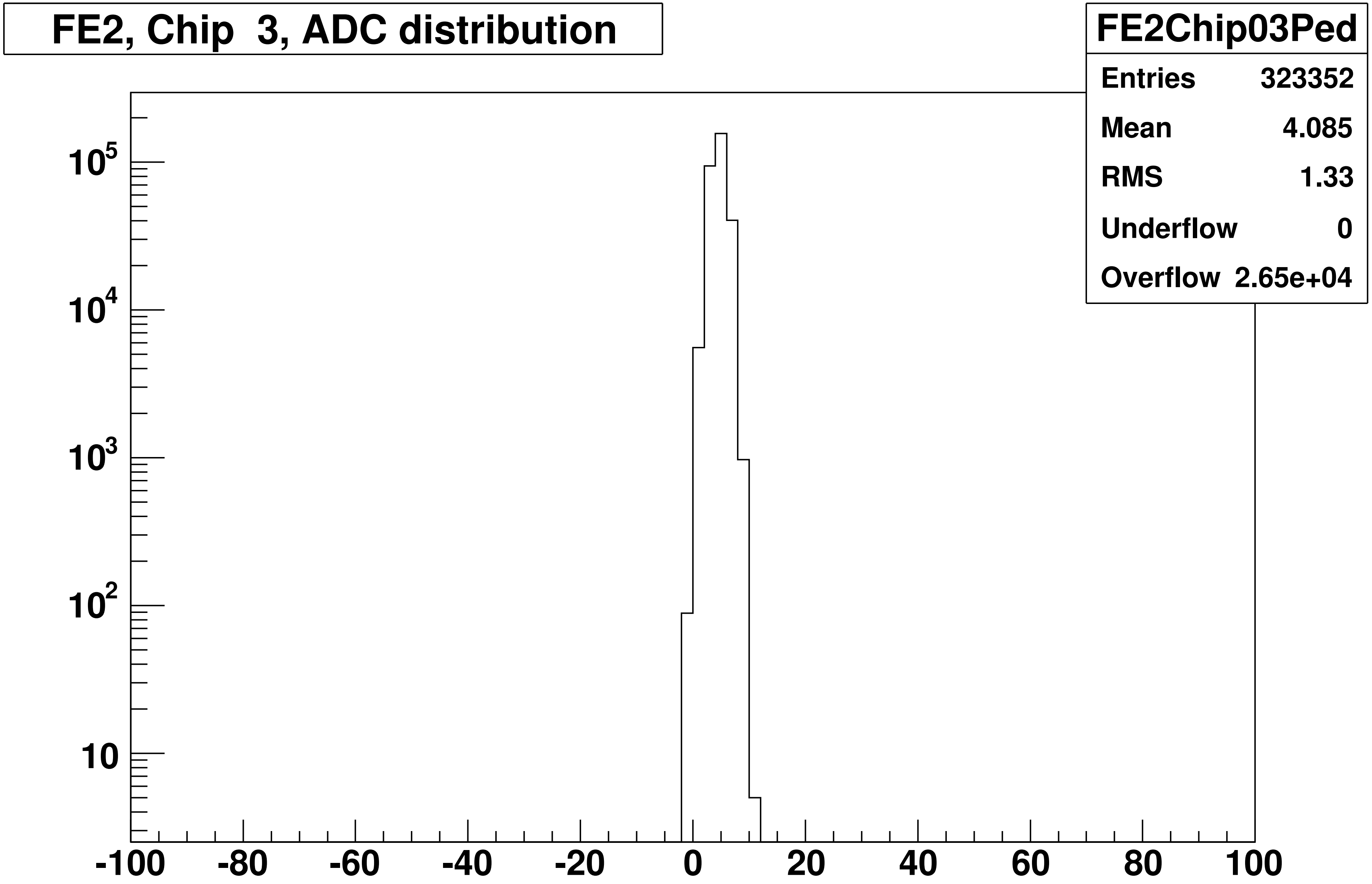

FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 323352
Mean 1517
RMS 5139
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

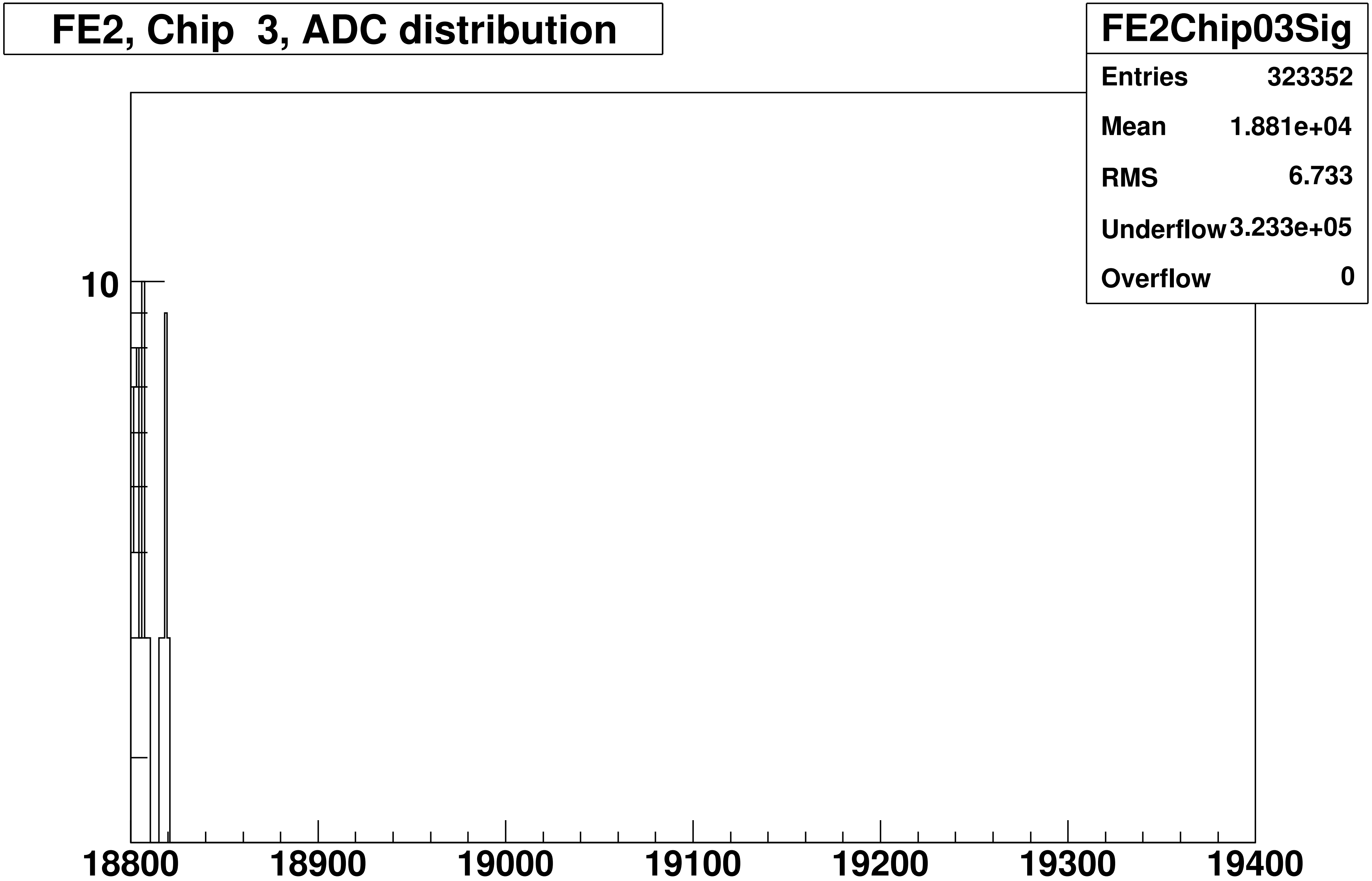

FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 323352
Mean -18.16
RMS 1.309
Underflow 0
Overflow 2.65e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

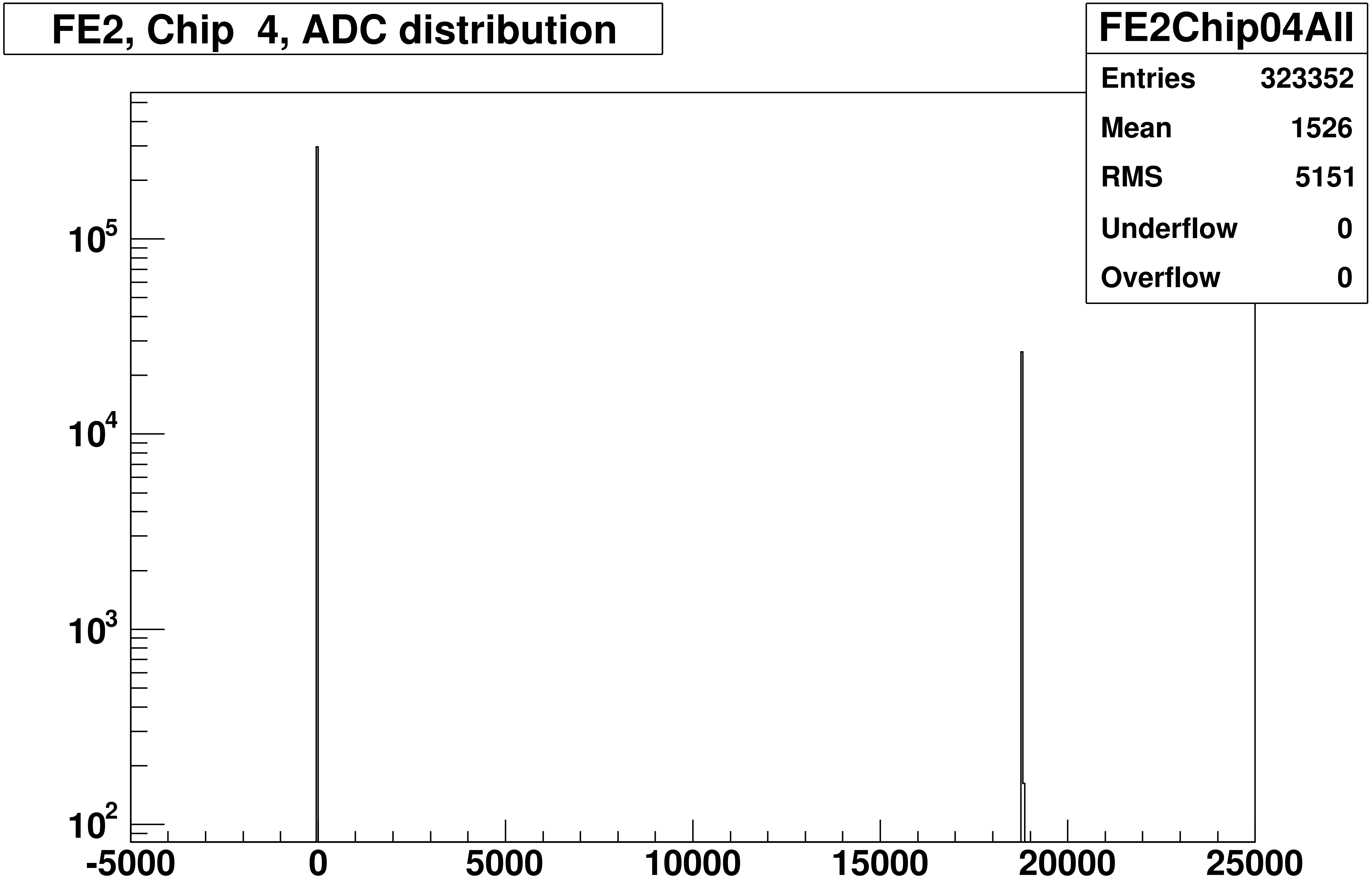

FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 323352
Mean 1549
RMS 5123
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

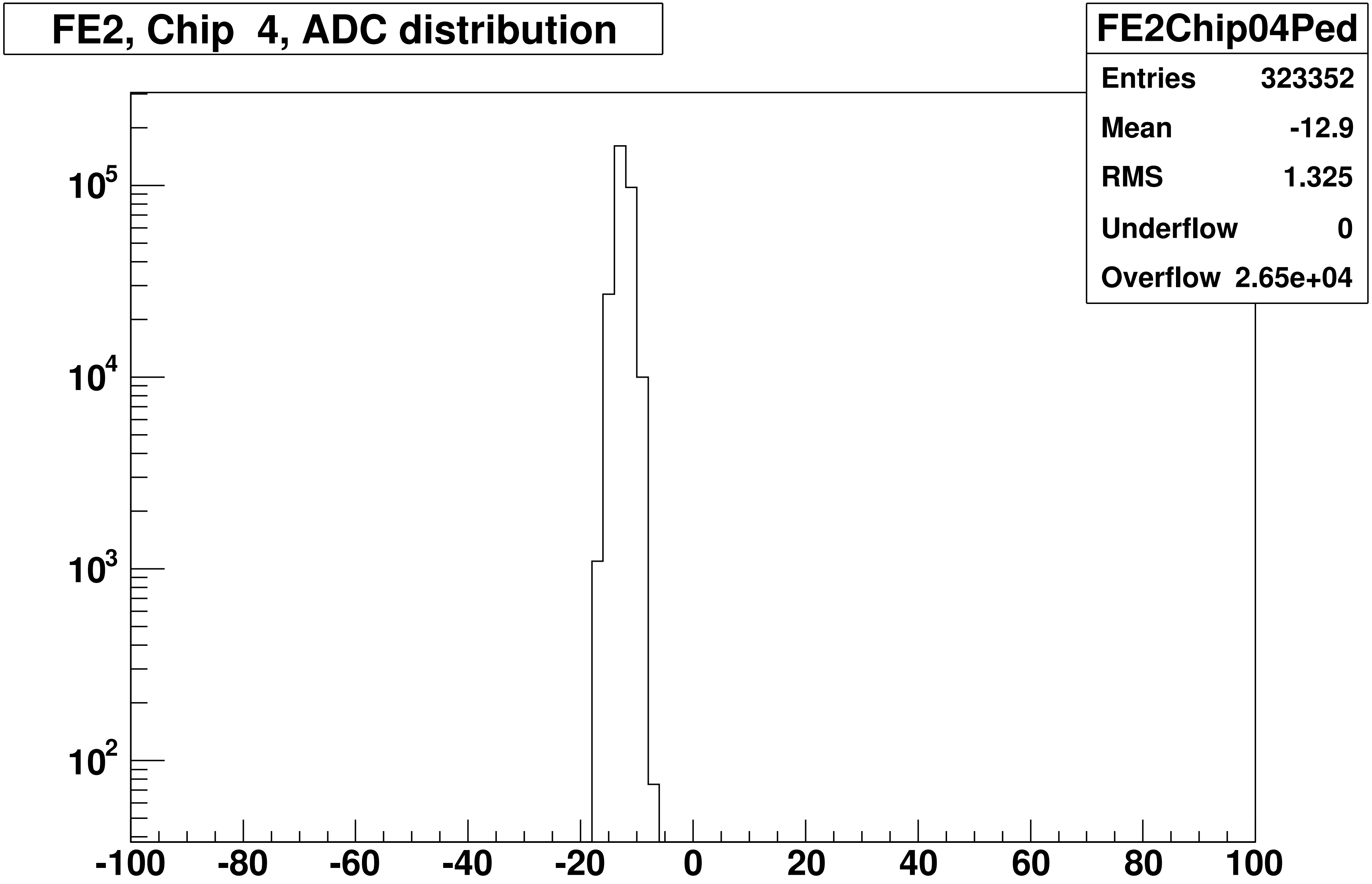

FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 323352
Mean 18.43
RMS 1.323
Underflow 0
Overflow 2.65e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

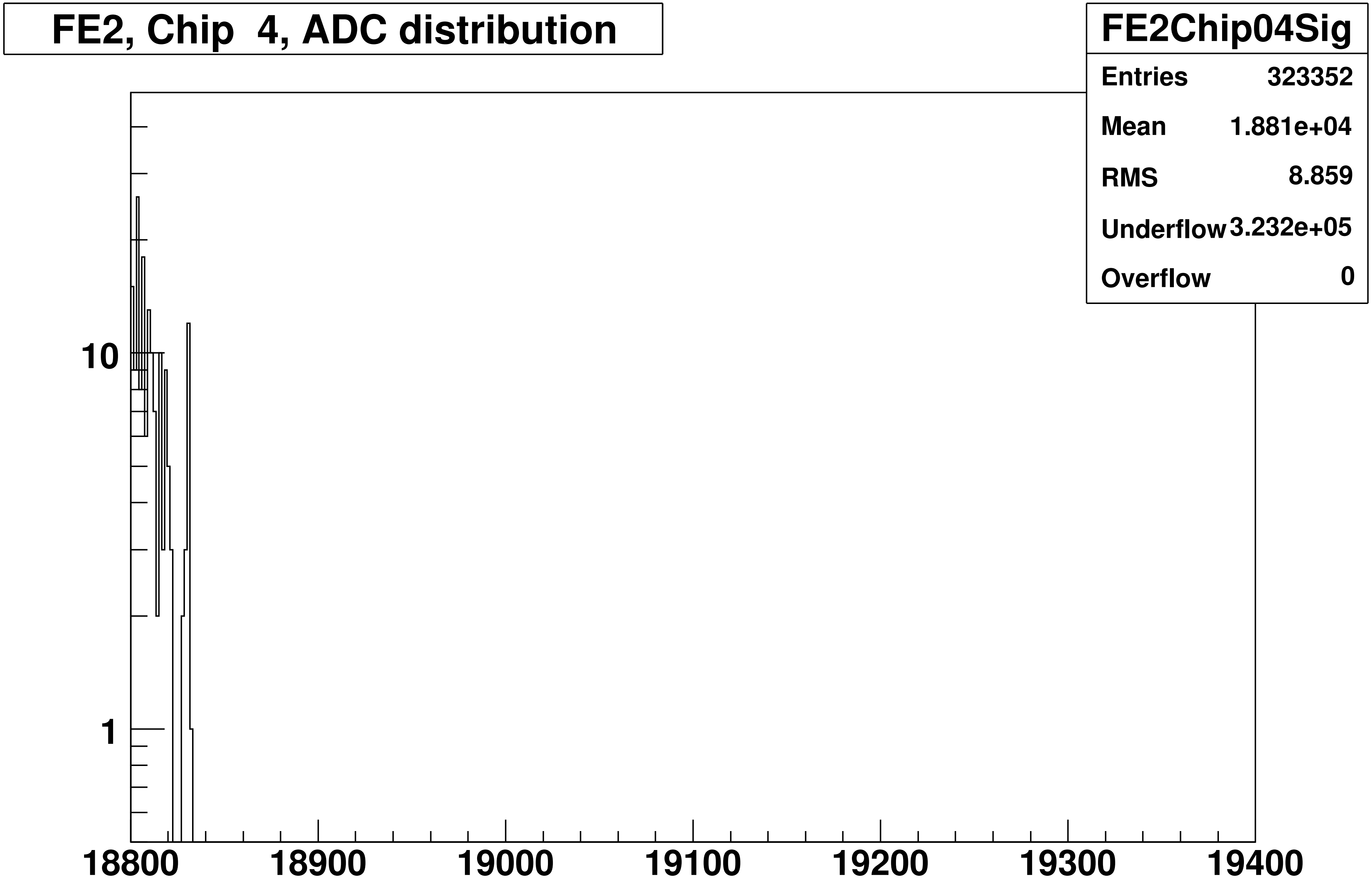

FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 323352
Mean 1541
RMS 5143
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

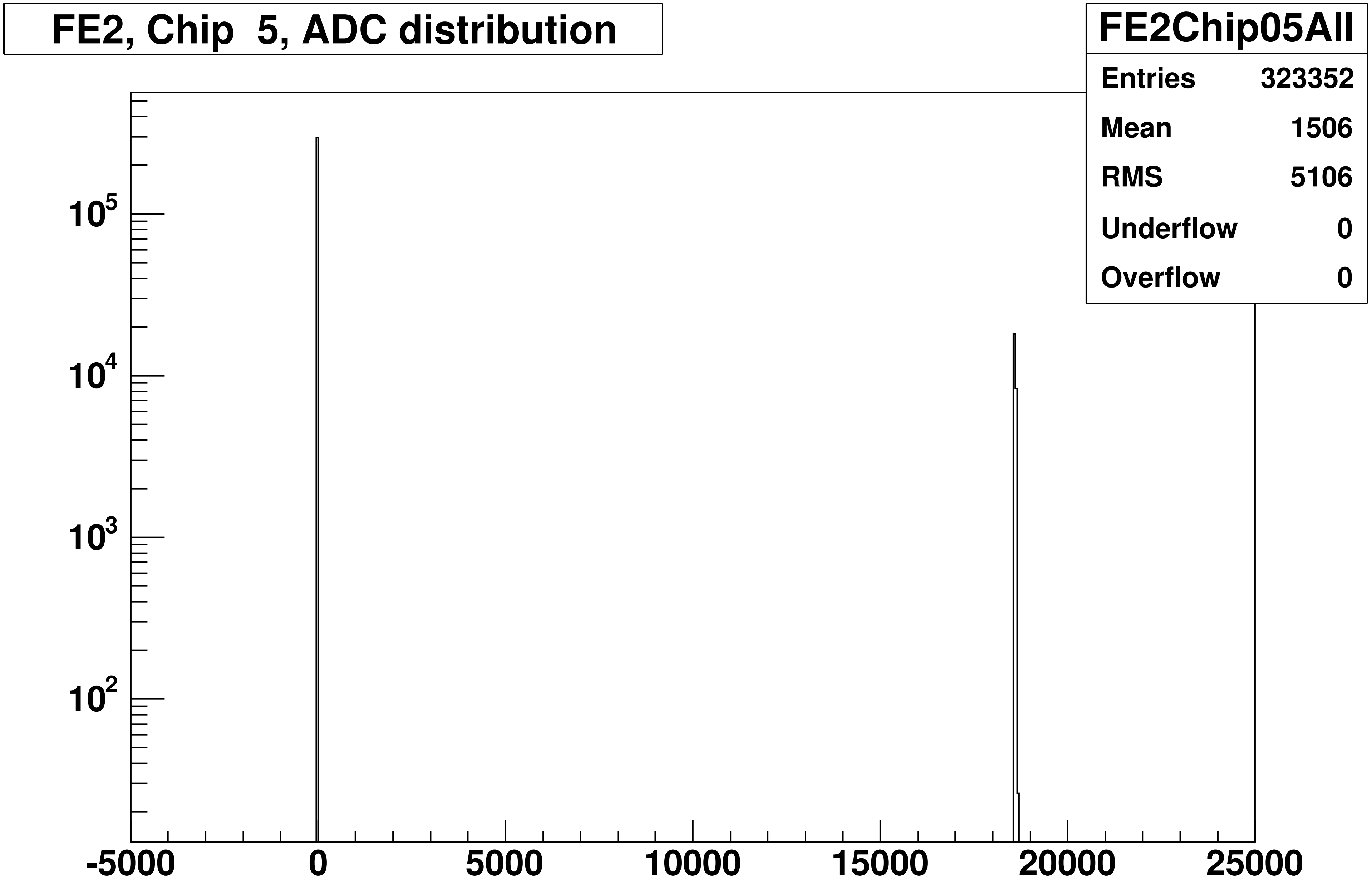

FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 323352
Mean 4.085
RMS 1.33
Underflow 0
Overflow 2.65e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

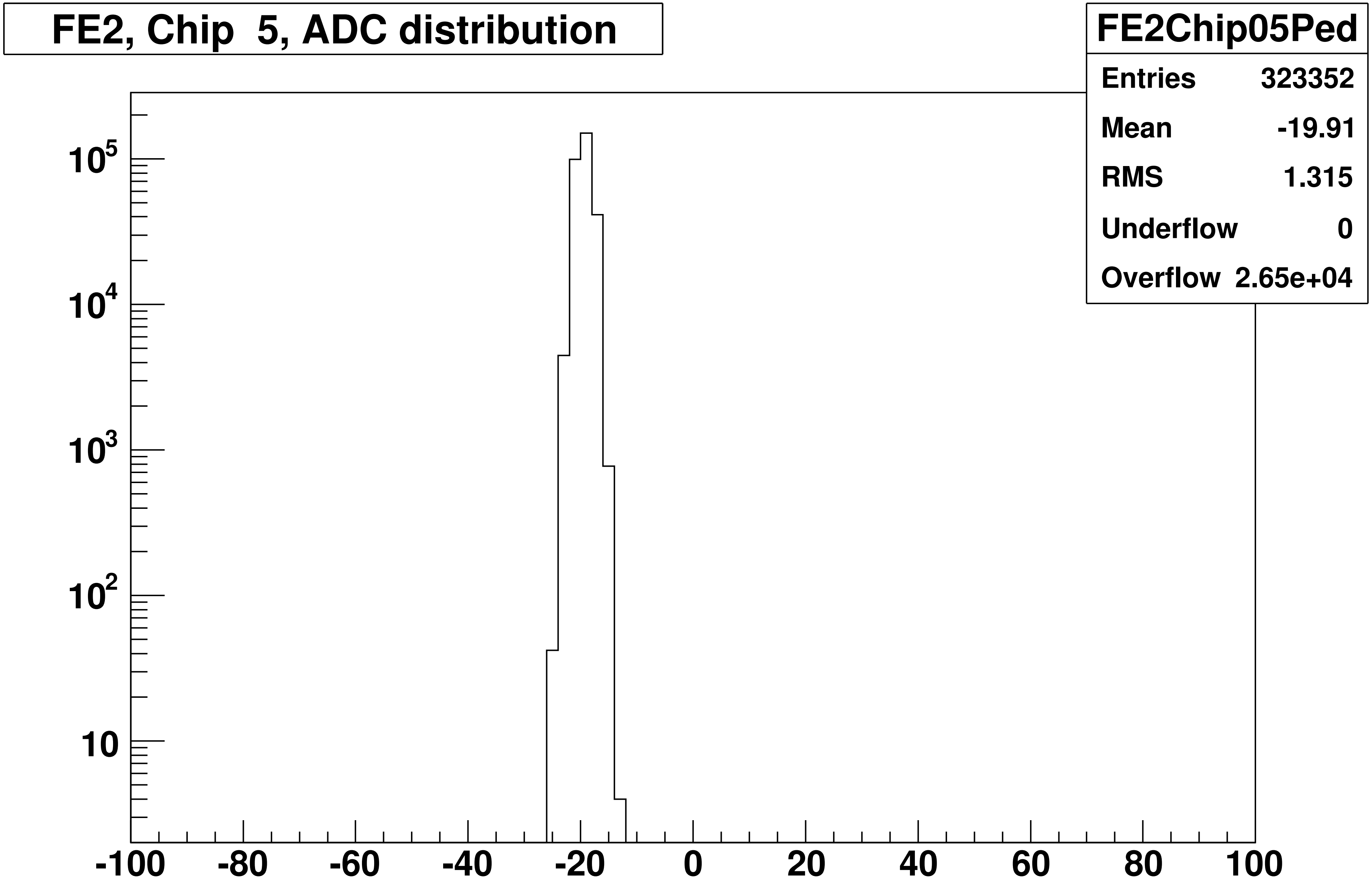

FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 323352
Mean 1.881e+04
RMS 6.733
Underflow 3.233e+05
Overflow 0
10
18800
18900
19000
19100
19200
19300
19400

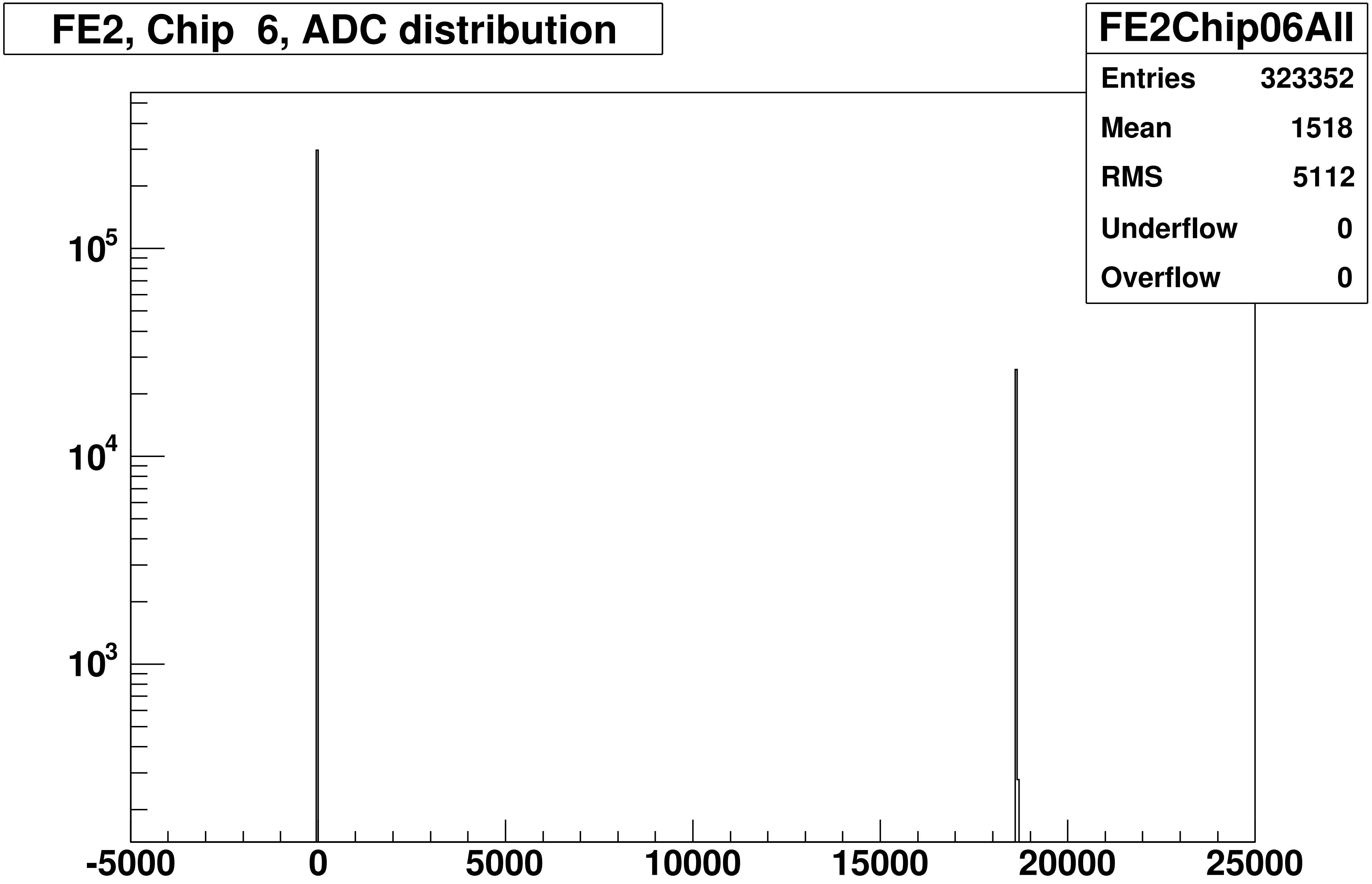

FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 323352
Mean 1526
RMS 5151
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

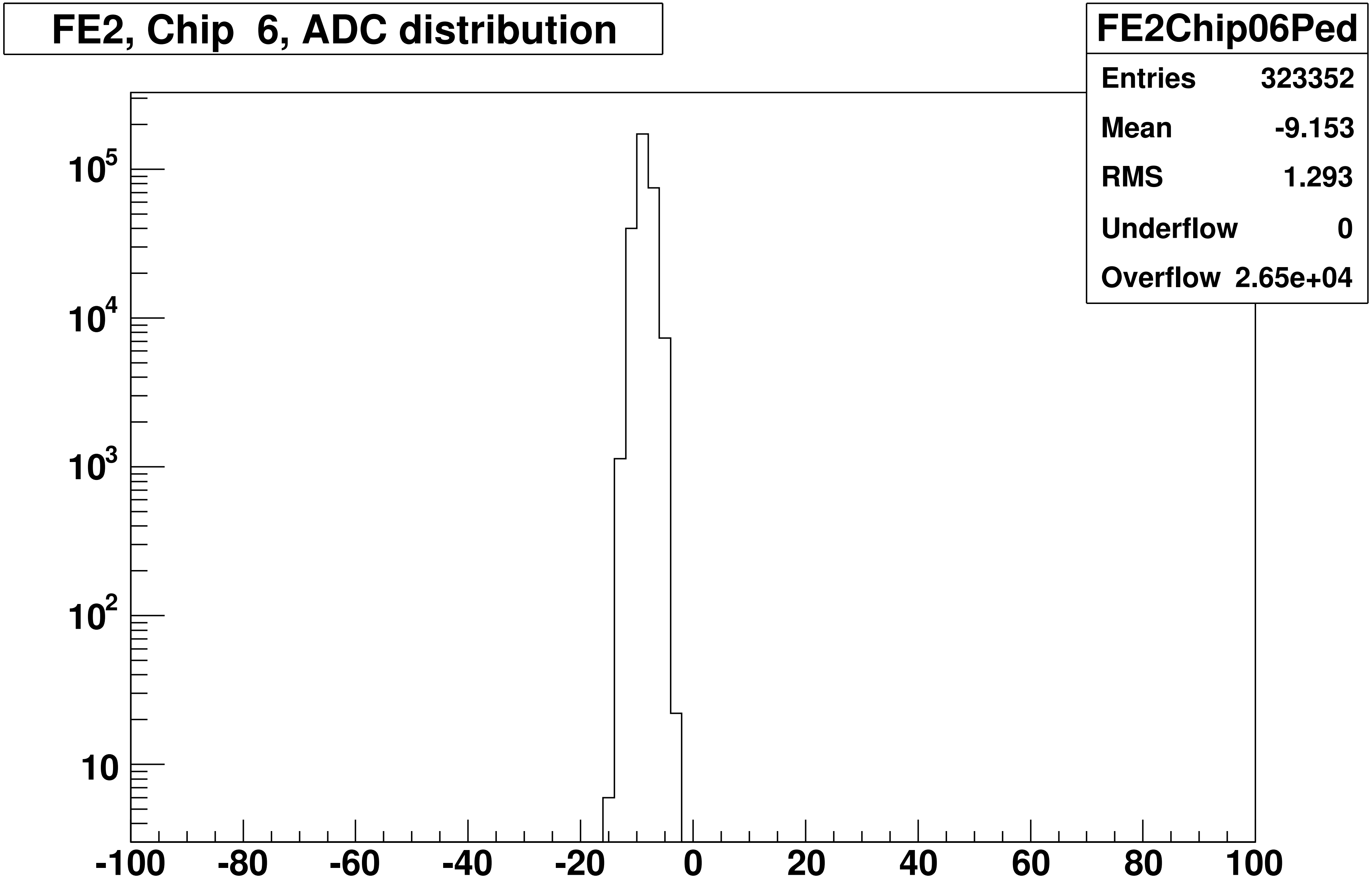

FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 323352
Mean -12.9
RMS 1.325
Underflow 0
Overflow 2.65e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

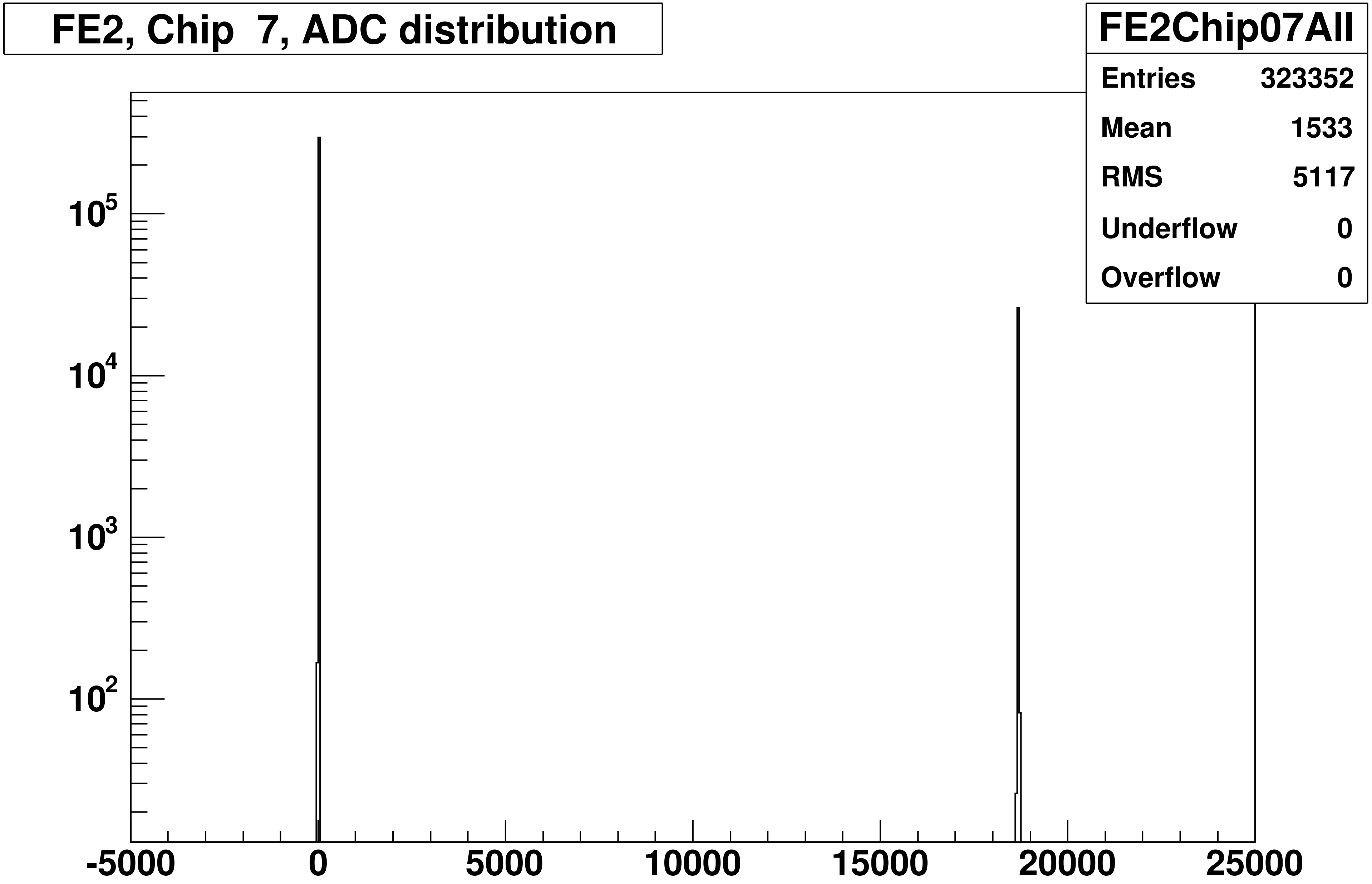

FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 323352
Mean 1.881e+04
RMS 8.859
Underflow 3.232e+05
Overflow 0
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 323352
Mean 1506
RMS 5106
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

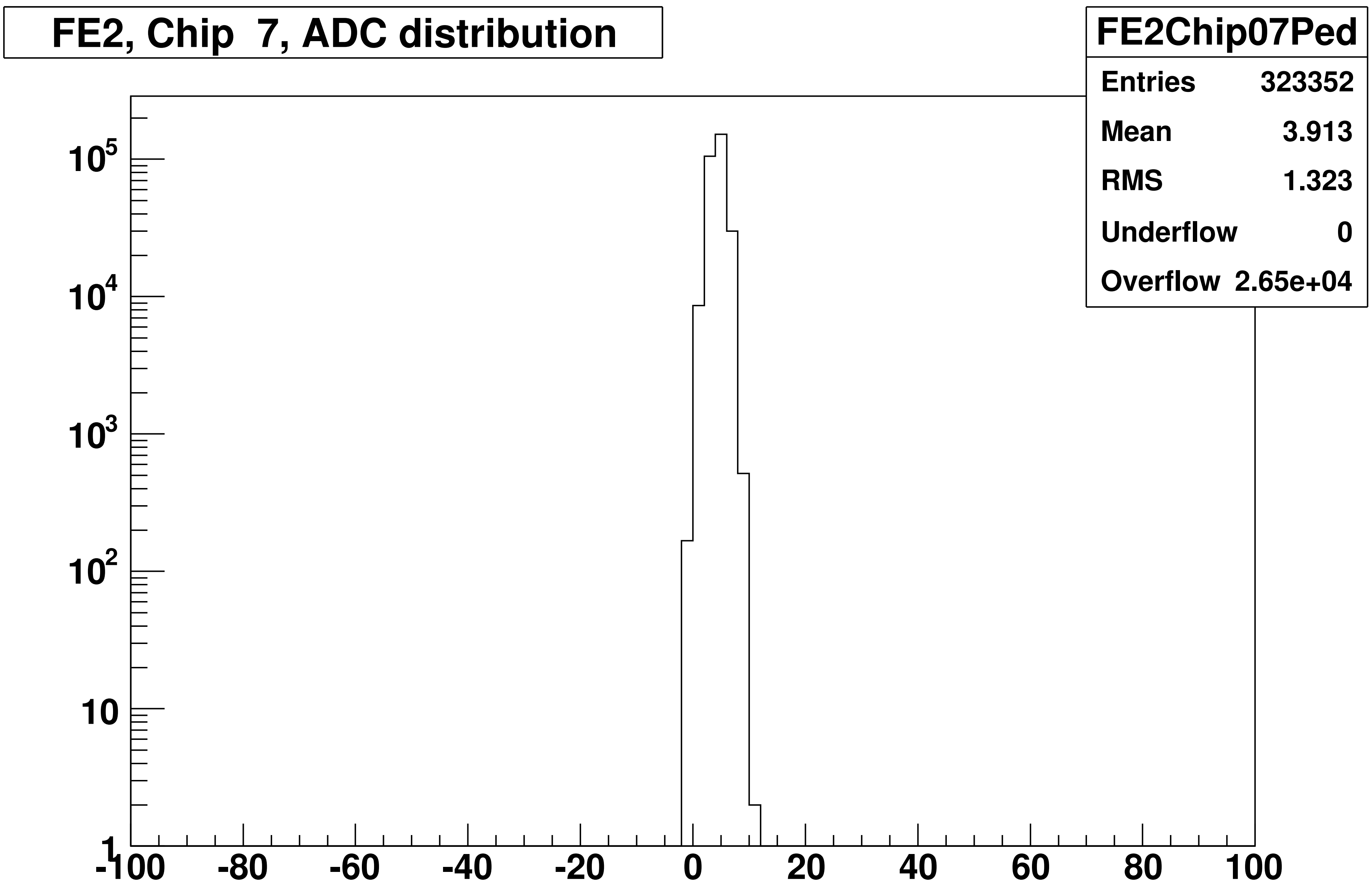

FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 323352
Mean -19.91
RMS 1.315
Underflow 0
Overflow 2.65e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 323352
Mean 1518
RMS 5112
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

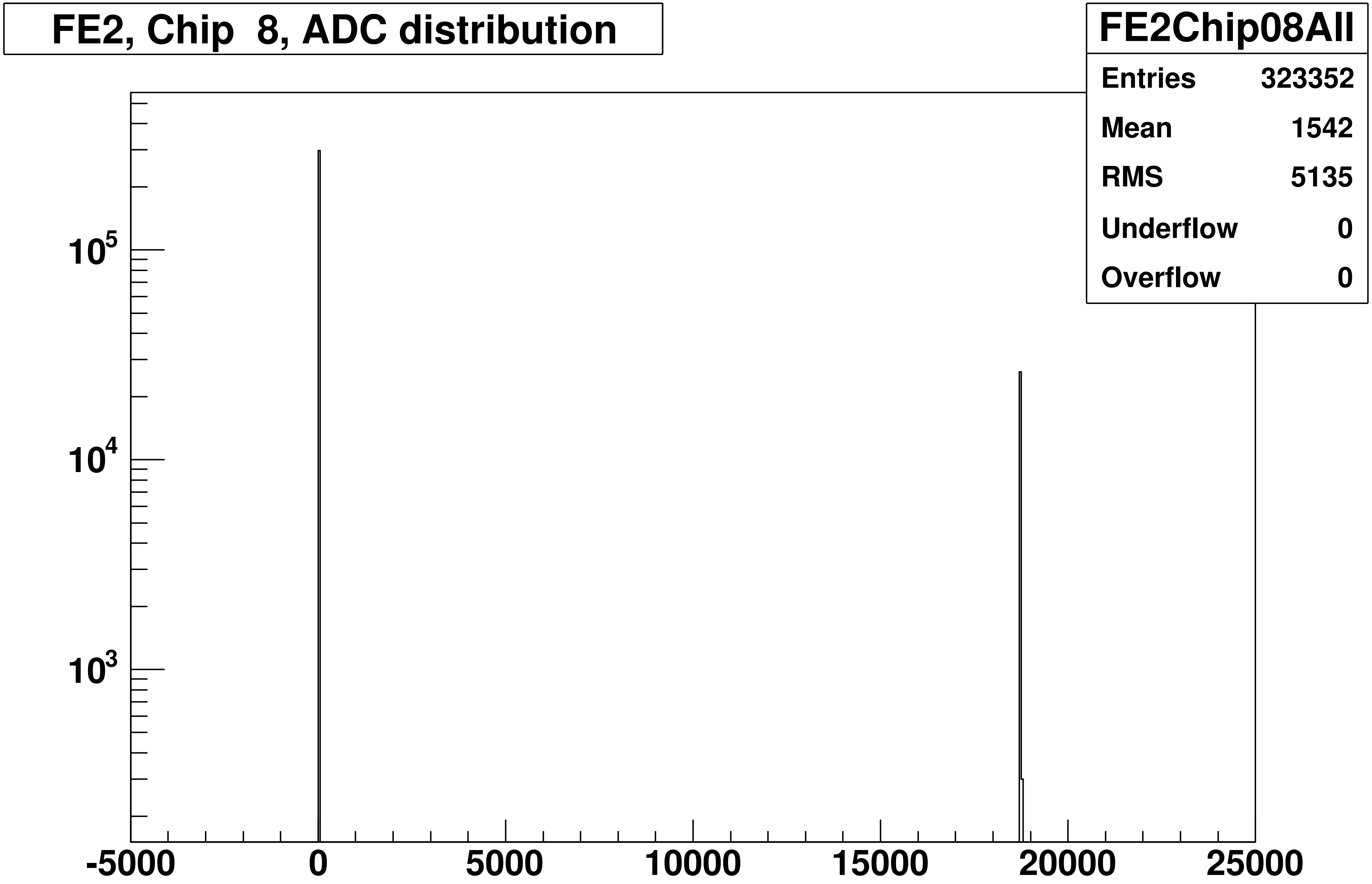

FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 323352
Mean -9.153
RMS 1.293
Underflow 0
Overflow 2.65e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

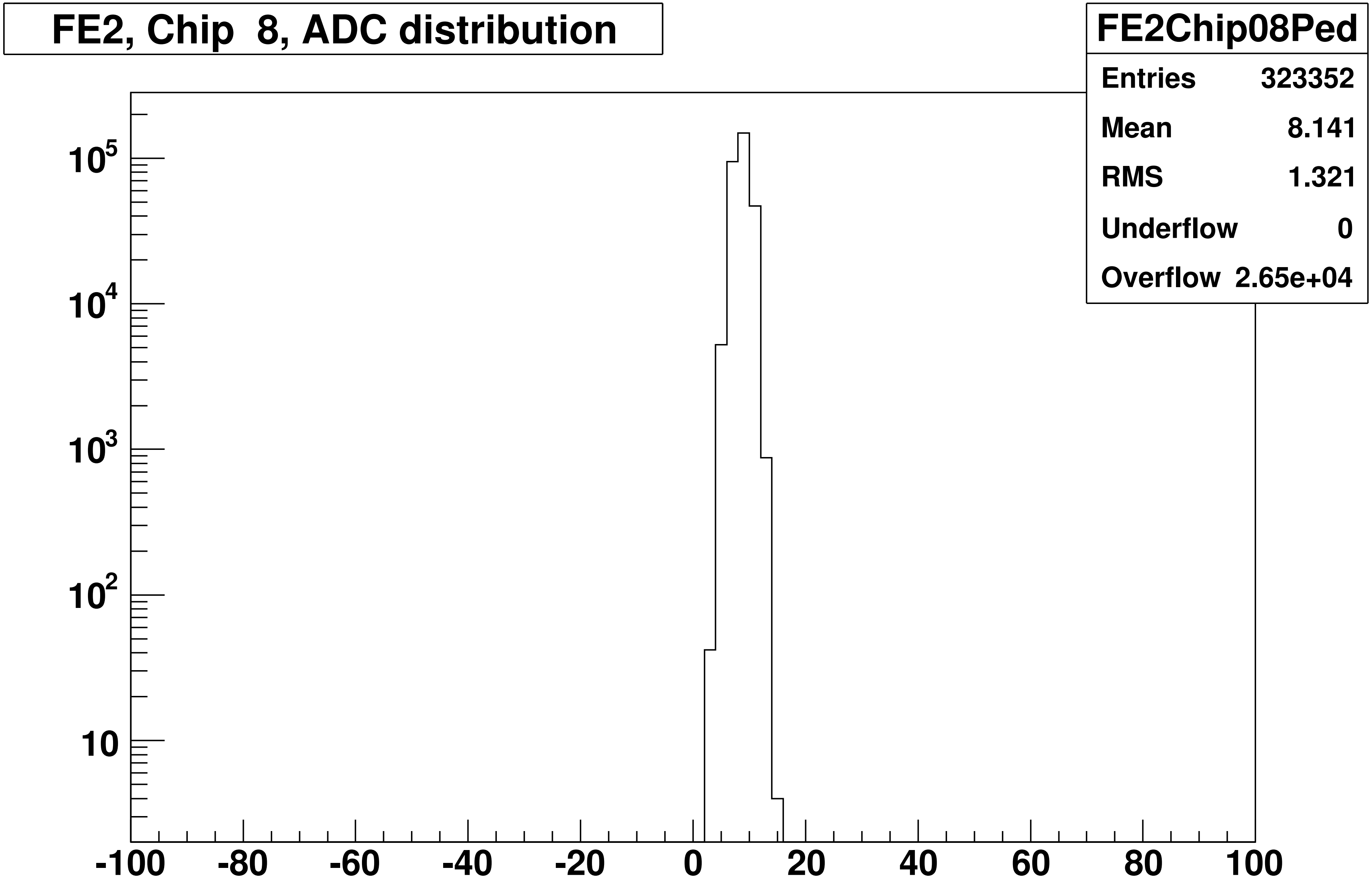


FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 323352
Mean 1533
RMS 5117
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

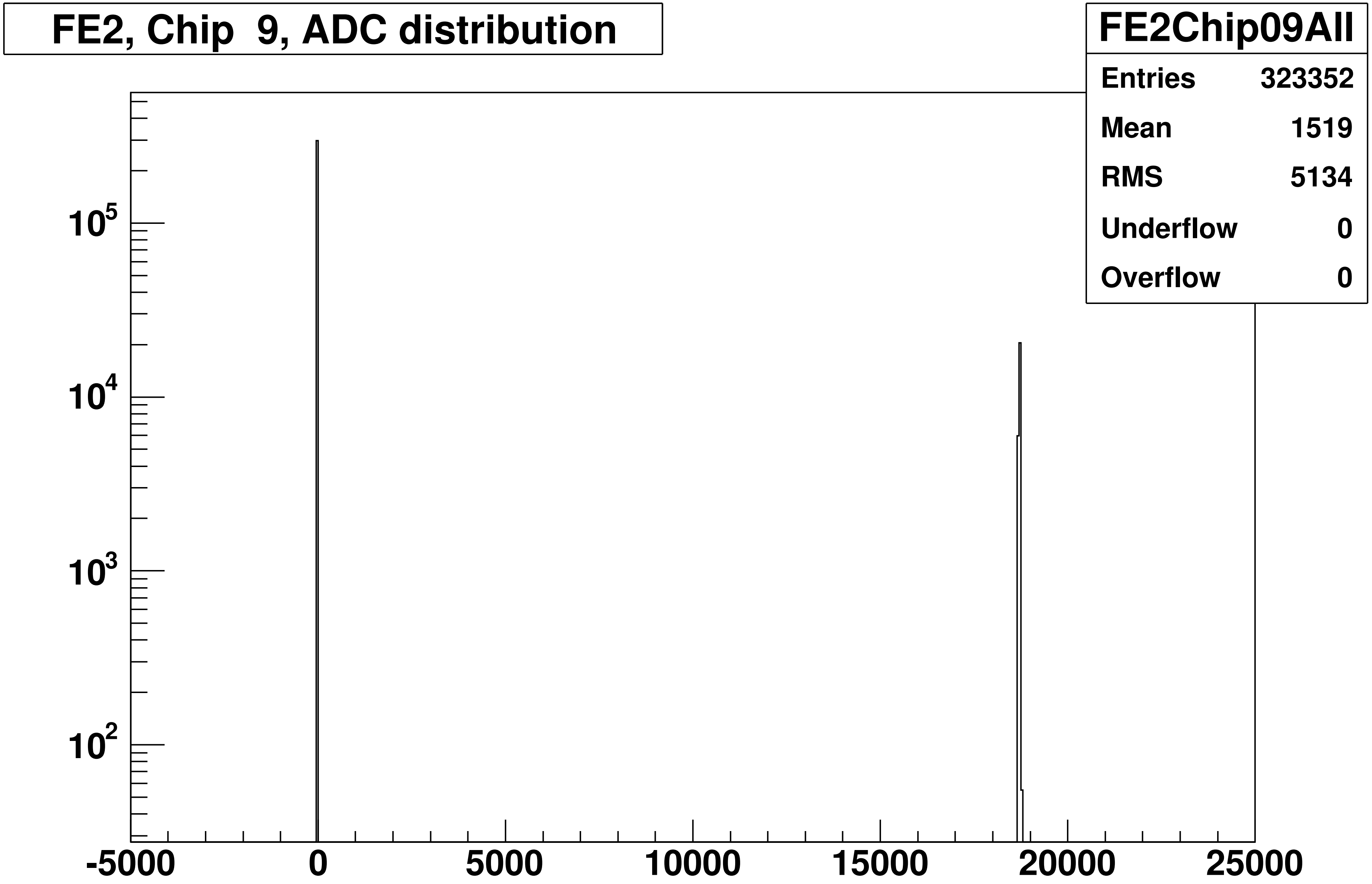


FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 323352
Mean 3.913
RMS 1.323
Underflow 0
Overflow 2.65e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

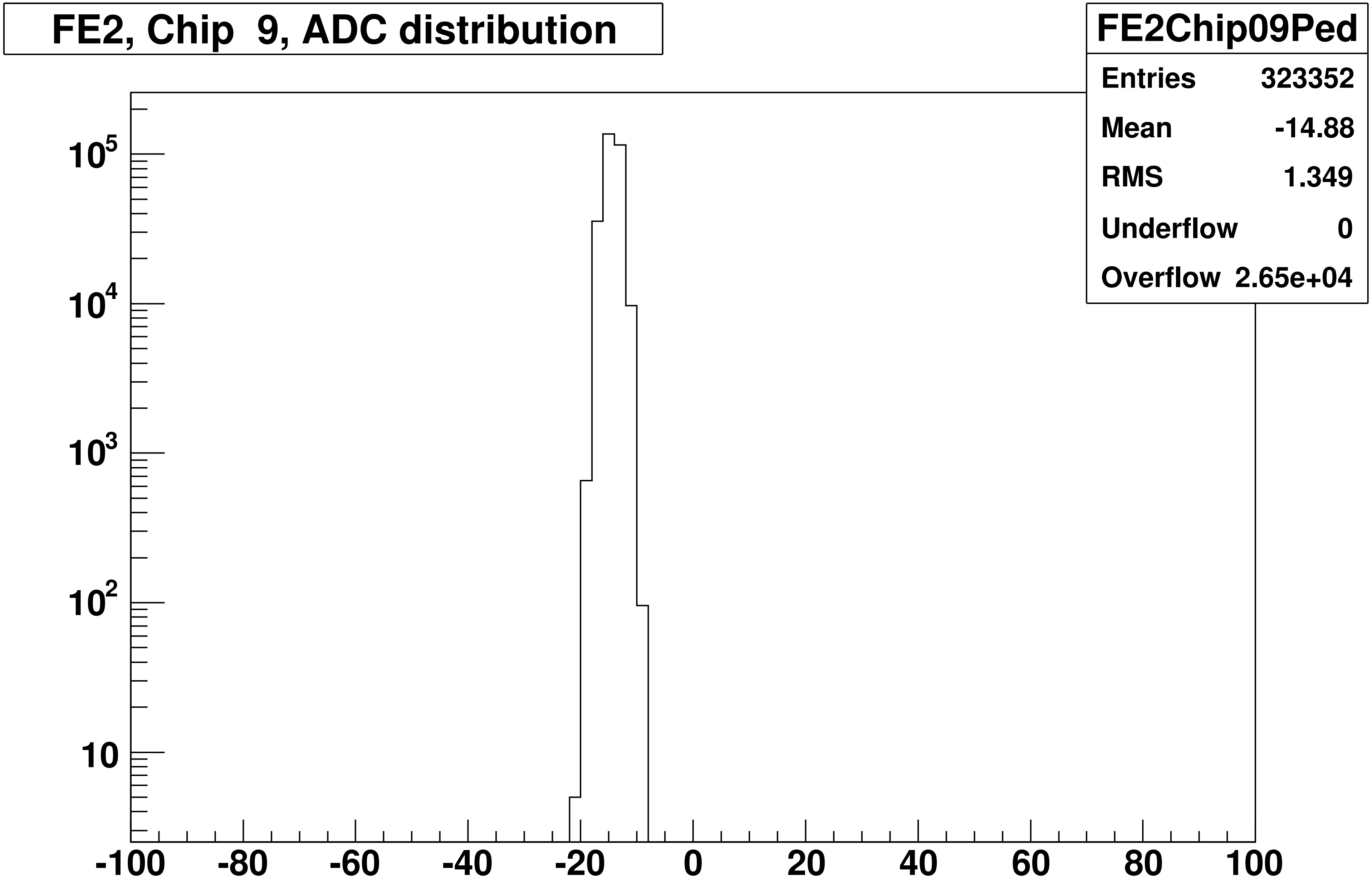

FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 323352
Mean 1542
RMS 5135
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

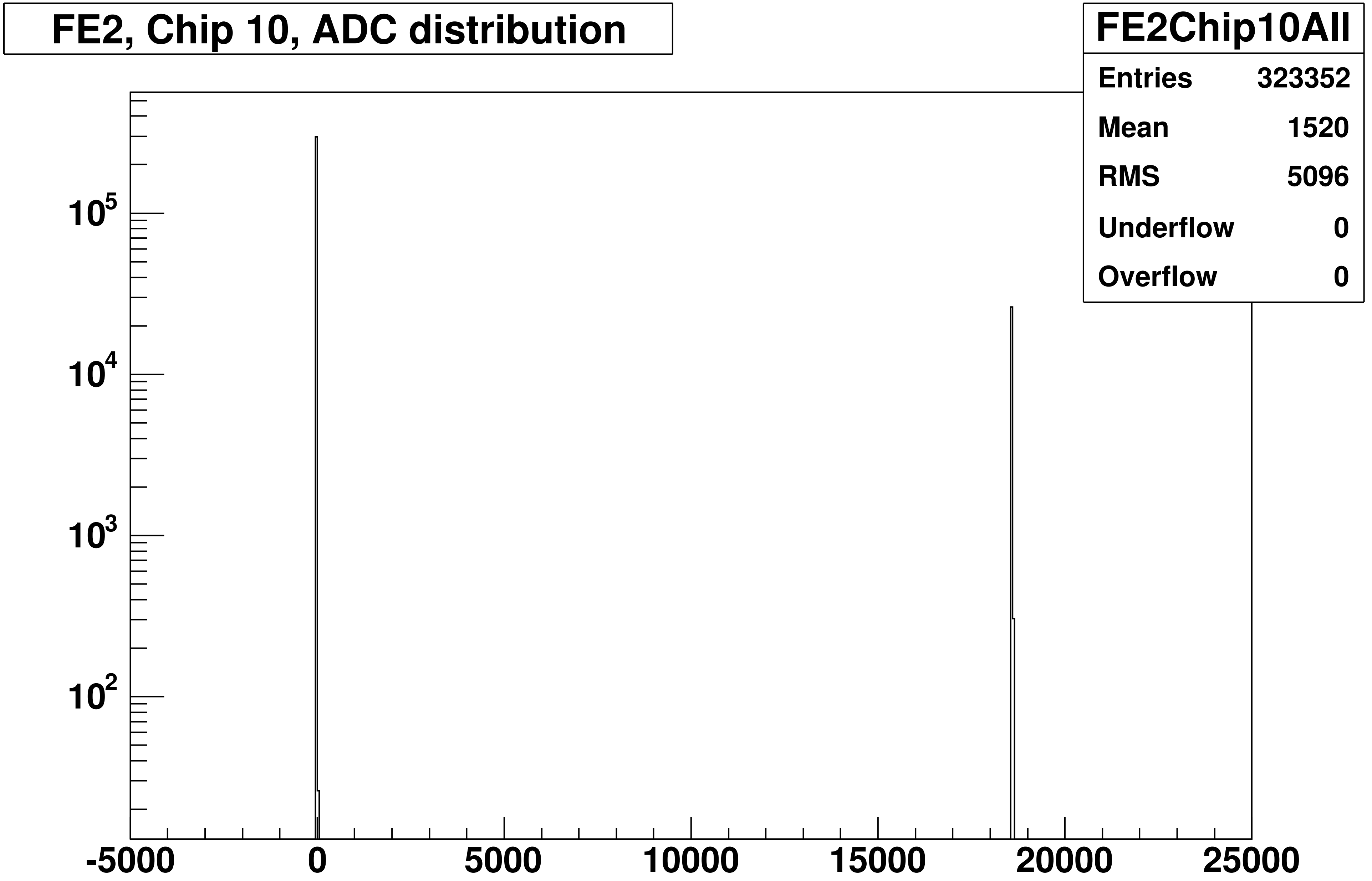


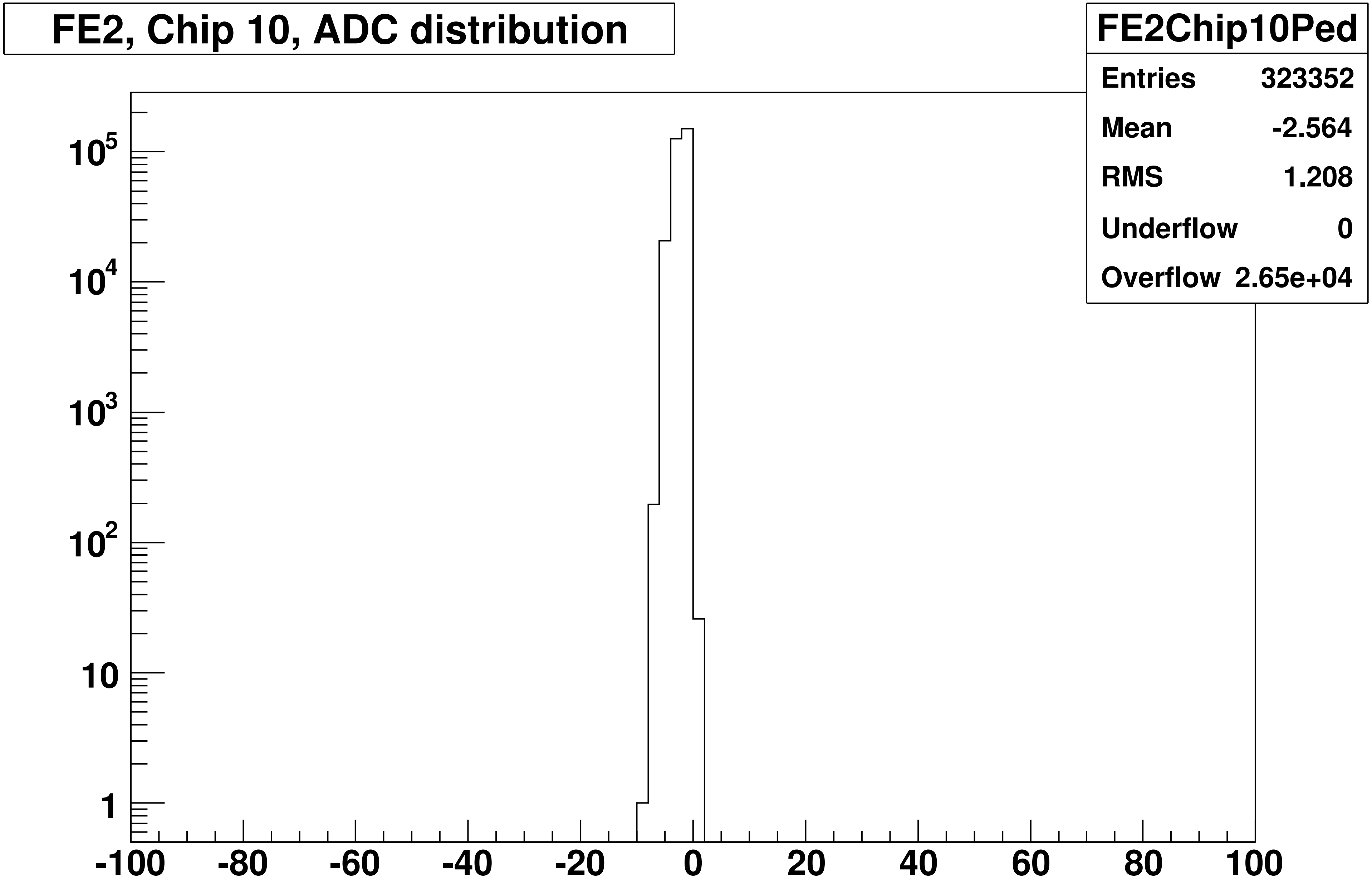
FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 323352
Mean 8.141
RMS 1.321
Underflow 0
Overflow 2.65e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



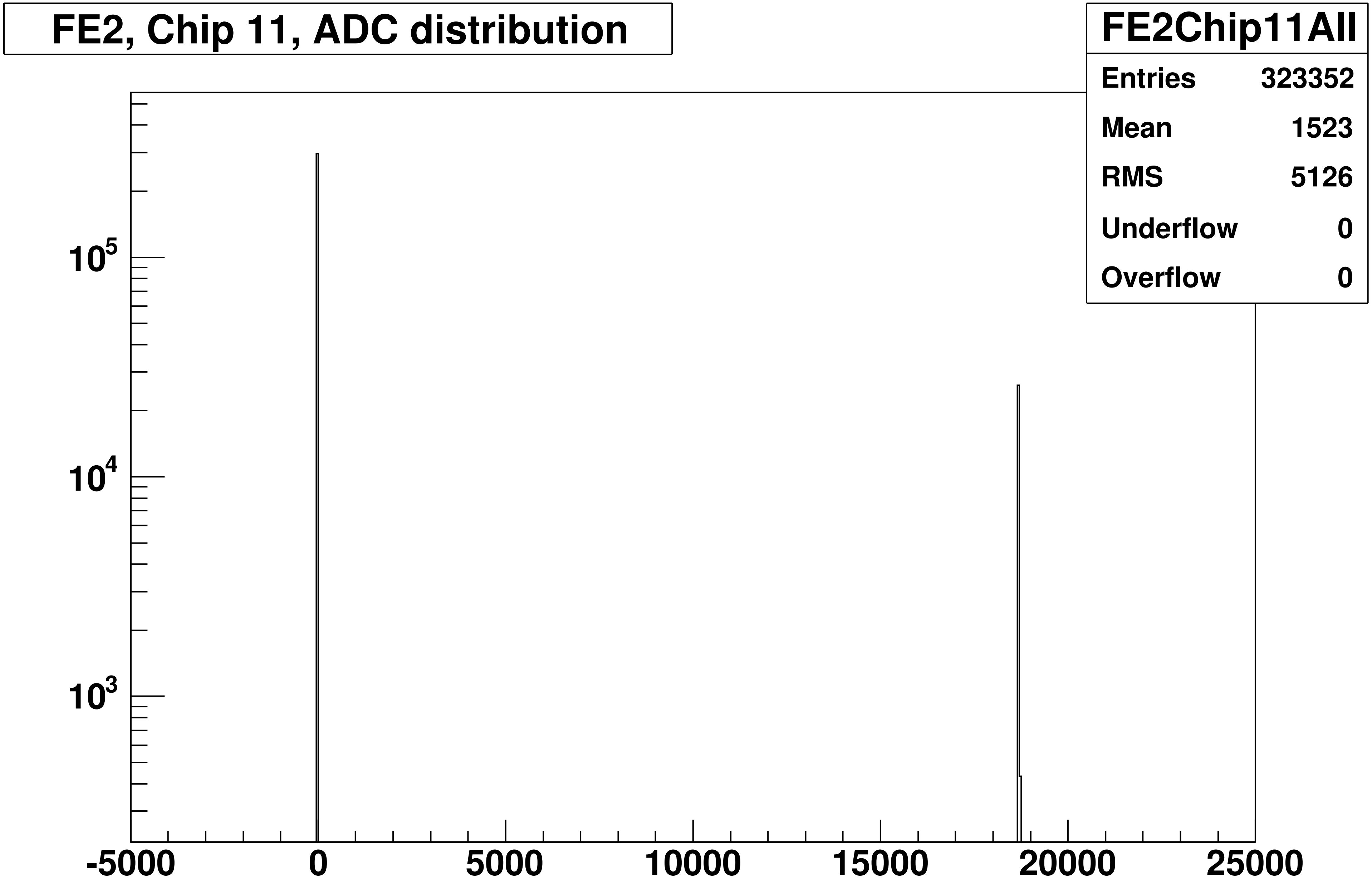
FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 323352
Mean 1519
RMS 5134
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

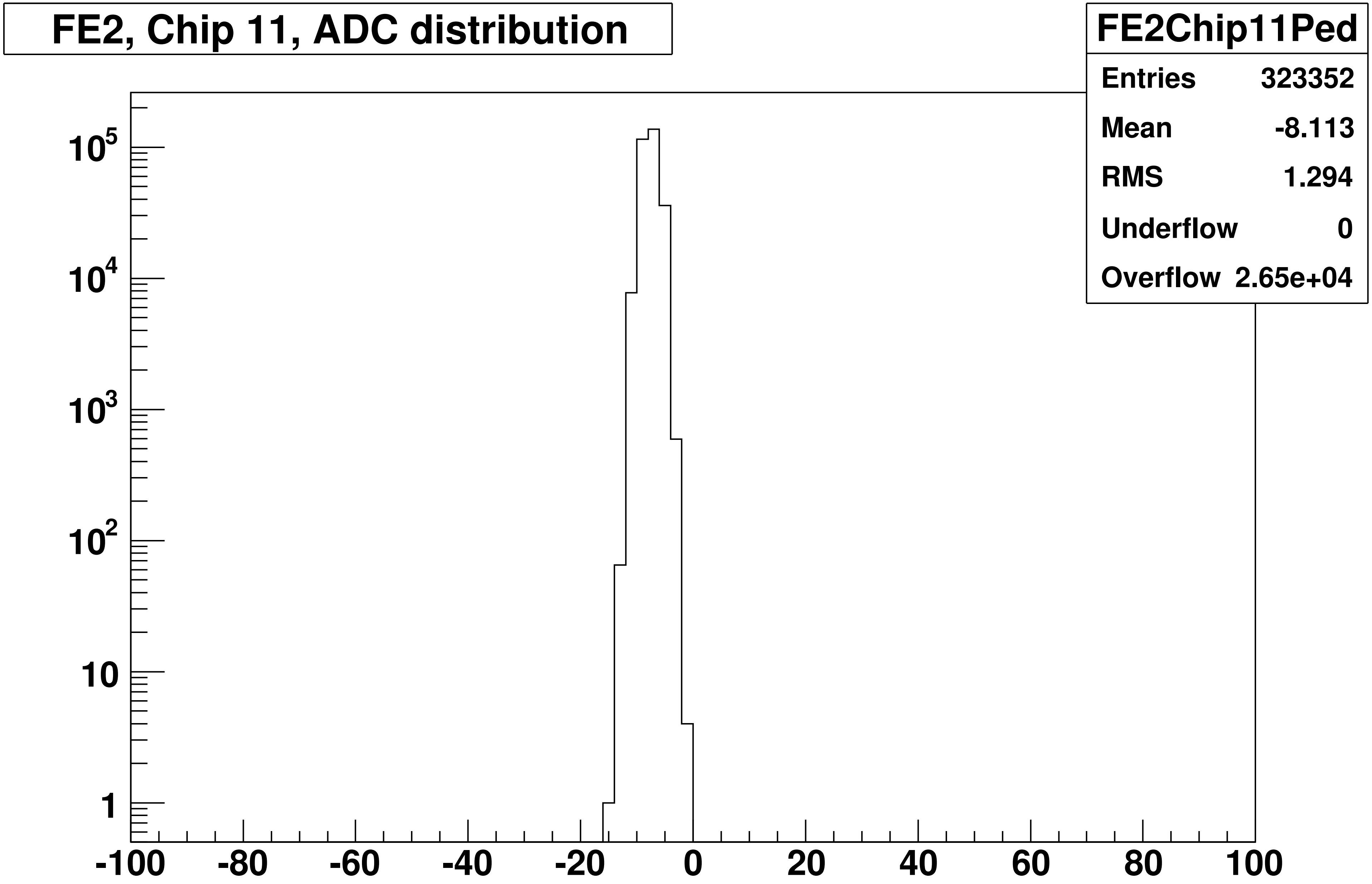

FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 323352
Mean -14.88
RMS 1.349
Underflow 0
Overflow 2.65e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

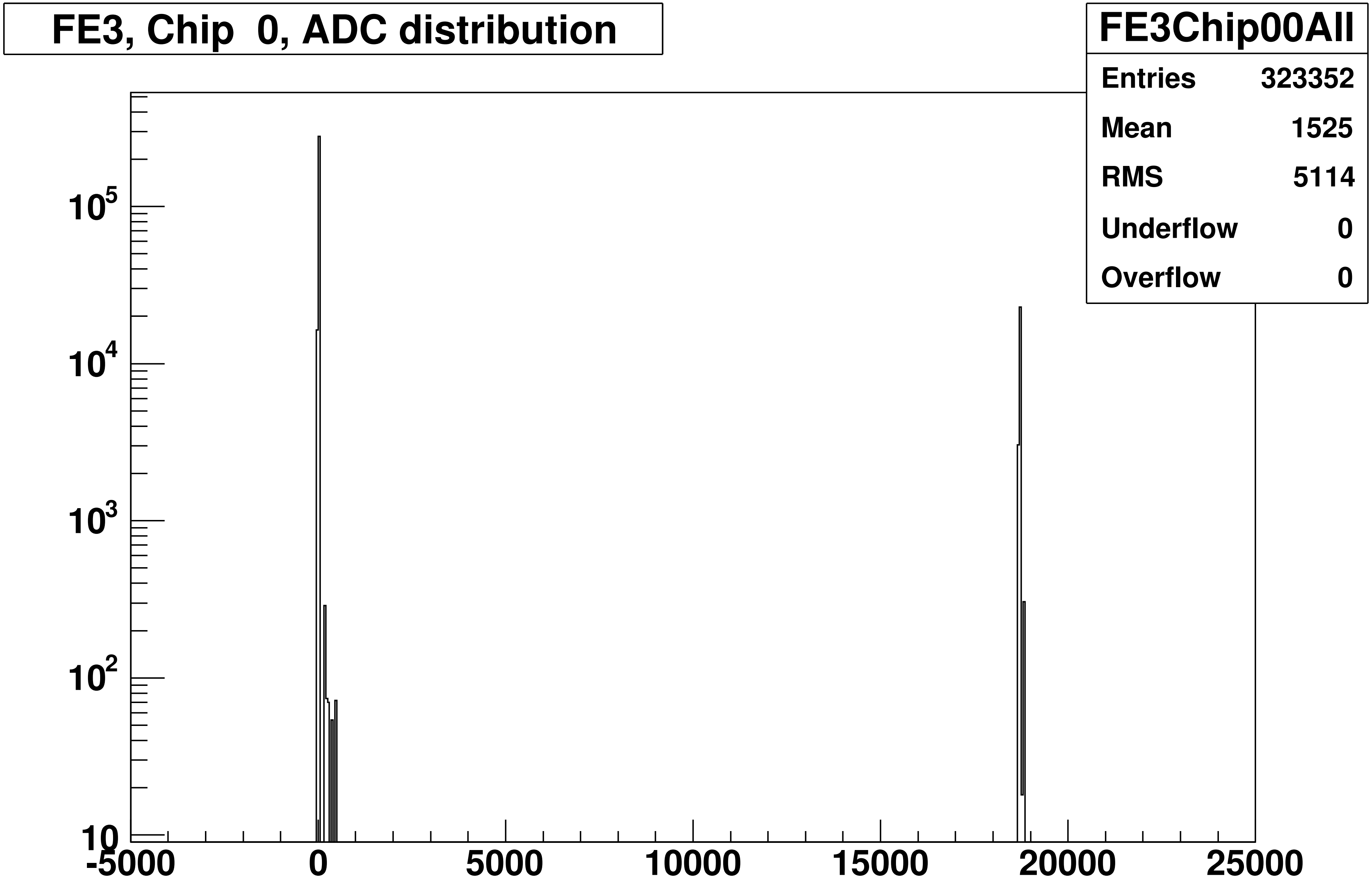

FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 323352
Mean 1520
RMS 5096
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

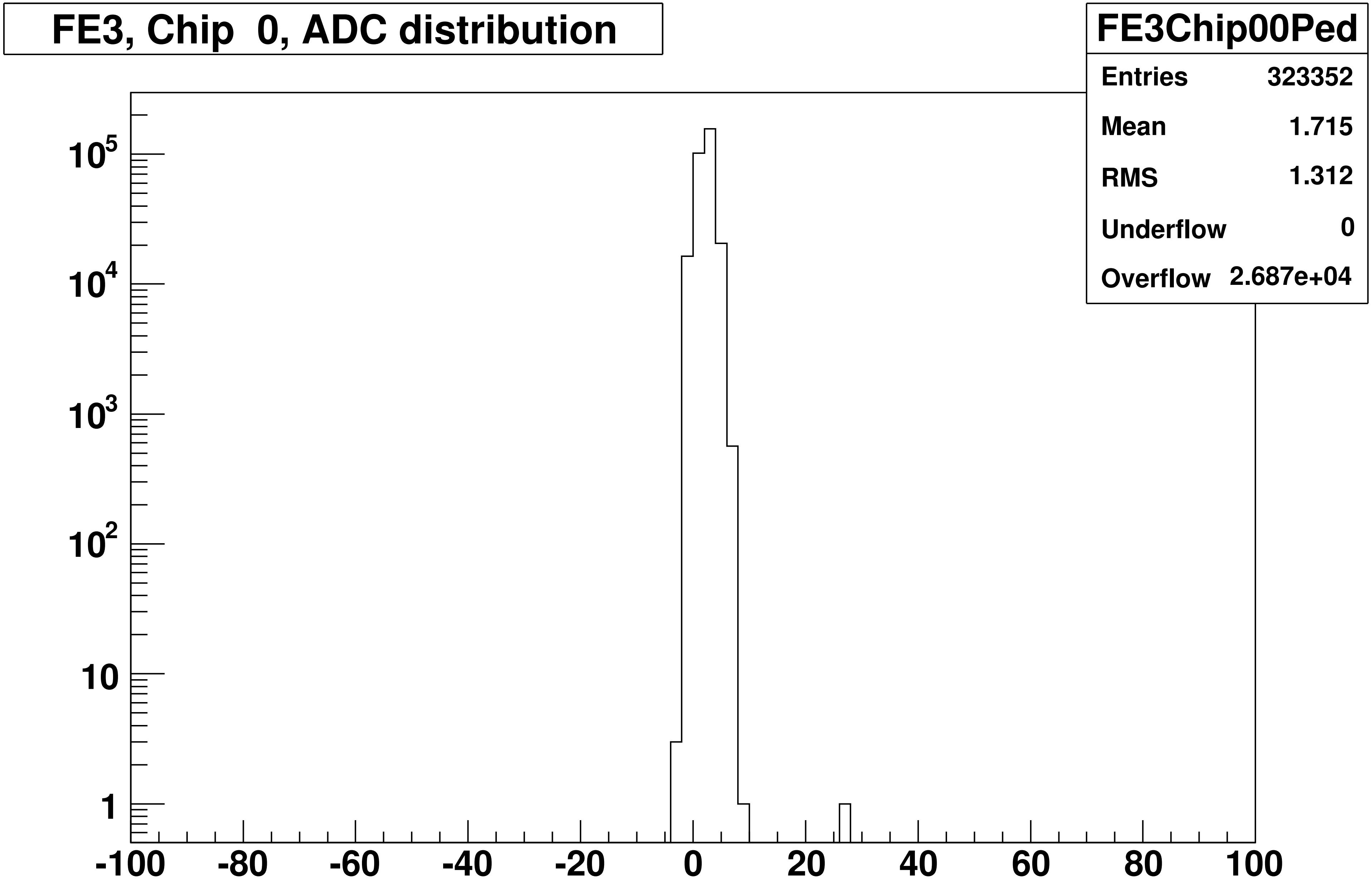


FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 323352
Mean -2.564
RMS 1.208
Underflow 0
Overflow 2.65e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

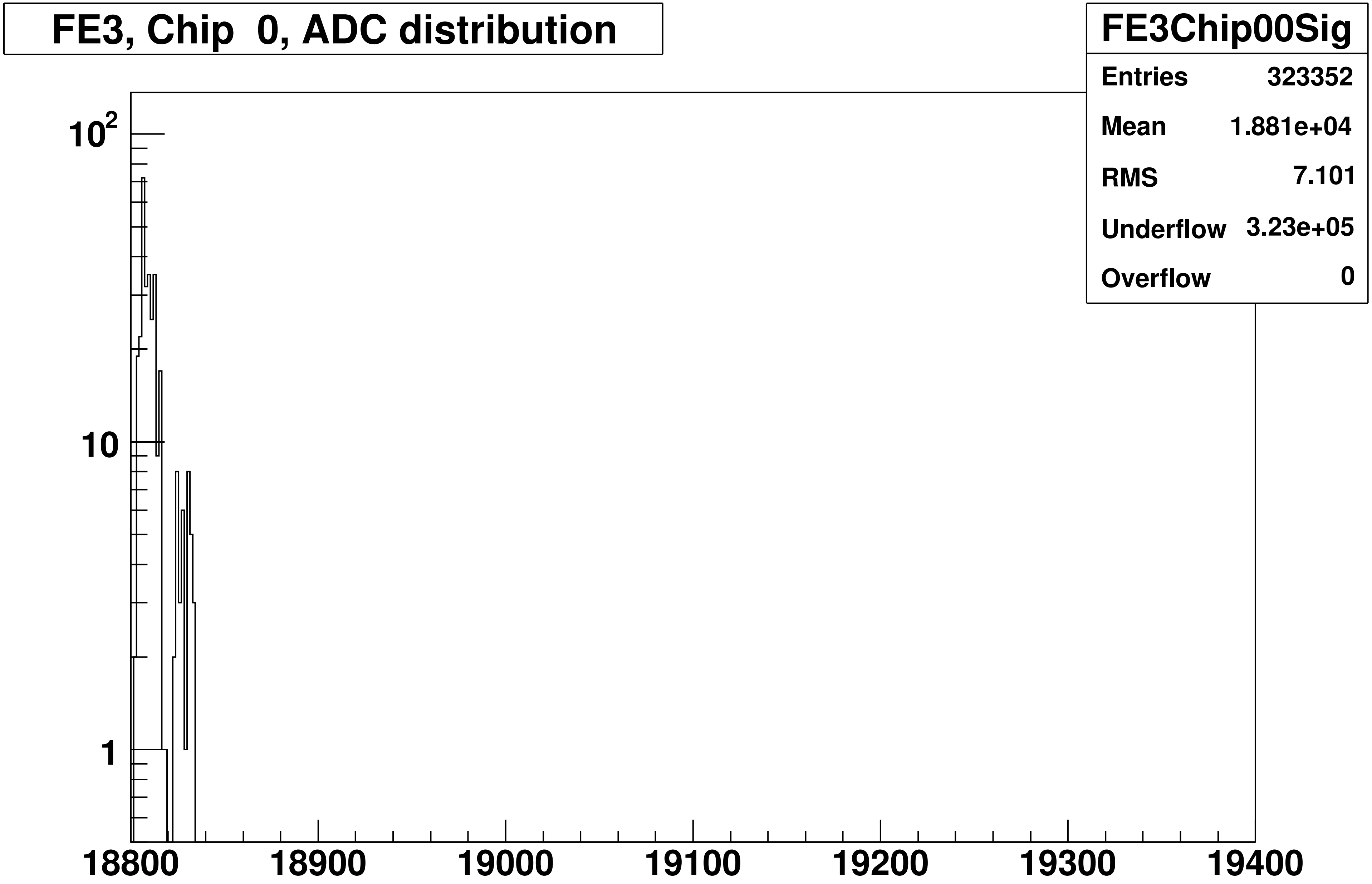

FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 323352
Mean 1523
RMS 5126
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

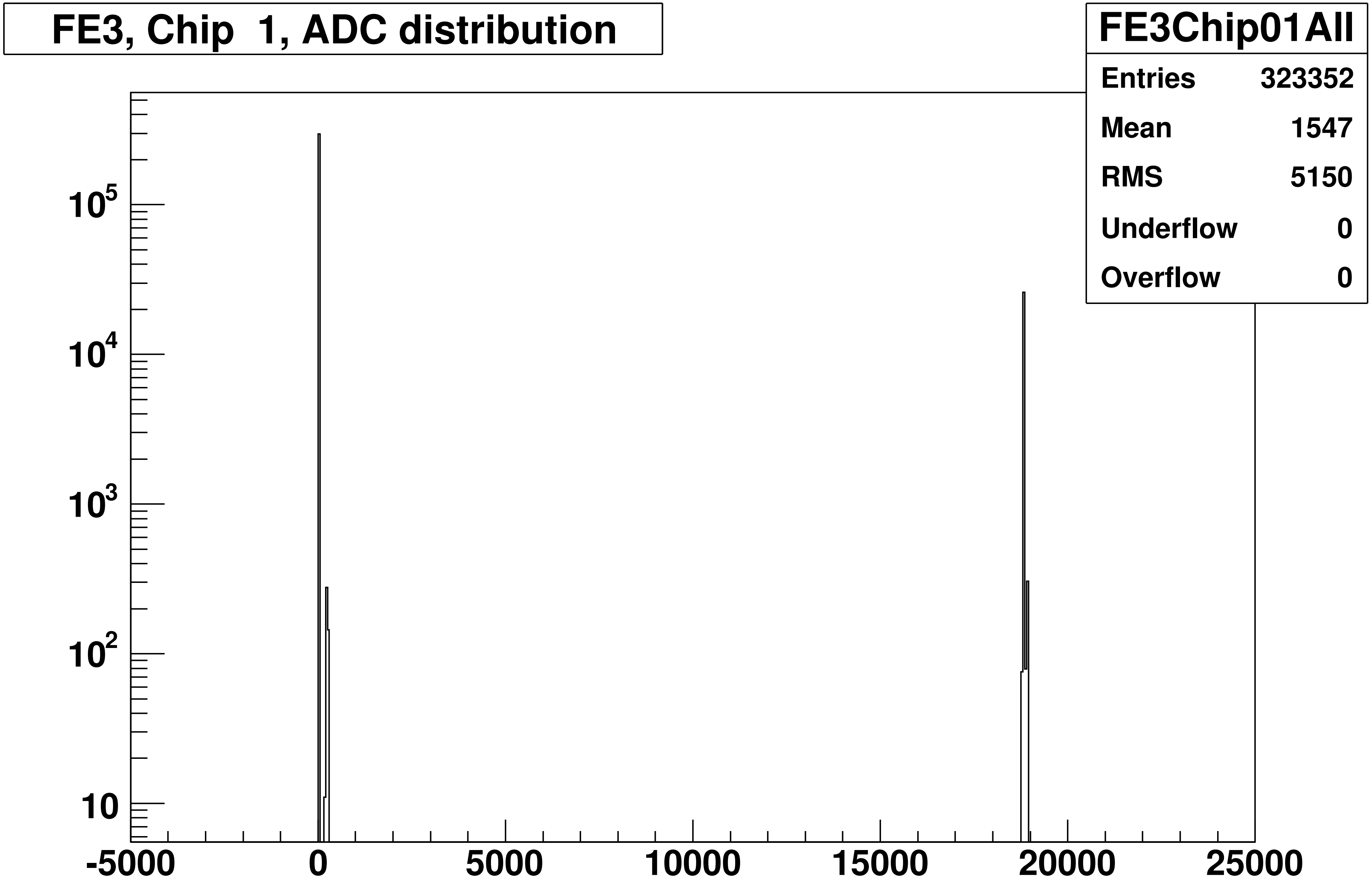

FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 323352
Mean -8.113
RMS 1.294
Underflow 0
Overflow 2.65e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

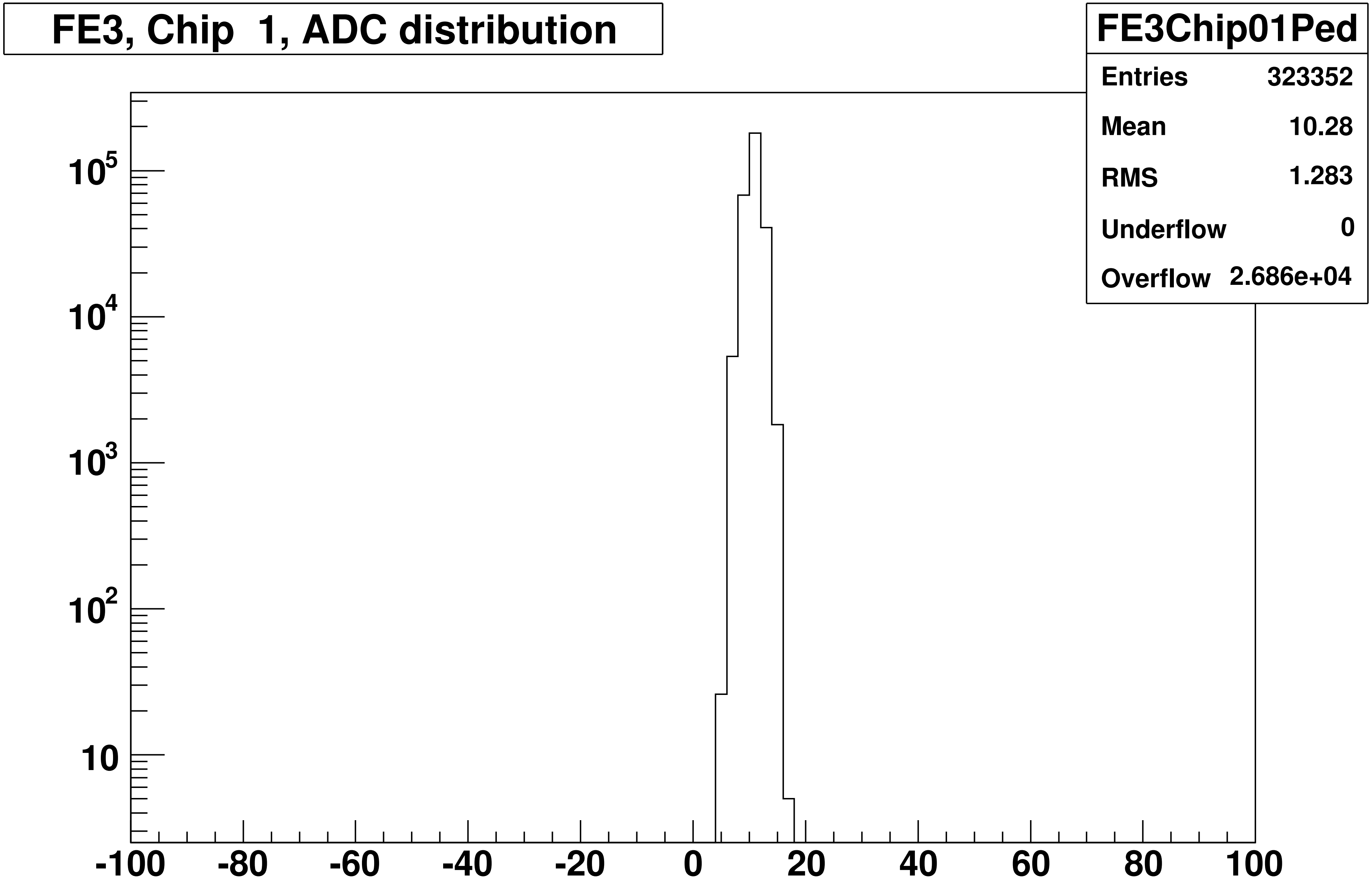

FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 323352
Mean 1525
RMS 5114
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

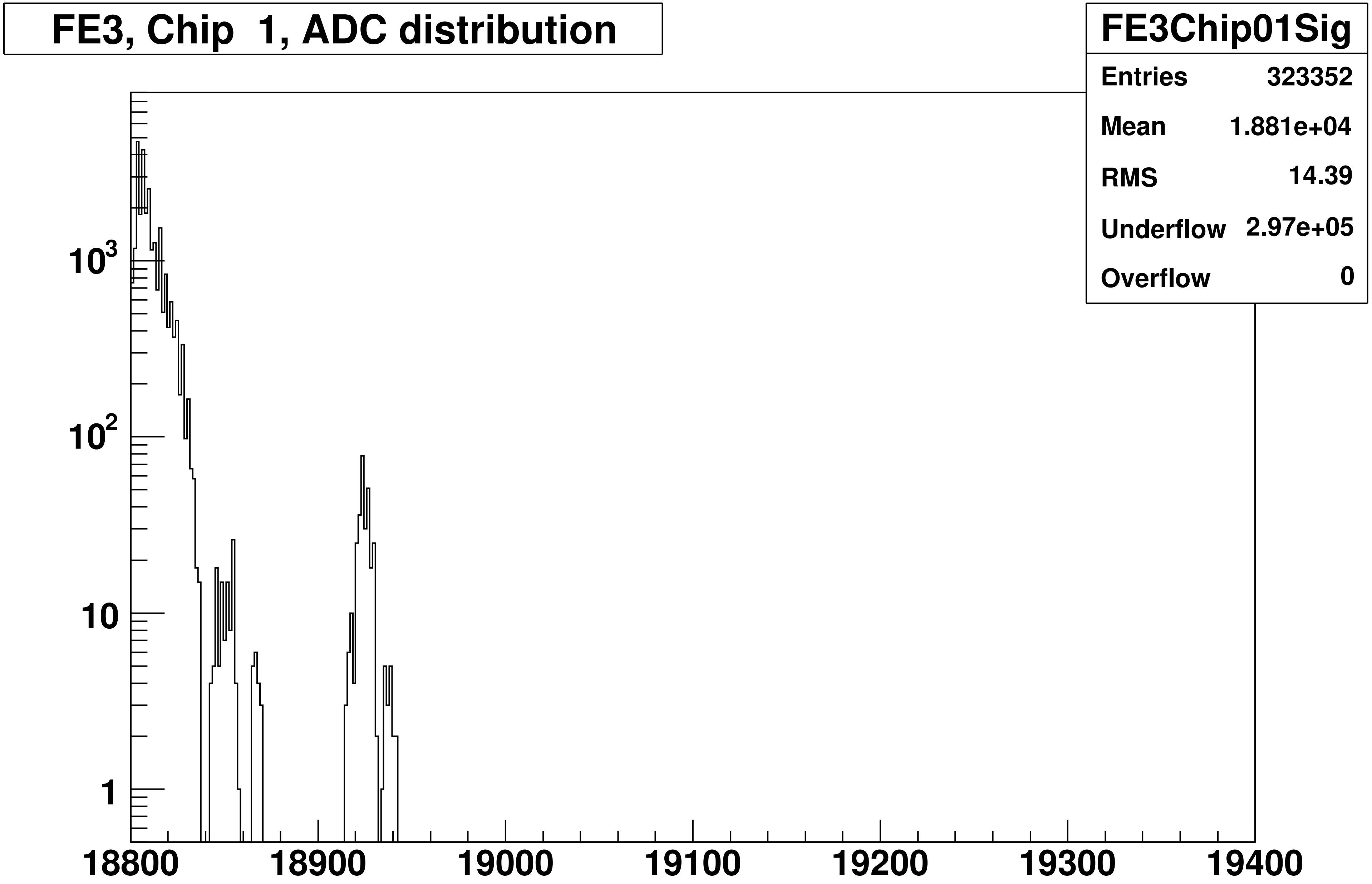


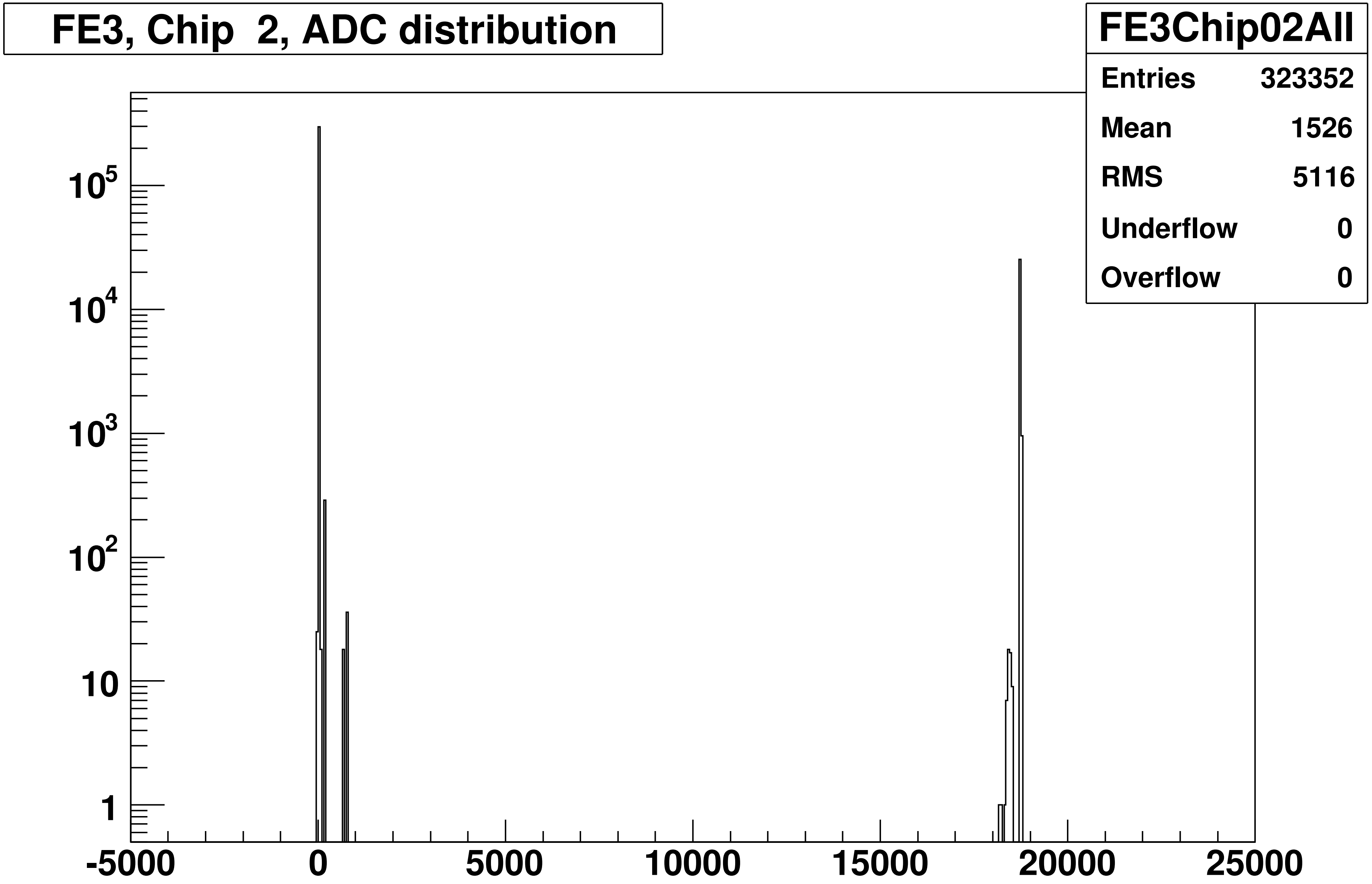
FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 323352
Mean 1.715
RMS 1.312
Underflow 0
Overflow 2.687e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 323352
Mean 1.881e+04
RMS 7.101
Underflow 3.23e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

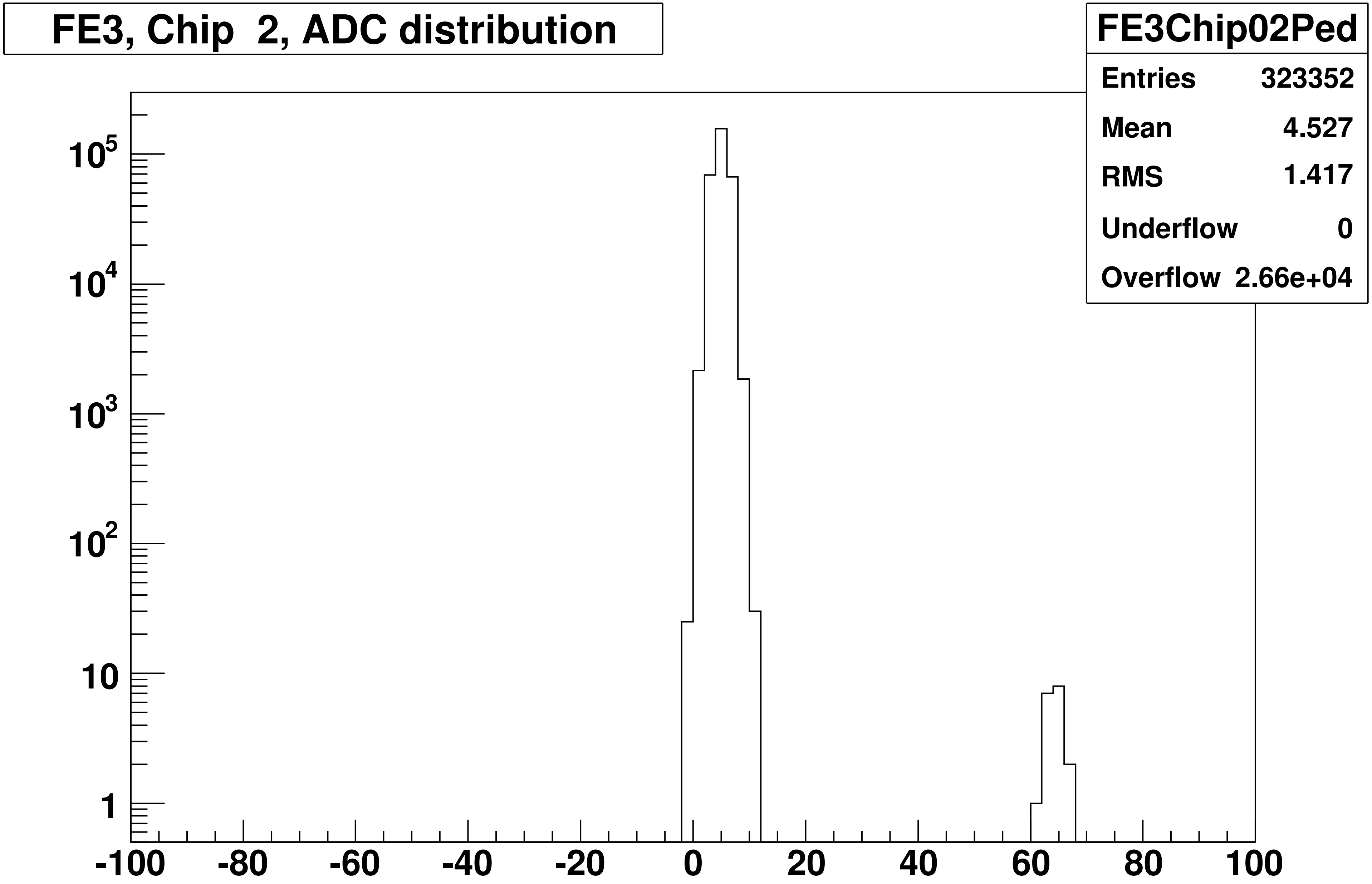

FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 323352
Mean 1547
RMS 5150
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

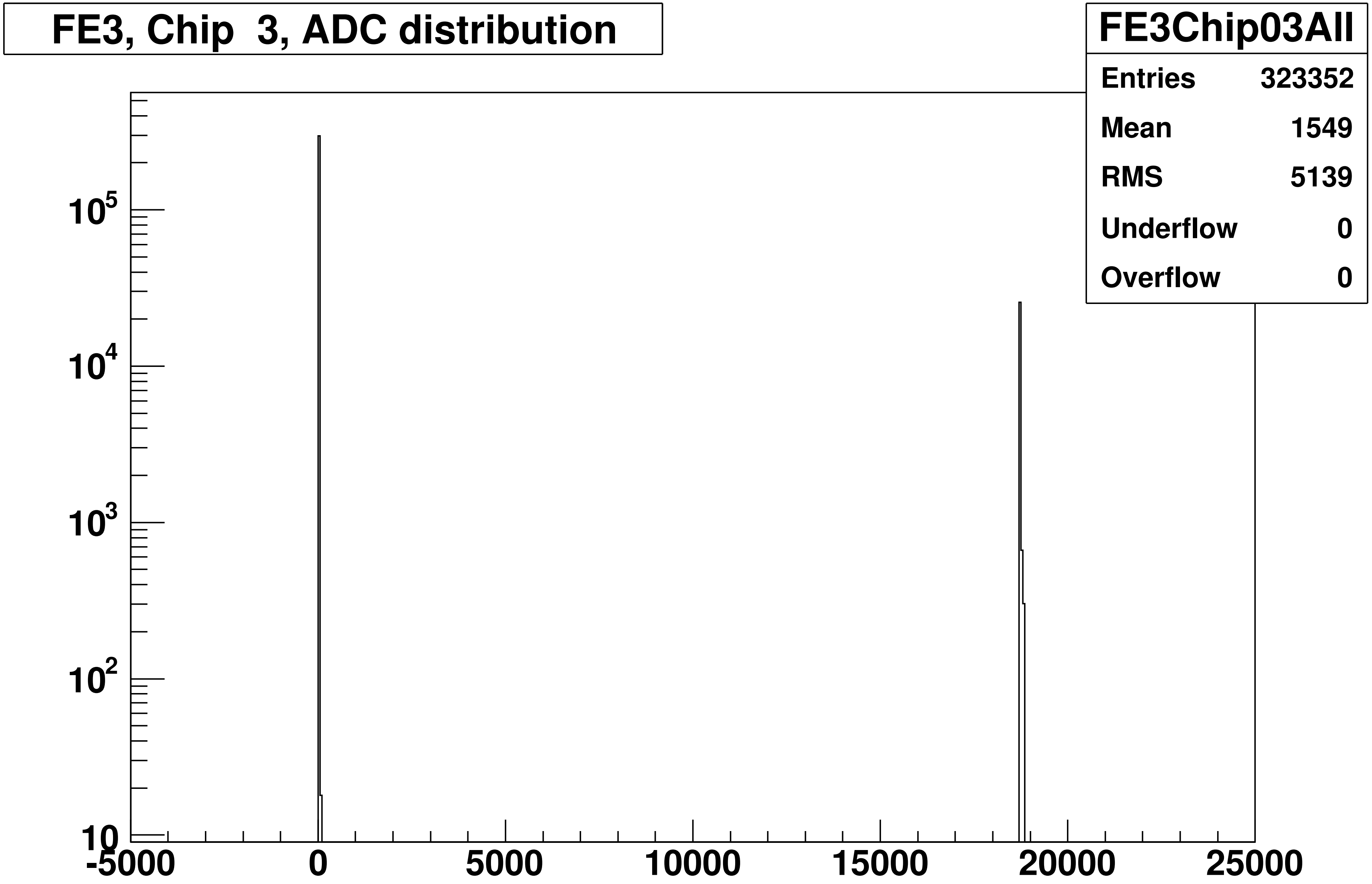

FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 323352
Mean 10.28
RMS 1.283
Underflow 0
Overflow 2.686e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

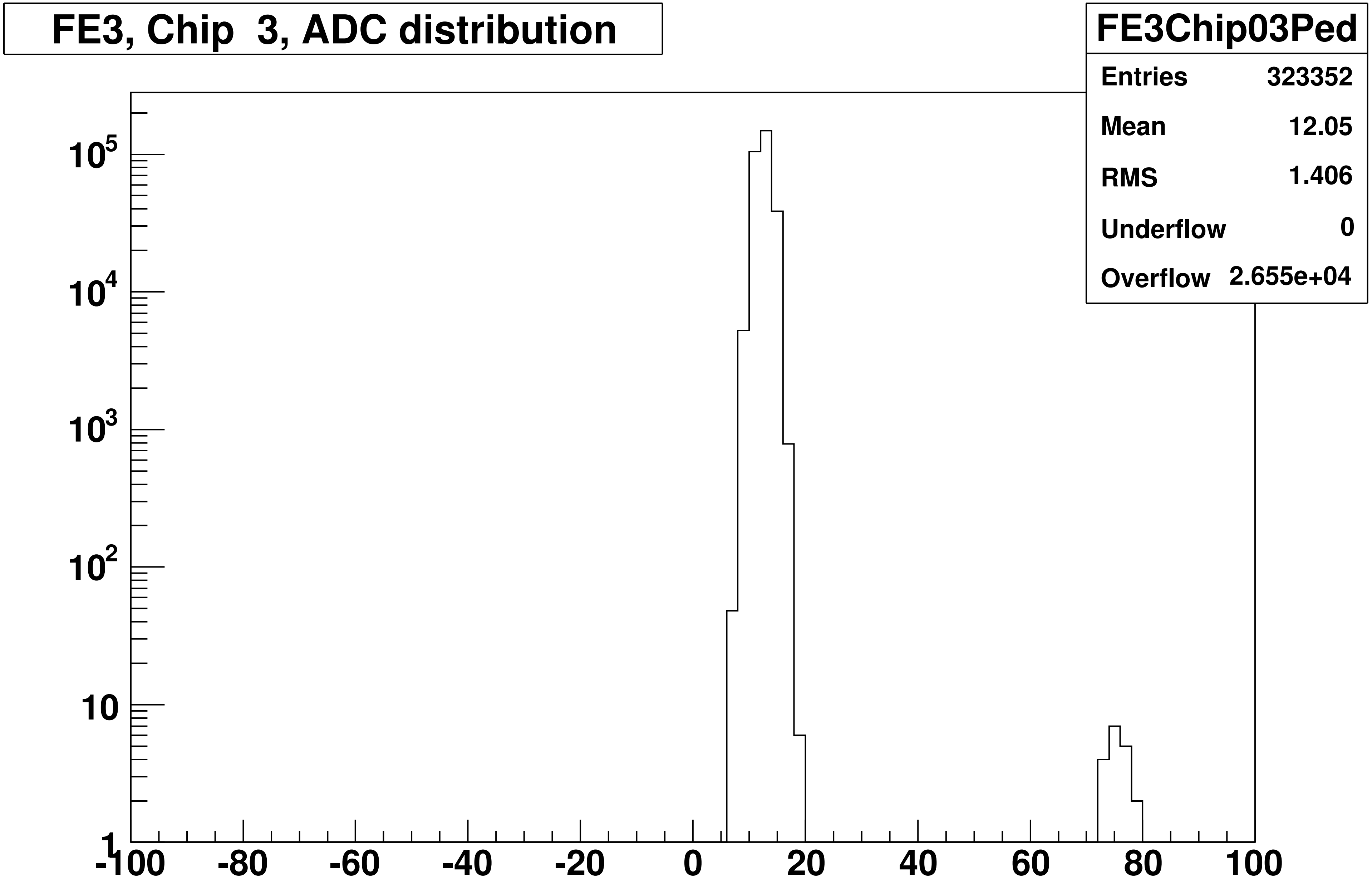

FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 323352
Mean 1.881e+04
RMS 14.39
Underflow 2.97e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

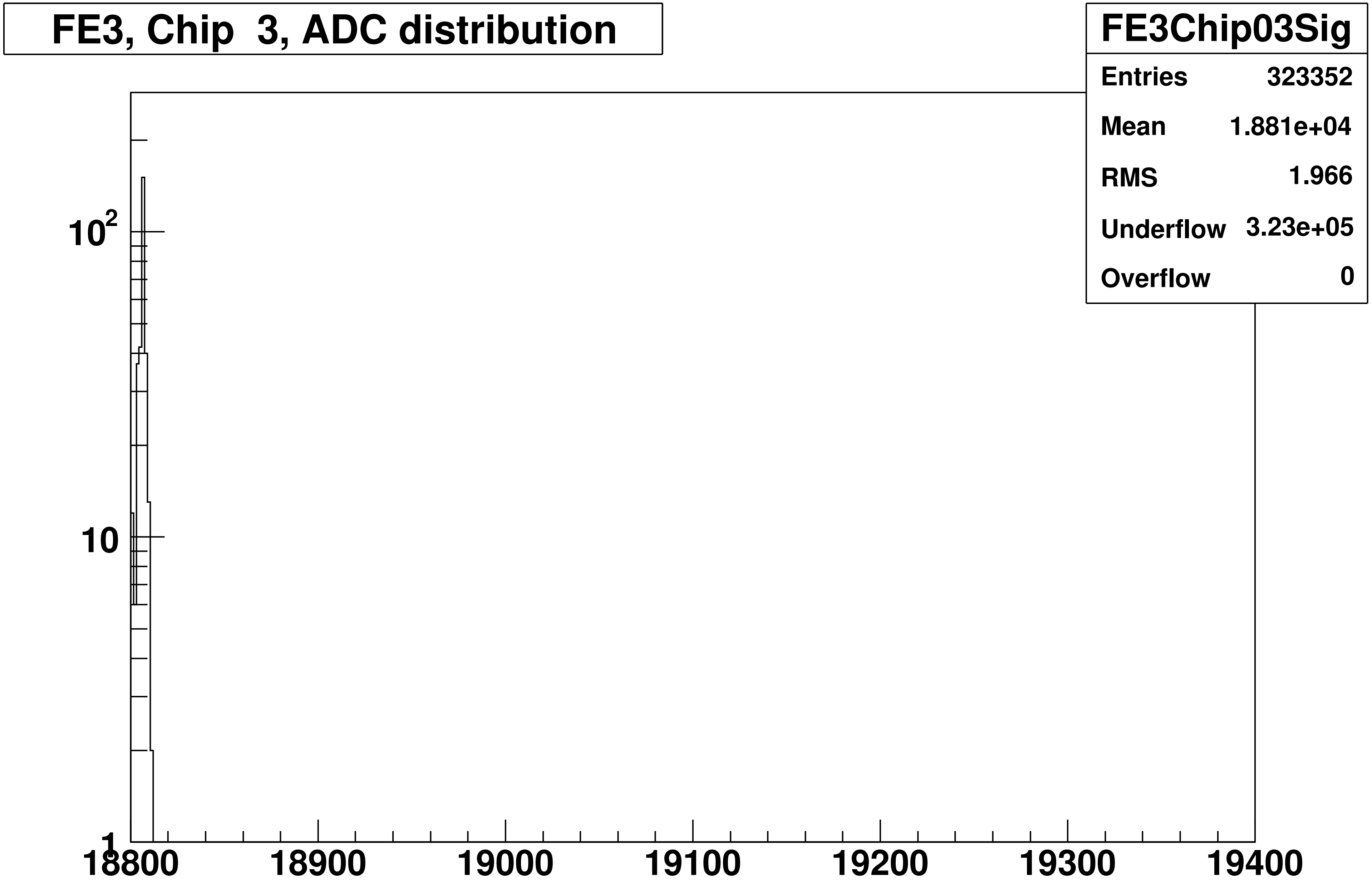


FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 323352
Mean 1526
RMS 5116
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

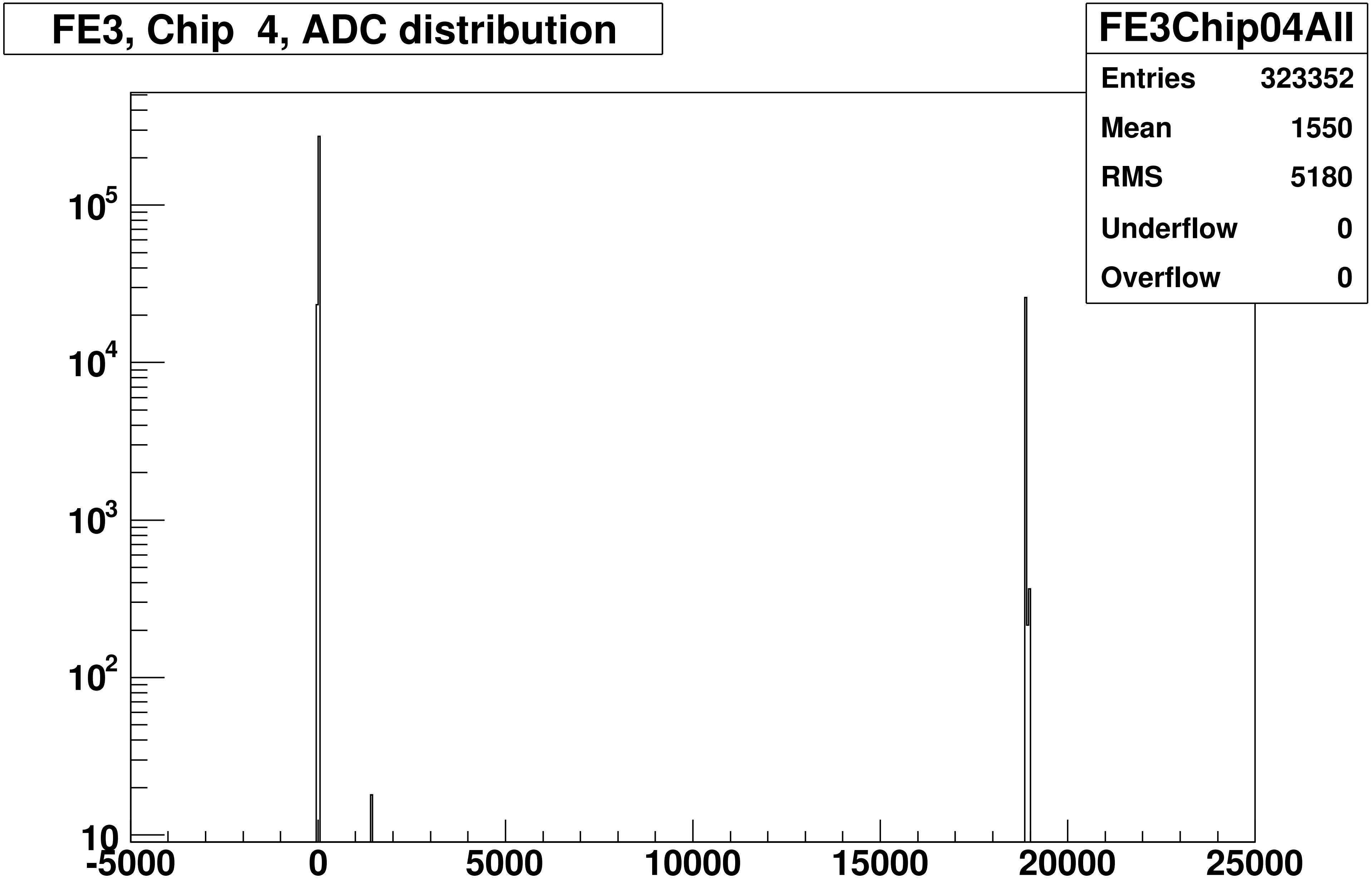

FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 323352
Mean 4.527
RMS 1.417
Underflow 0
Overflow 2.66e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

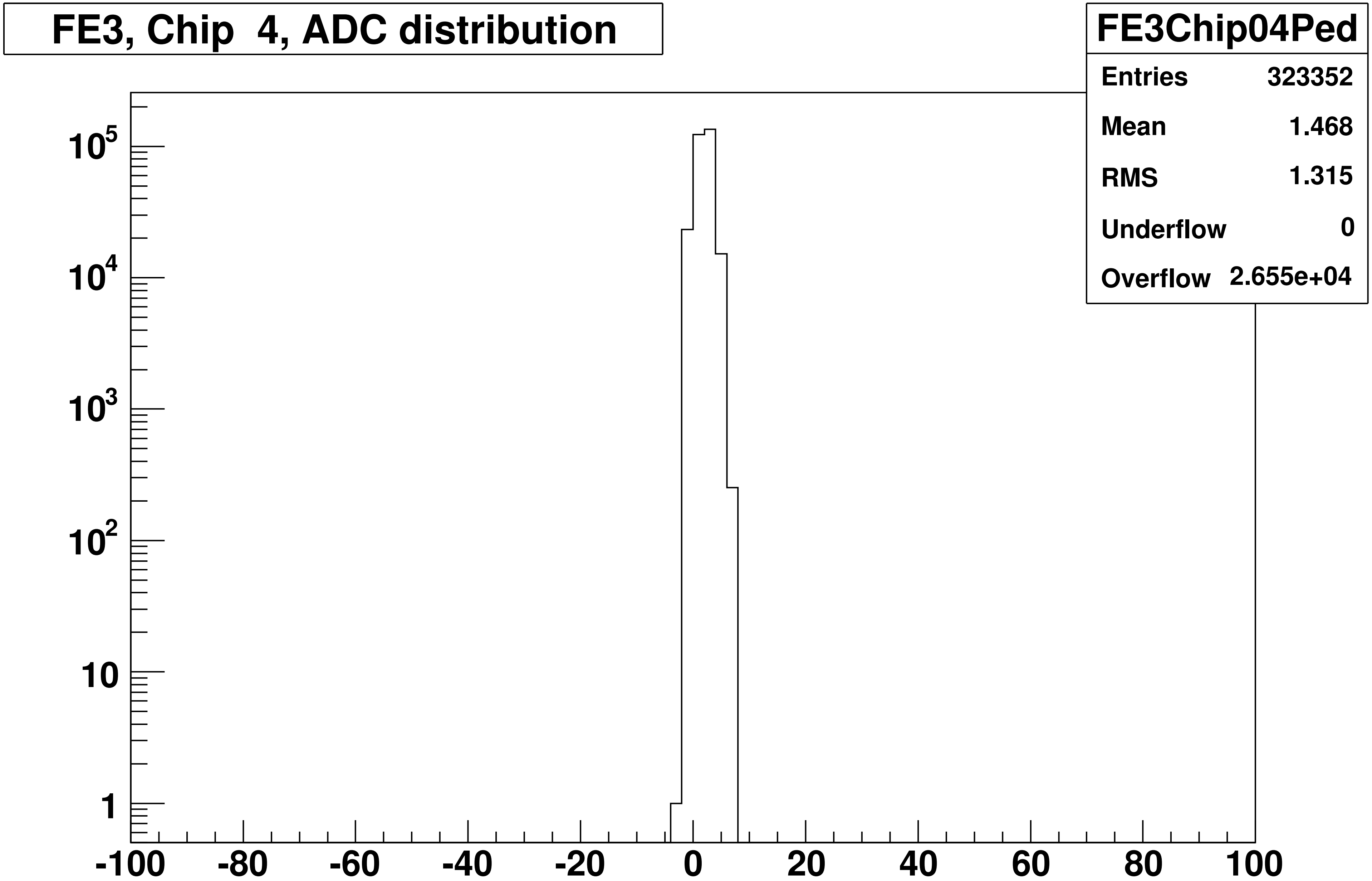

FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 323352
Mean 1549
RMS 5139
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 323352
Mean 12.05
RMS 1.406
Underflow 0
Overflow 2.655e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

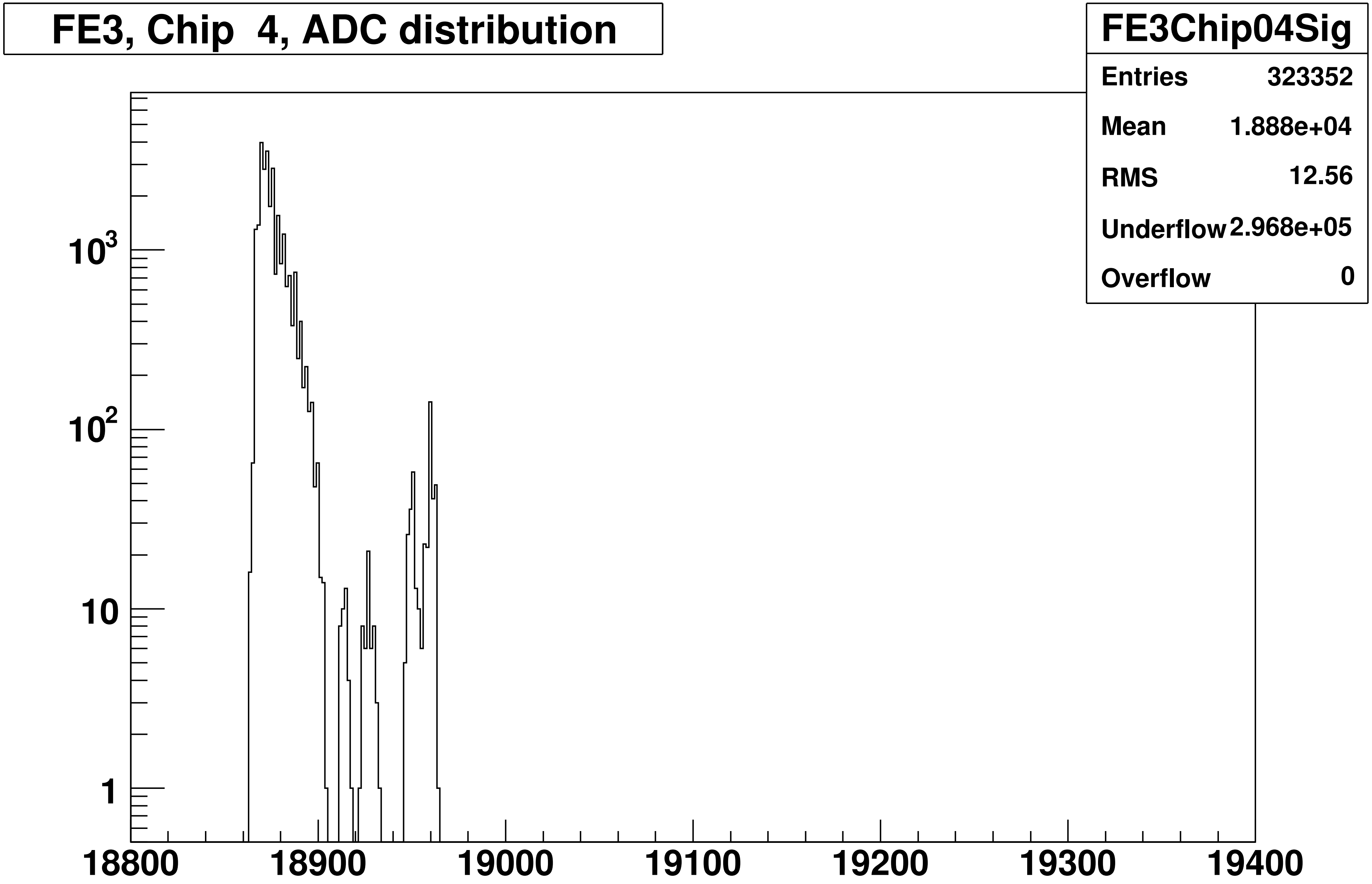


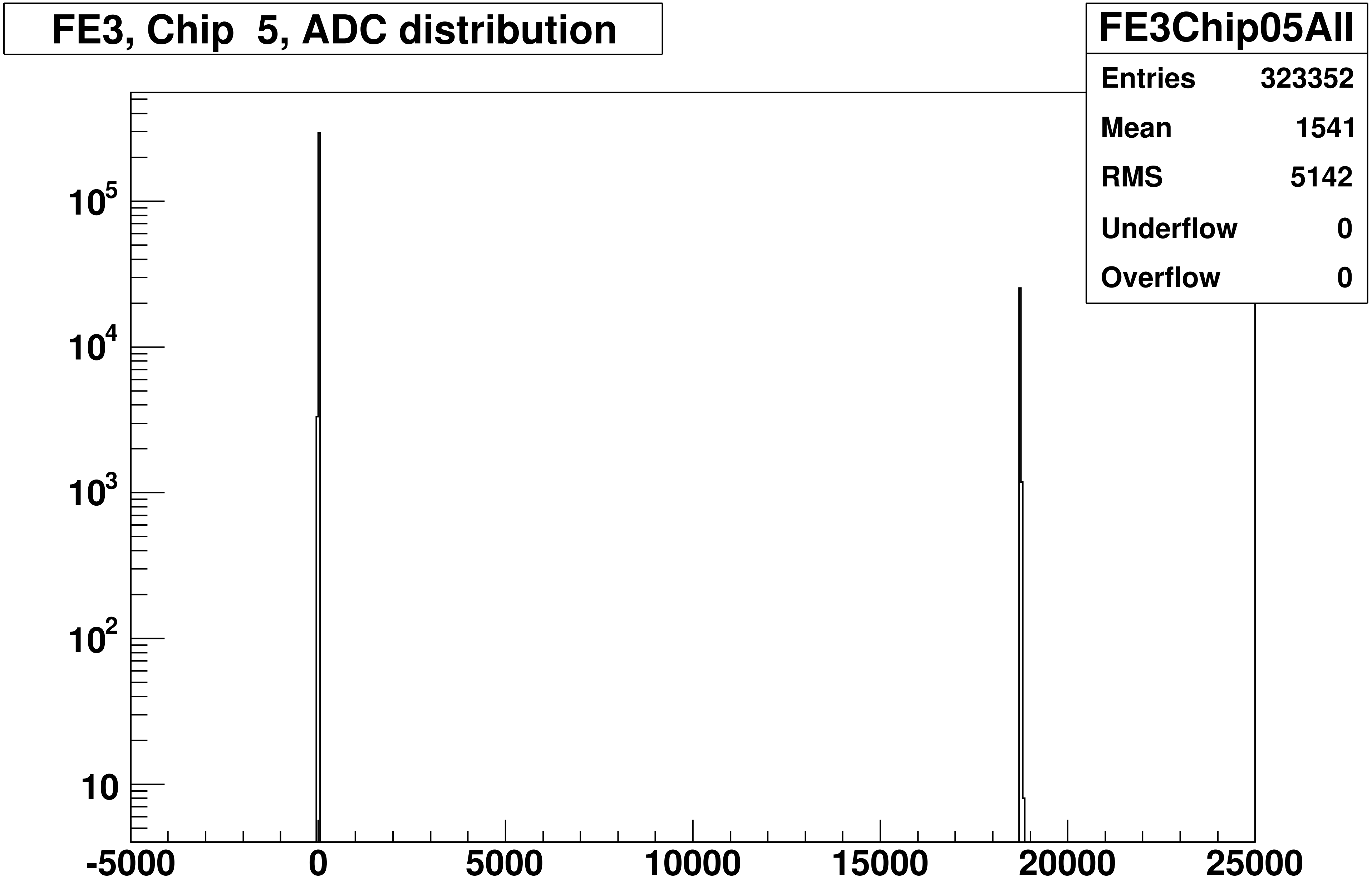
FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 323352
Mean 1.881e+04
RMS 1.966
Underflow 3.23e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

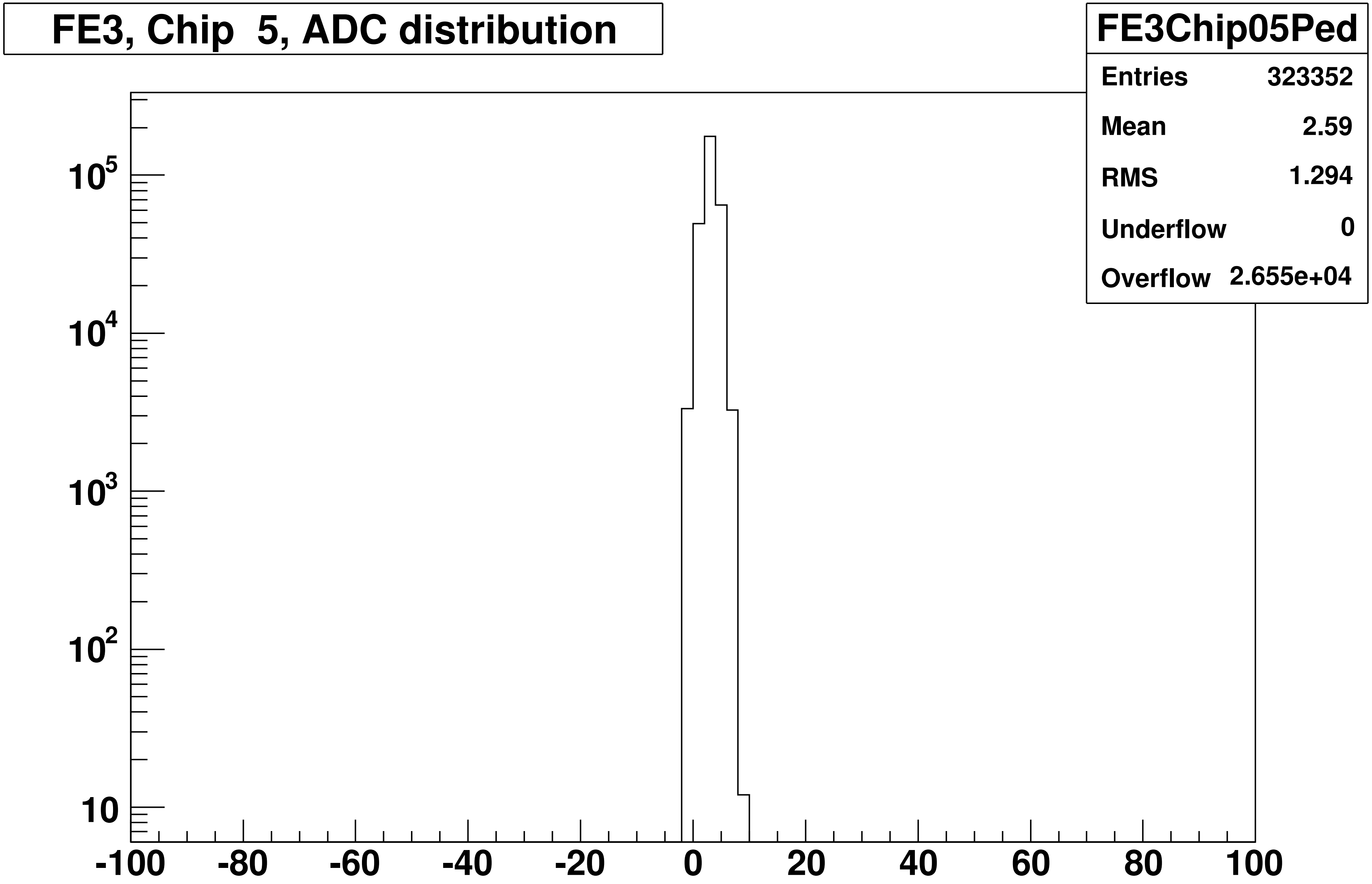

FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 323352
Mean 1550
RMS 5180
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

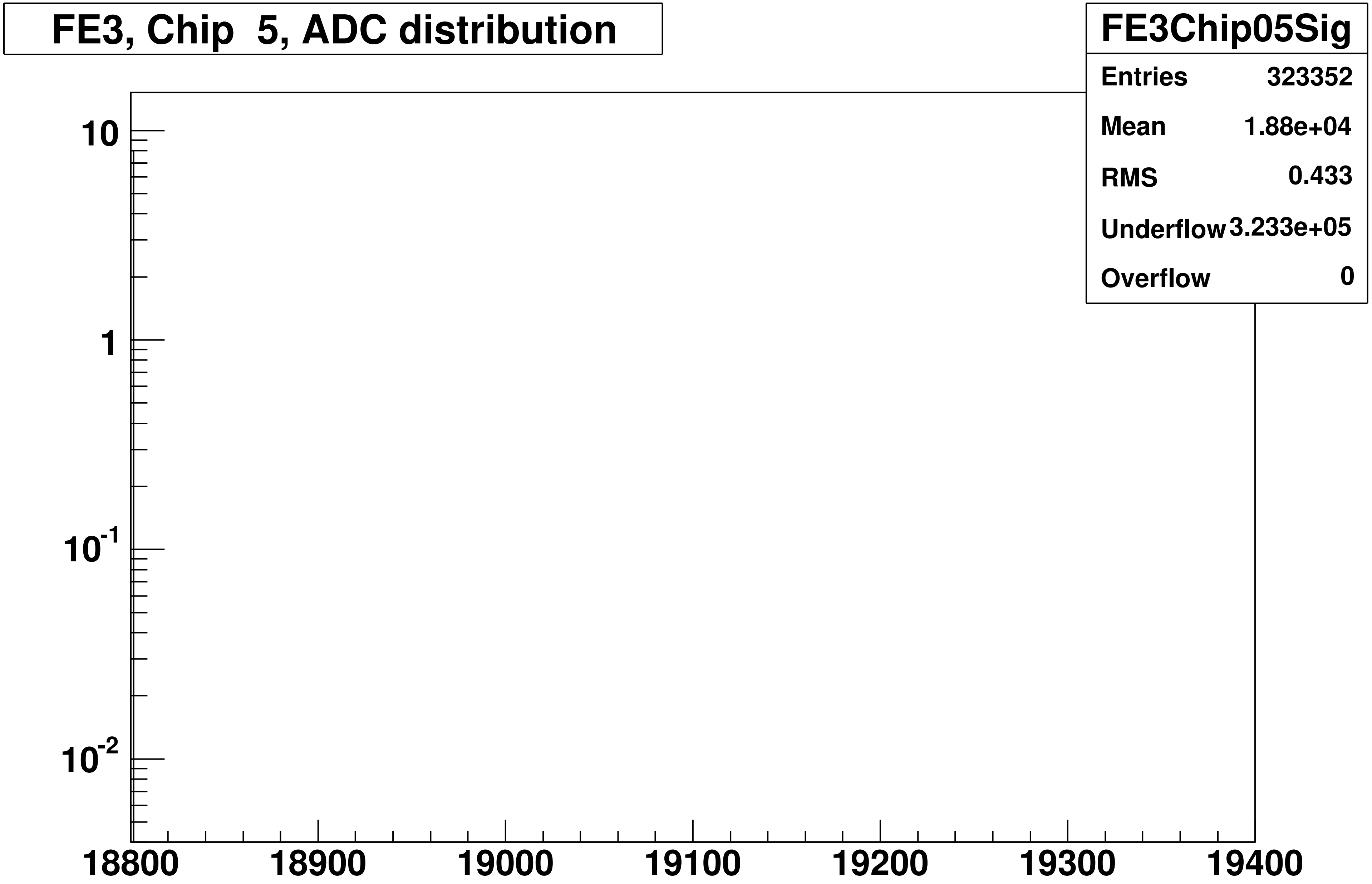


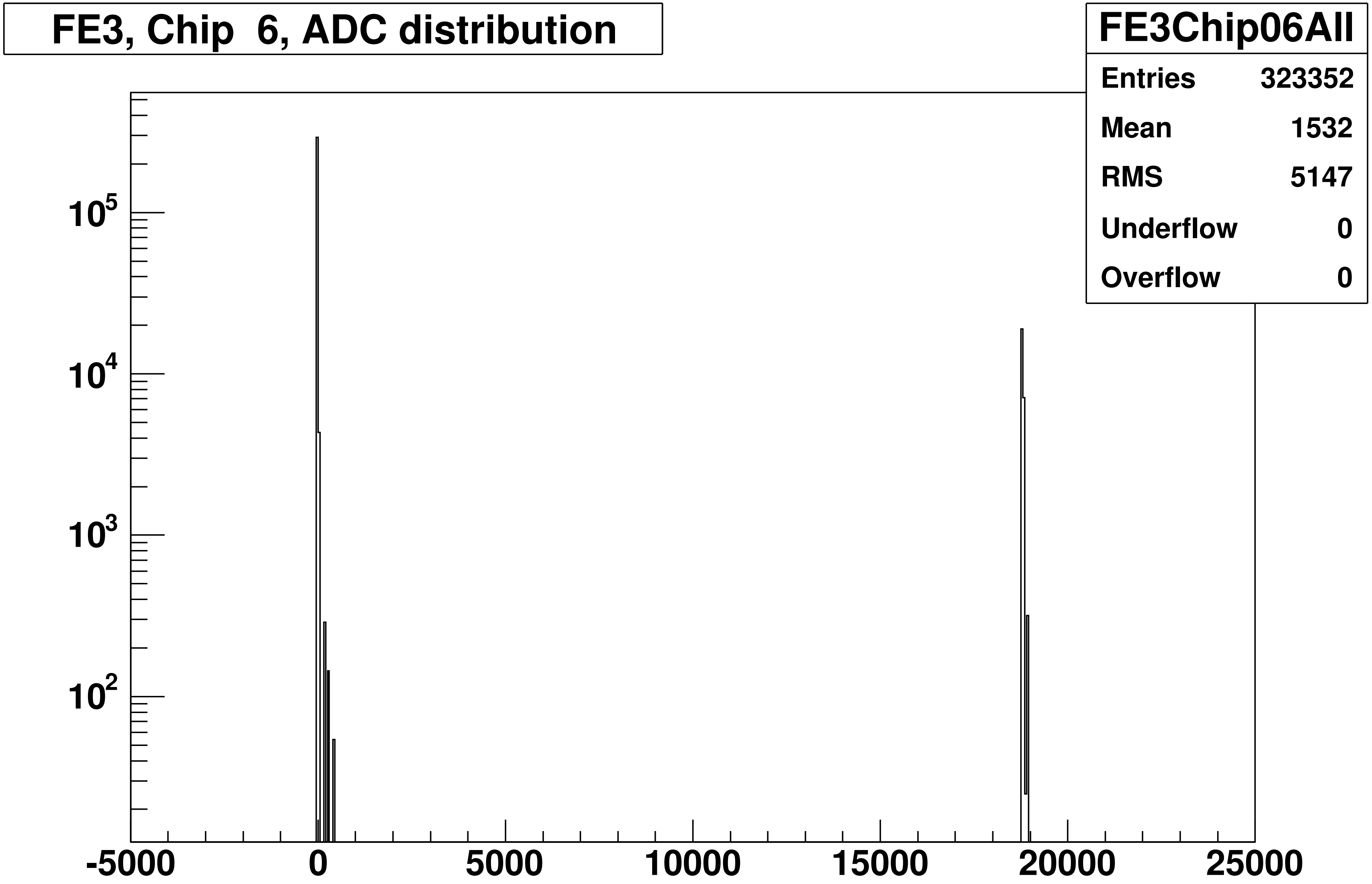
FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 323352
Mean 1.468
RMS 1.315
Underflow 0
Overflow 2.655e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 323352
Mean 1.888e+04
RMS 12.56
Underflow 2.968e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

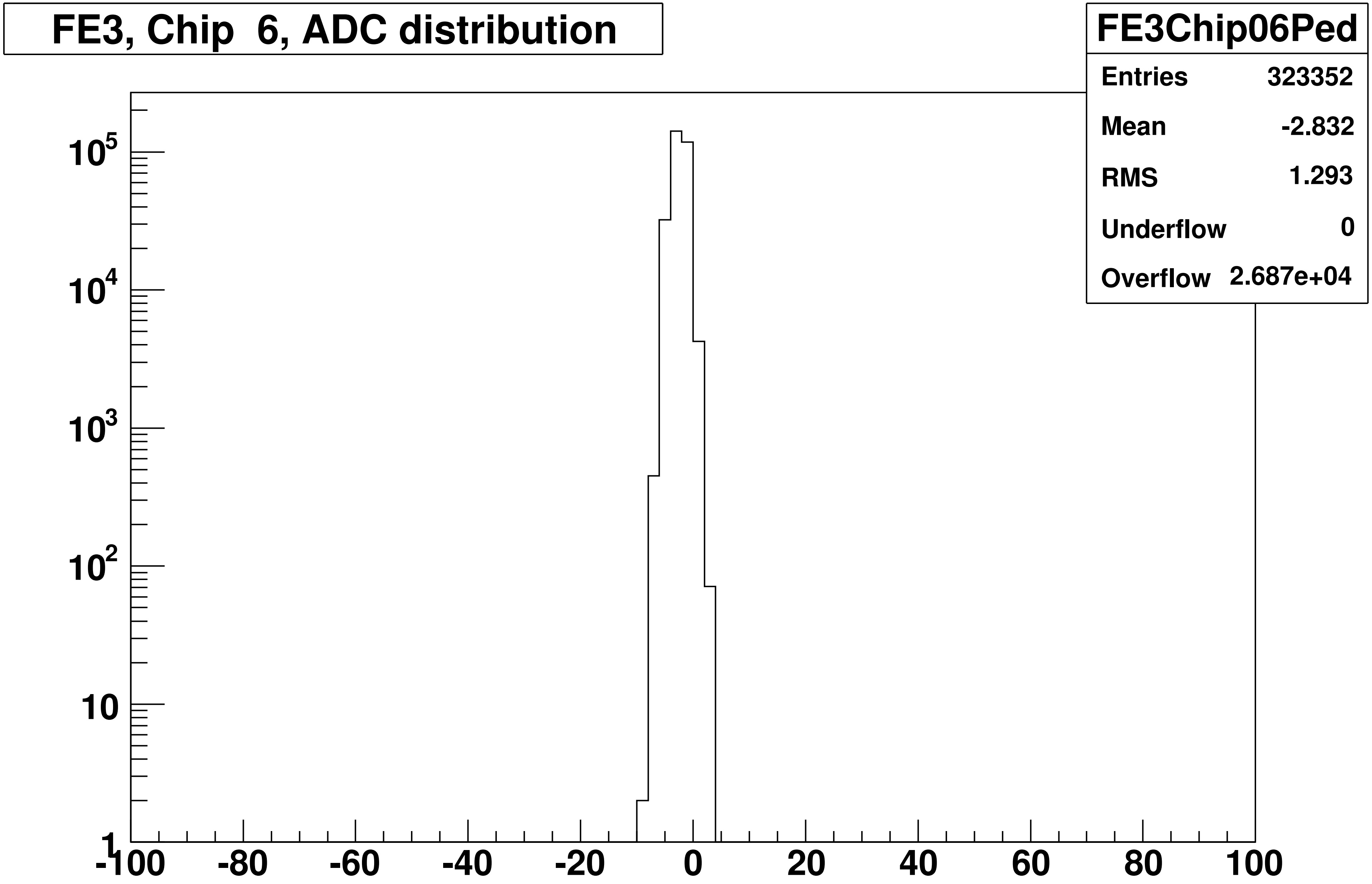

FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 323352
Mean 1541
RMS 5142
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 323352
Mean 2.59
RMS 1.294
Underflow 0
Overflow 2.655e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

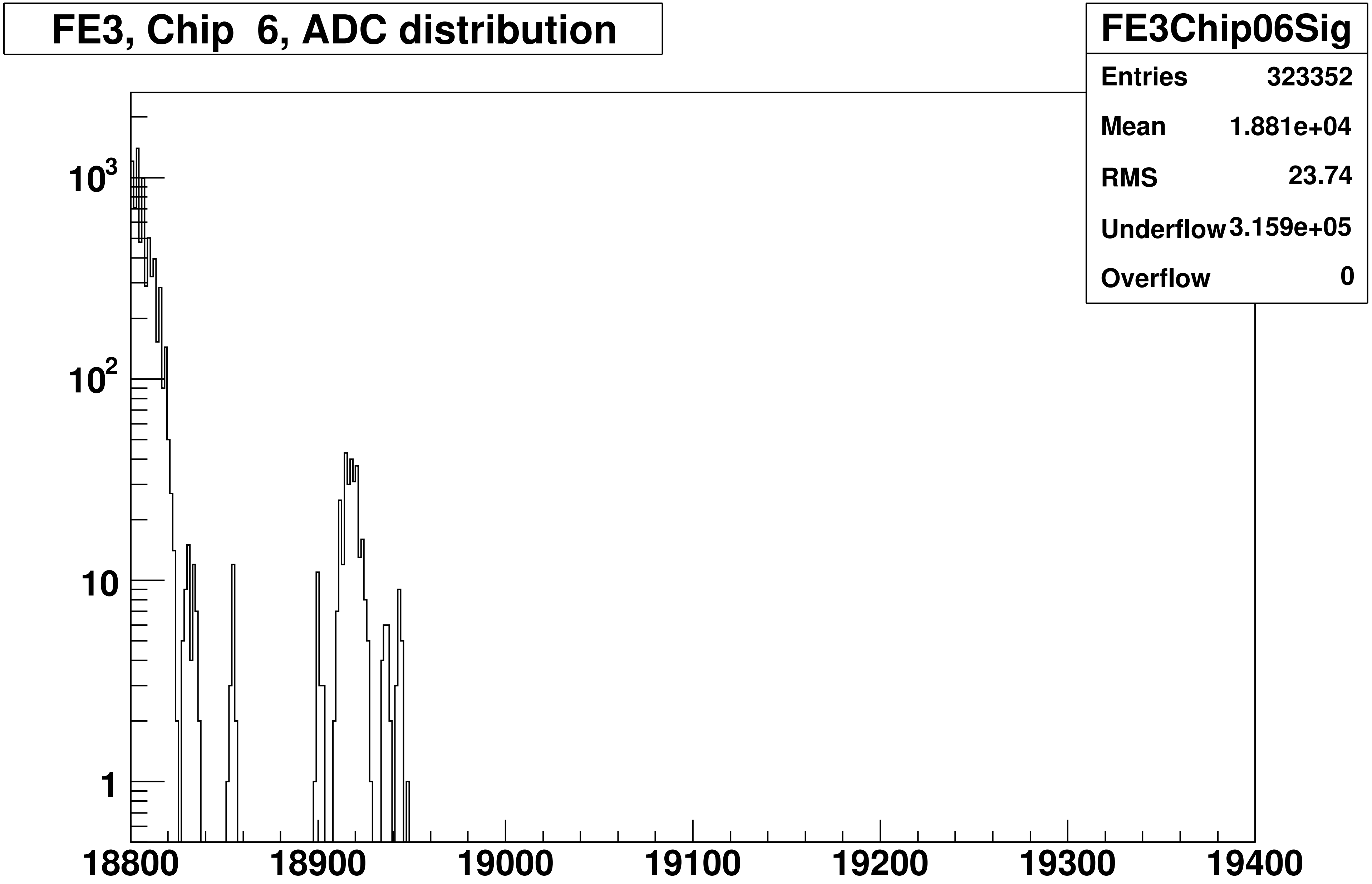

FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 323352
Mean 1.88e+04
RMS 0.433
Underflow 3.233e+05
Overflow 0
10
1
10^{-1}
10^{-2}
18800
18900
19000
19100
19200
19300
19400

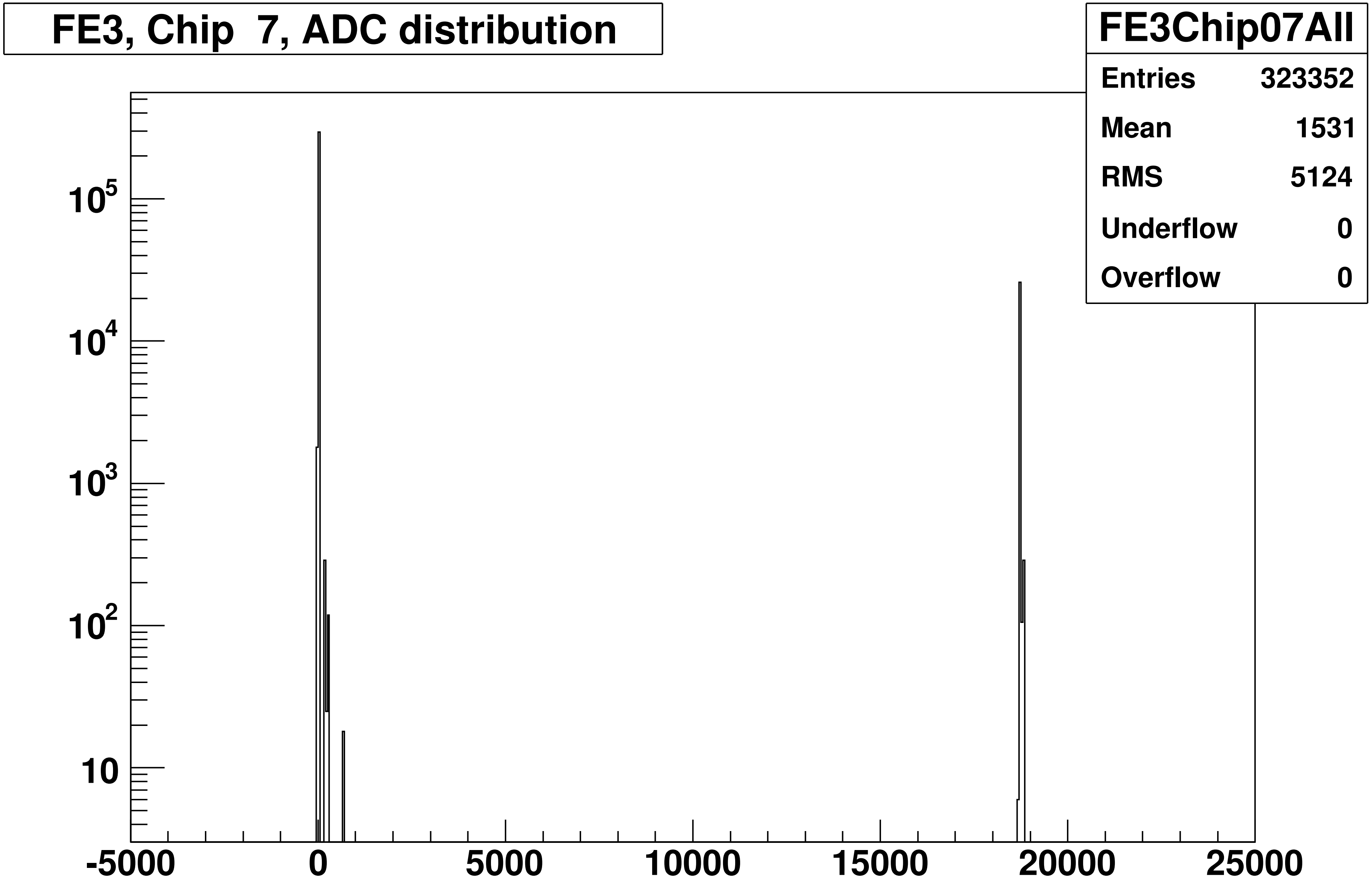


FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 323352
Mean 1532
RMS 5147
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

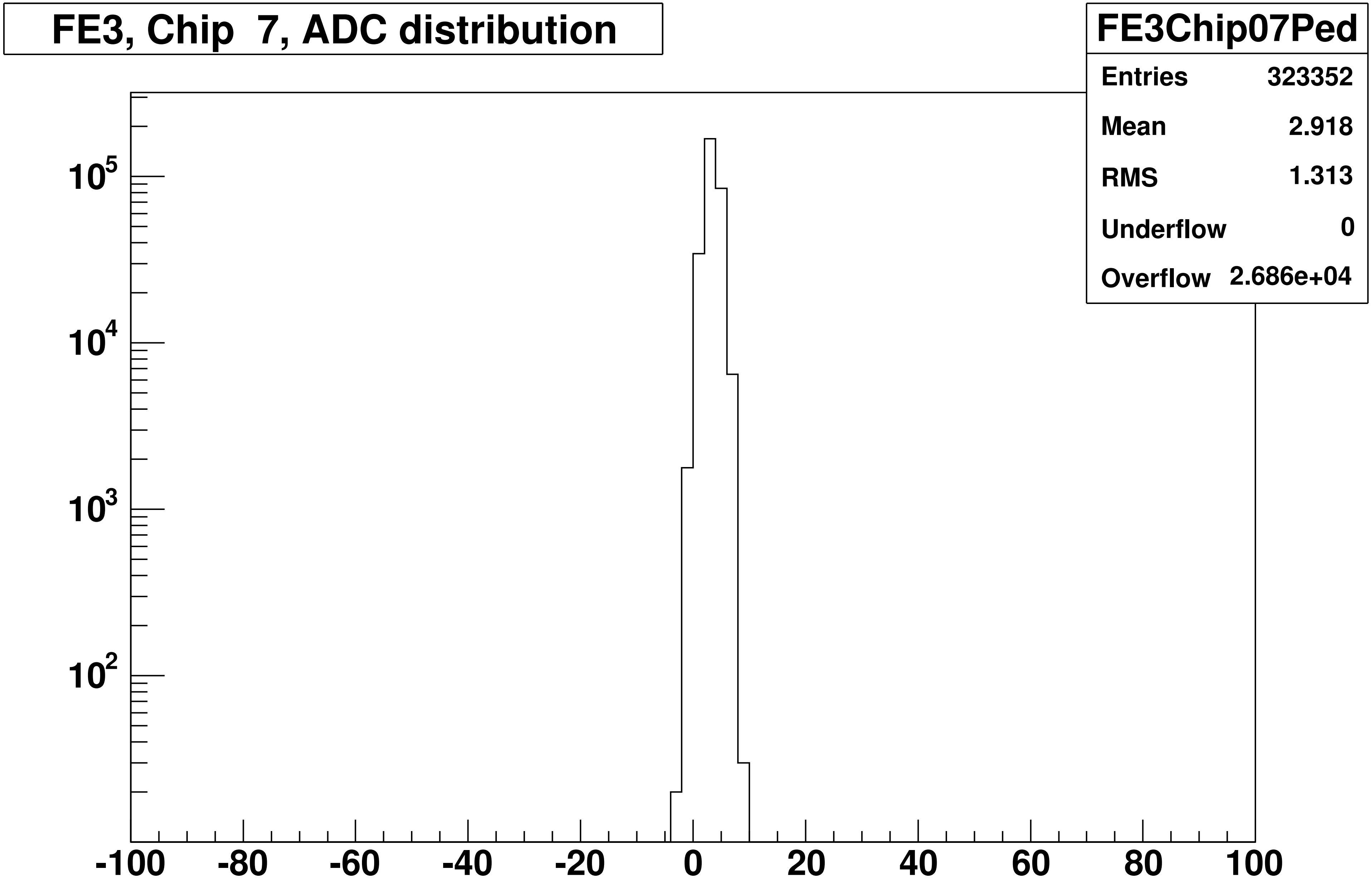


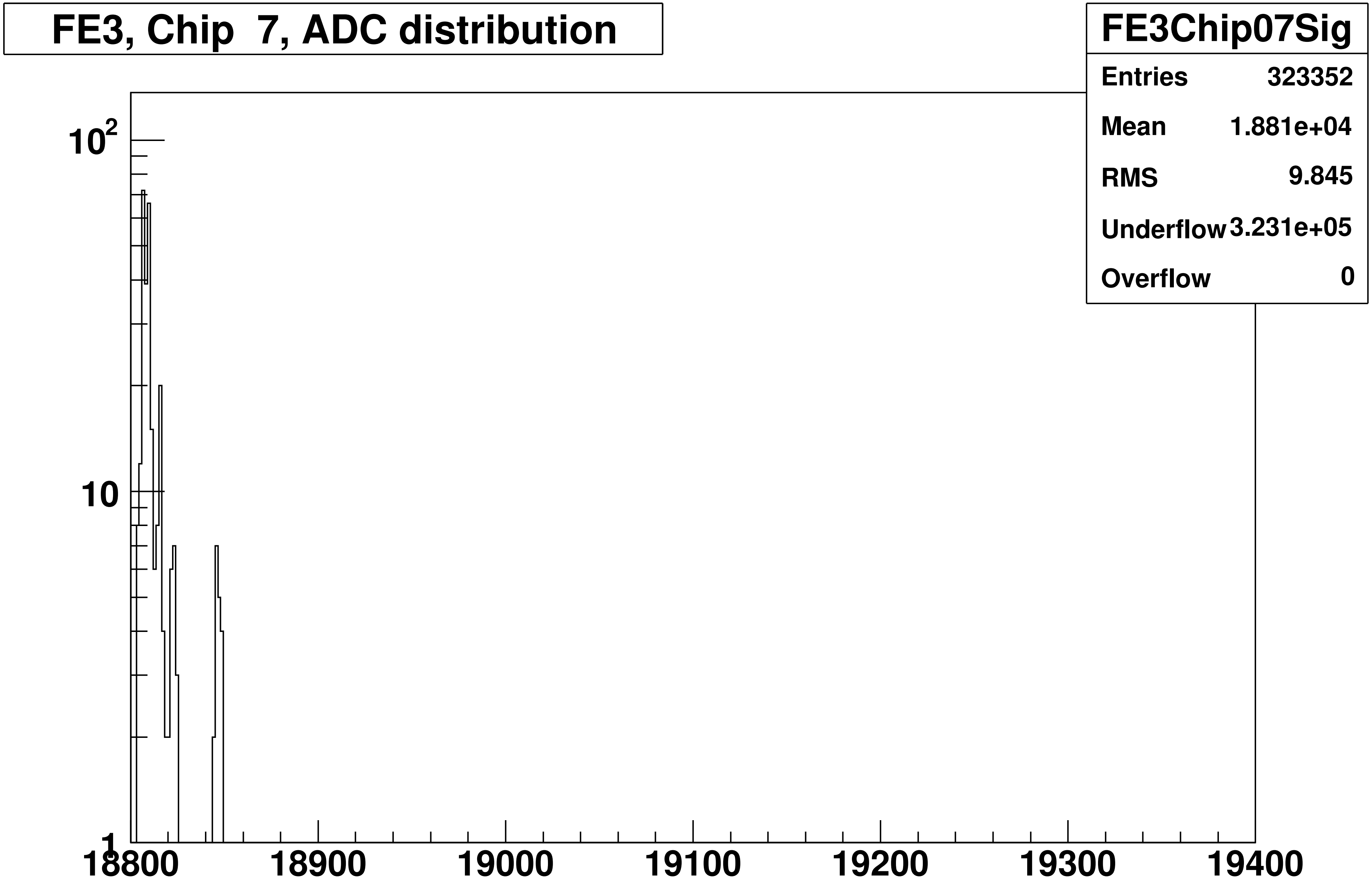
FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 323352
Mean -2.832
RMS 1.293
Underflow 0
Overflow 2.687e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

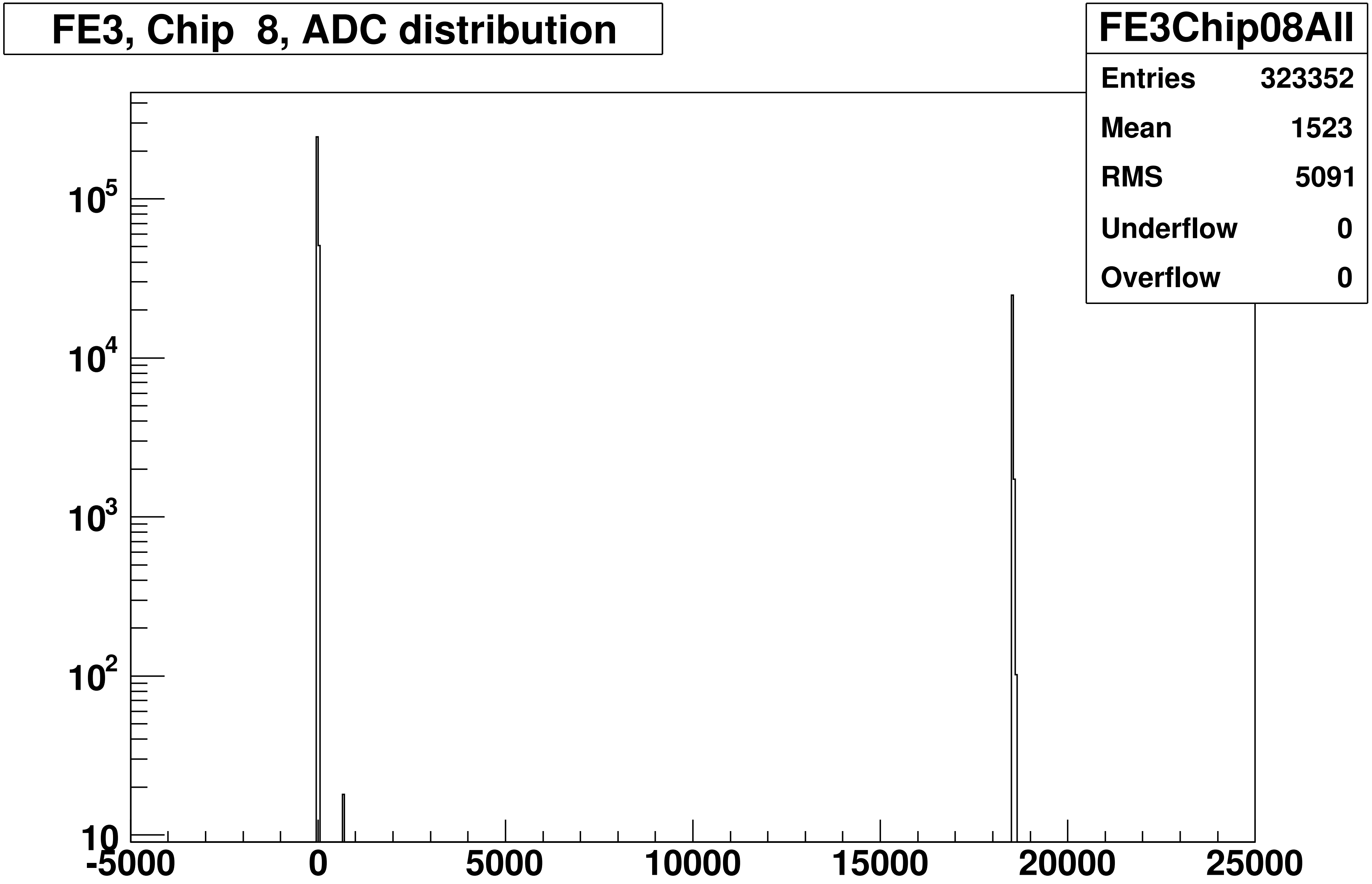

FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 323352
Mean 1.881e+04
RMS 23.74
Underflow 3.159e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

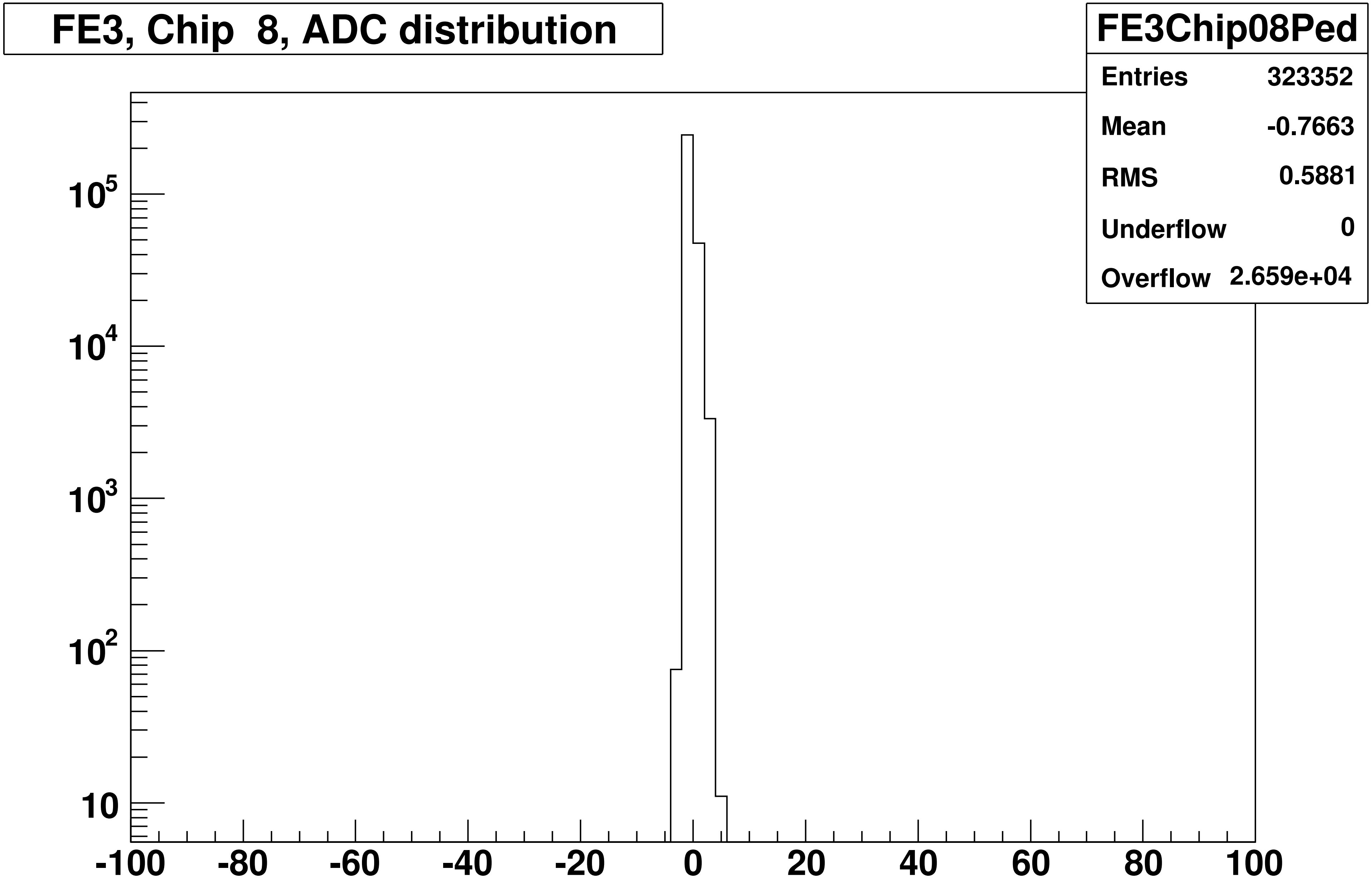


FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 323352
Mean 1531
RMS 5124
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

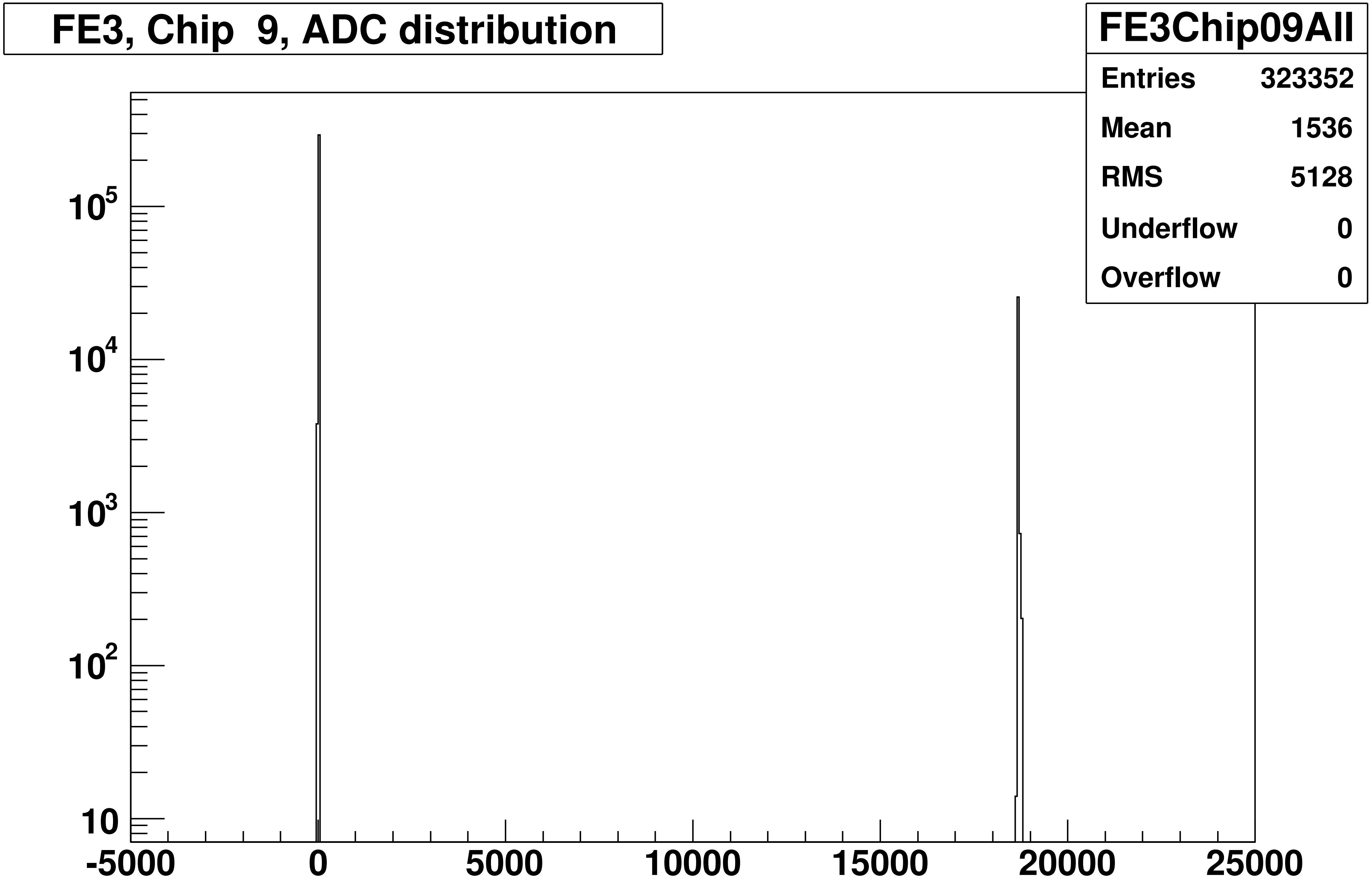

FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 323352
Mean 2.918
RMS 1.313
Underflow 0
Overflow 2.686e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

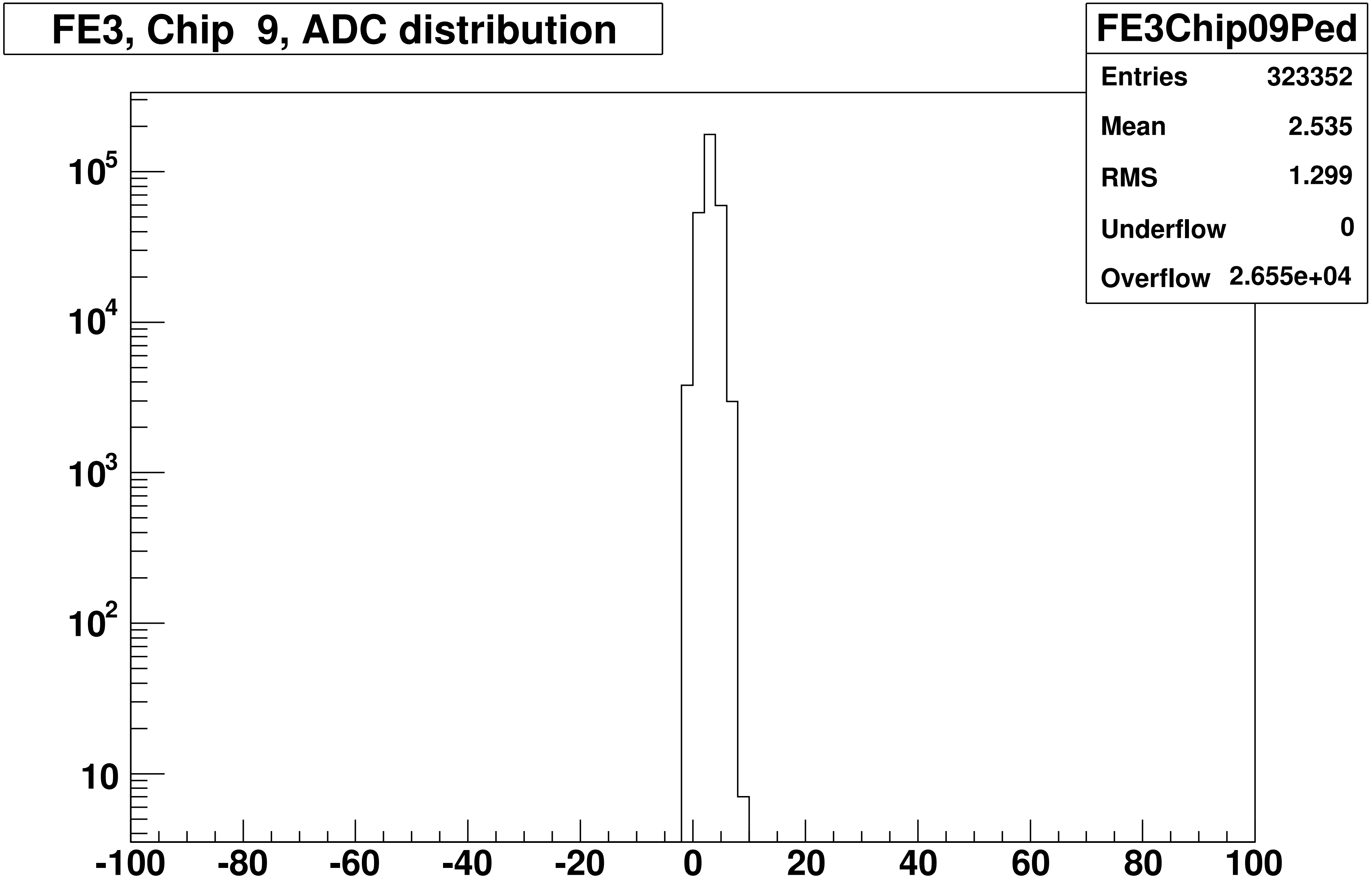

FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 323352
Mean 1.881e+04
RMS 9.845
Underflow 3.231e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

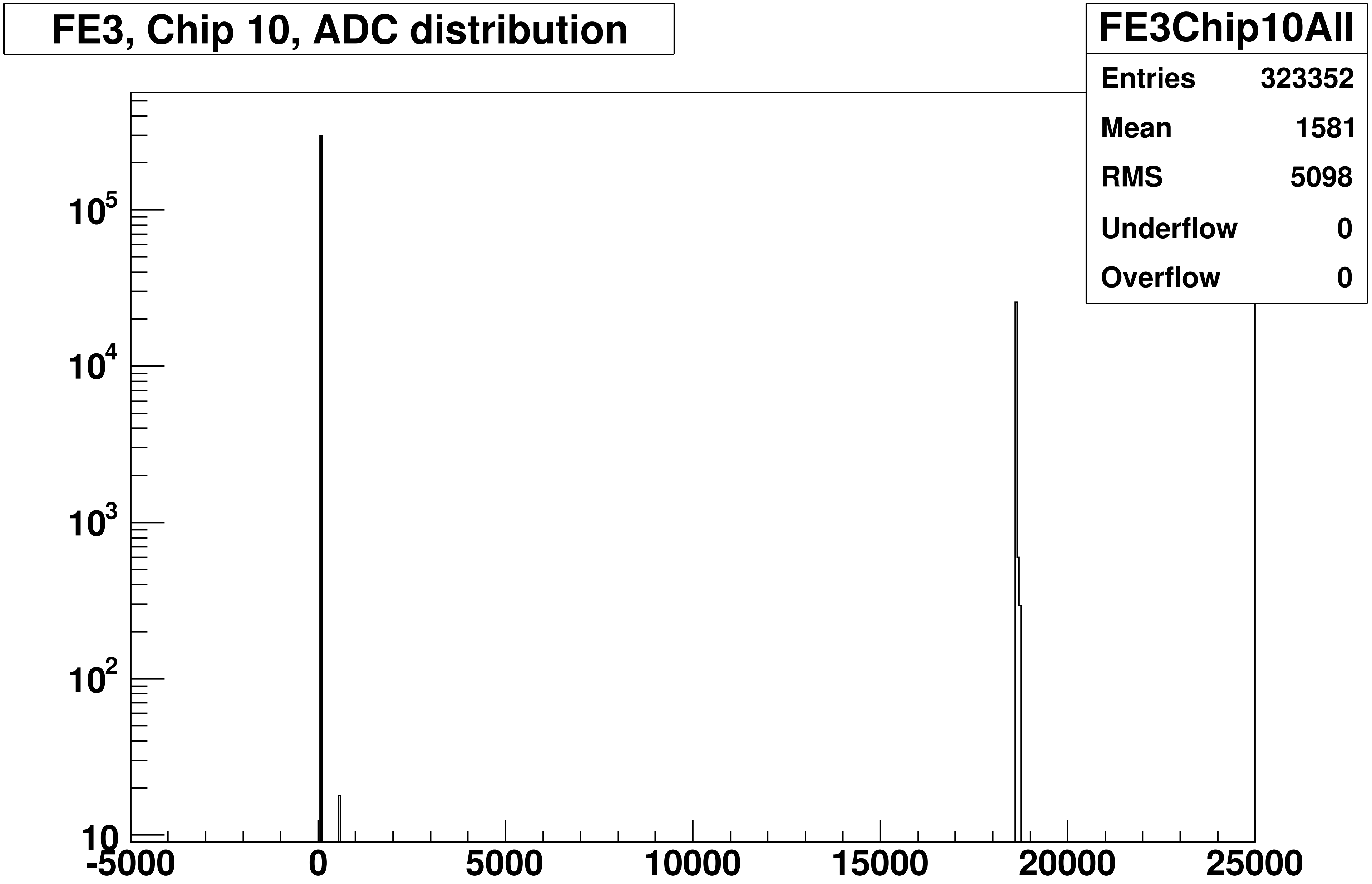

FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 323352
Mean 1523
RMS 5091
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

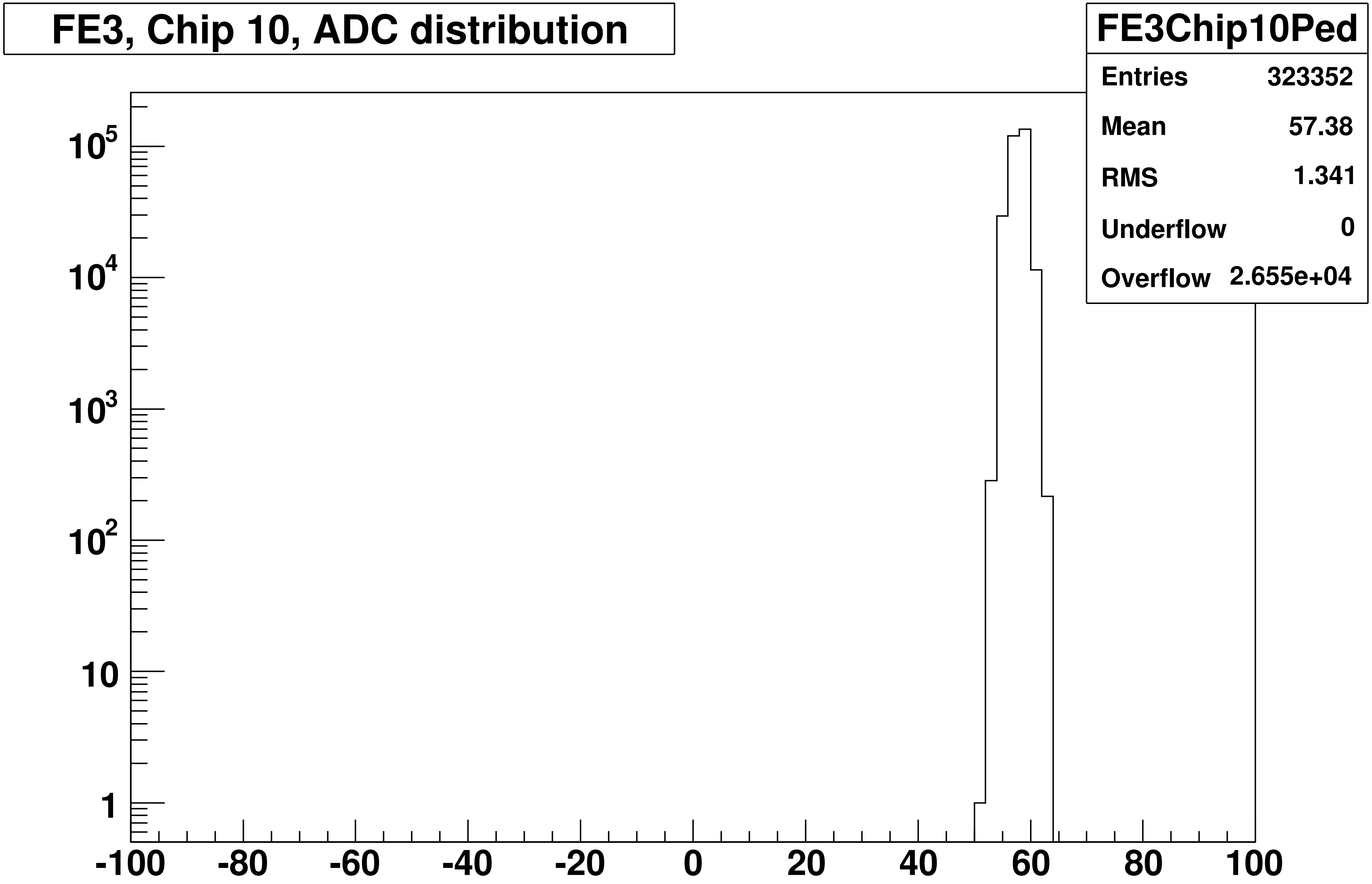


FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 323352
Mean -0.7663
RMS 0.5881
Underflow 0
Overflow 2.659e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

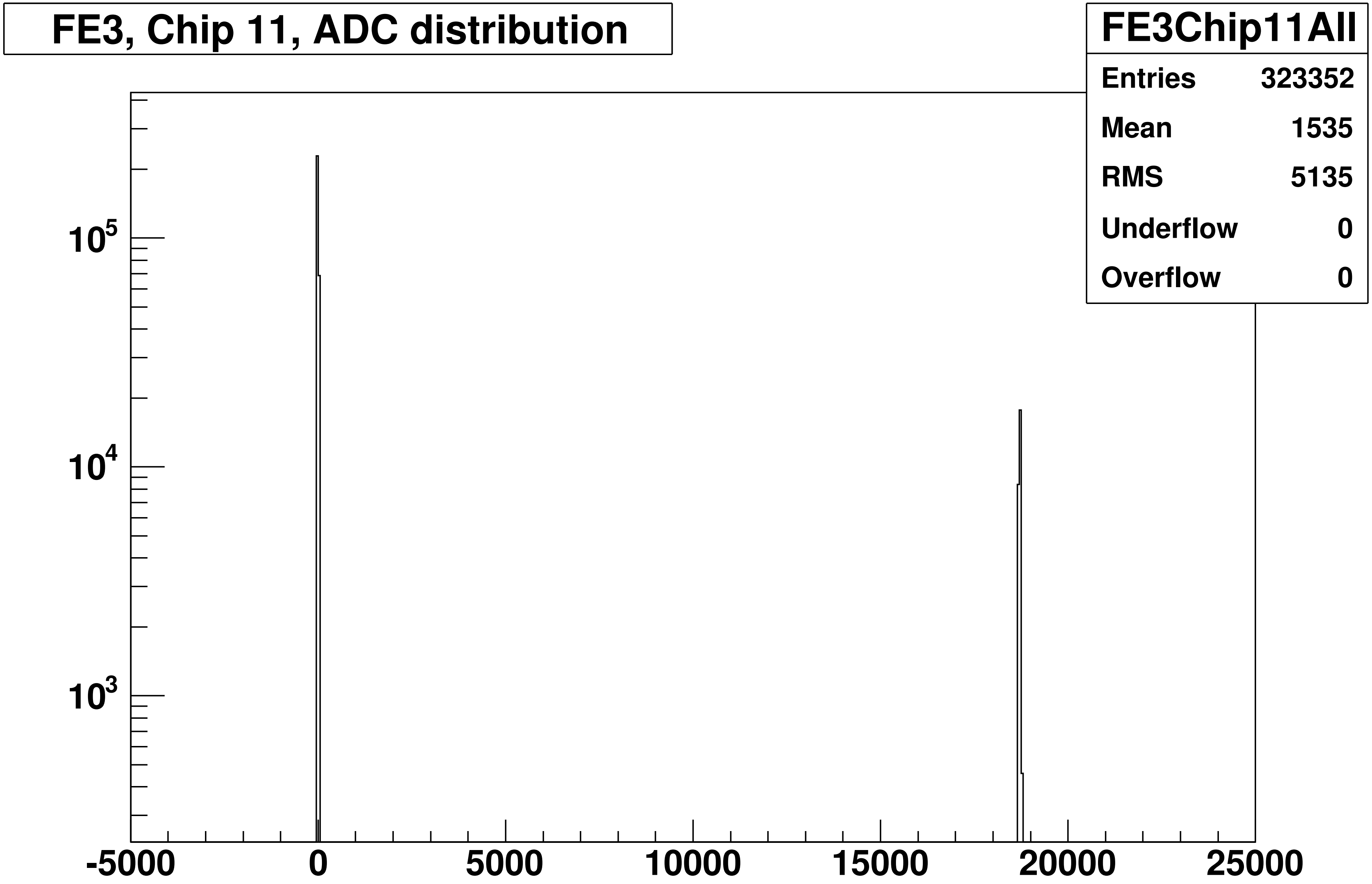

FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 323352
Mean 1536
RMS 5128
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

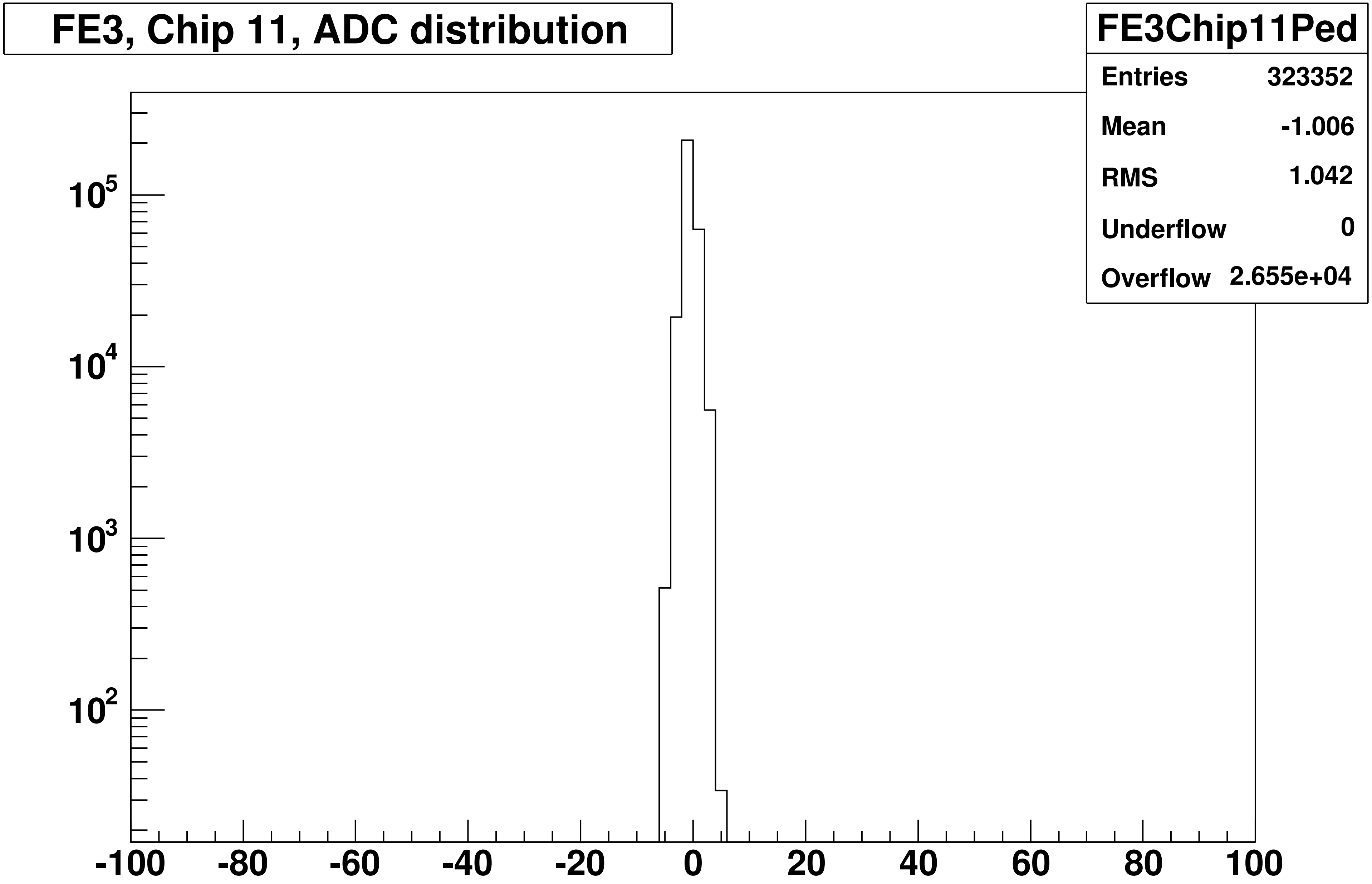

FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 323352
Mean 2.535
RMS 1.299
Underflow 0
Overflow 2.655e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

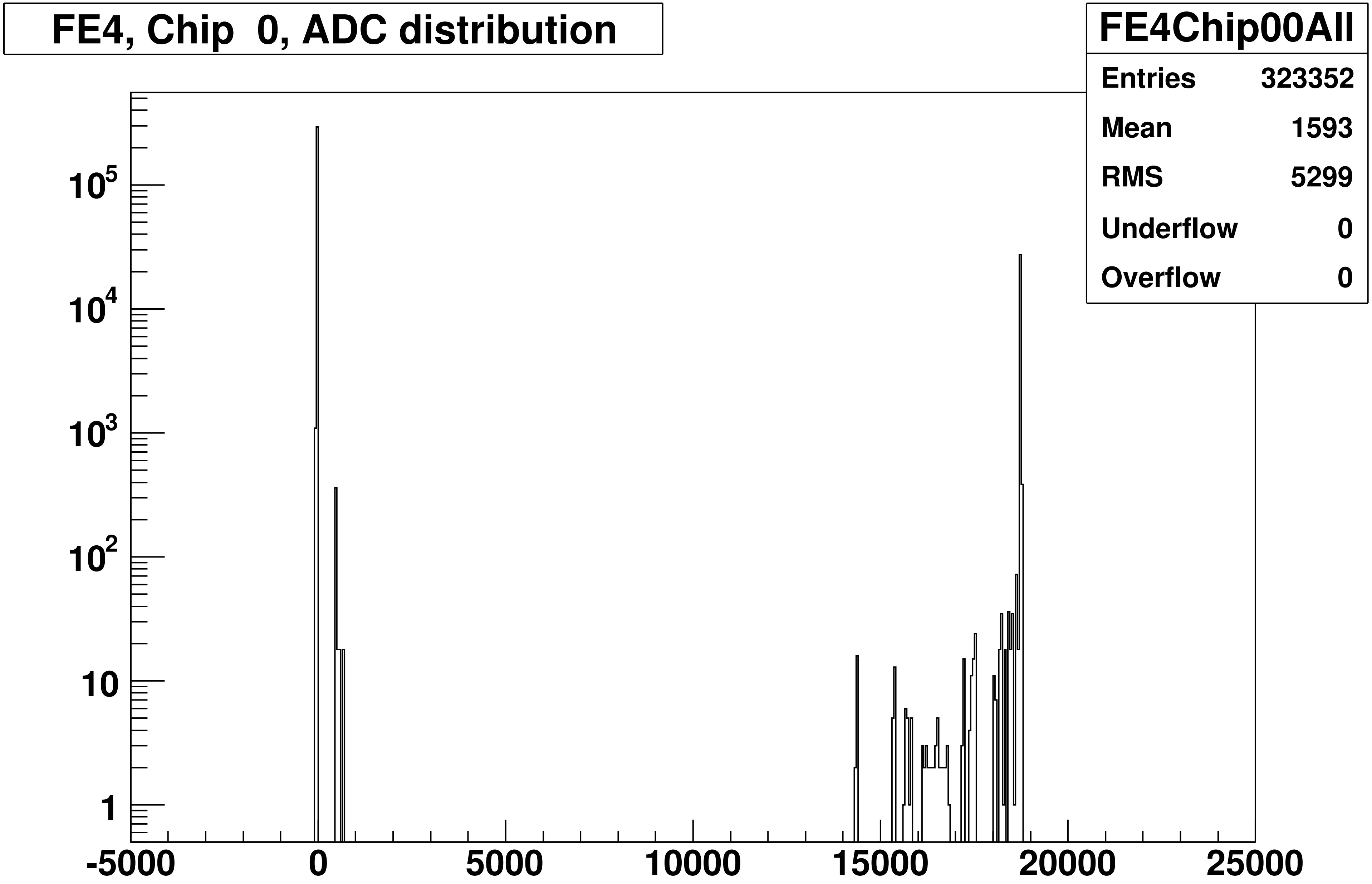

FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 323352
Mean 1581
RMS 5098
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



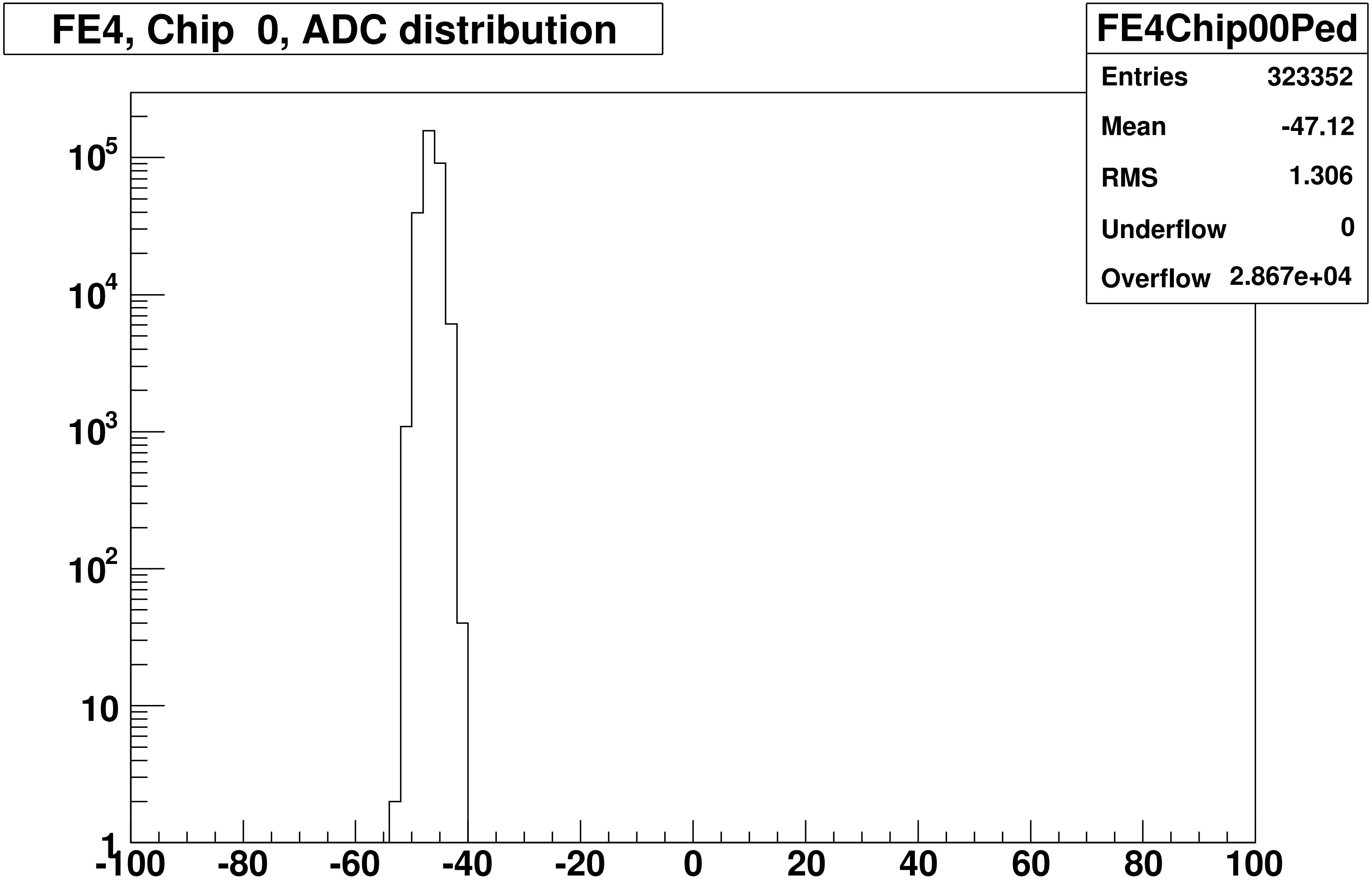
FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 323352
Mean 57.38
RMS 1.341
Underflow 0
Overflow 2.655e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

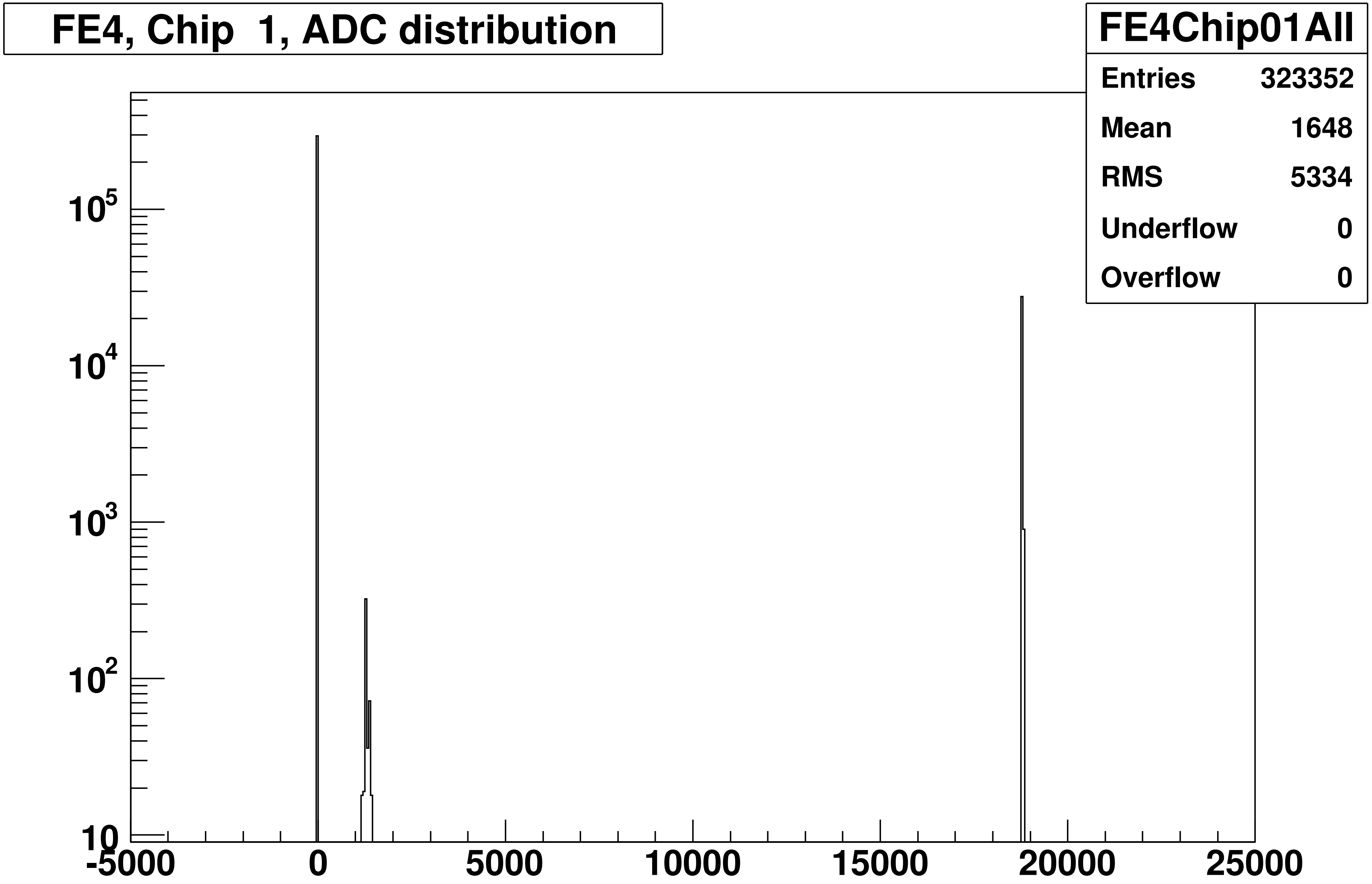

FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 323352
Mean 1535
RMS 5135
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

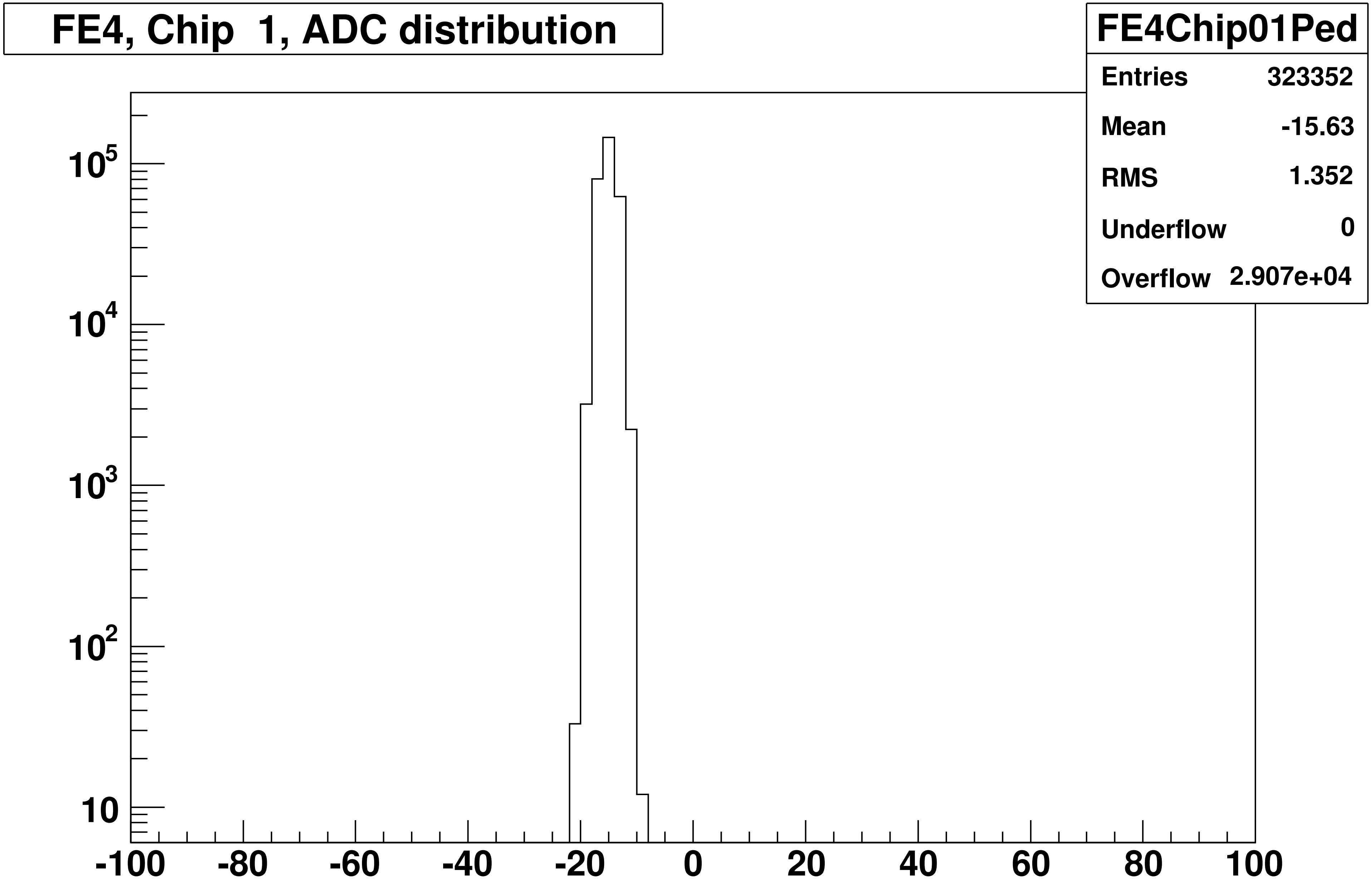

FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 323352
Mean -1.006
RMS 1.042
Underflow 0
Overflow 2.655e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

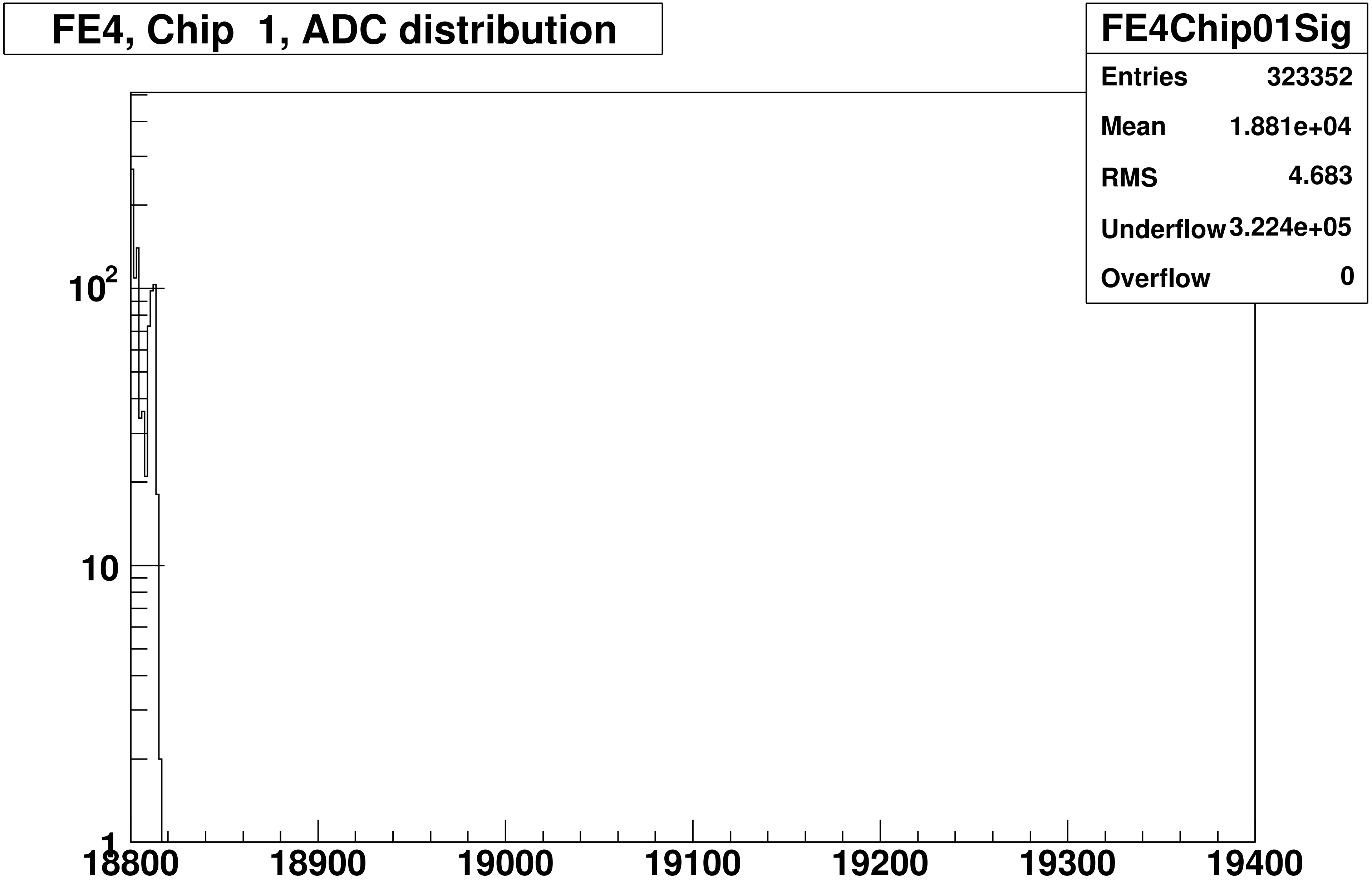


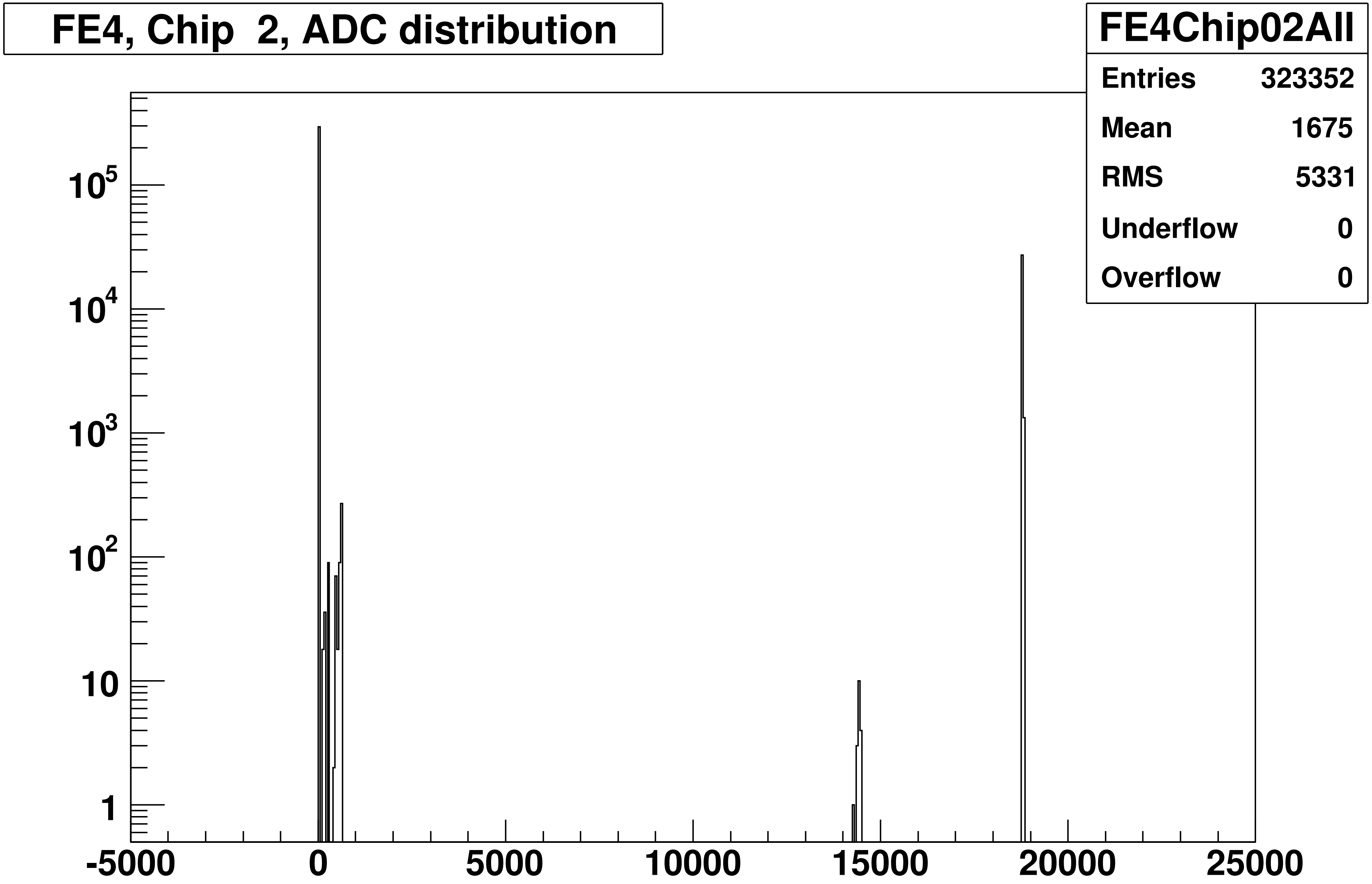
FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 323352
Mean 1593
RMS 5299
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

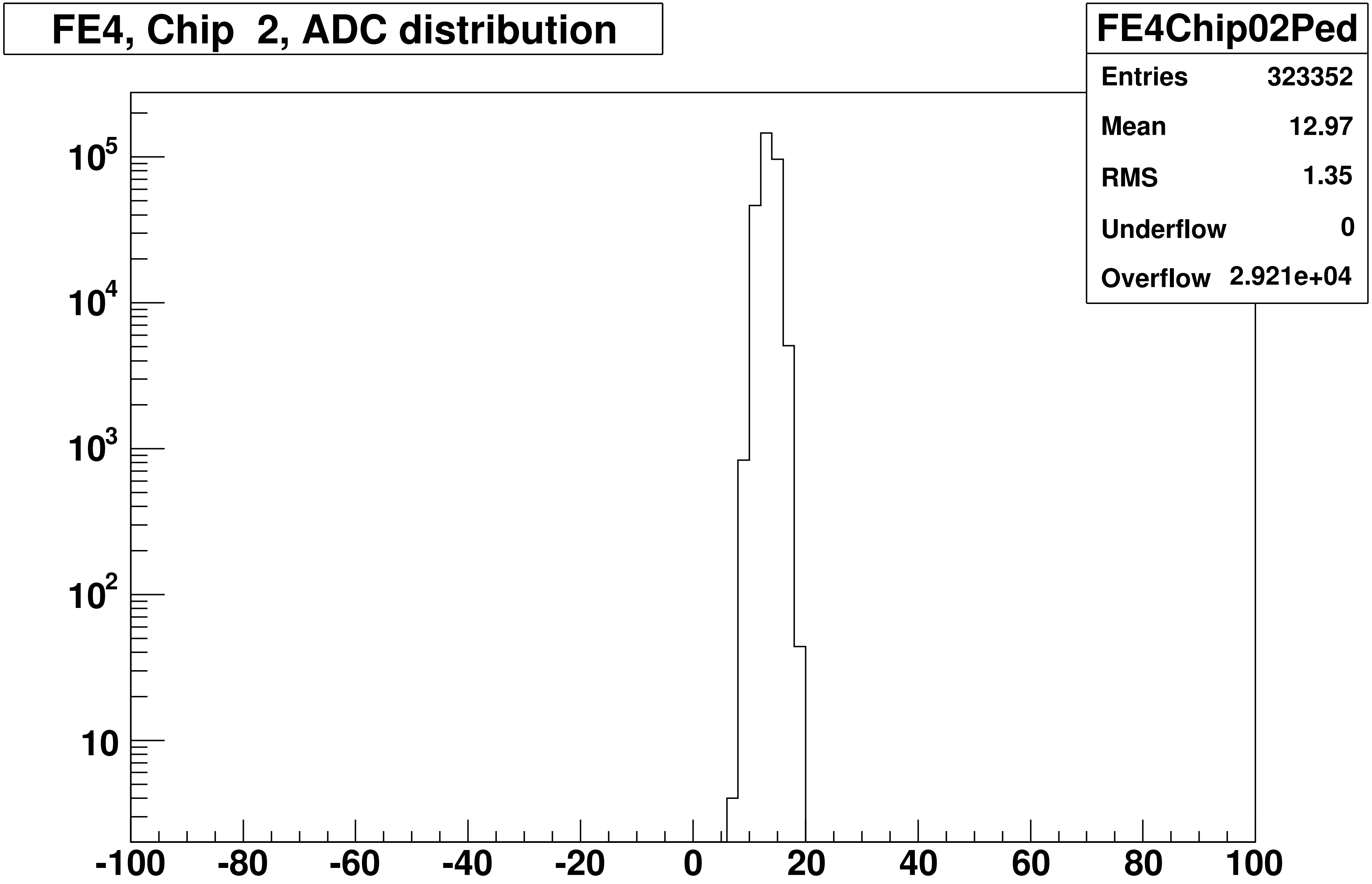

FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 323352
Mean -47.12
RMS 1.306
Underflow 0
Overflow 2.867e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

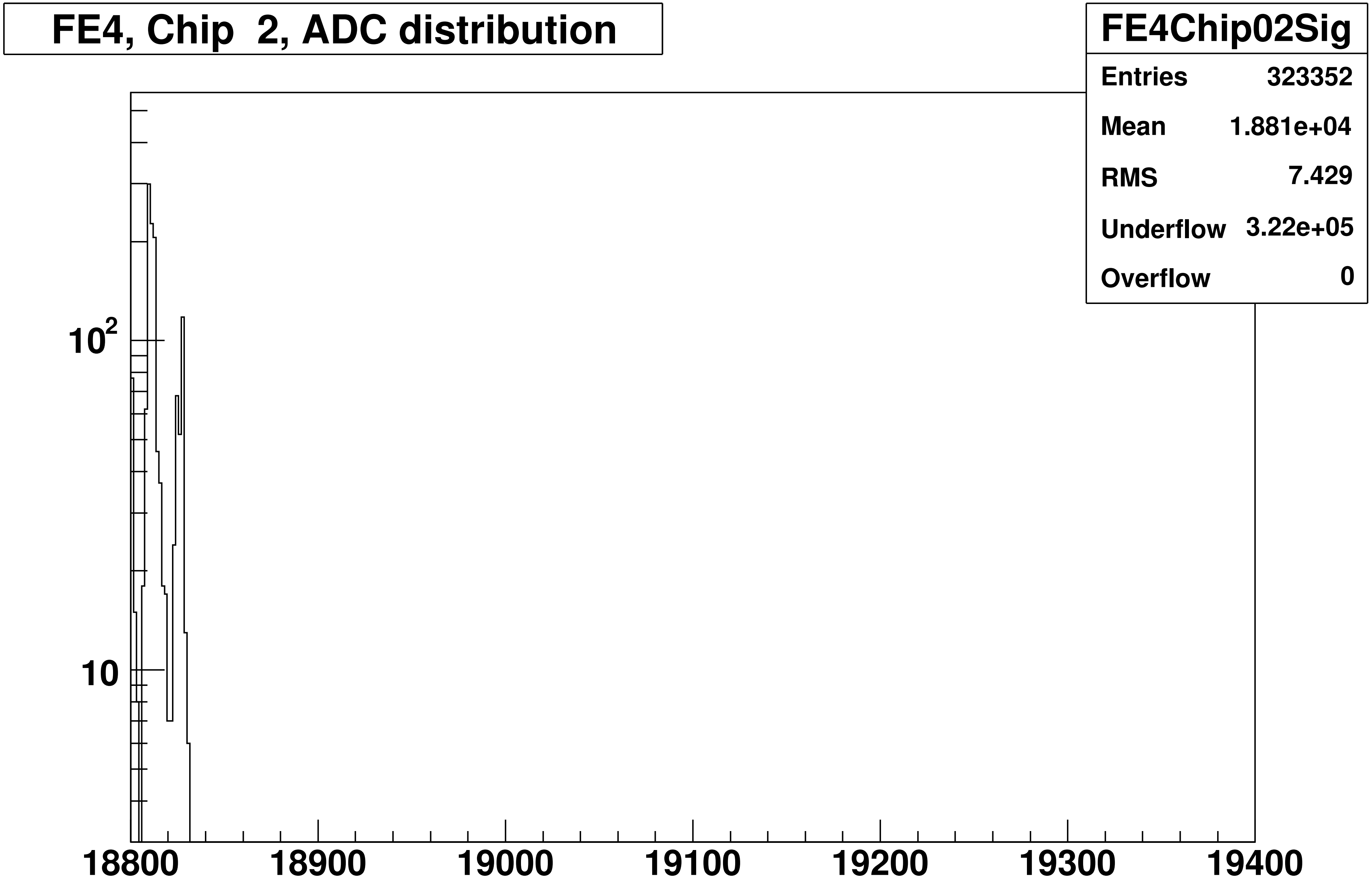

FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 323352
Mean 1648
RMS 5334
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

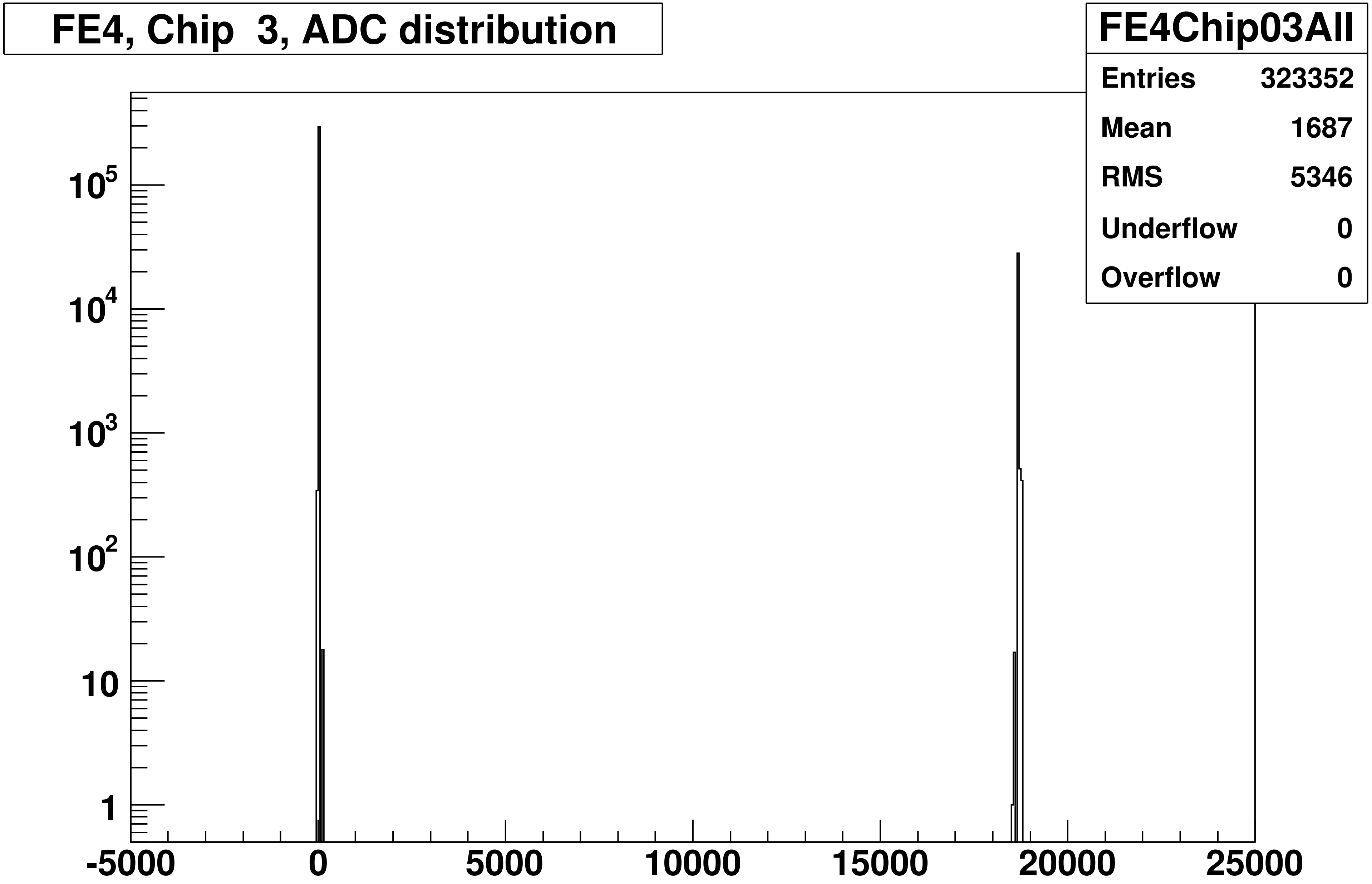

FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 323352
Mean -15.63
RMS 1.352
Underflow 0
Overflow 2.907e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

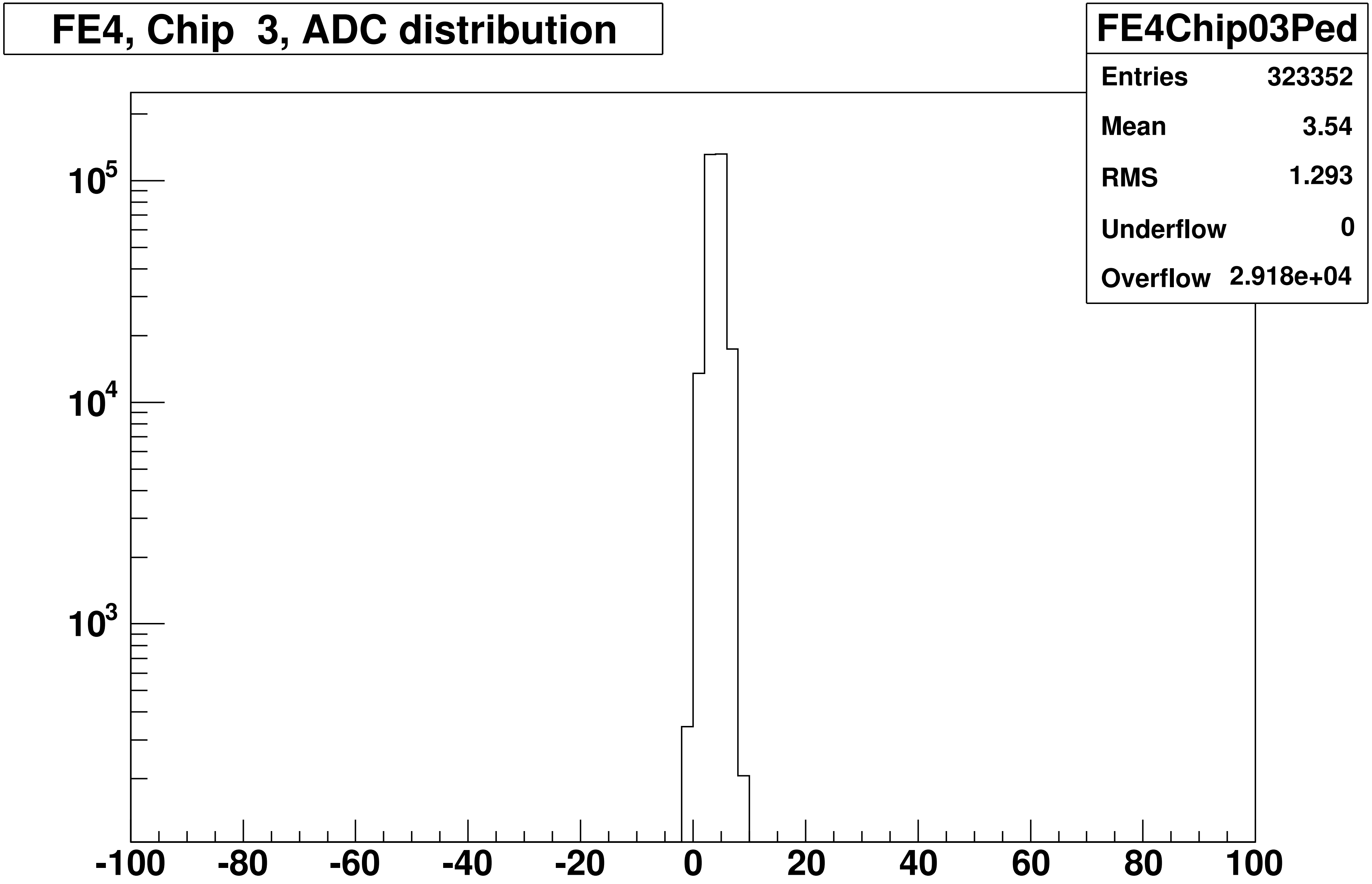

FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 323352
Mean 1.881e+04
RMS 4.683
Underflow 3.224e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

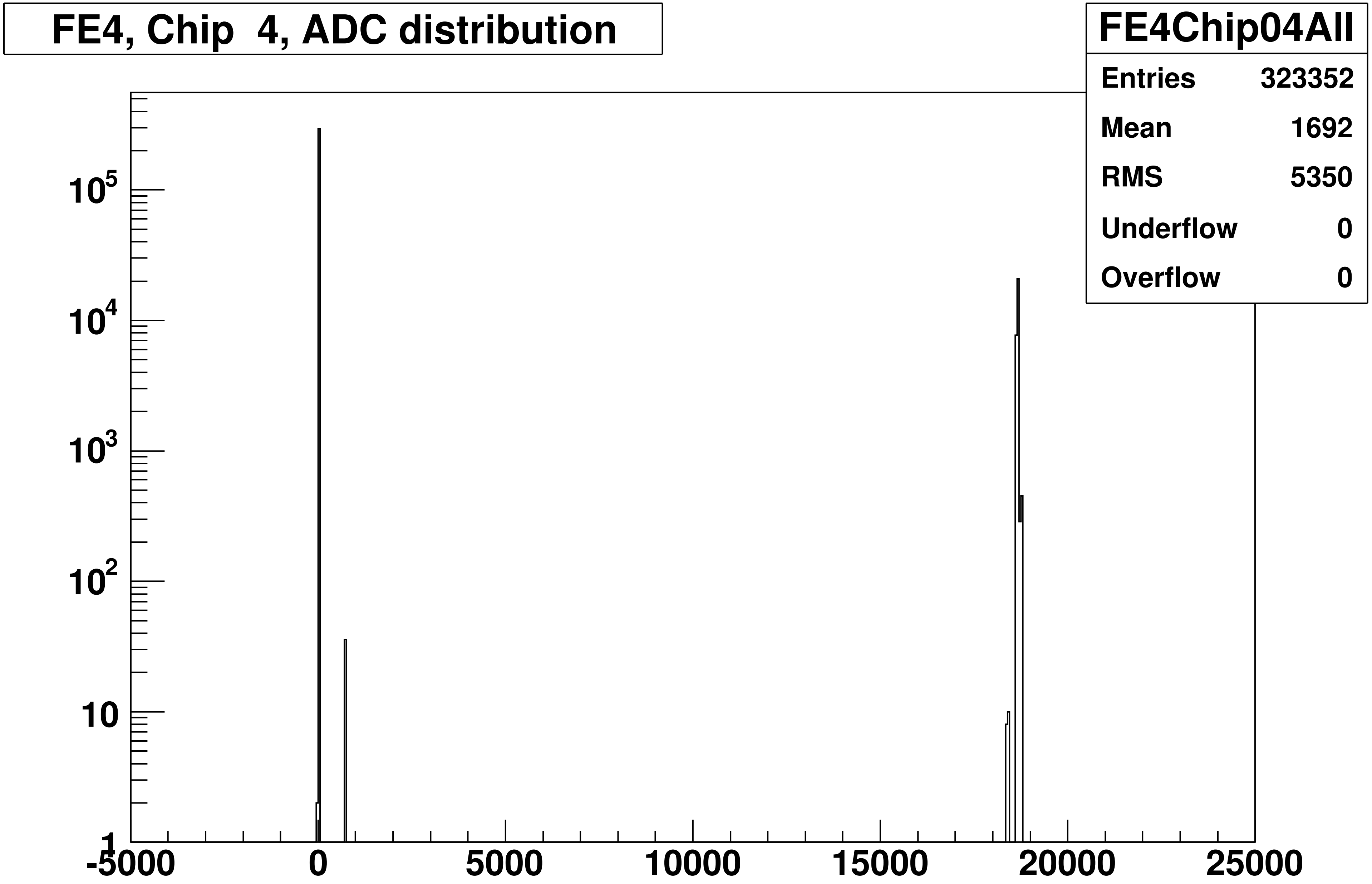

FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 323352
Mean 1675
RMS 5331
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

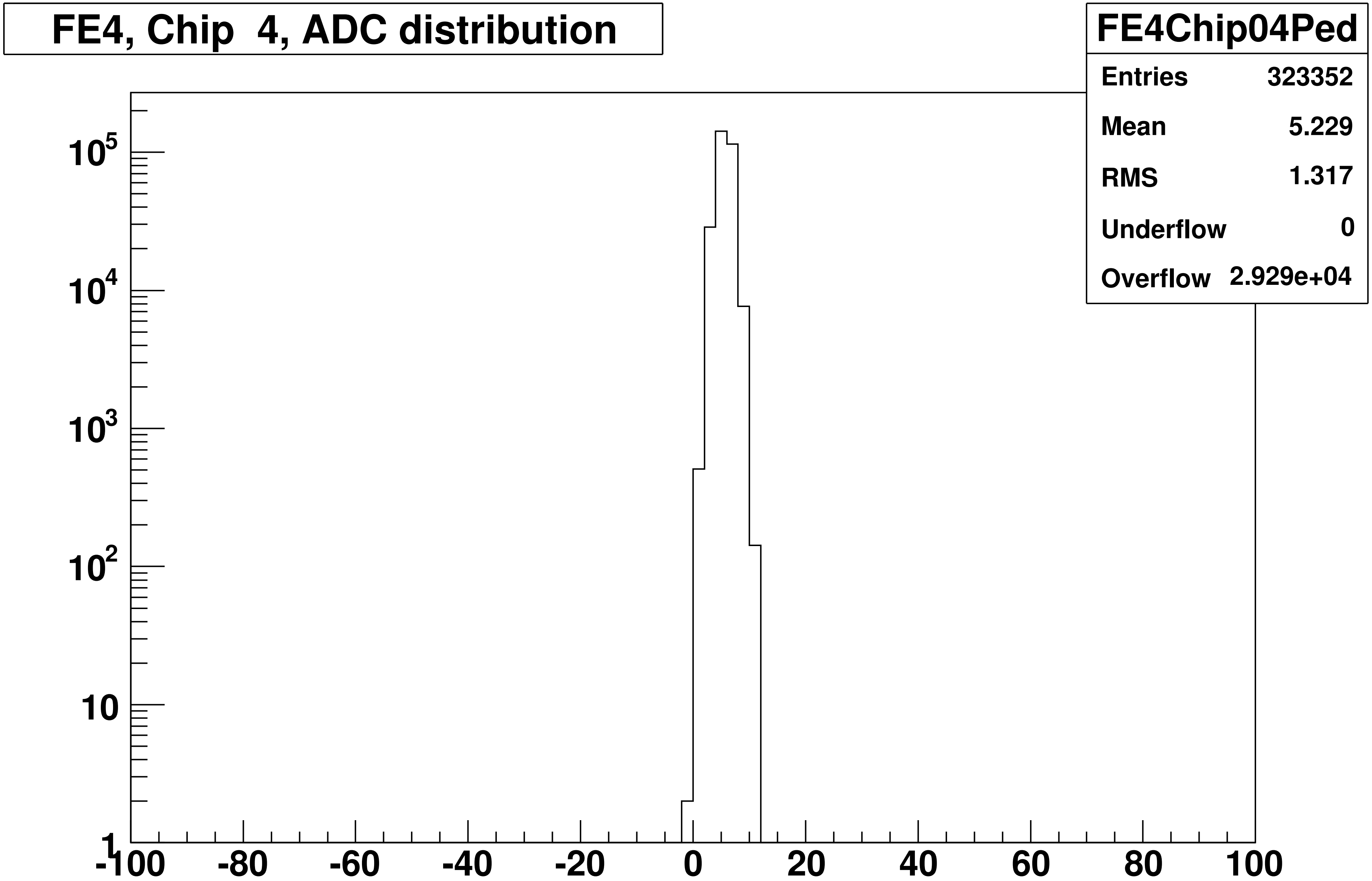

FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 323352
Mean 12.97
RMS 1.35
Underflow 0
Overflow 2.921e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

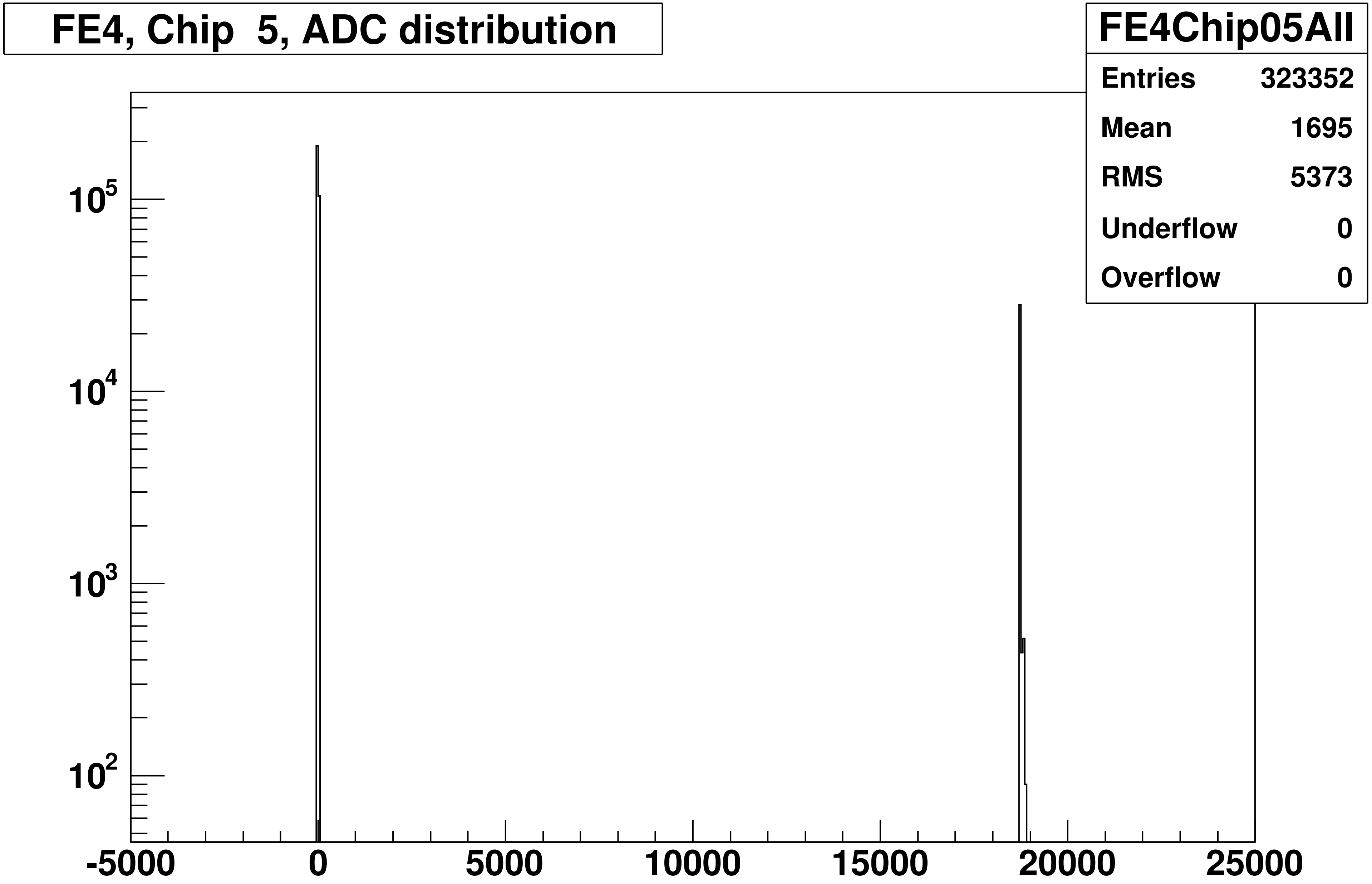

FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 323352
Mean 1.881e+04
RMS 7.429
Underflow 3.22e+05
Overflow 0
10^2
10
18800
18900
19000
19100
19200
19300
19400

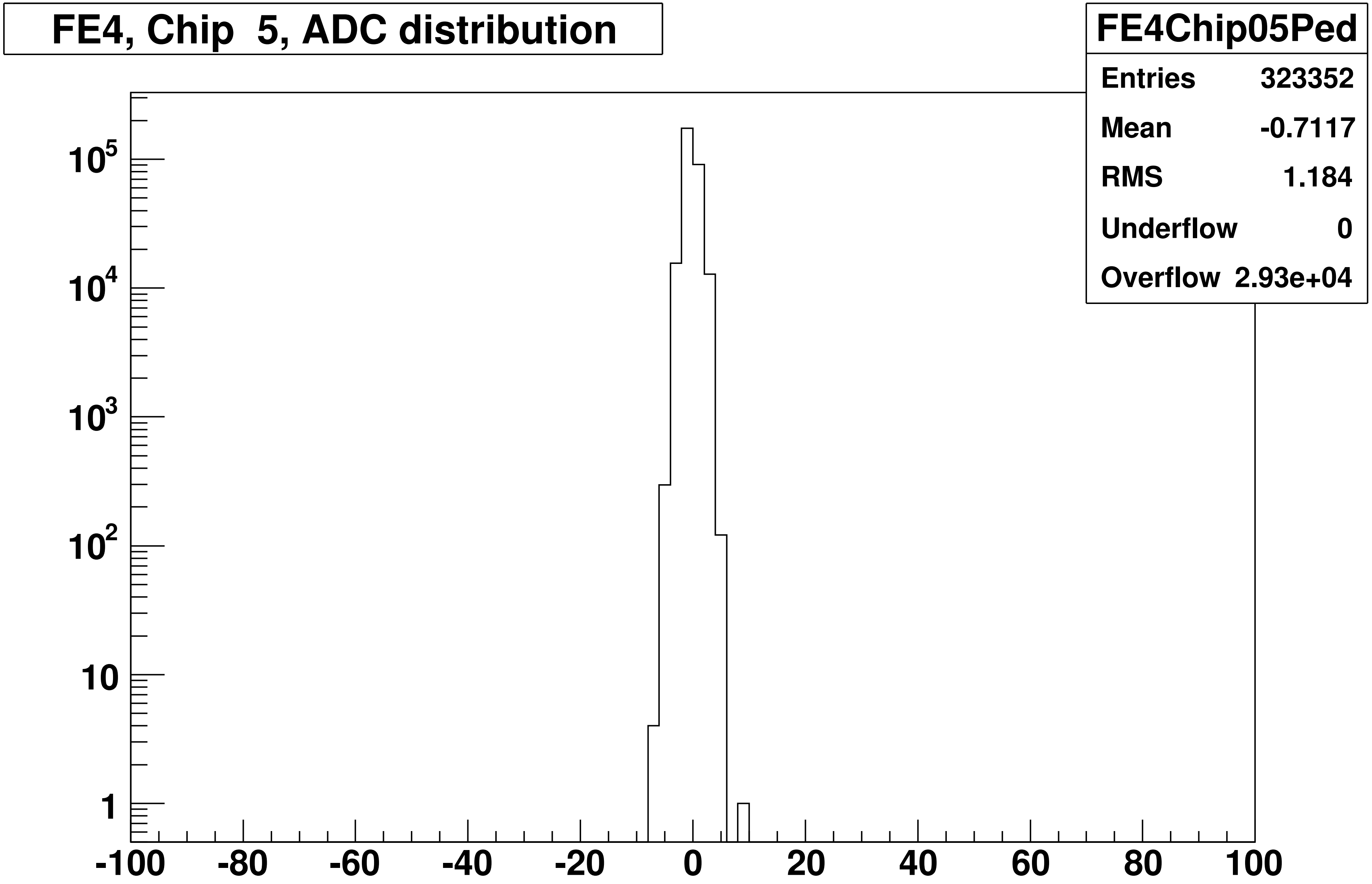

FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 323352
Mean 1687
RMS 5346
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 323352
Mean 3.54
RMS 1.293
Underflow 0
Overflow 2.918e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100

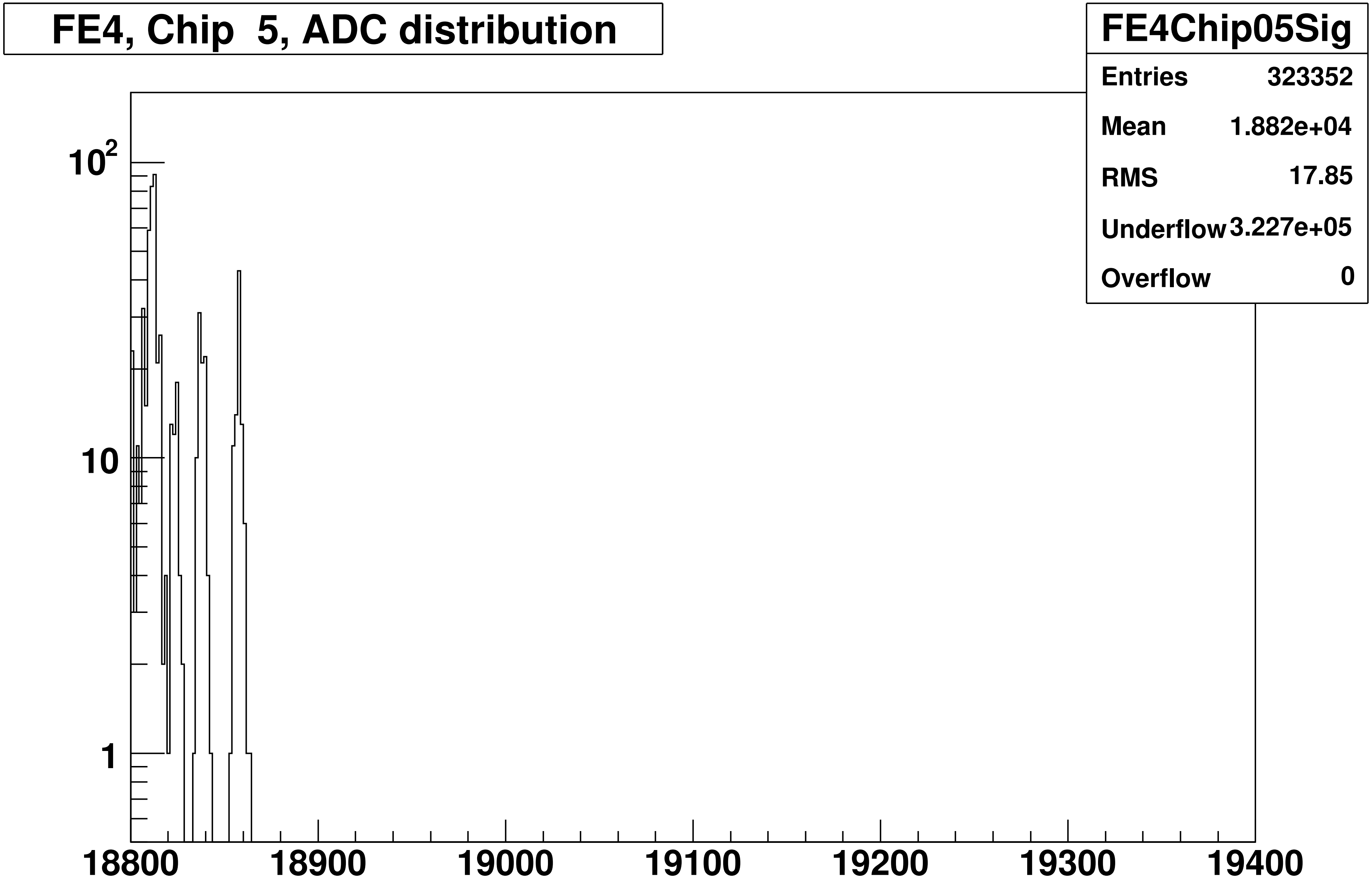

FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 323352
Mean 1692
RMS 5350
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

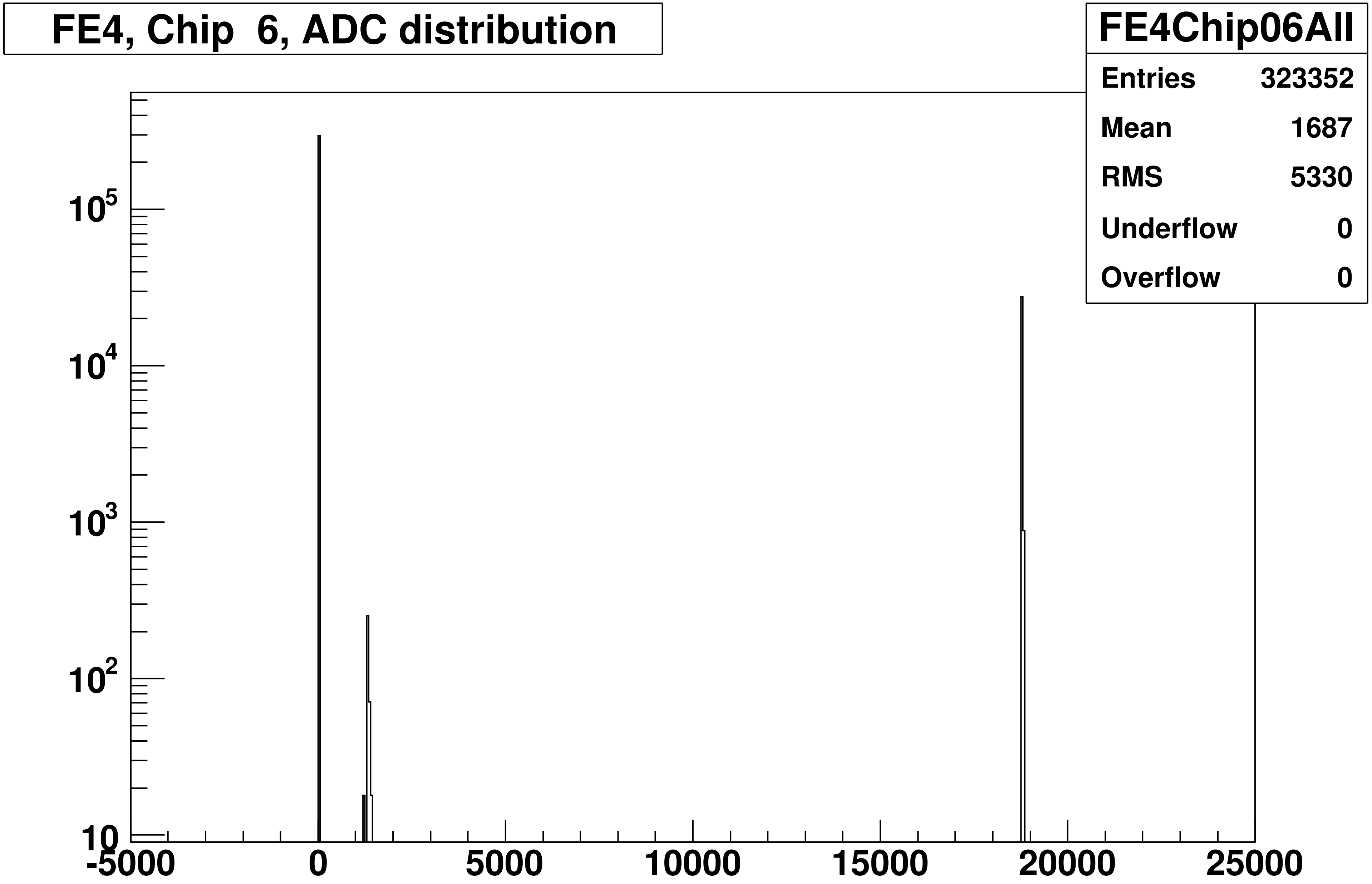

FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 323352
Mean 5.229
RMS 1.317
Underflow 0
Overflow 2.929e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

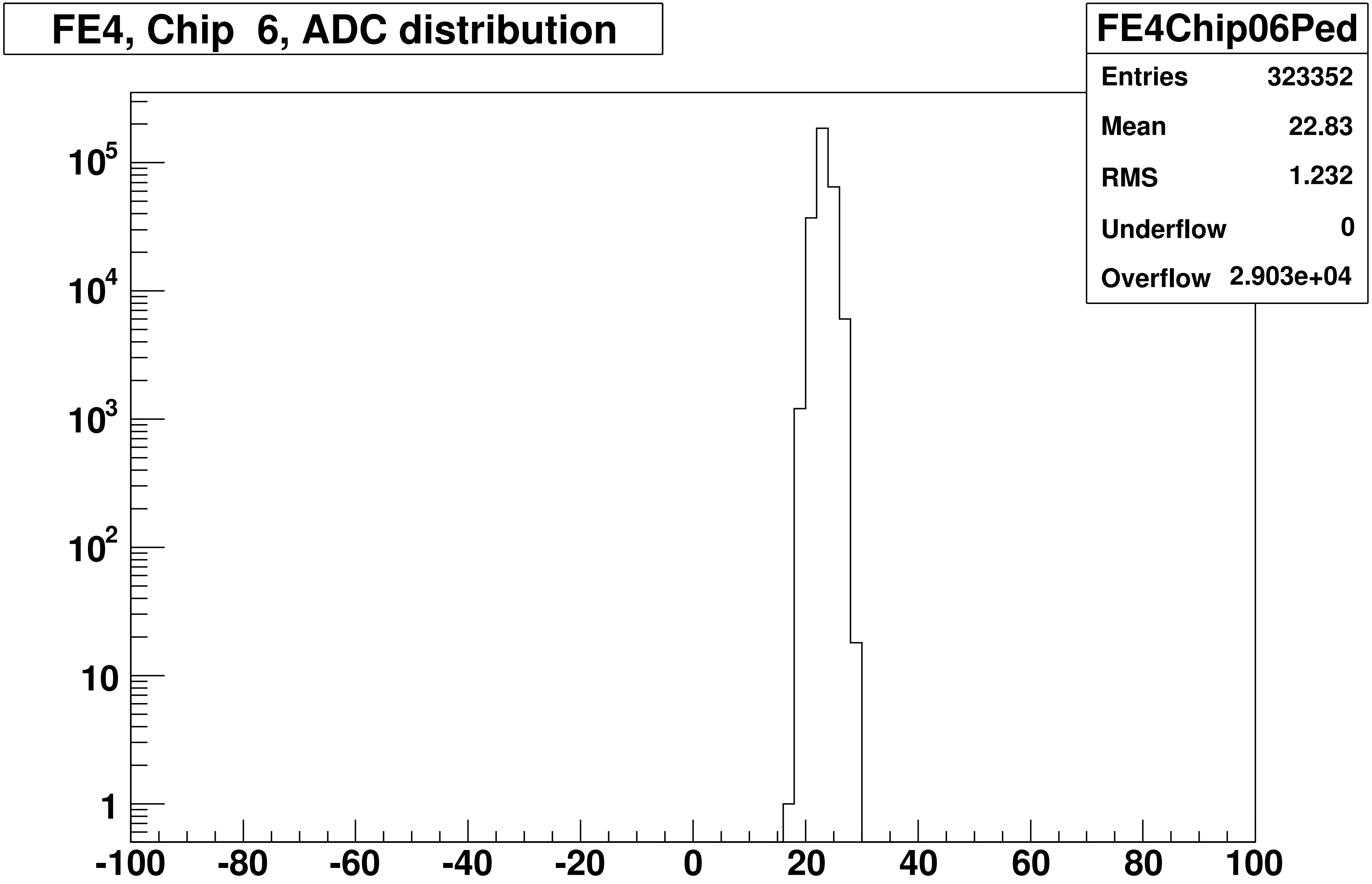

FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 323352
Mean 1695
RMS 5373
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



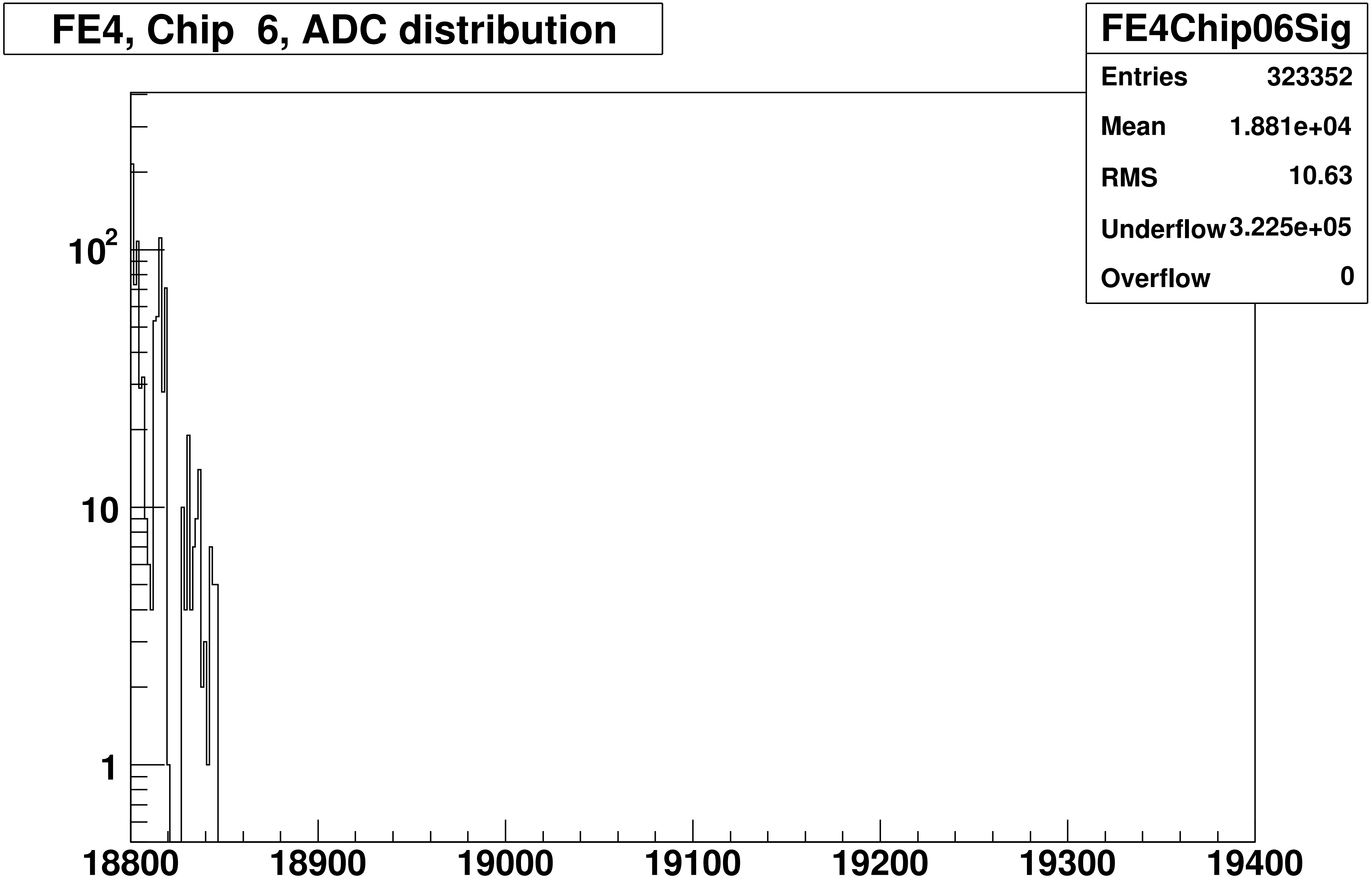
FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 323352
Mean -0.7117
RMS 1.184
Underflow 0
Overflow 2.93e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

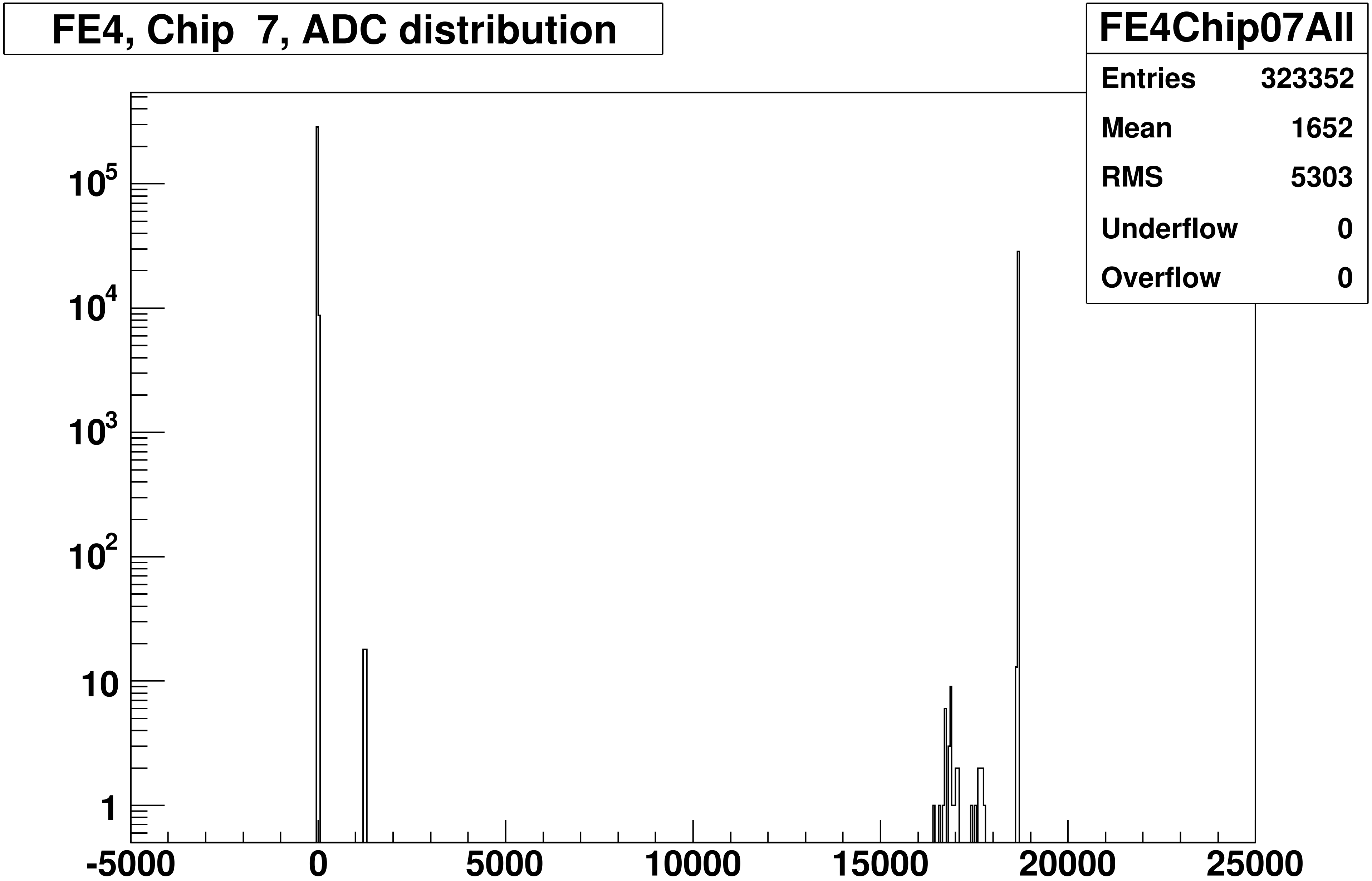

FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 323352
Mean 1.882e+04
RMS 17.85
Underflow 3.227e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 323352
Mean 1687
RMS 5330
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

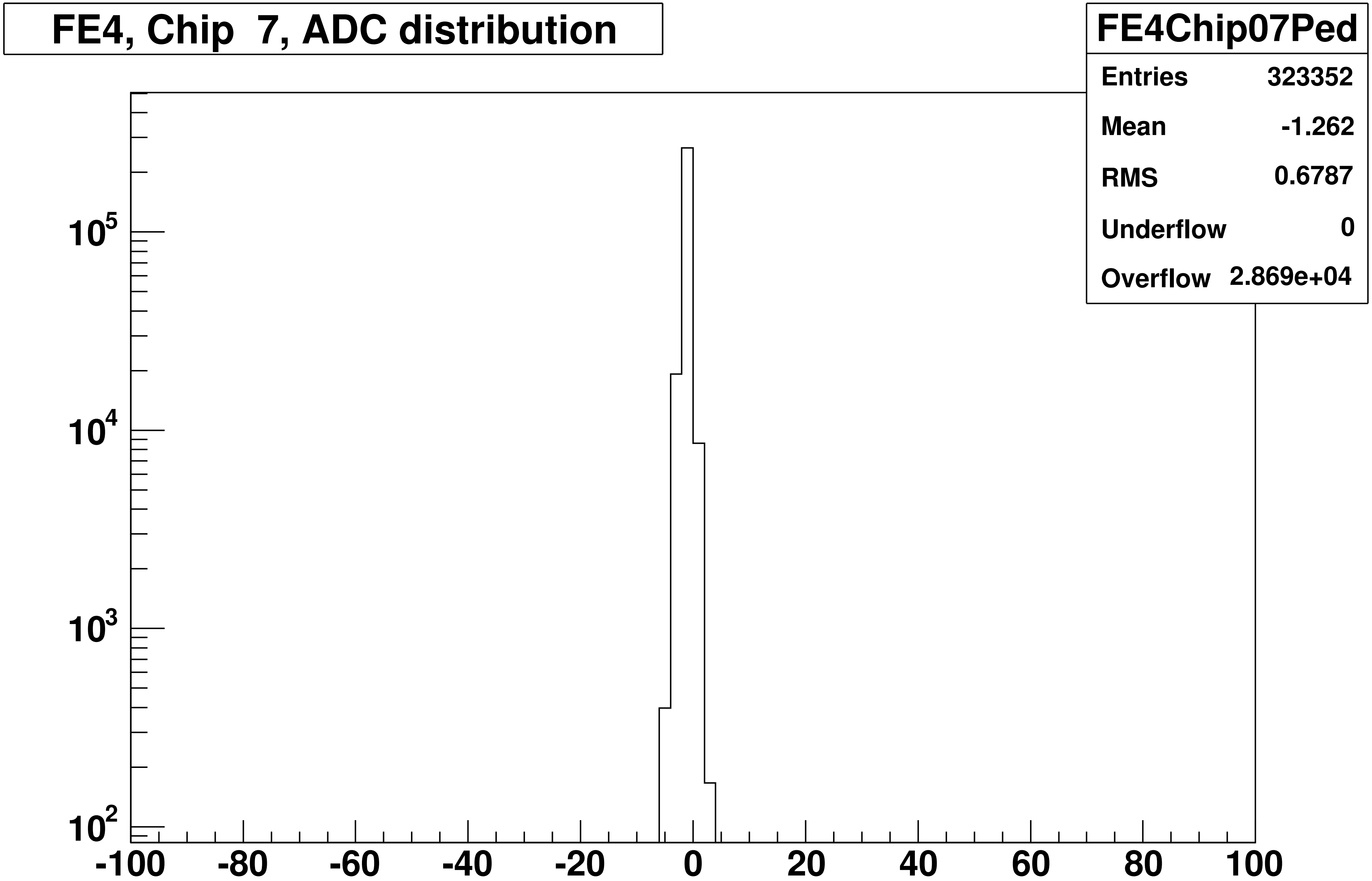


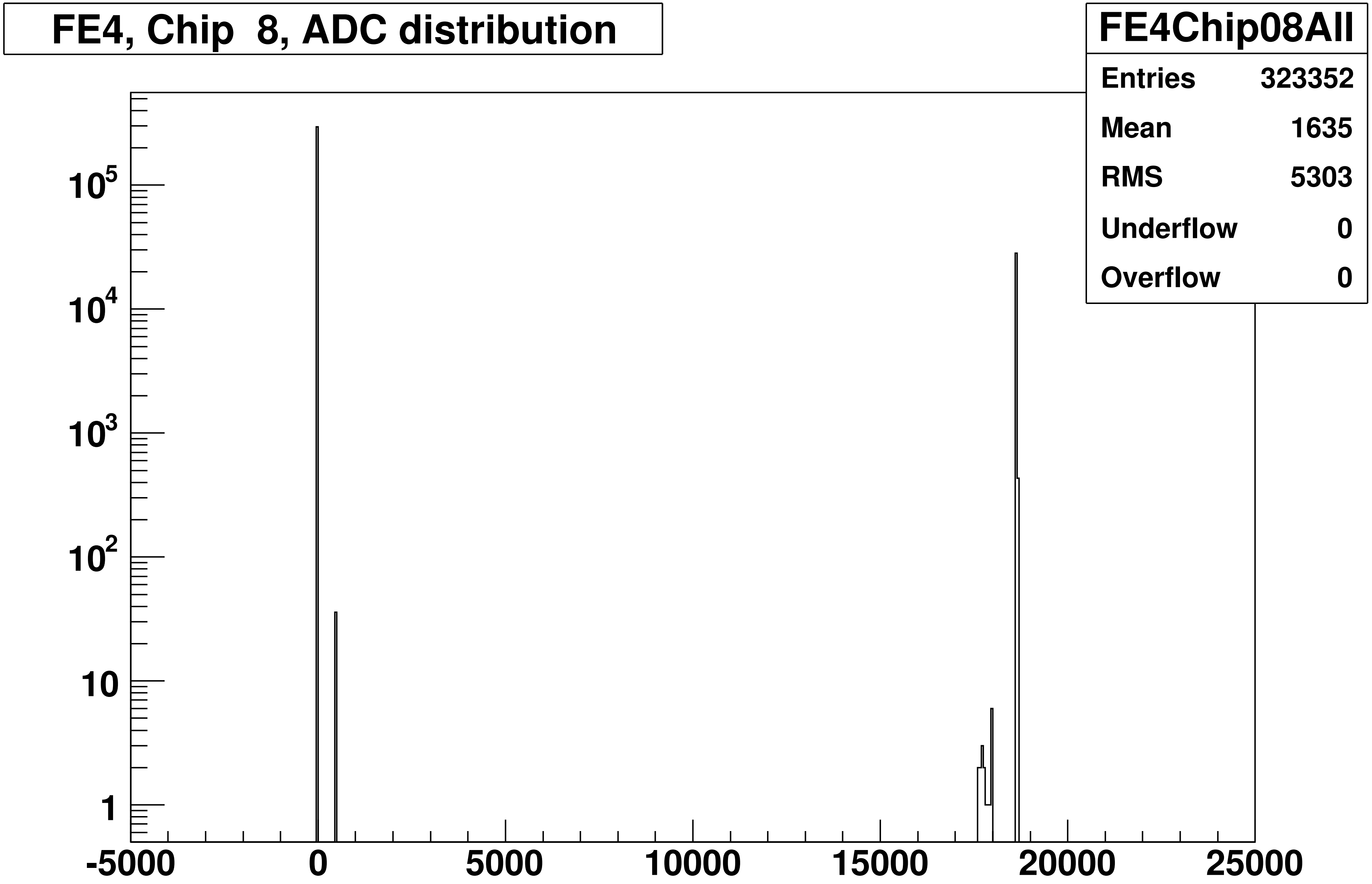
FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 323352
Mean 22.83
RMS 1.232
Underflow 0
Overflow 2.903e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

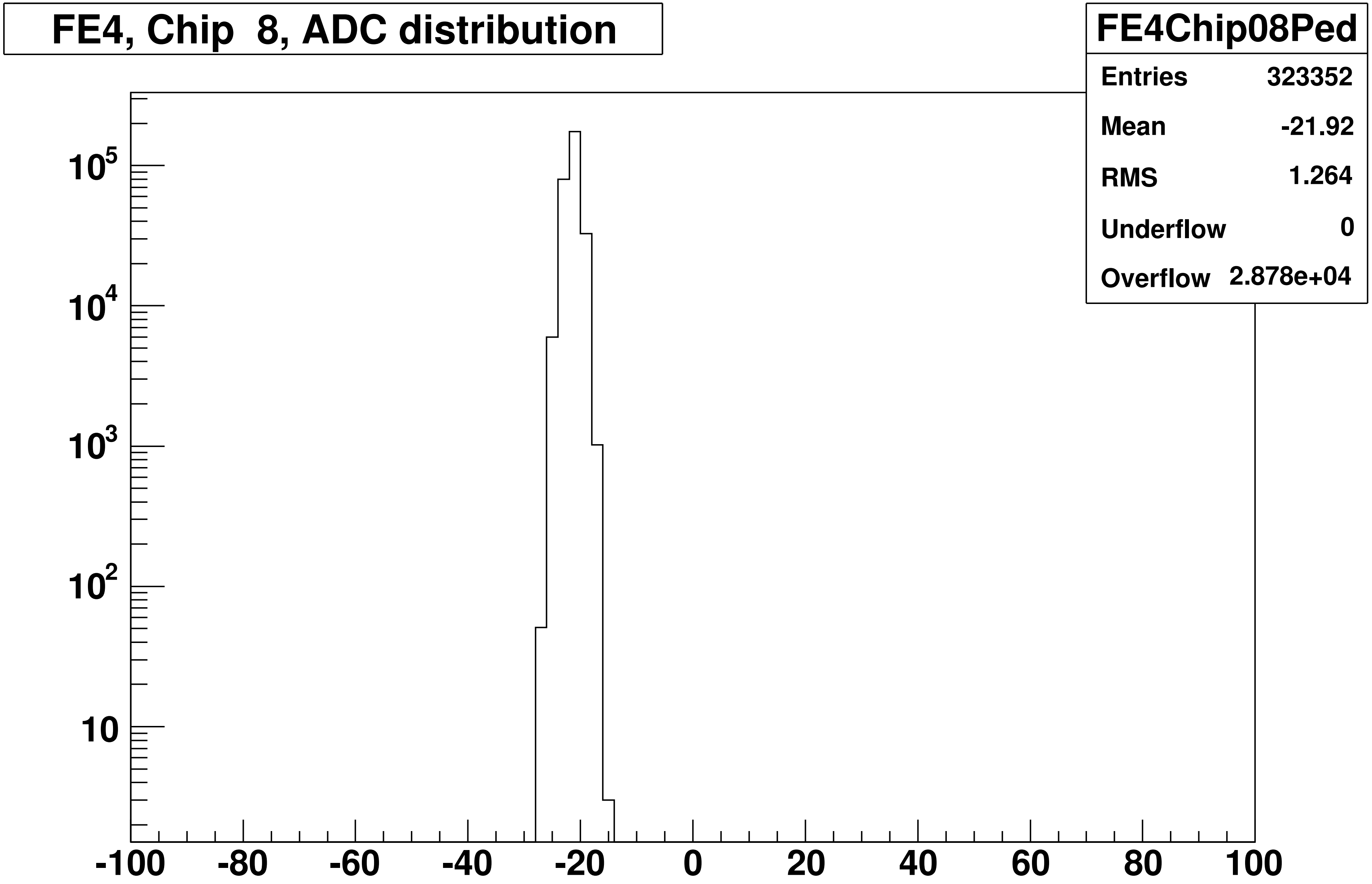

FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 323352
Mean 1.881e+04
RMS 10.63
Underflow 3.225e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



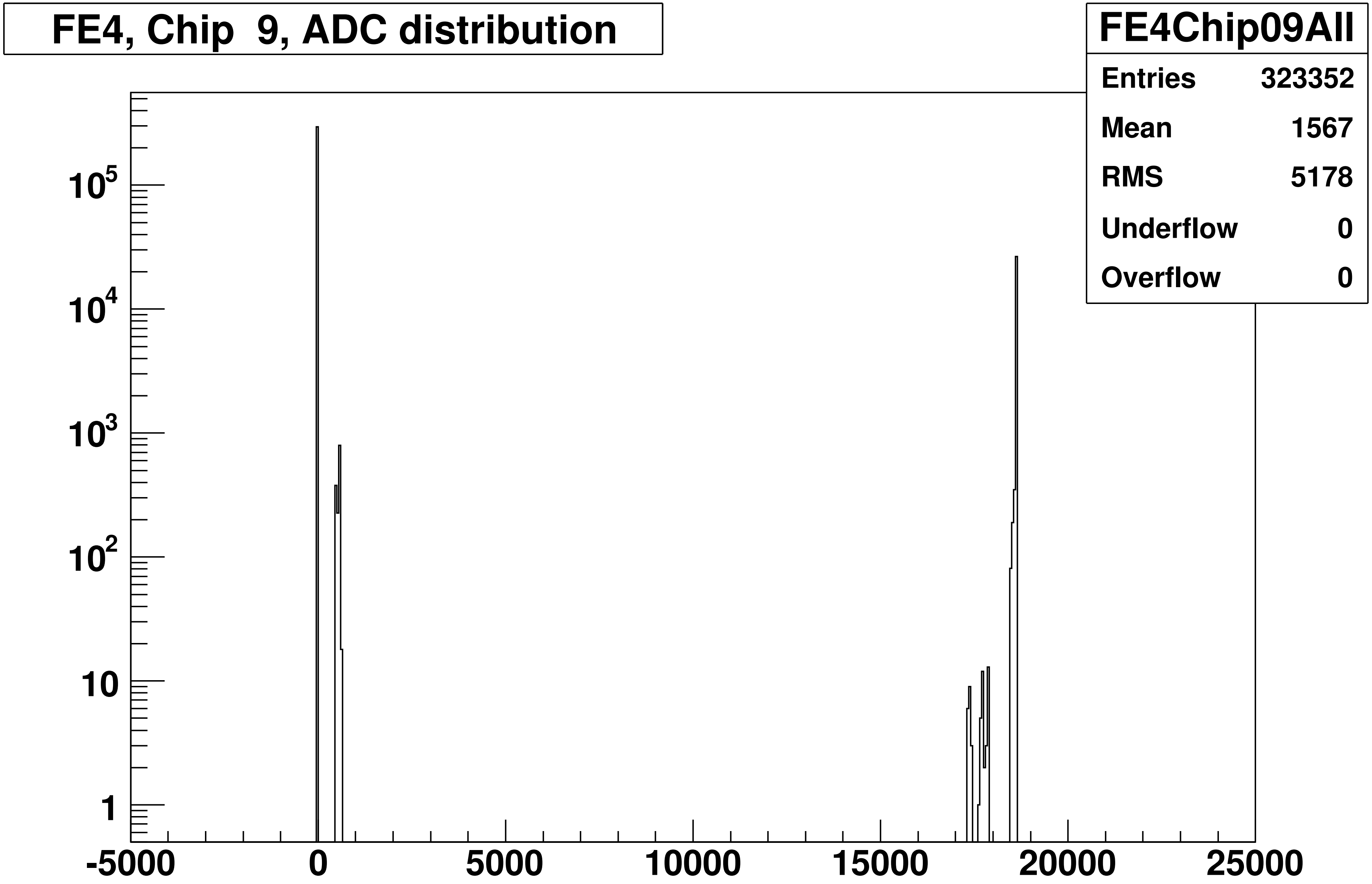
FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 323352
Mean 1652
RMS 5303
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

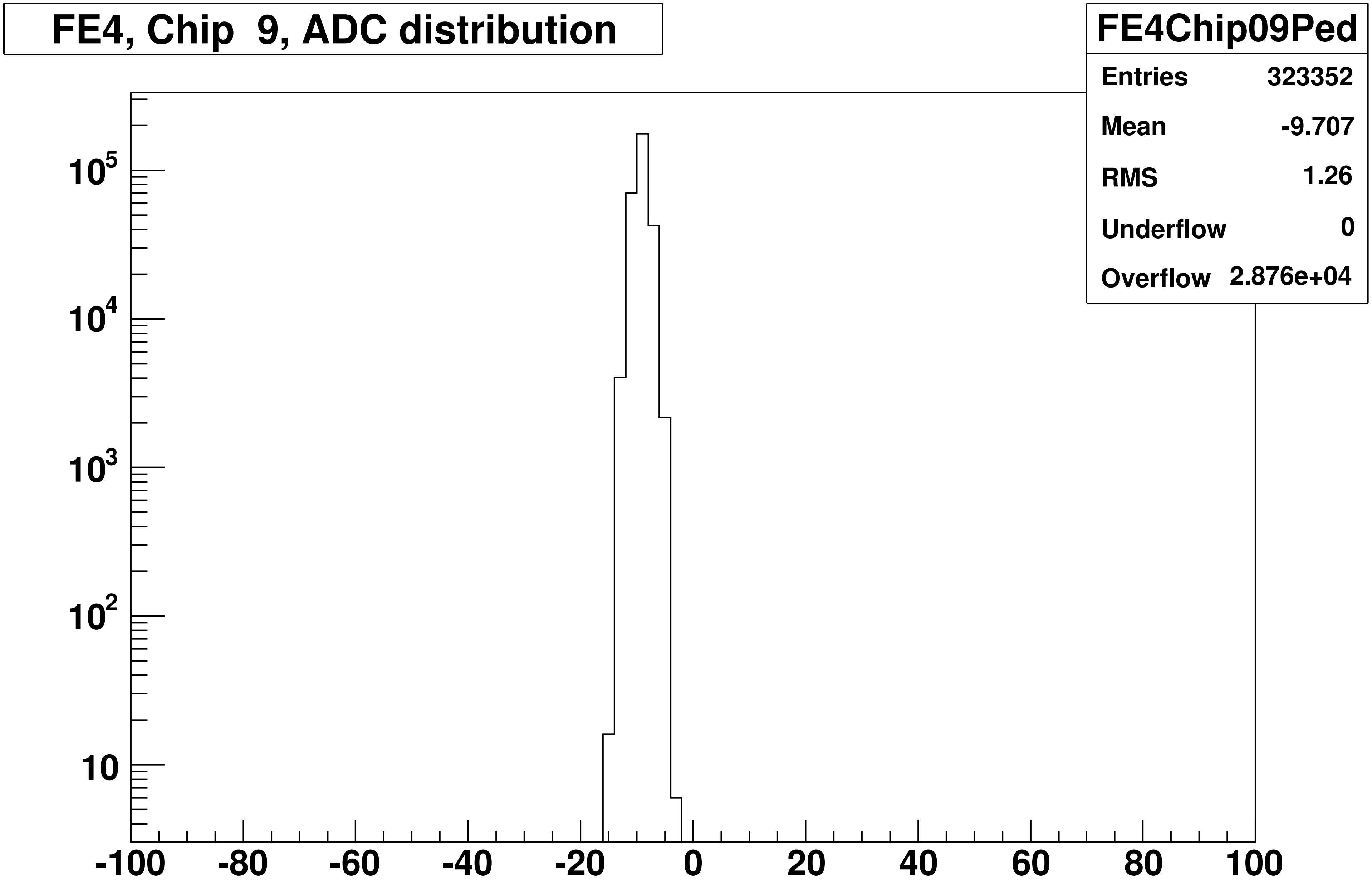

FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 323352
Mean -1.262
RMS 0.6787
Underflow 0
Overflow 2.869e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

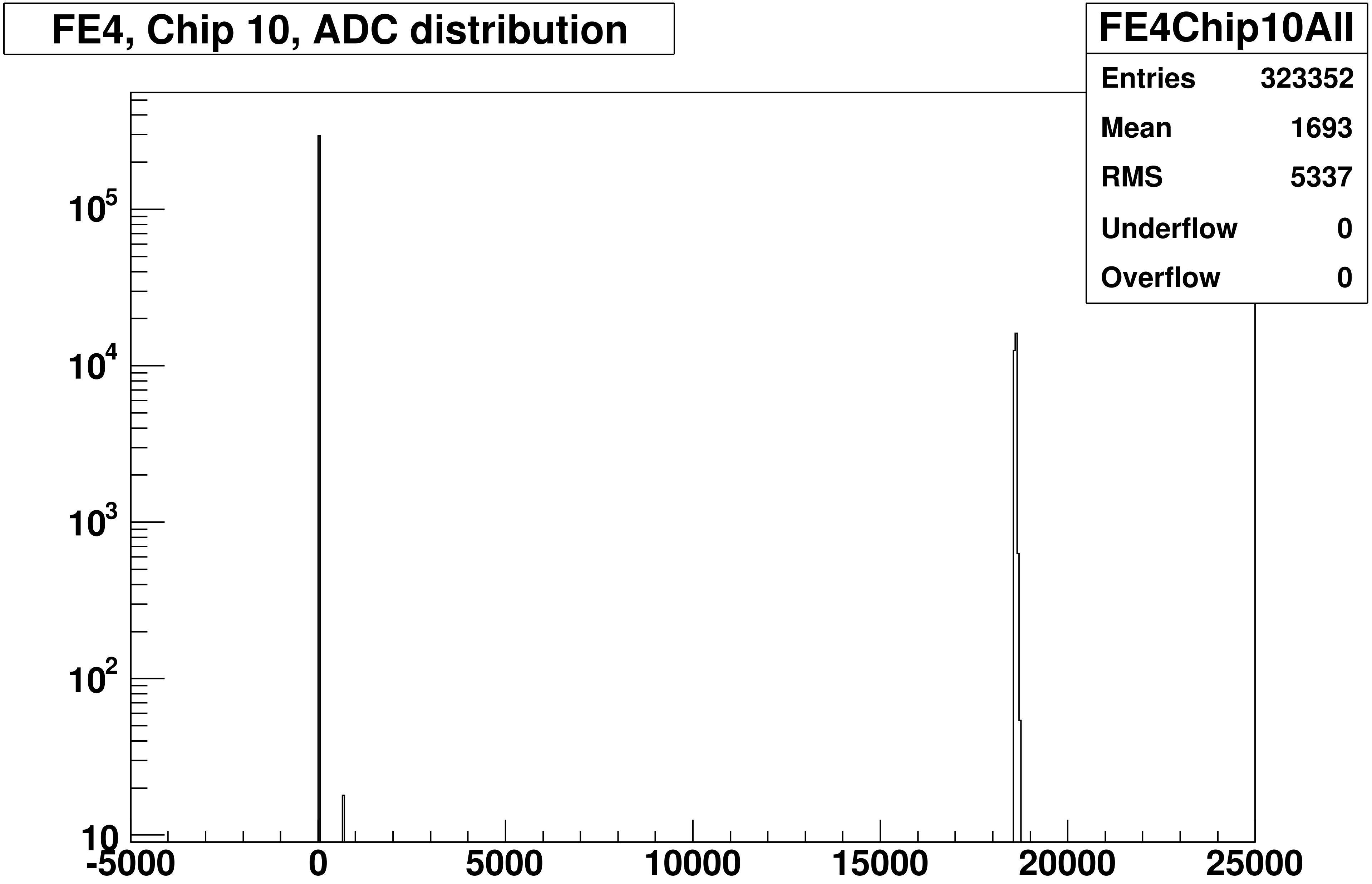

FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 323352
Mean 1635
RMS 5303
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

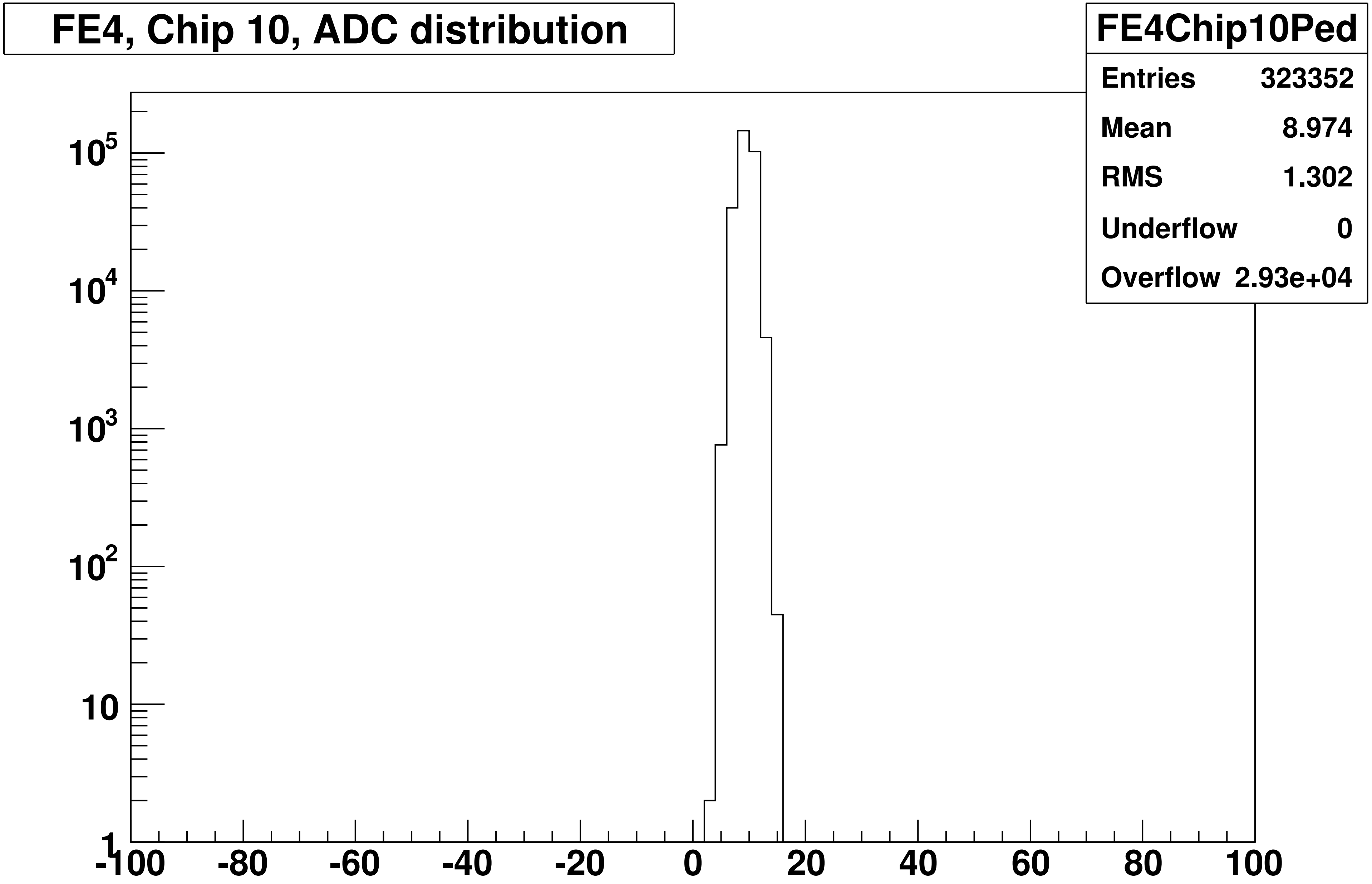


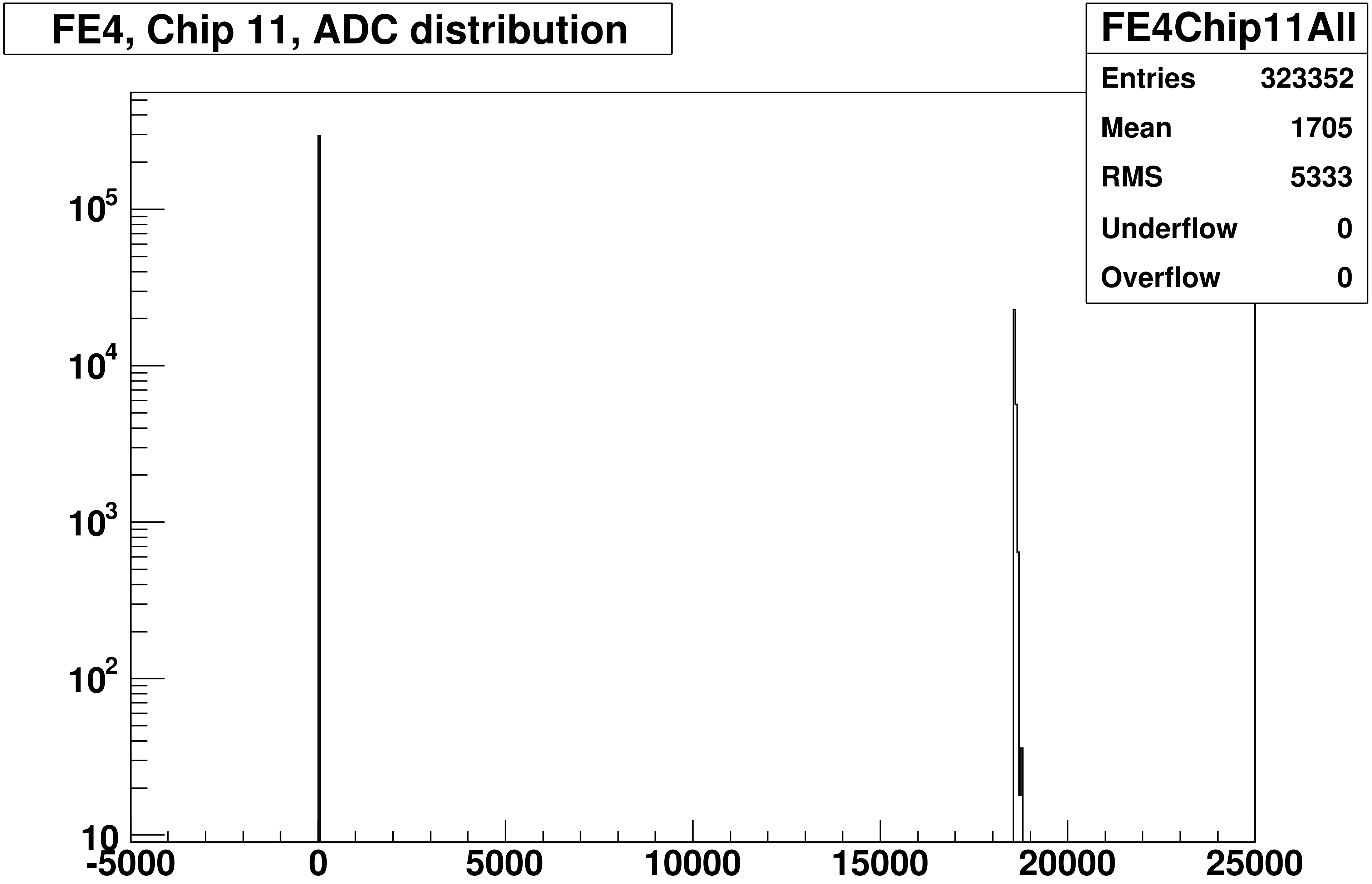
FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 323352
Mean -21.92
RMS 1.264
Underflow 0
Overflow 2.878e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

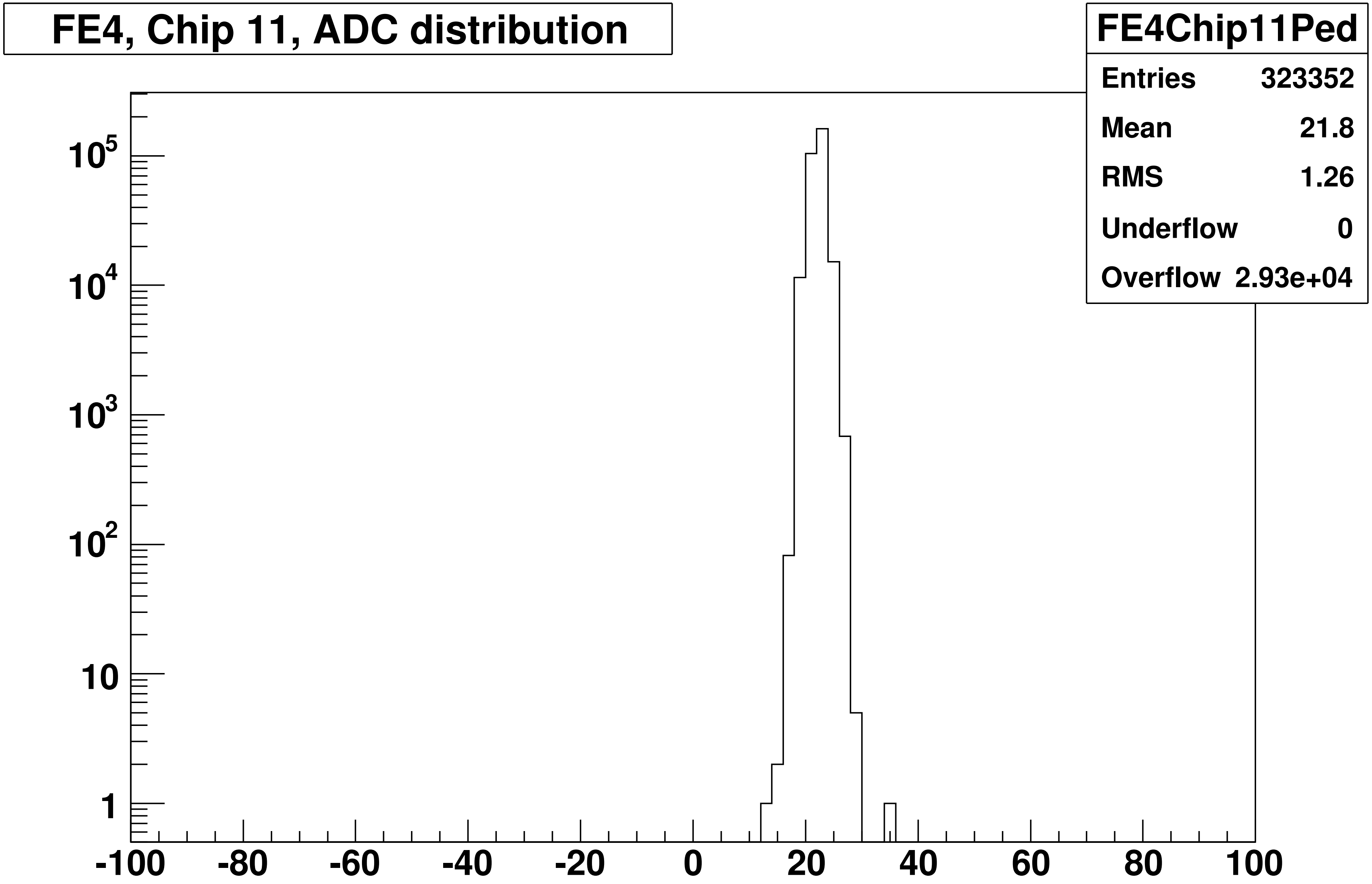

FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 323352
Mean 1567
RMS 5178
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 323352
Mean -9.707
RMS 1.26
Underflow 0
Overflow 2.876e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

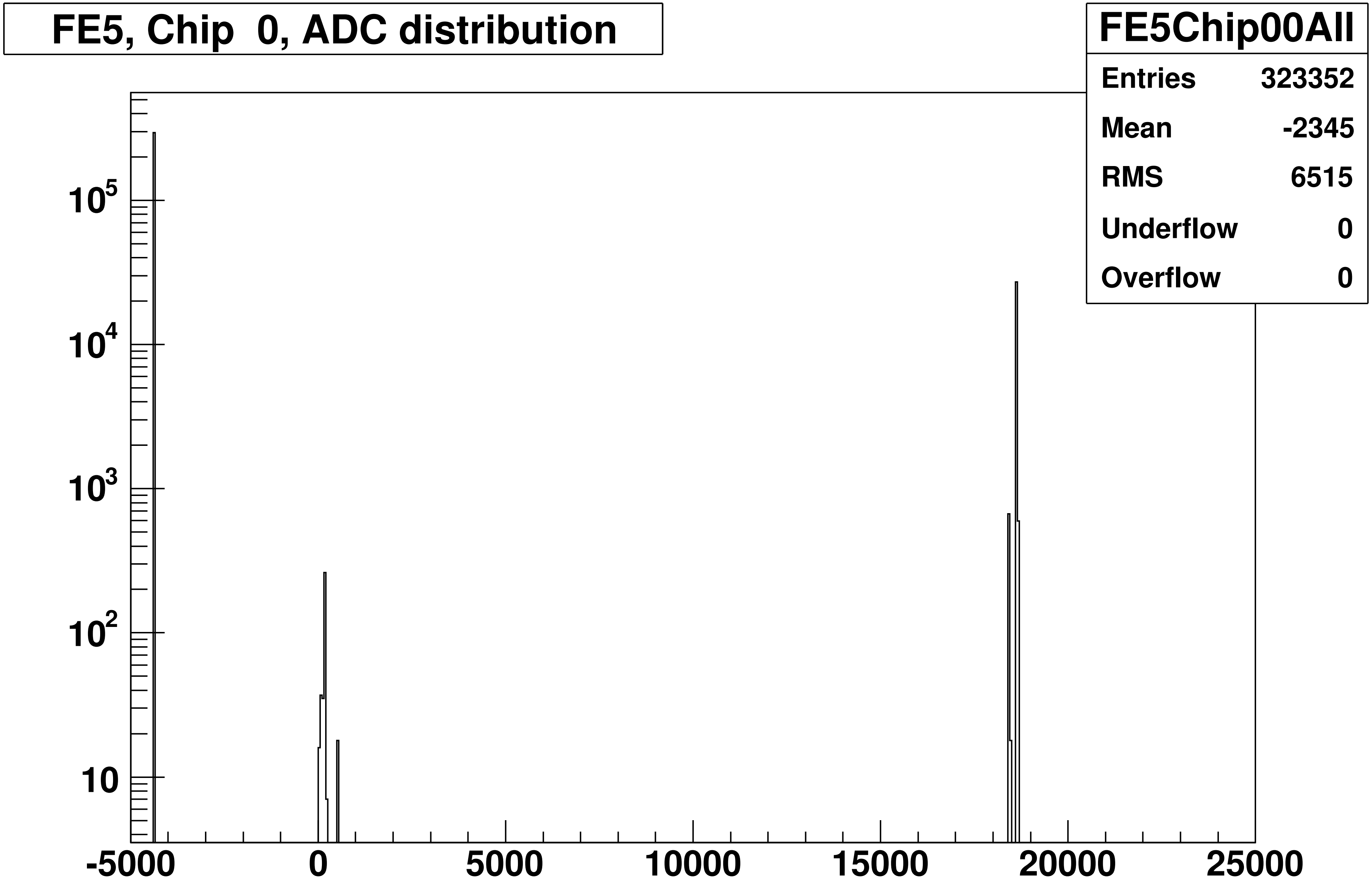

FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 323352
Mean 1693
RMS 5337
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

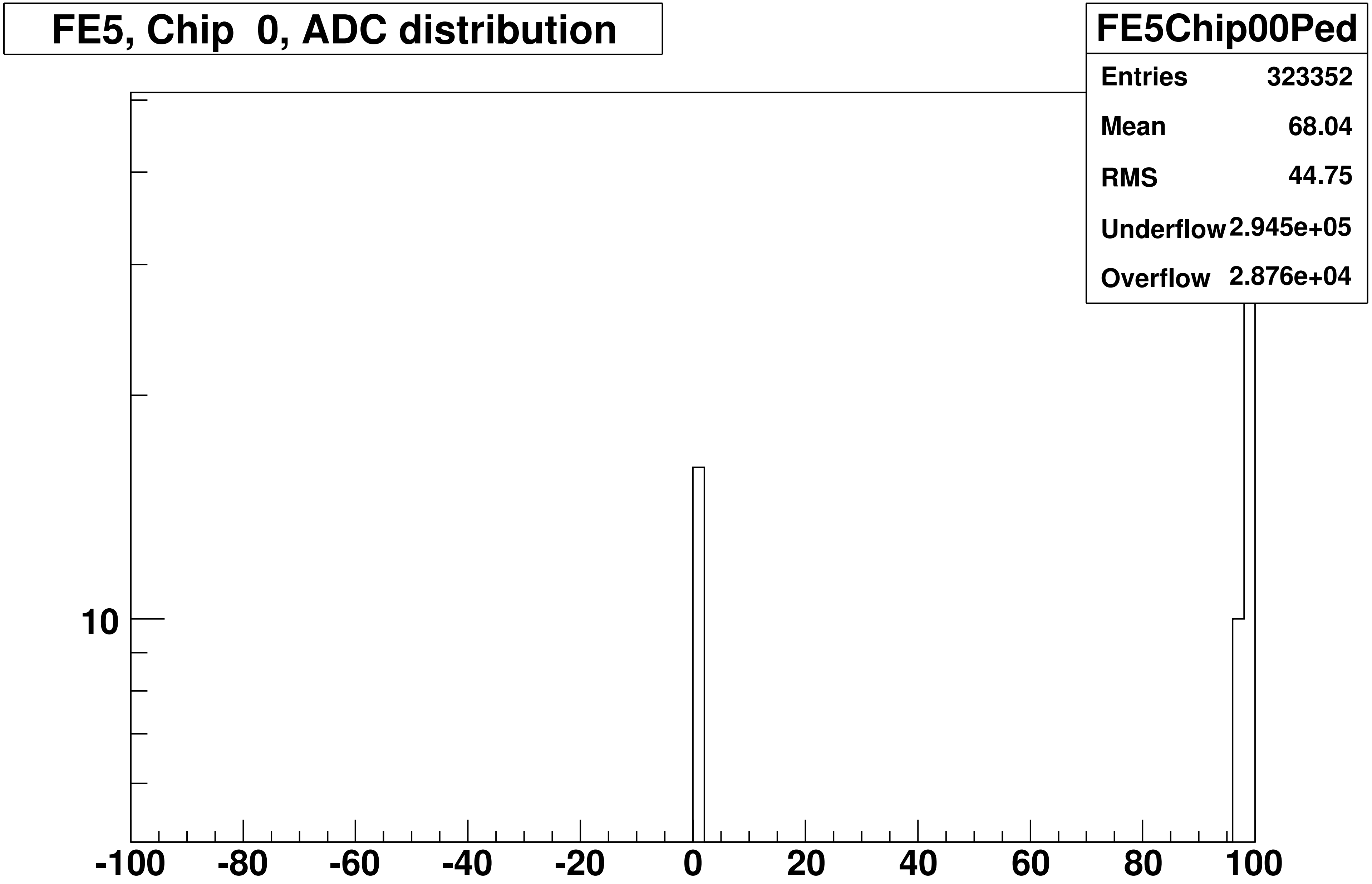

FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 323352
Mean 8.974
RMS 1.302
Underflow 0
Overflow 2.93e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

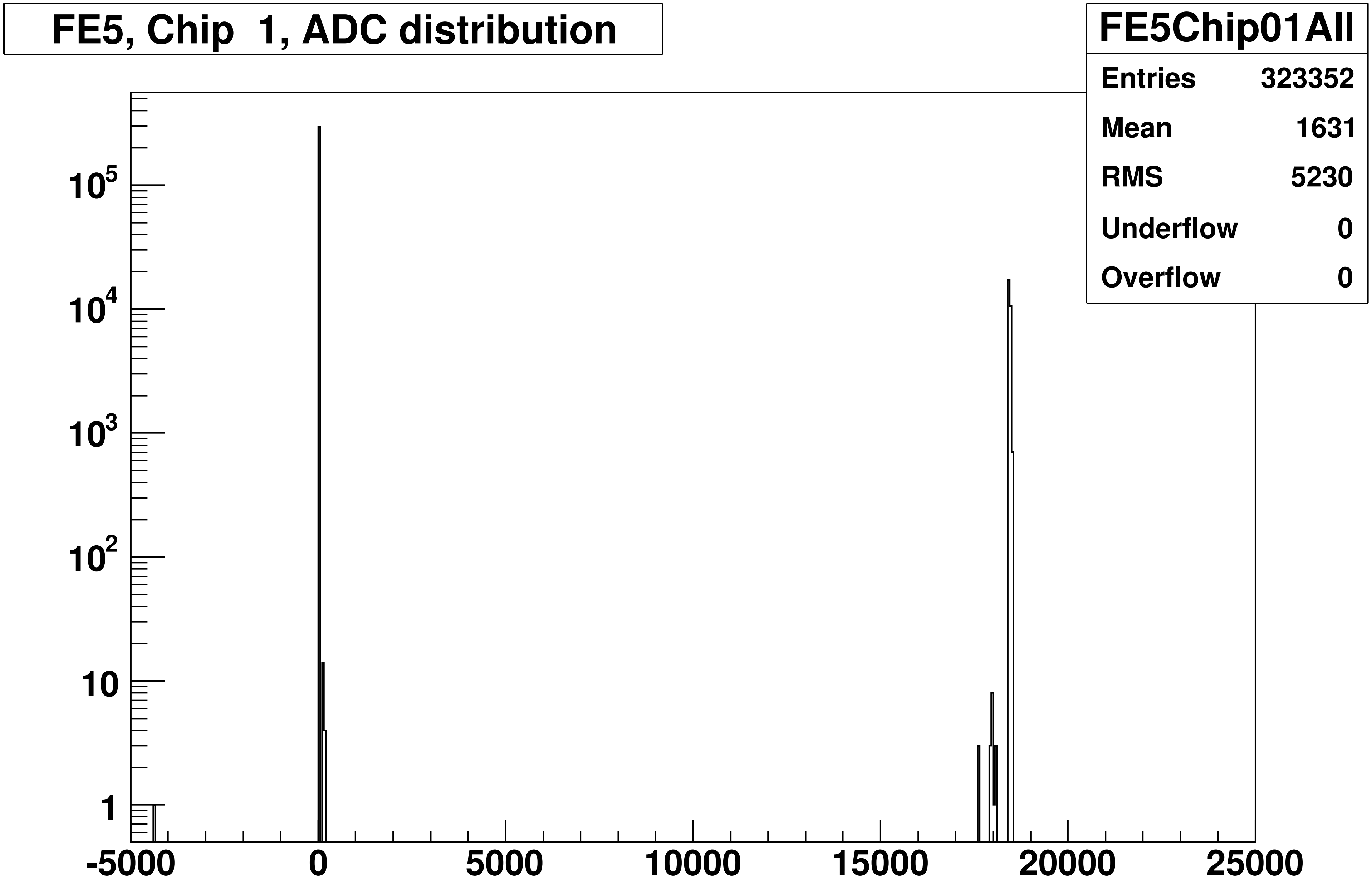

FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 323352
Mean 1705
RMS 5333
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

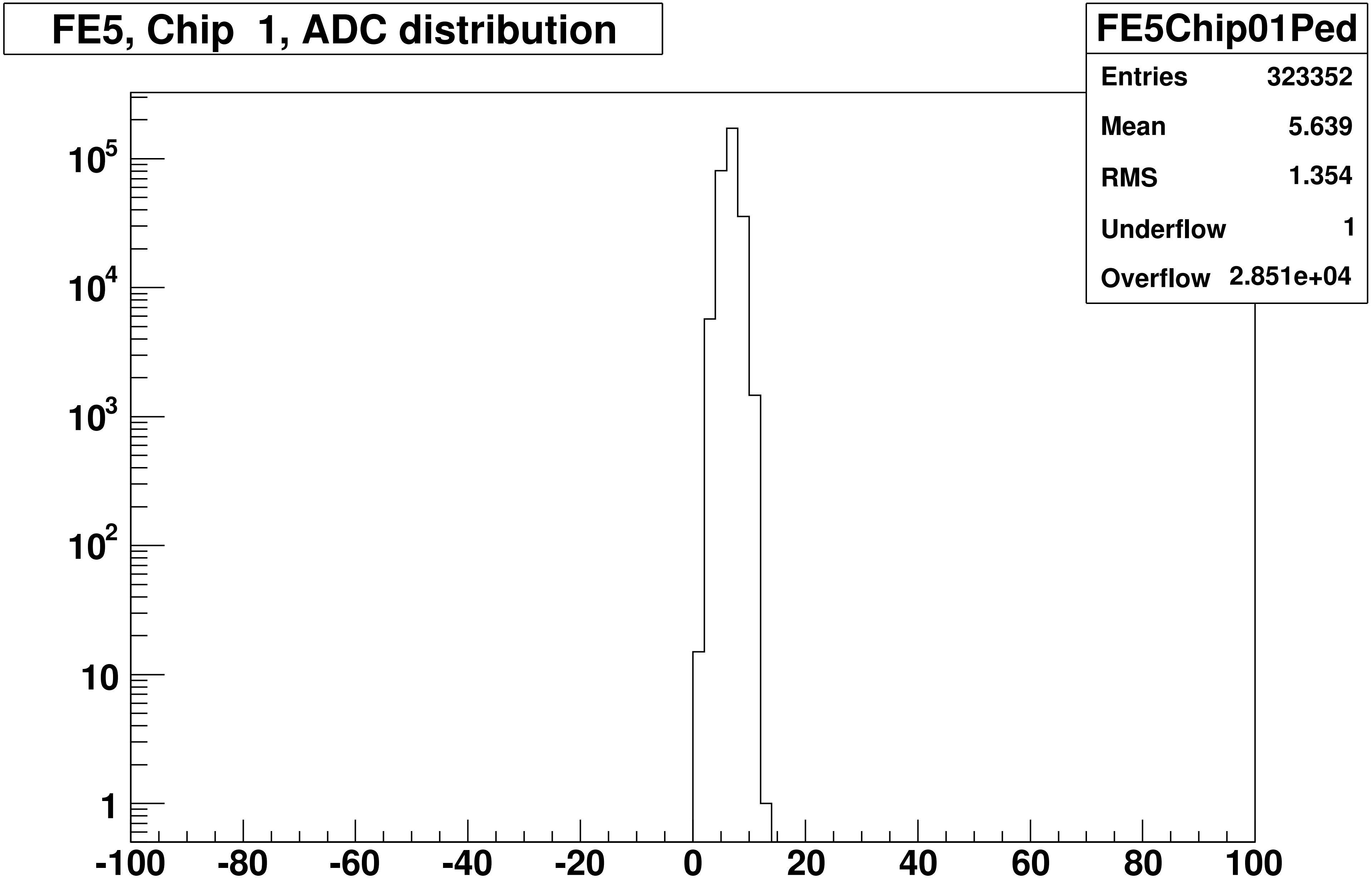


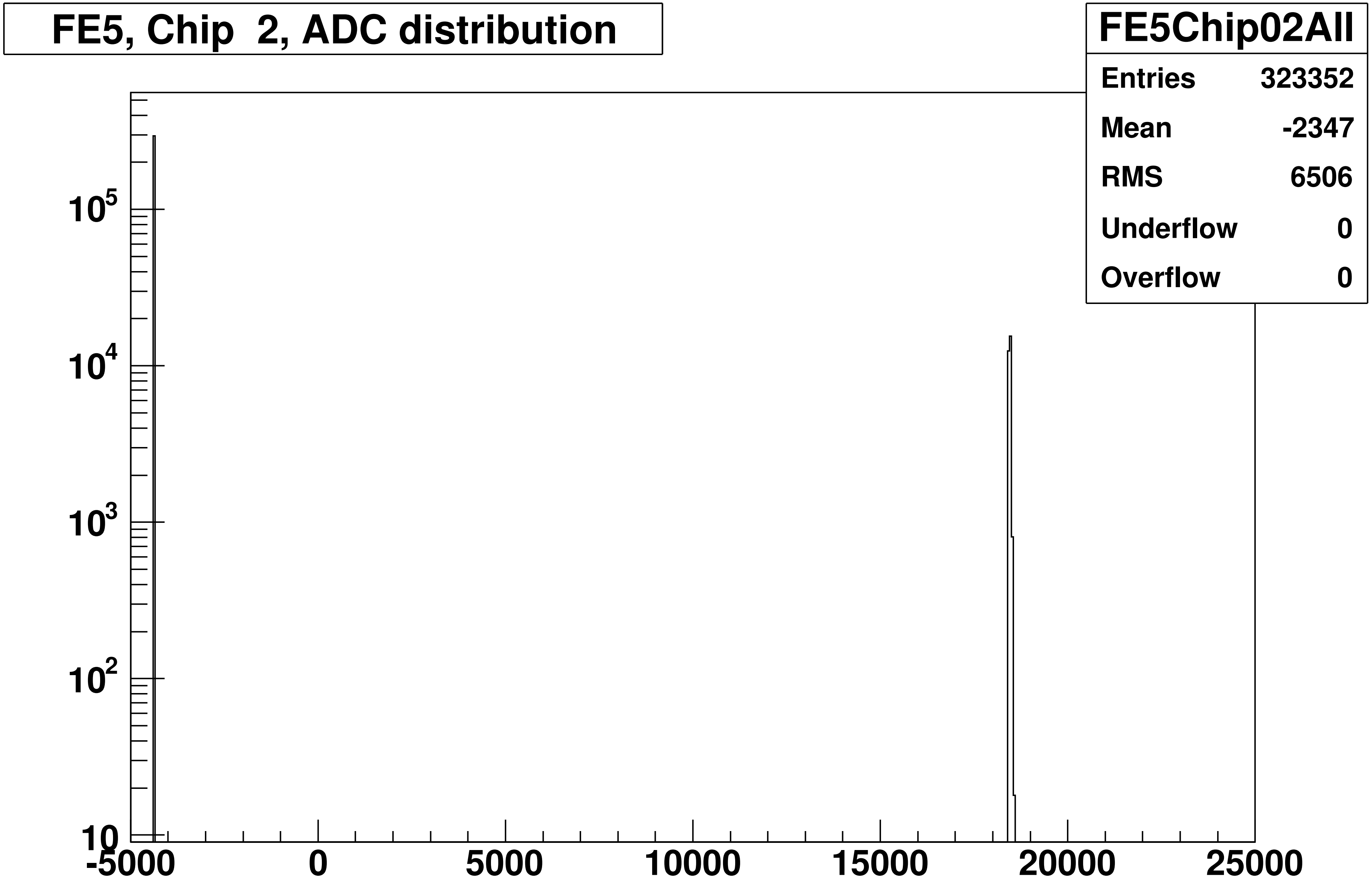
FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 323352
Mean 21.8
RMS 1.26
Underflow 0
Overflow 2.93e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

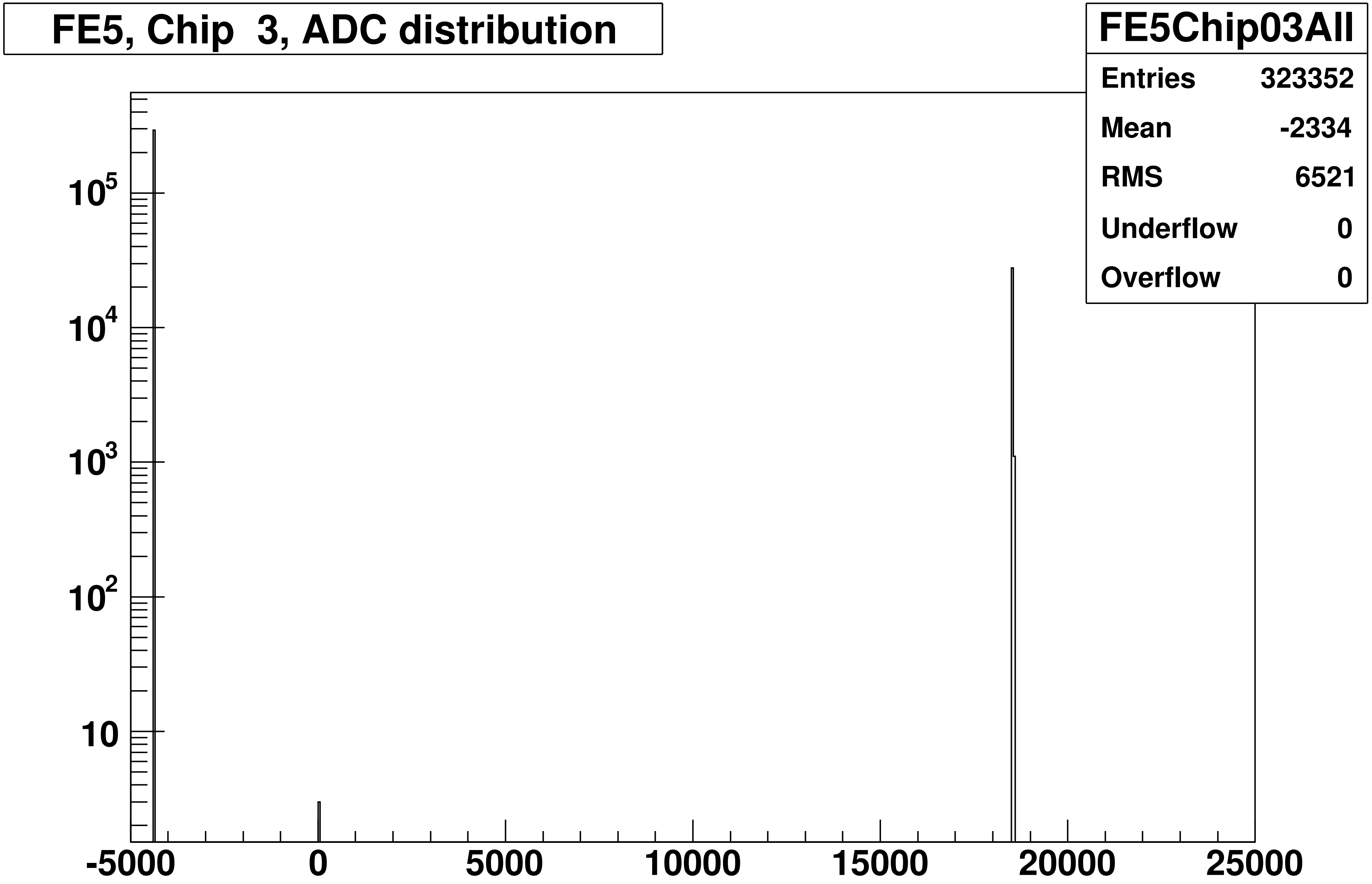

FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 323352
Mean -2345
RMS 6515
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

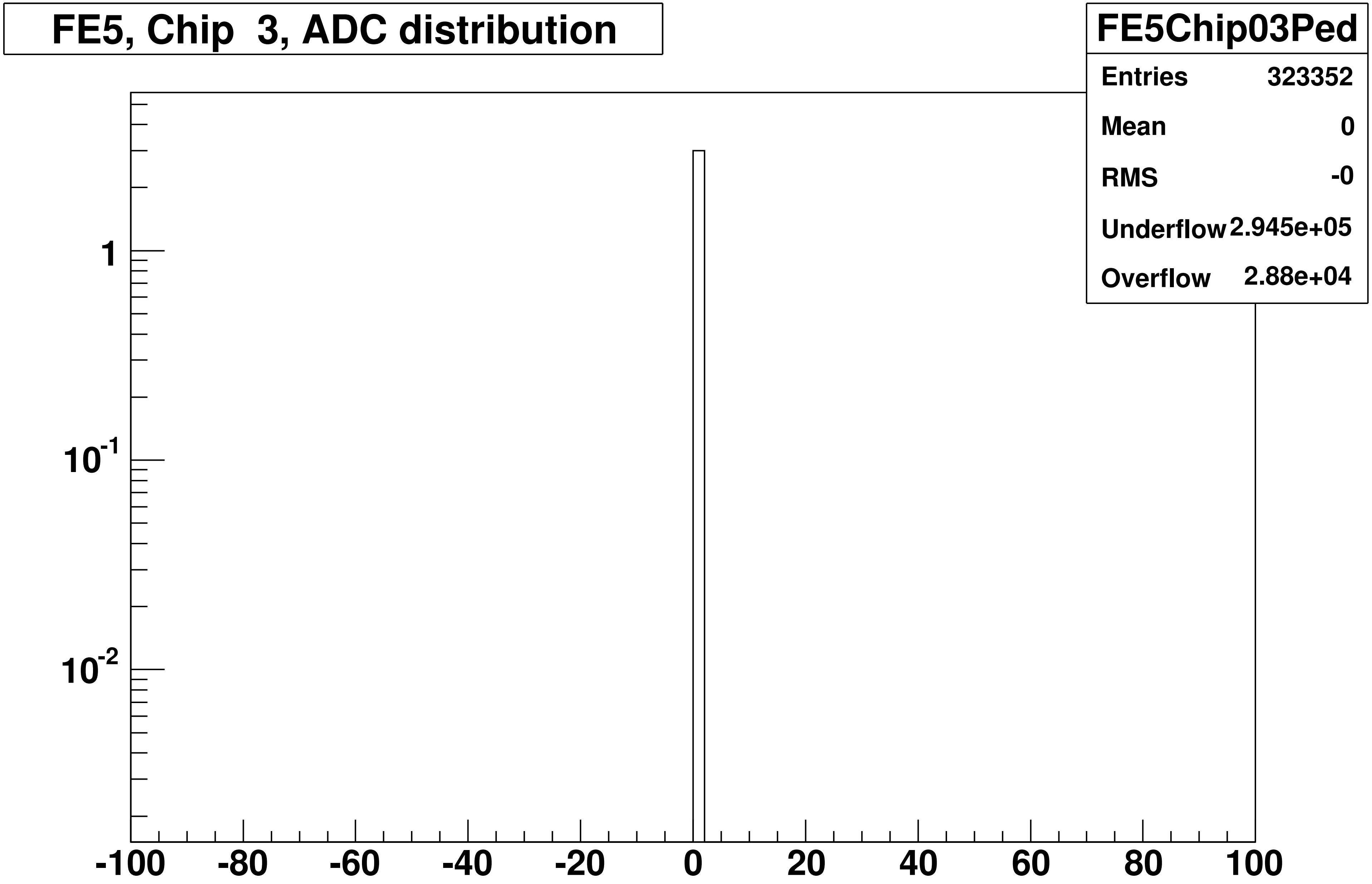

FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 323352
Mean 68.04
RMS 44.75
Underflow 2.945e+05
Overflow 2.876e+04
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 323352
Mean 1631
RMS 5230
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

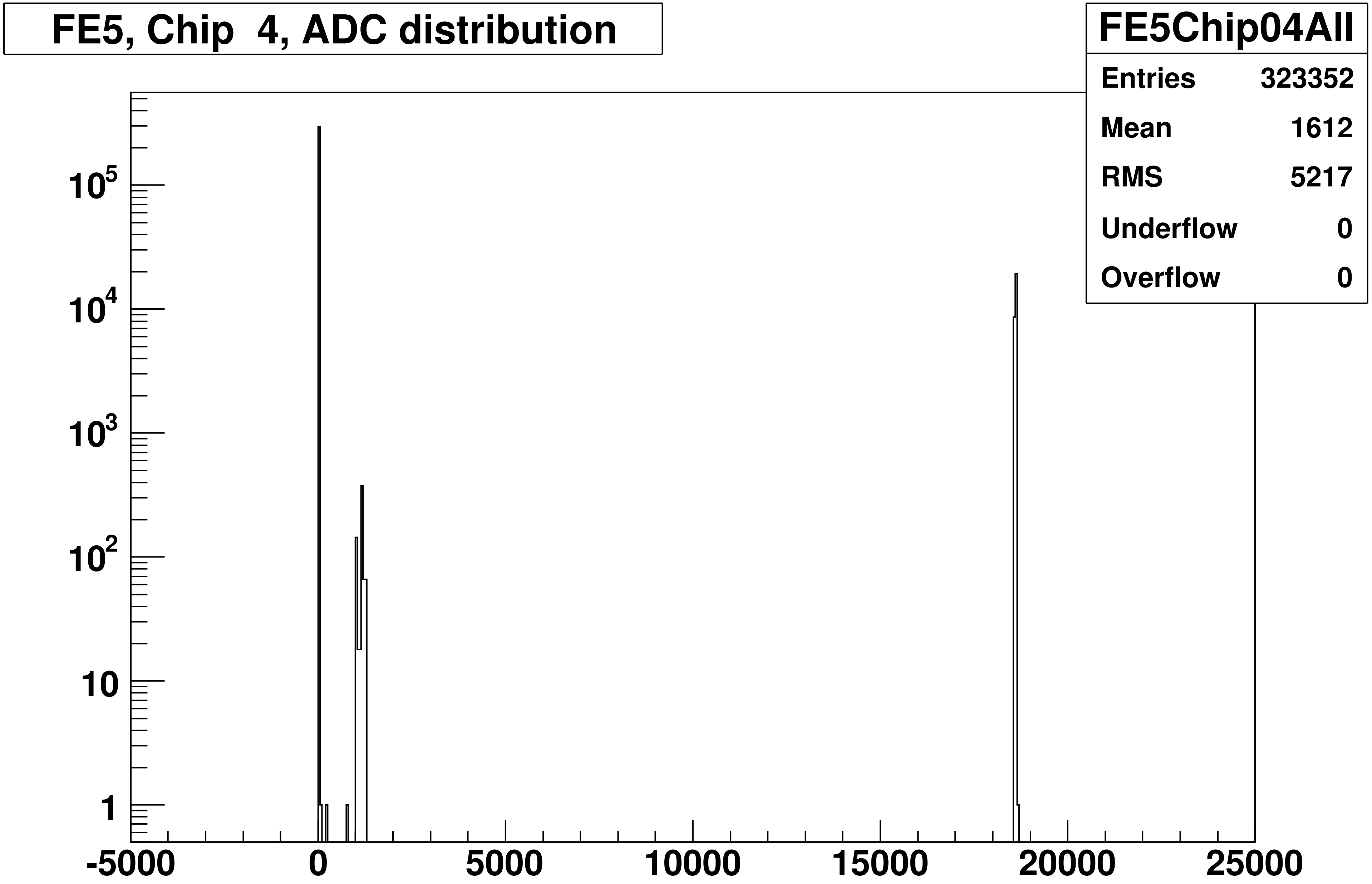

FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 323352
Mean 5.639
RMS 1.354
Underflow 1
Overflow 2.851e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

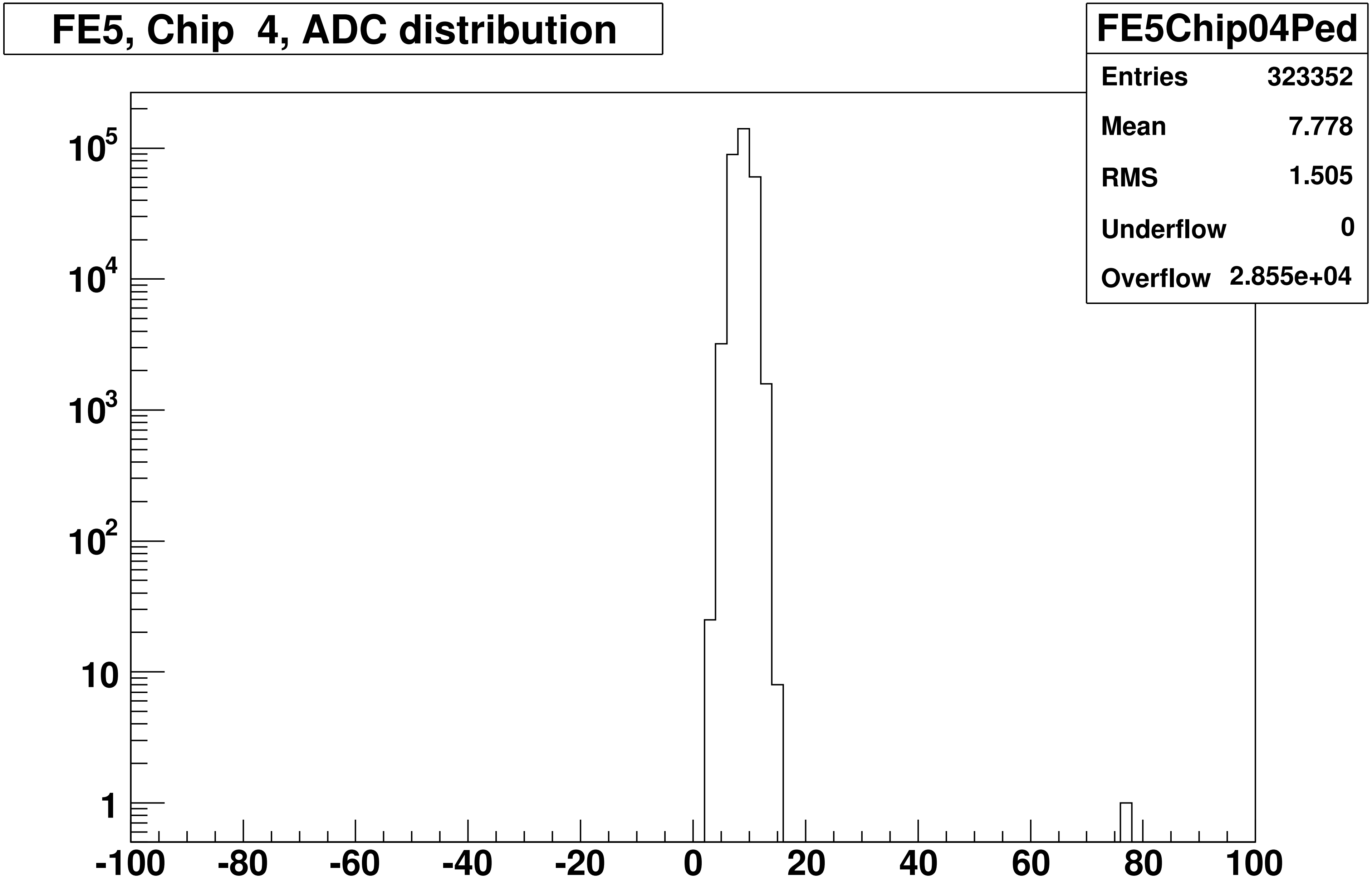

FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 323352
Mean -2347
RMS 6506
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 323352
Mean -2334
RMS 6521
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

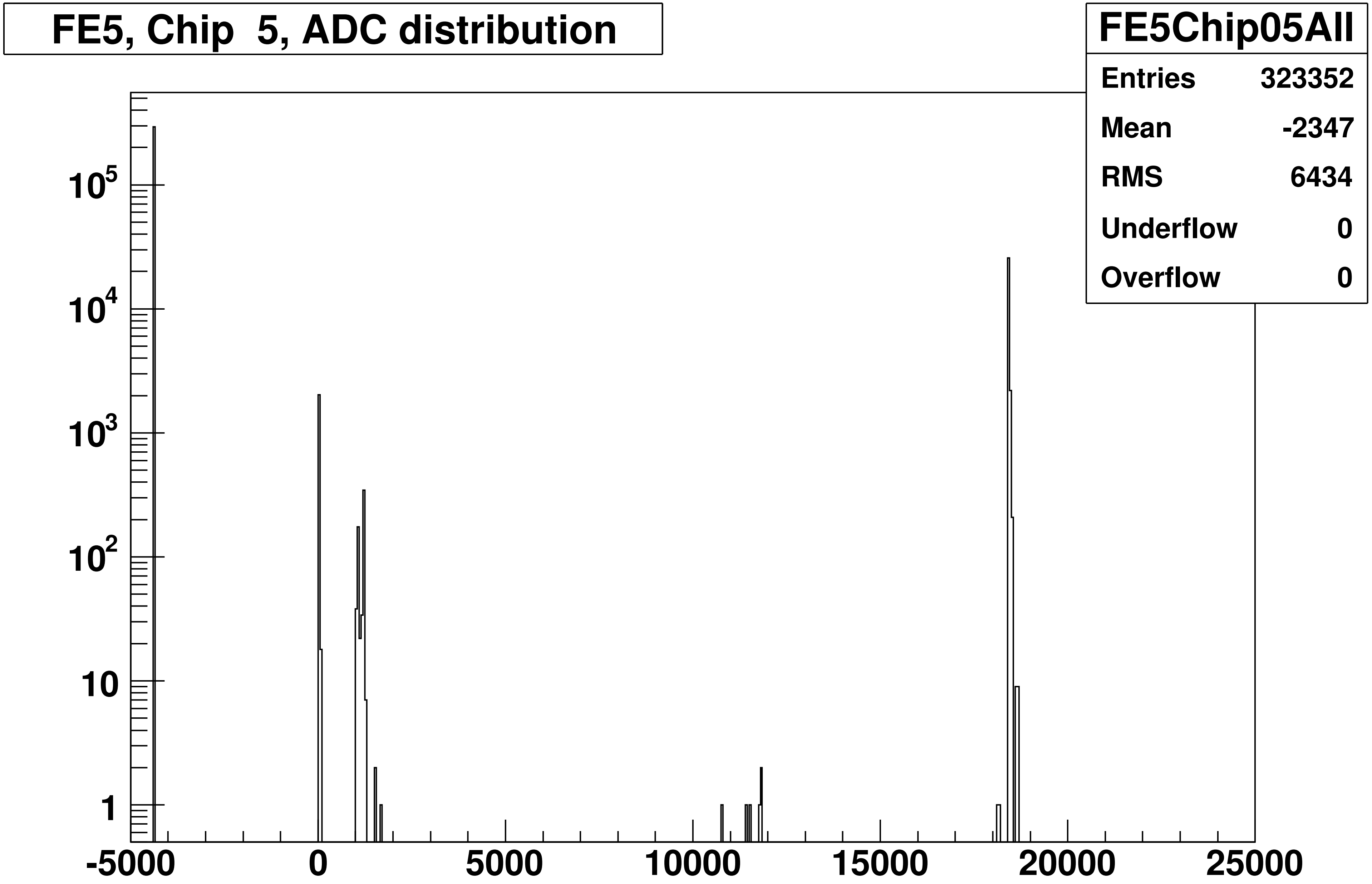


FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 323352
Mean 0
RMS -0
Underflow 2.945e+05
Overflow 2.88e+04
1
10-1
10-2
-100
-80
-60
-40
-20
0
20
40
60
80
100

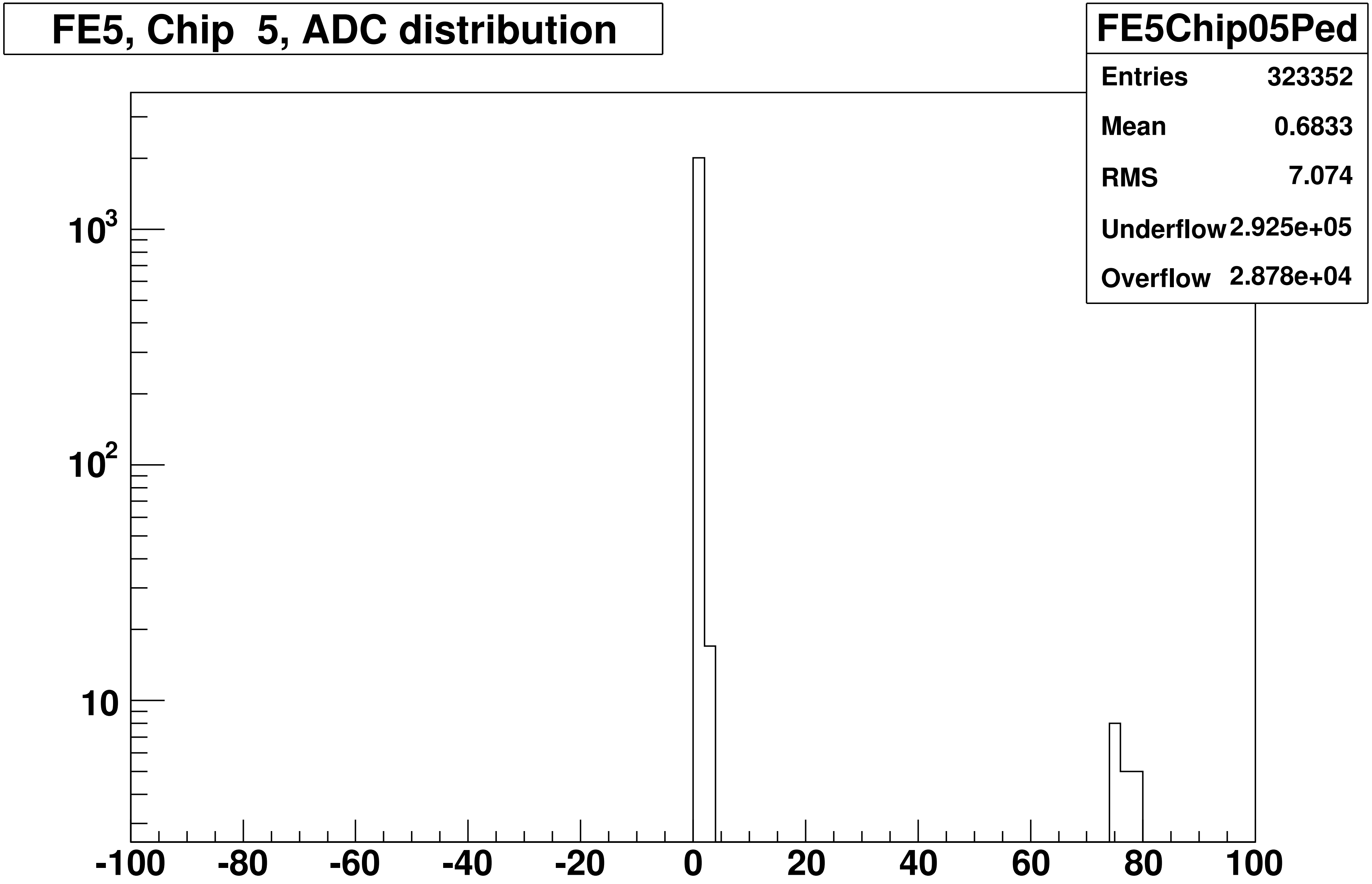

FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 323352
Mean 1612
RMS 5217
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



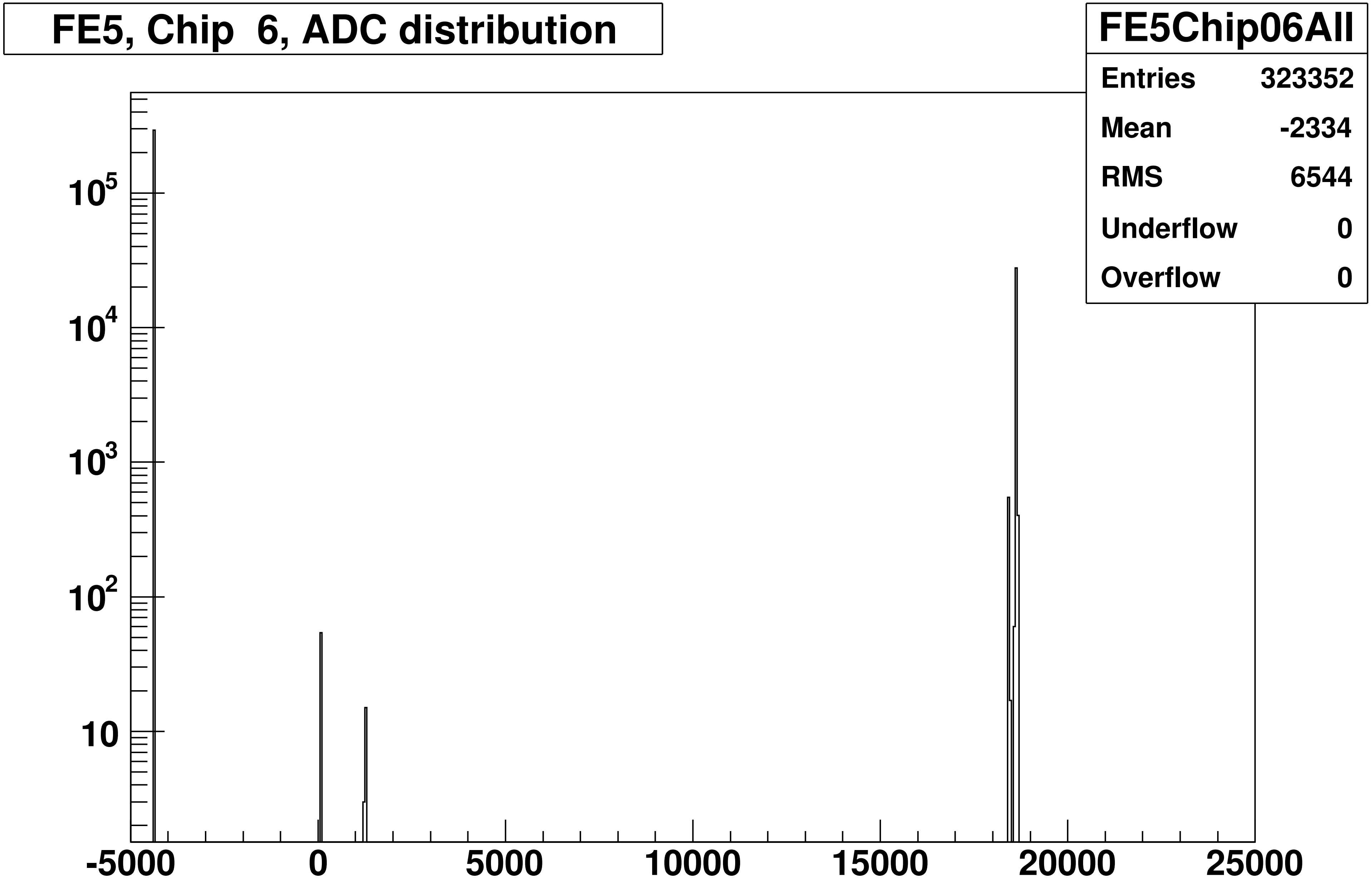
FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 323352
Mean 7.778
RMS 1.505
Underflow 0
Overflow 2.855e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

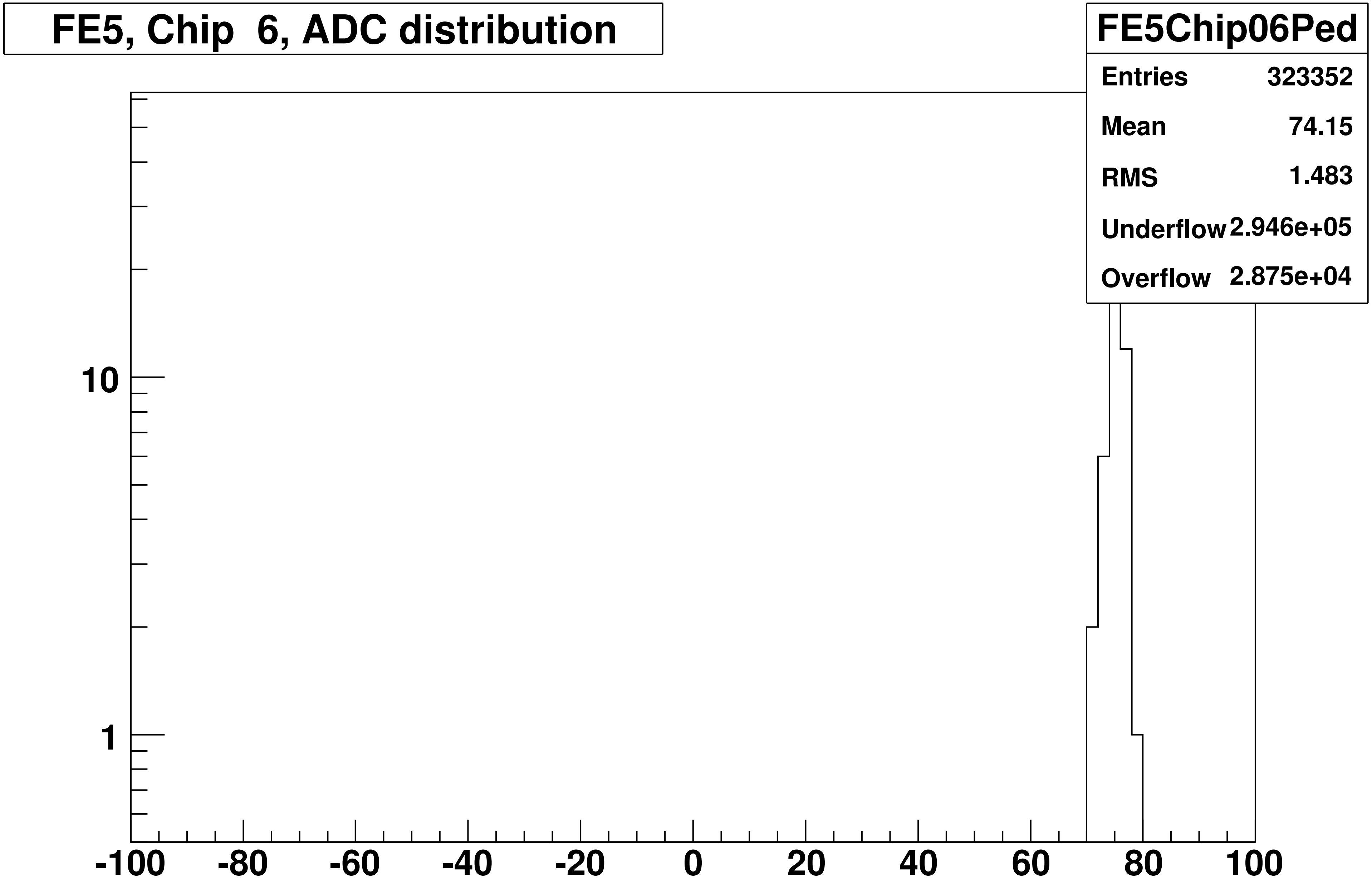


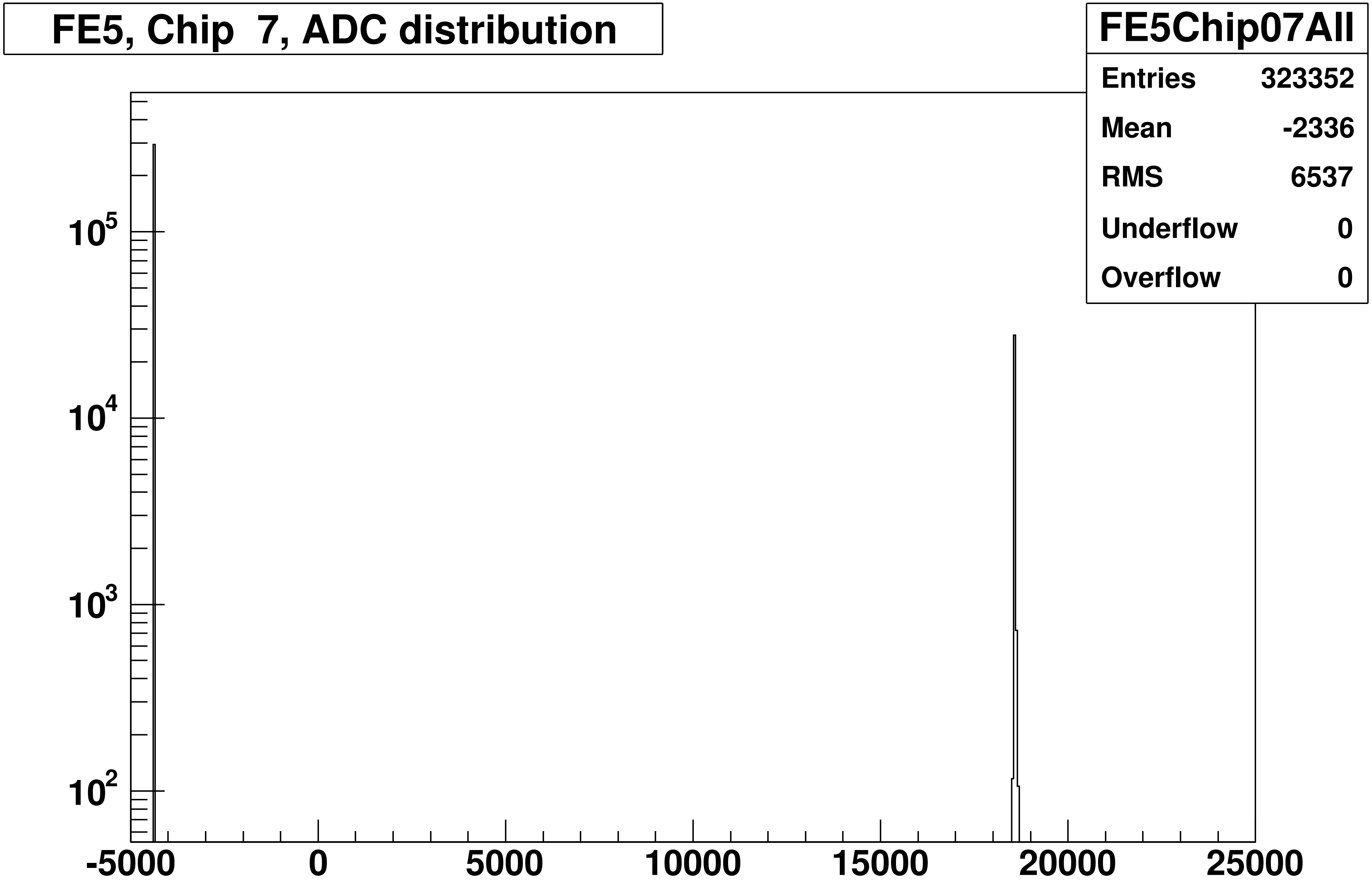
FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 323352
Mean -2347
RMS 6434
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



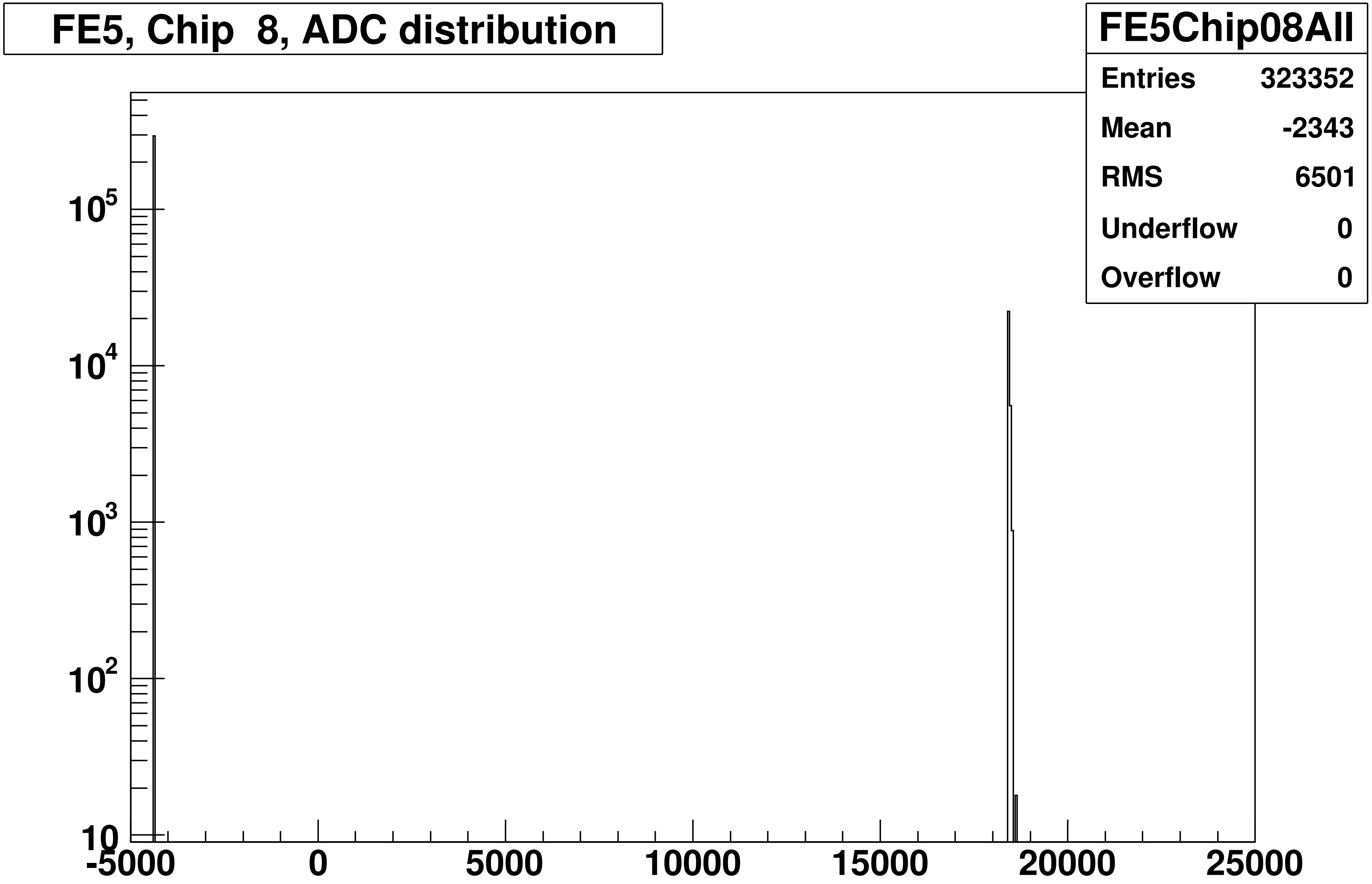
FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 323352
Mean 0.6833
RMS 7.074
Underflow 2.925e+05
Overflow 2.878e+04
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

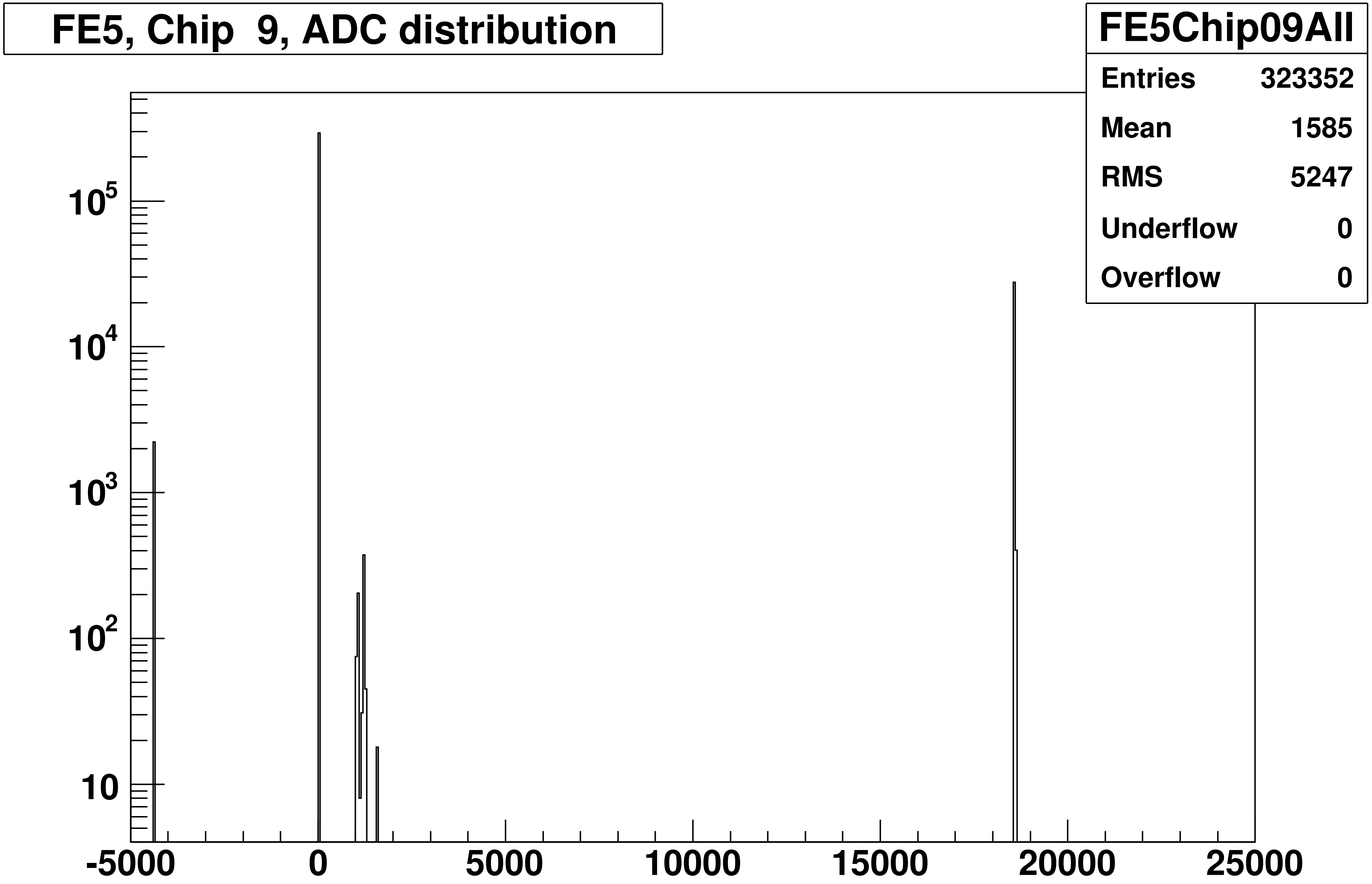

FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 323352
Mean -2334
RMS 6544
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 323352
Mean 74.15
RMS 1.483
Underflow 2.946e+05
Overflow 2.875e+04
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

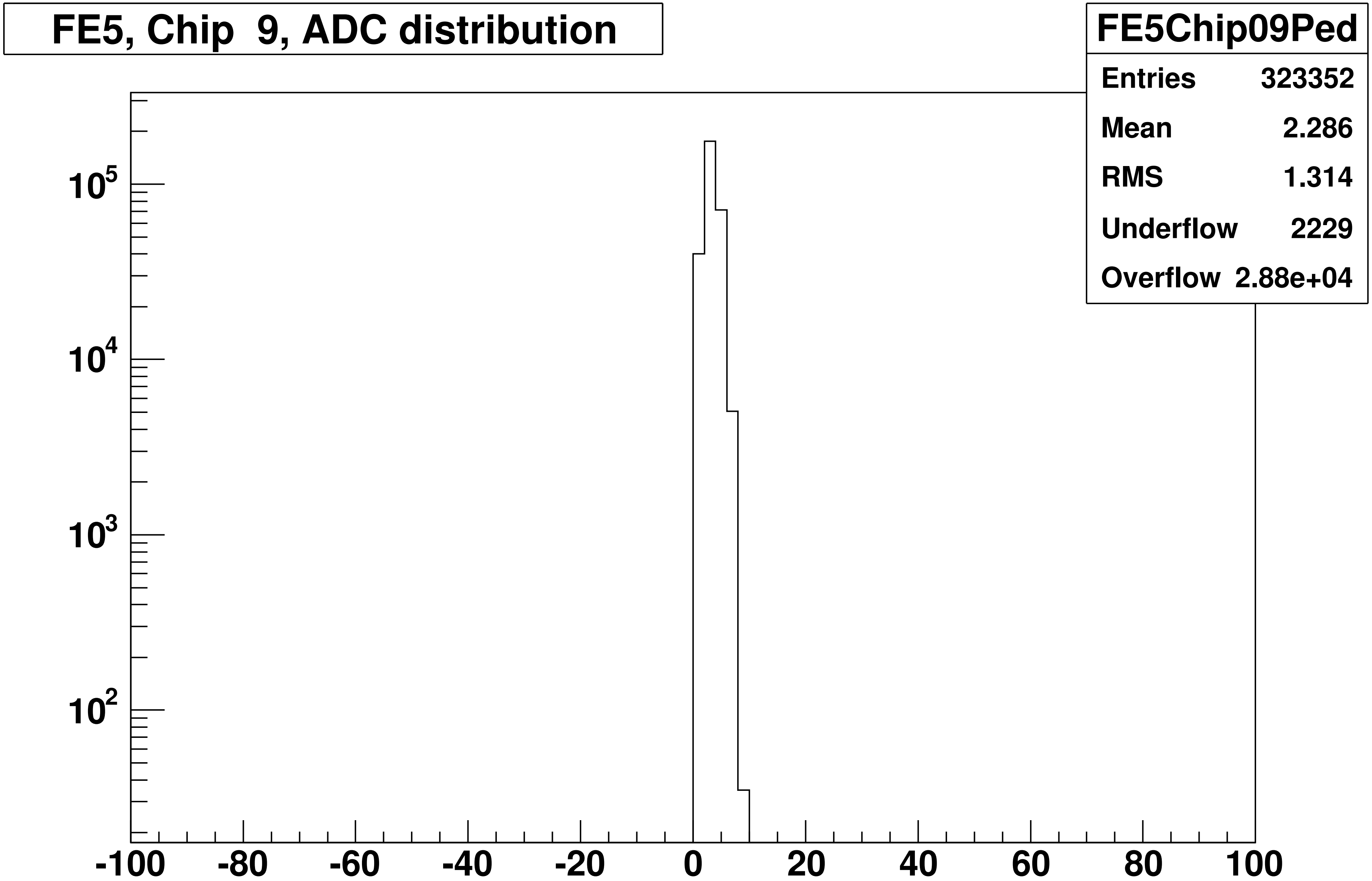


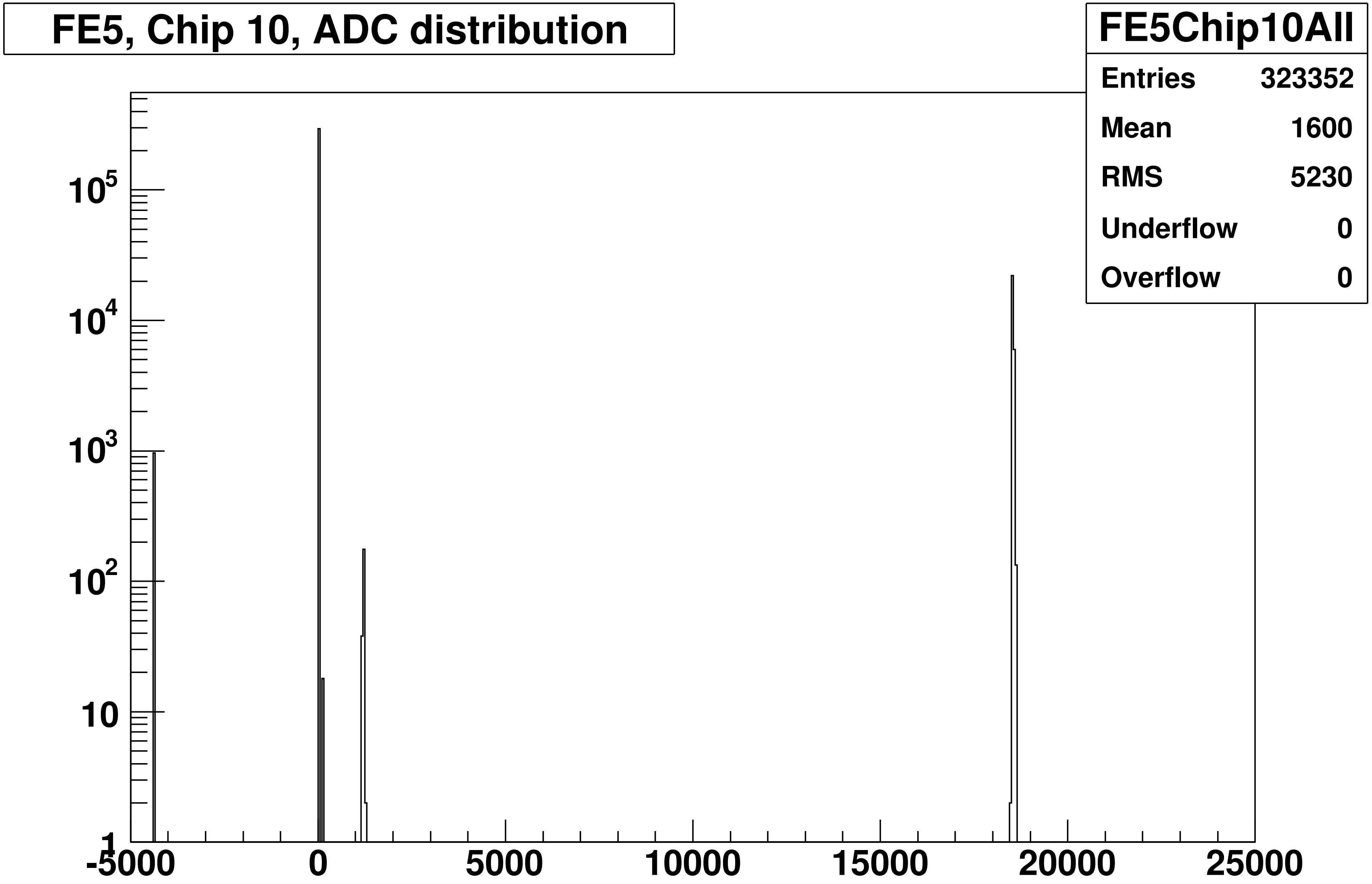
FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 323352
Mean -2336
RMS 6537
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 323352
Mean -2343
RMS 6501
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

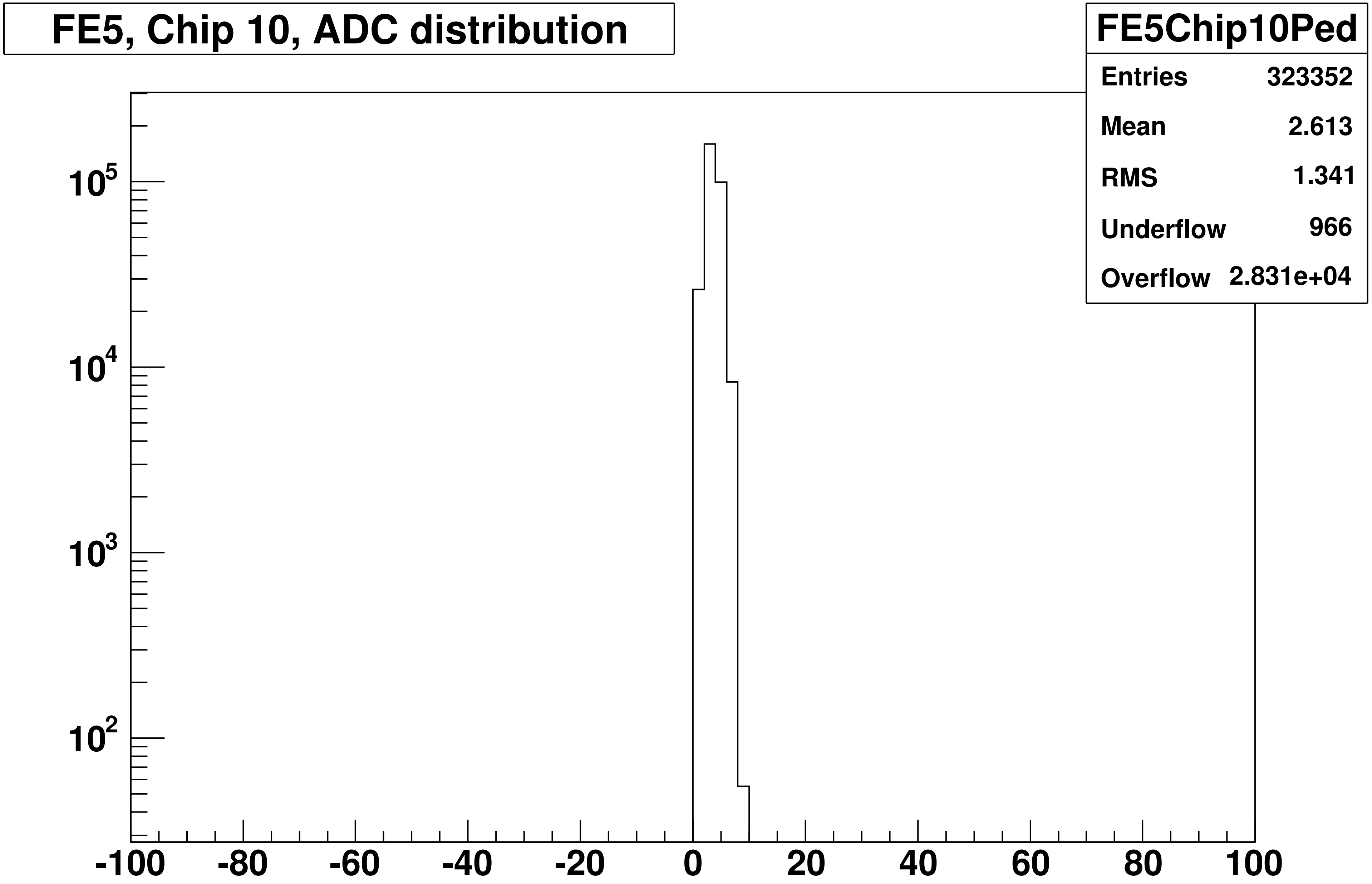


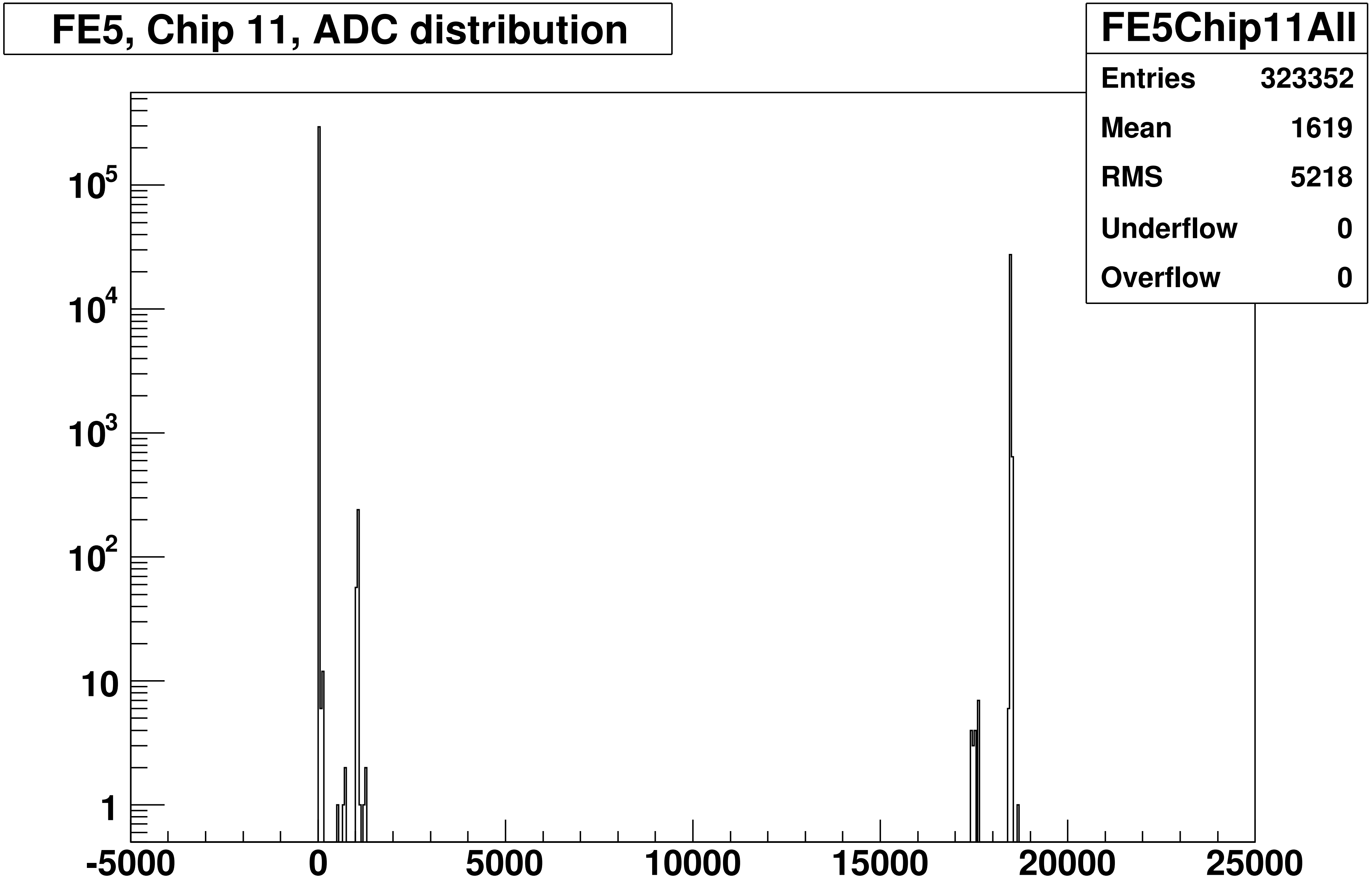
FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 323352
Mean 1585
RMS 5247
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

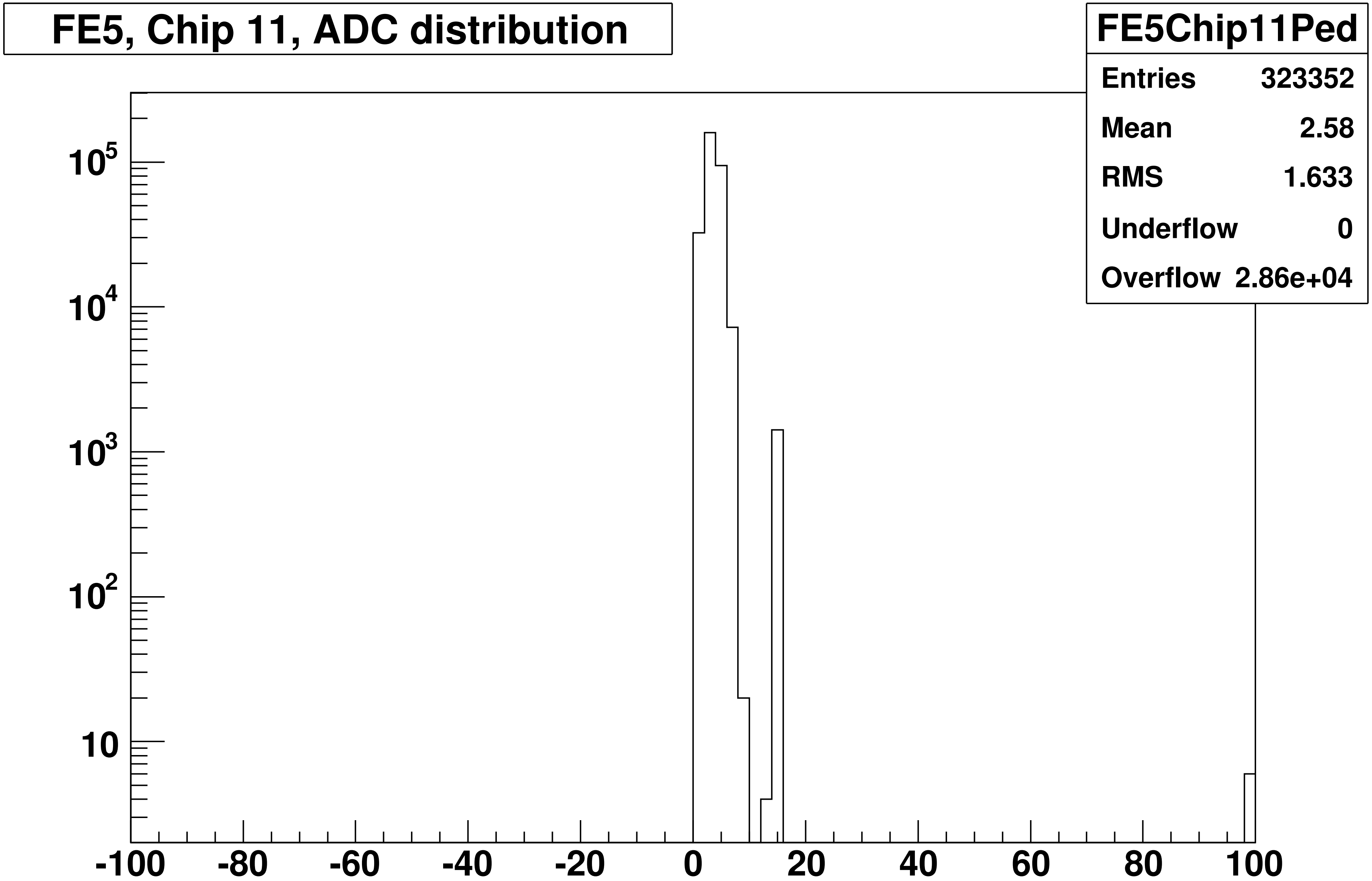

FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 323352
Mean 2.286
RMS 1.314
Underflow 2229
Overflow 2.88e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

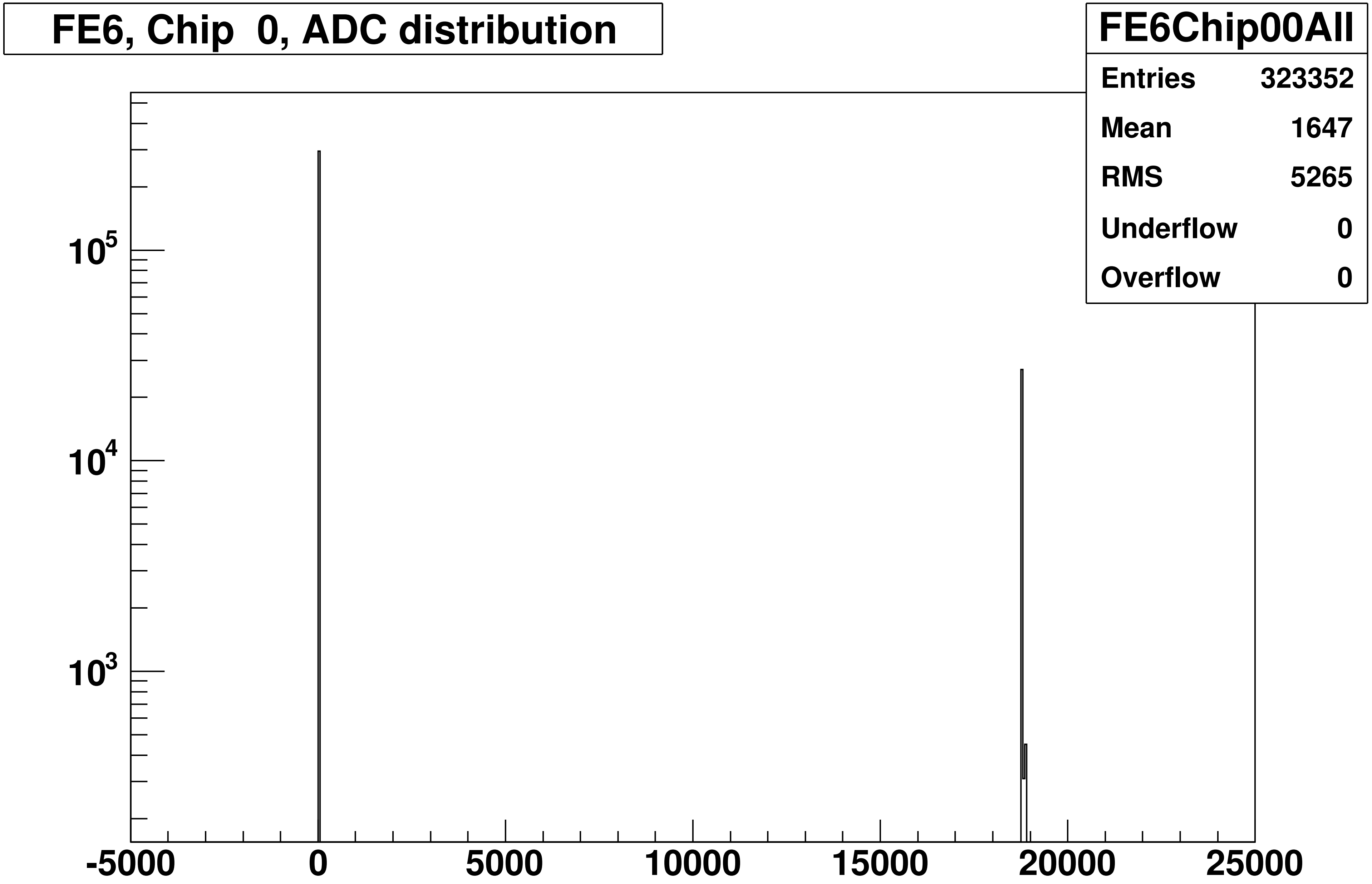


FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 323352
Mean 1600
RMS 5230
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

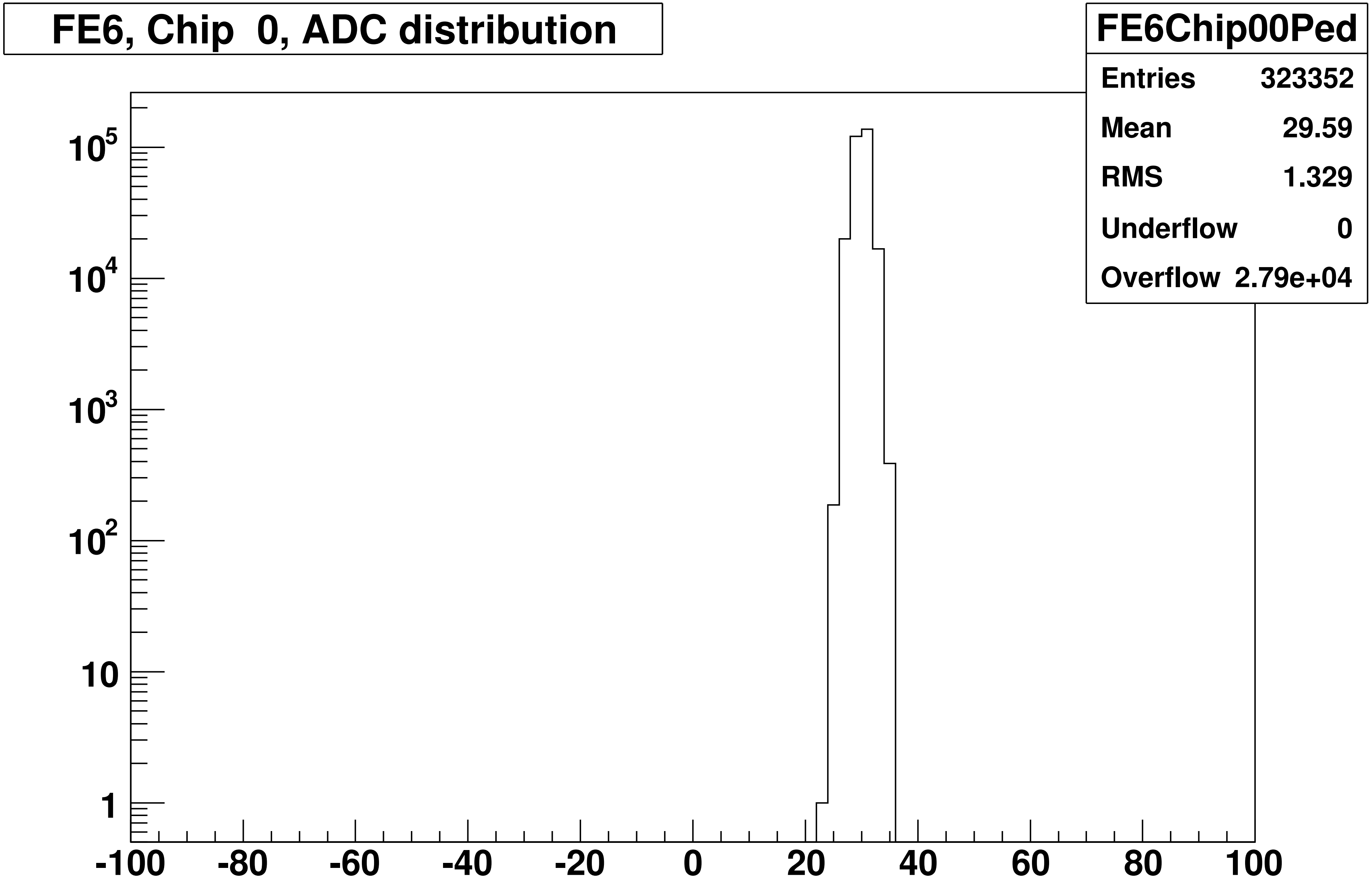

FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 323352
Mean 2.613
RMS 1.341
Underflow 966
Overflow 2.831e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

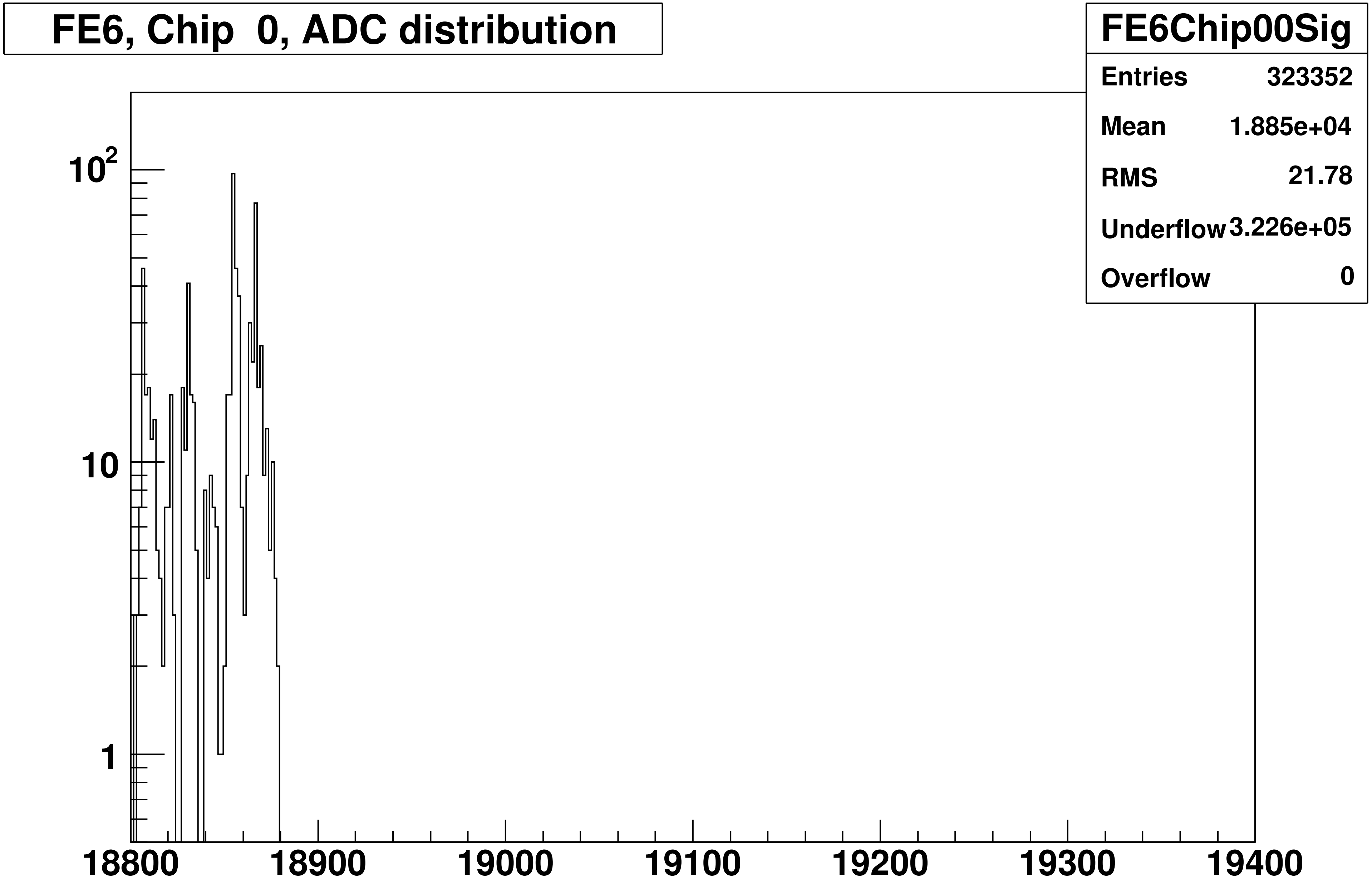

FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 323352
Mean 1619
RMS 5218
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 323352
Mean 2.58
RMS 1.633
Underflow 0
Overflow 2.86e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

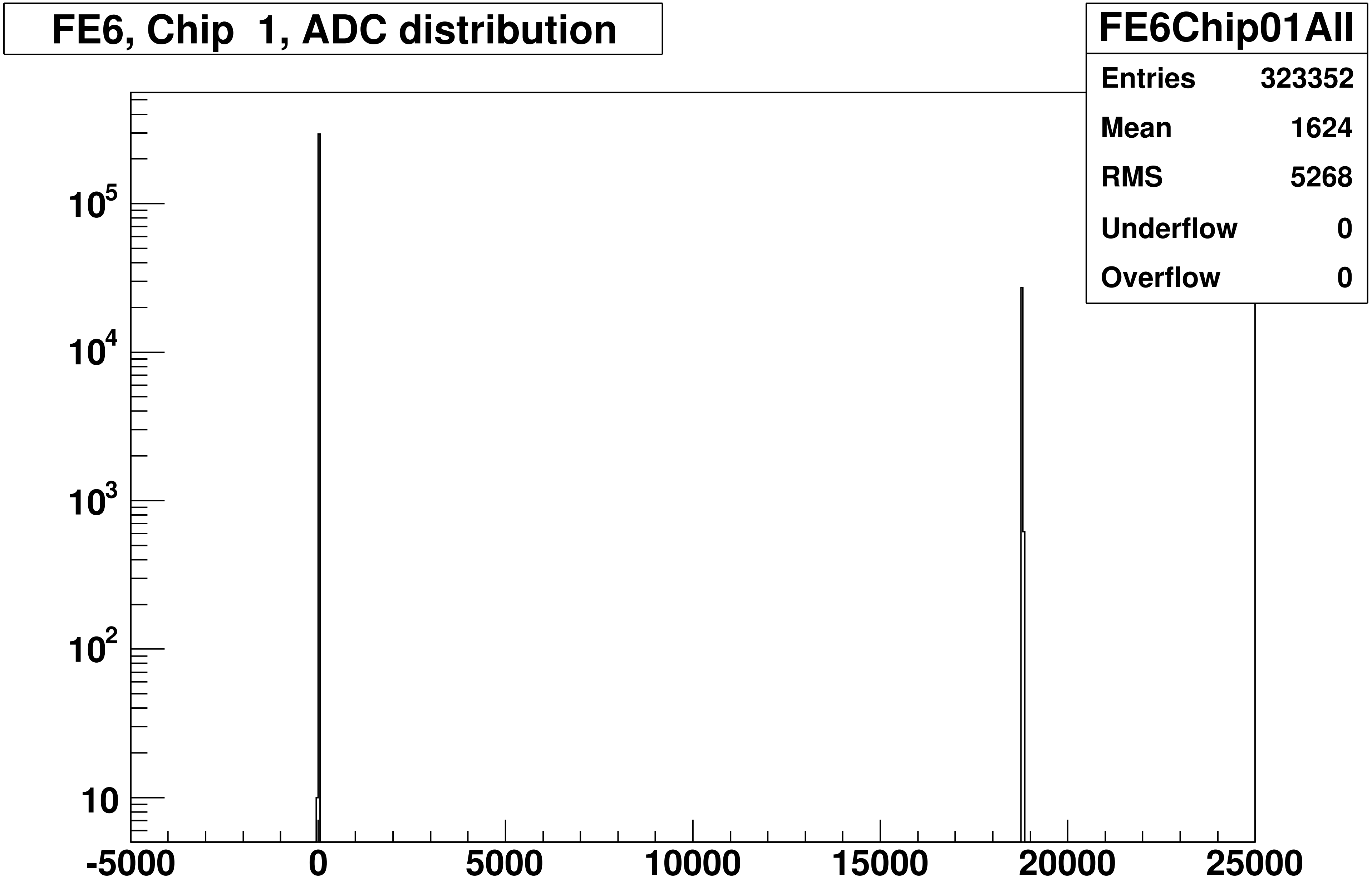


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 323352
Mean 1647
RMS 5265
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

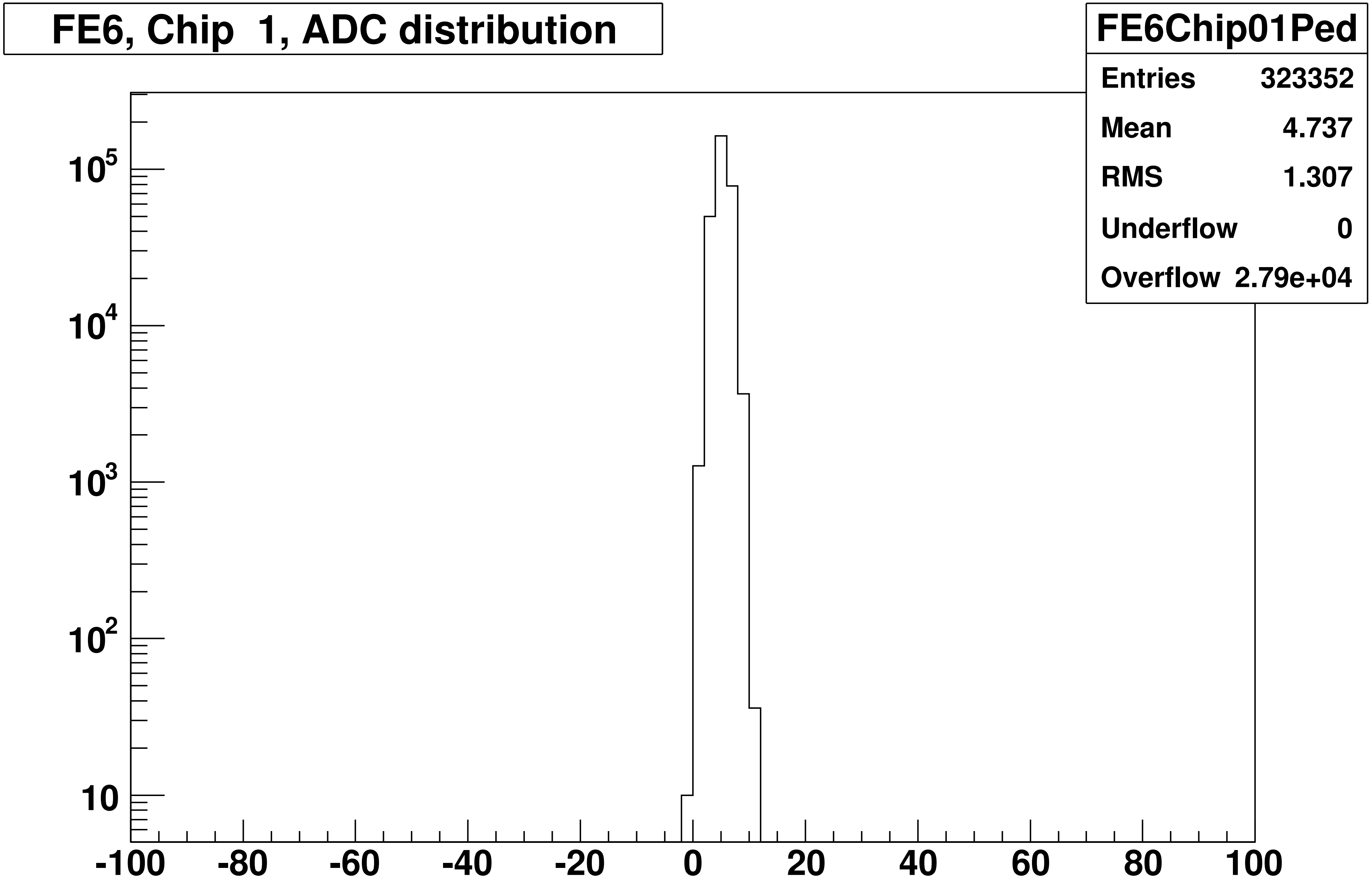

FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 323352
Mean 29.59
RMS 1.329
Underflow 0
Overflow 2.79e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

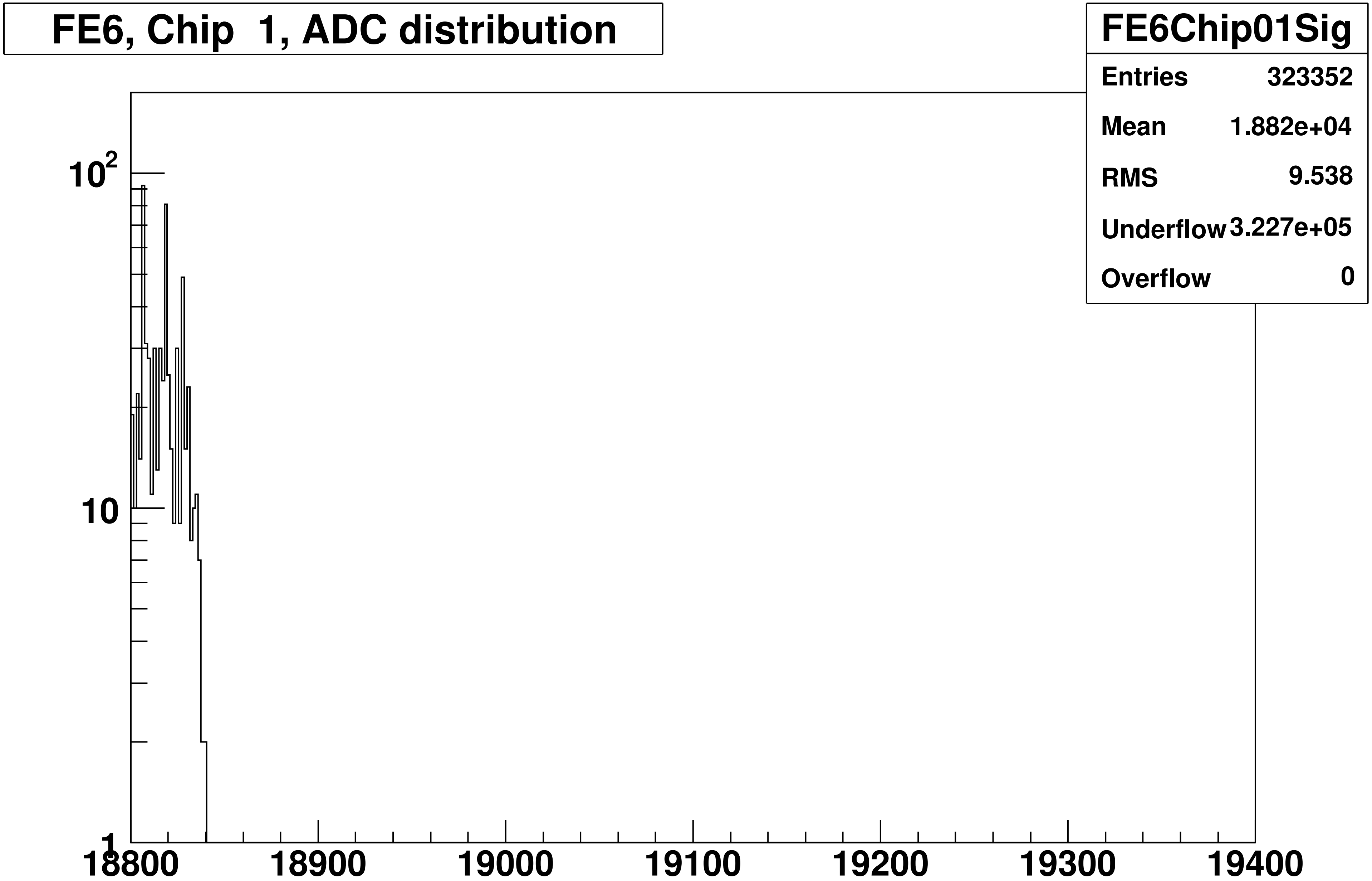


FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 323352
Mean 1.885e+04
RMS 21.78
Underflow 3.226e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

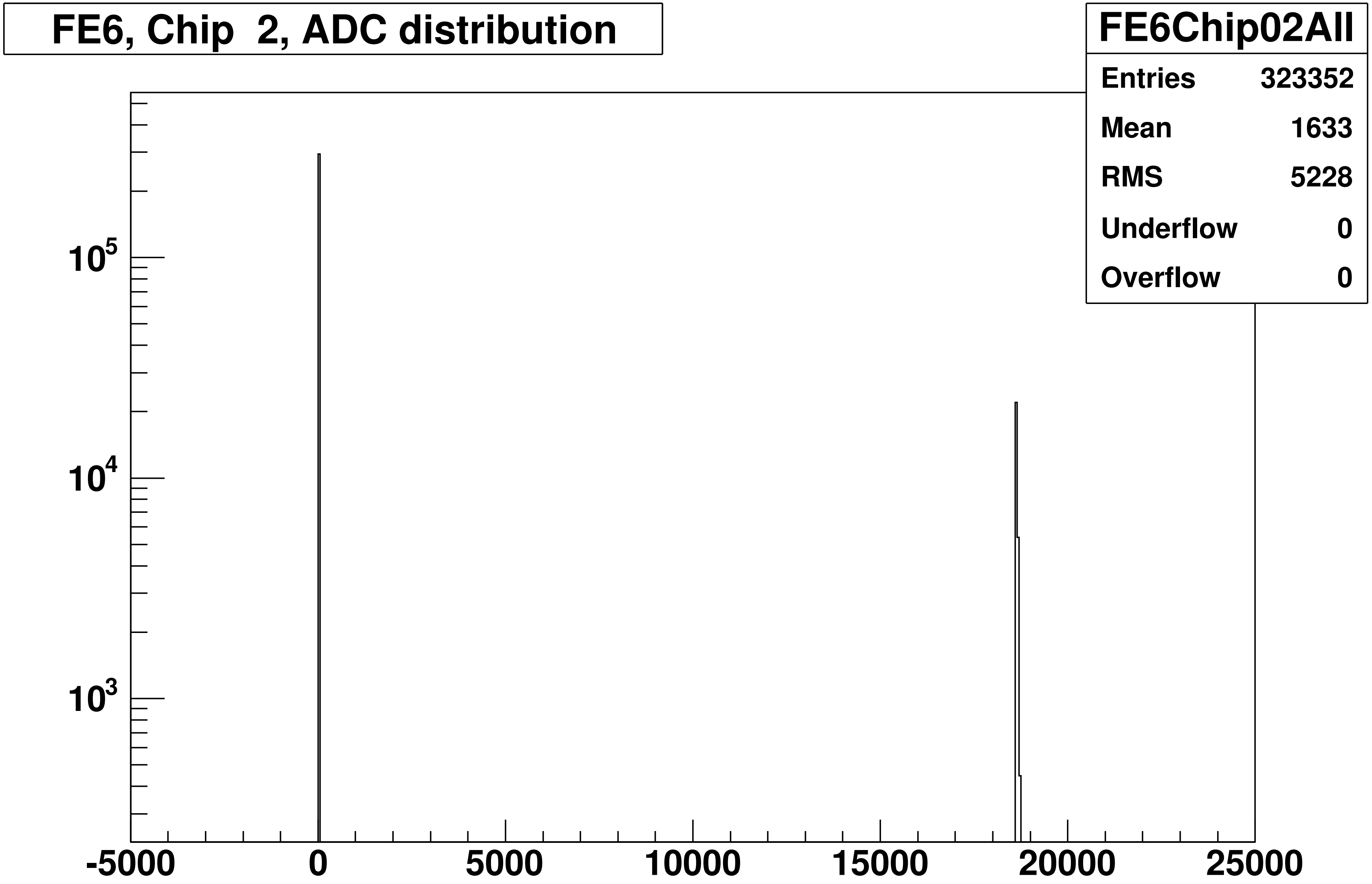


FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 323352
Mean 1624
RMS 5268
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

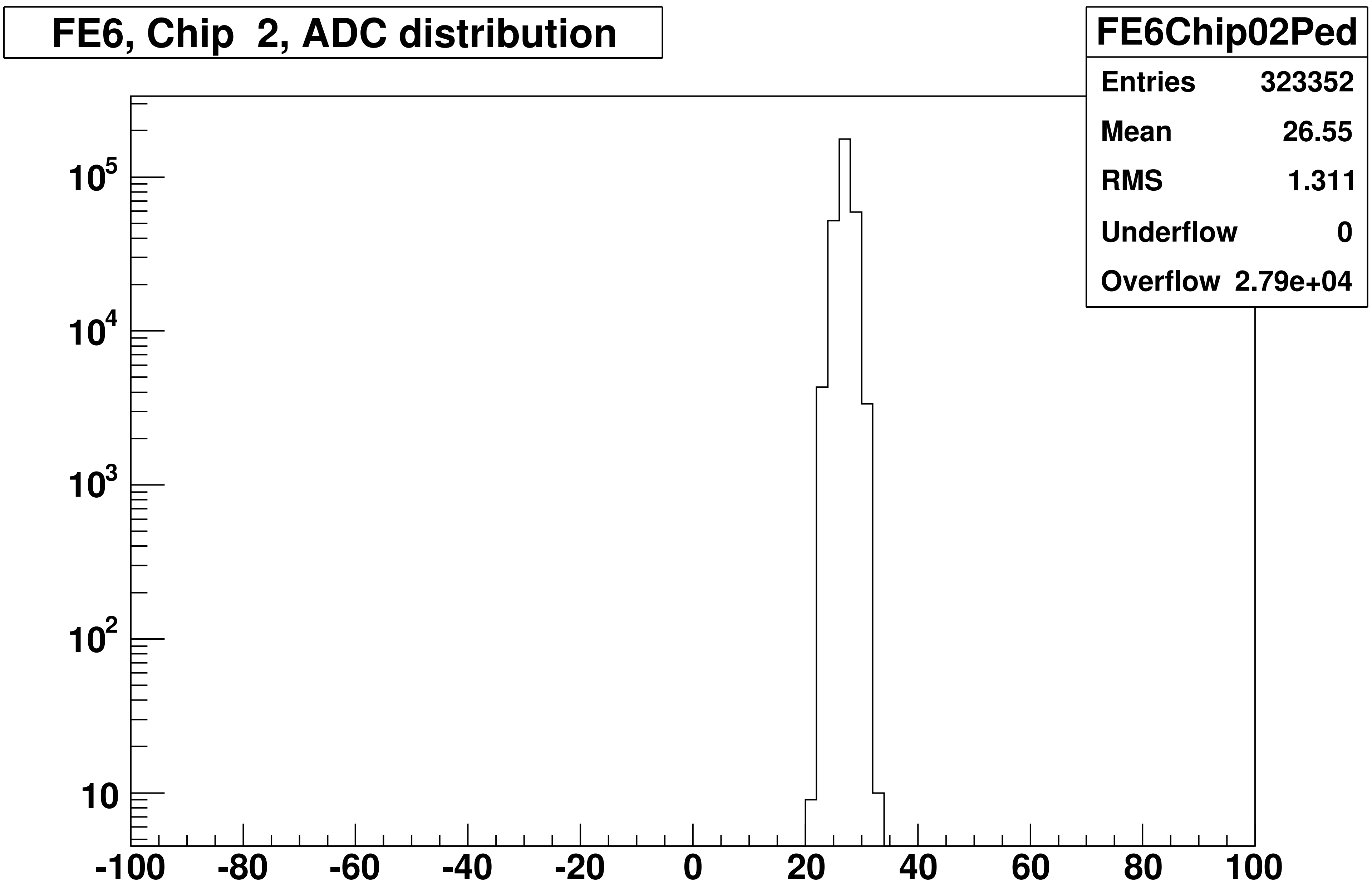

FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 323352
Mean 4.737
RMS 1.307
Underflow 0
Overflow 2.79e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

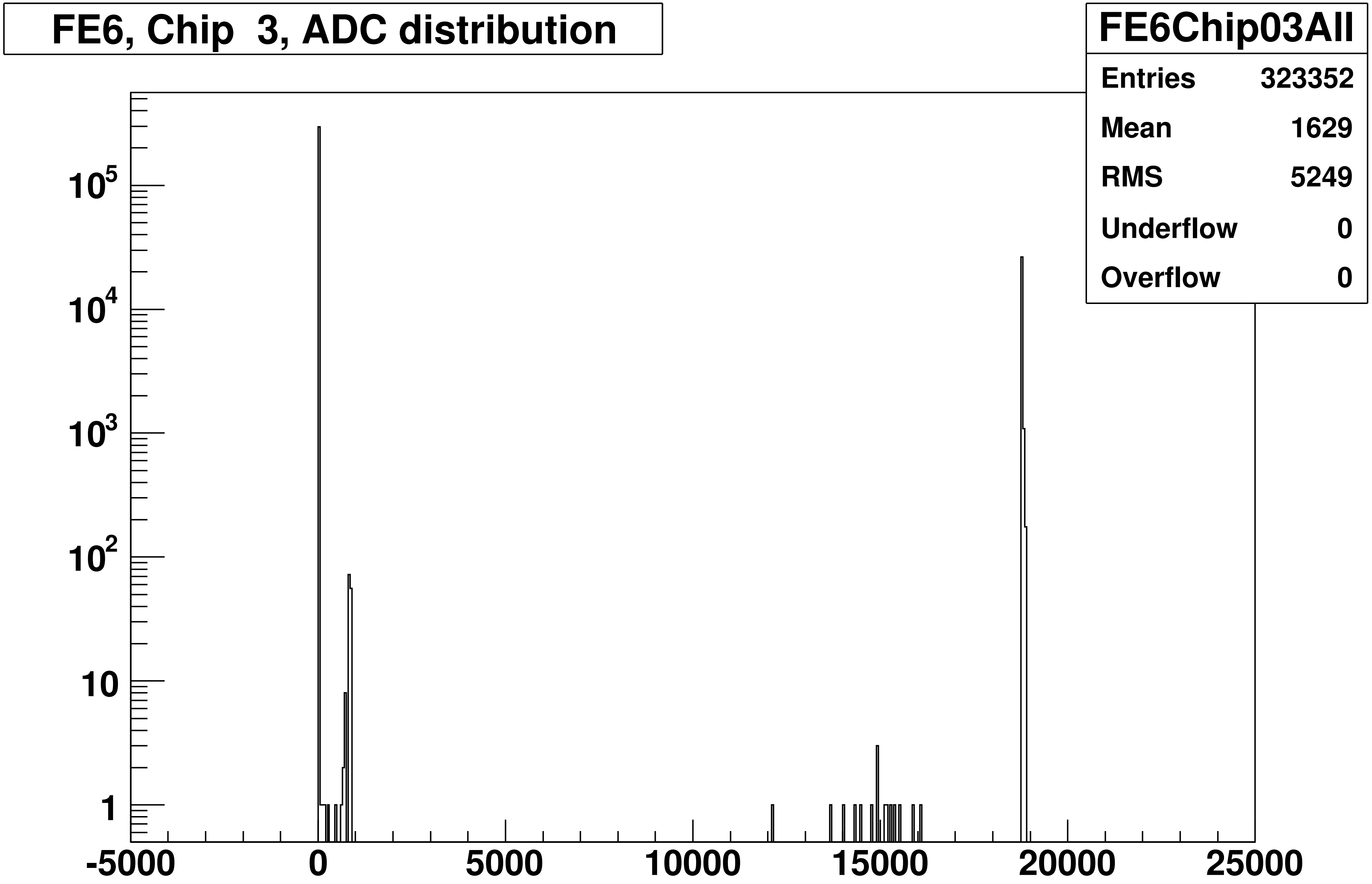


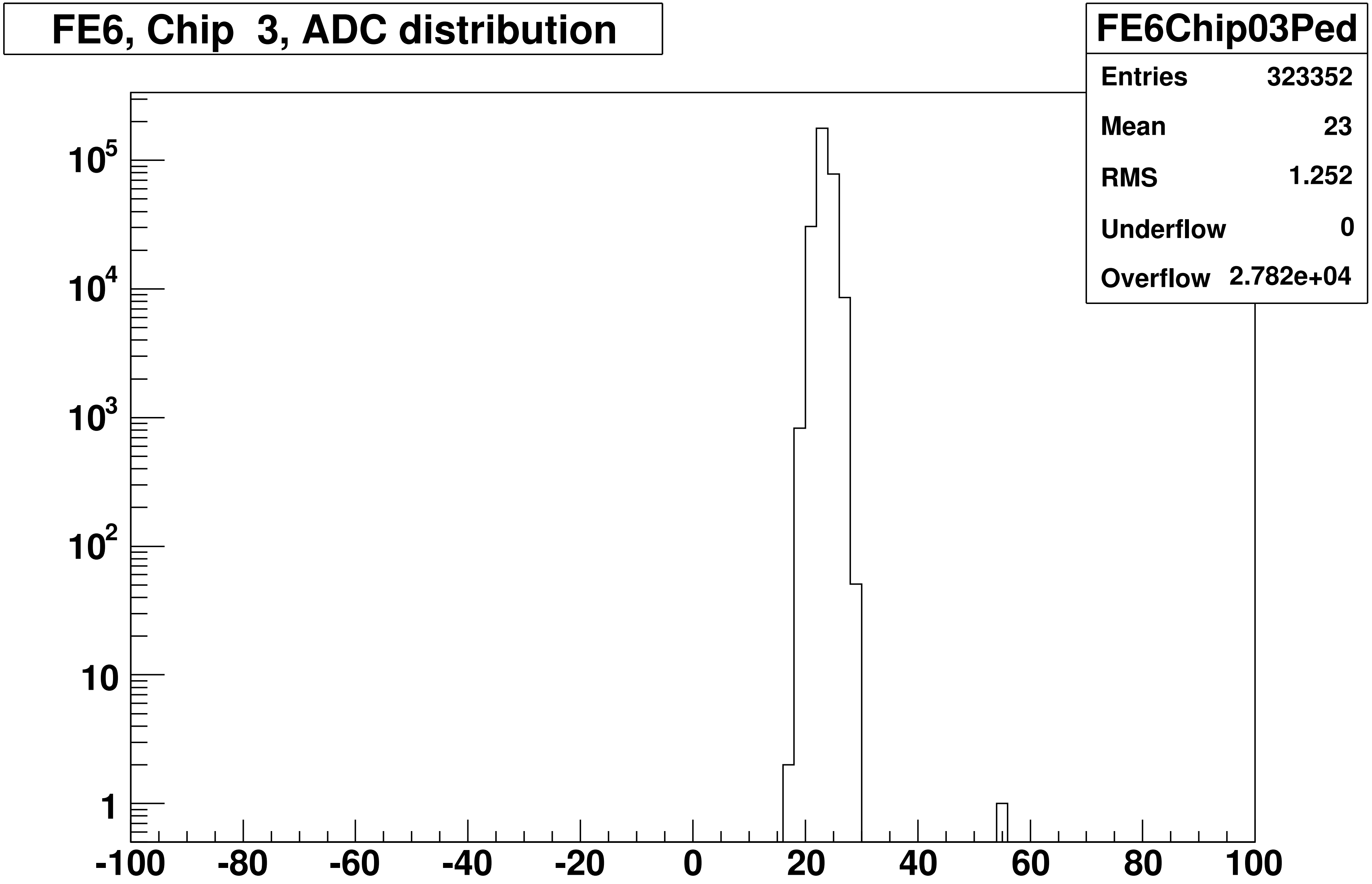
FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 323352
Mean 1.882e+04
RMS 9.538
Underflow 3.227e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



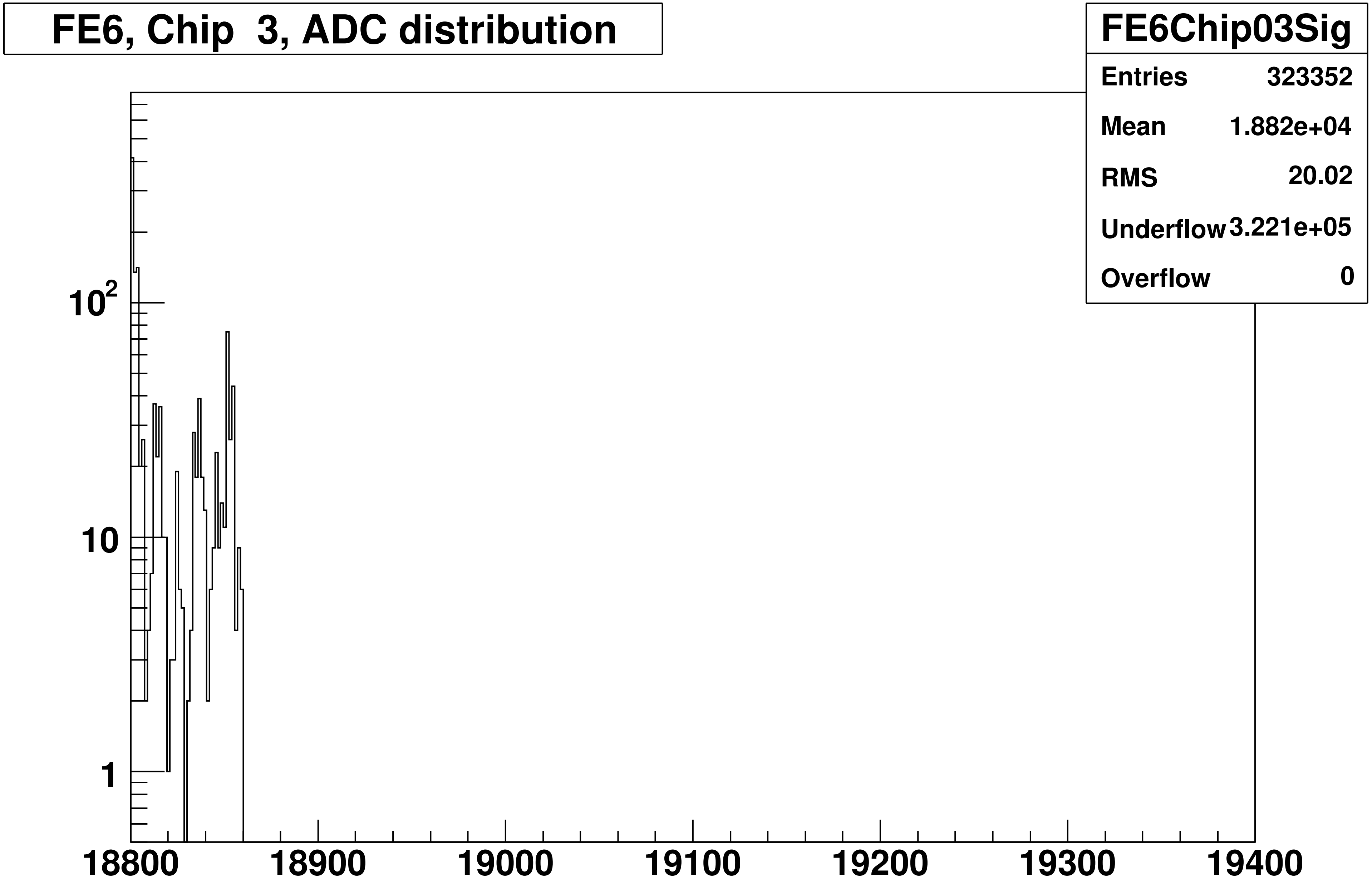
FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 323352
Mean 1633
RMS 5228
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

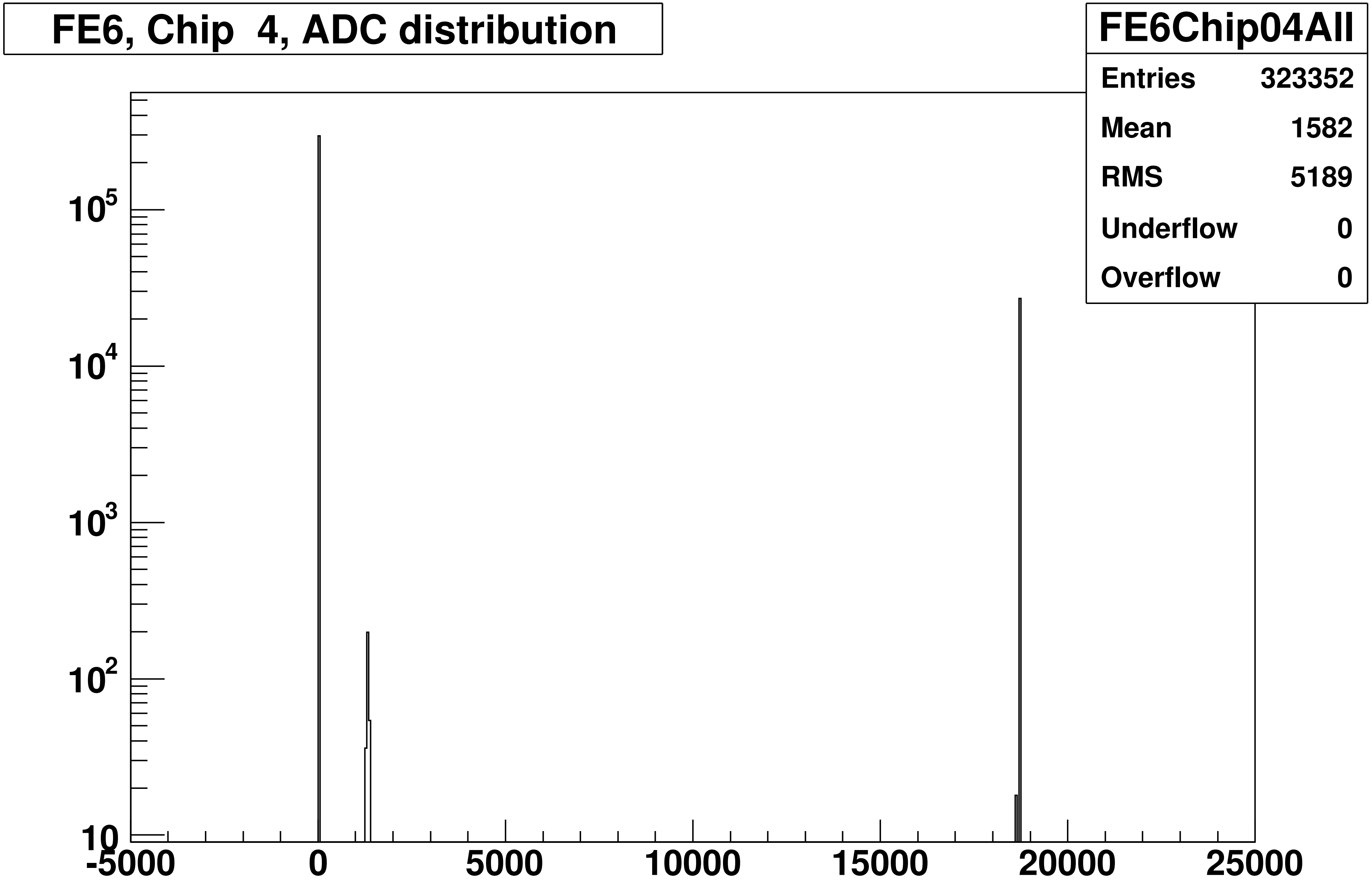

FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 323352
Mean 26.55
RMS 1.311
Underflow 0
Overflow 2.79e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

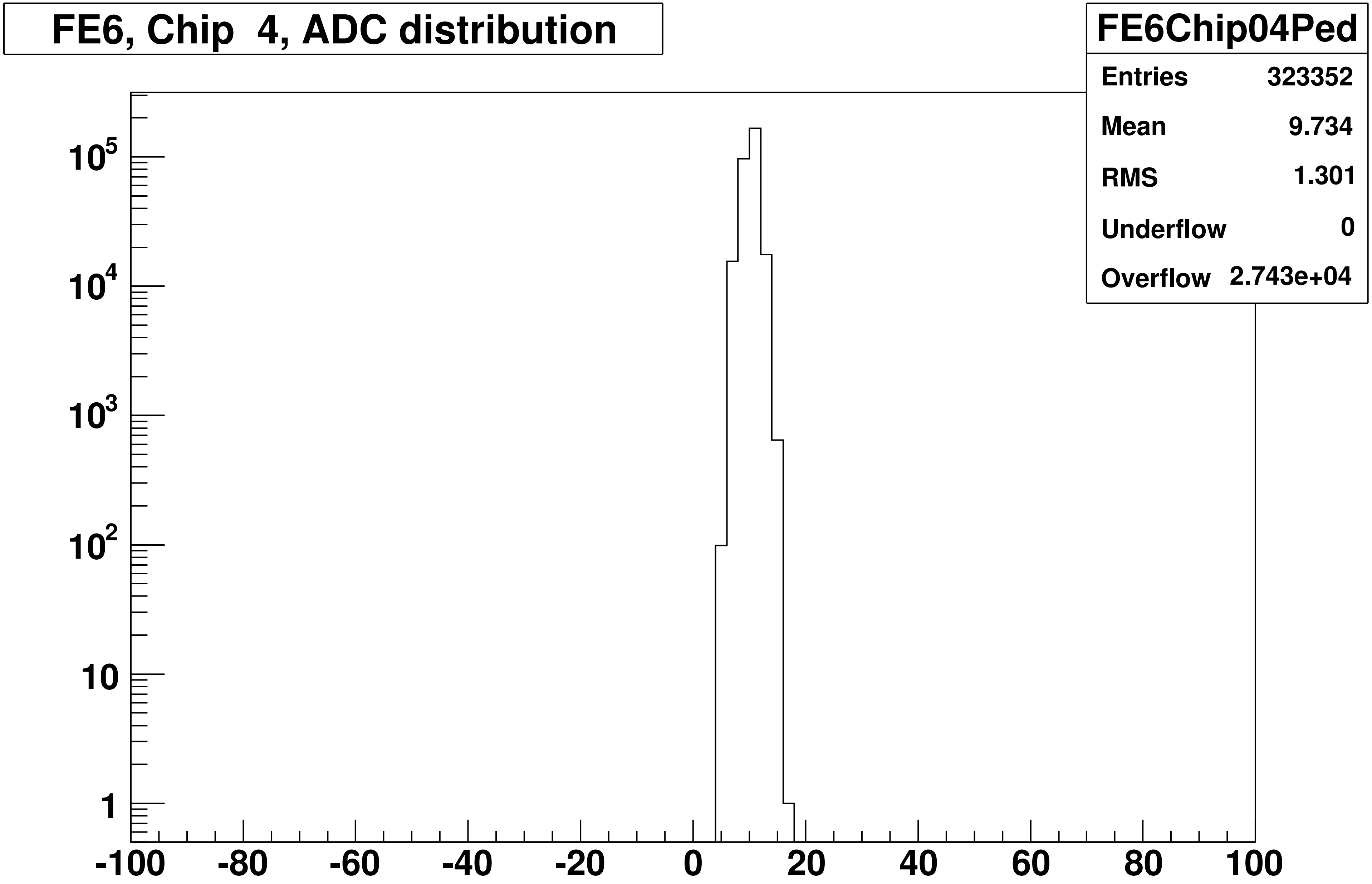

FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 323352
Mean 1629
RMS 5249
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



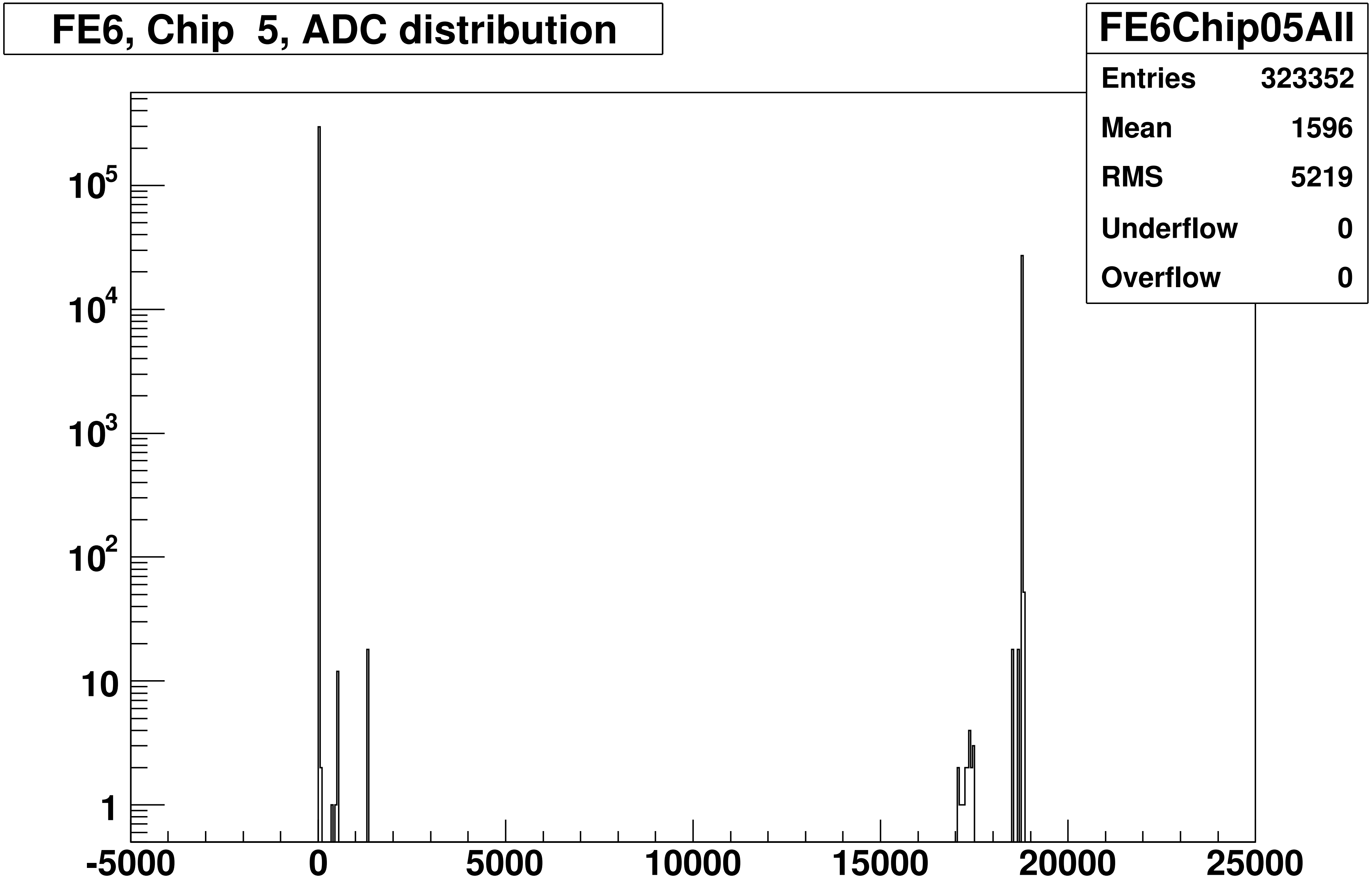
FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 323352
Mean 23
RMS 1.252
Underflow 0
Overflow 2.782e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 323352
Mean 1.882e+04
RMS 20.02
Underflow 3.221e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

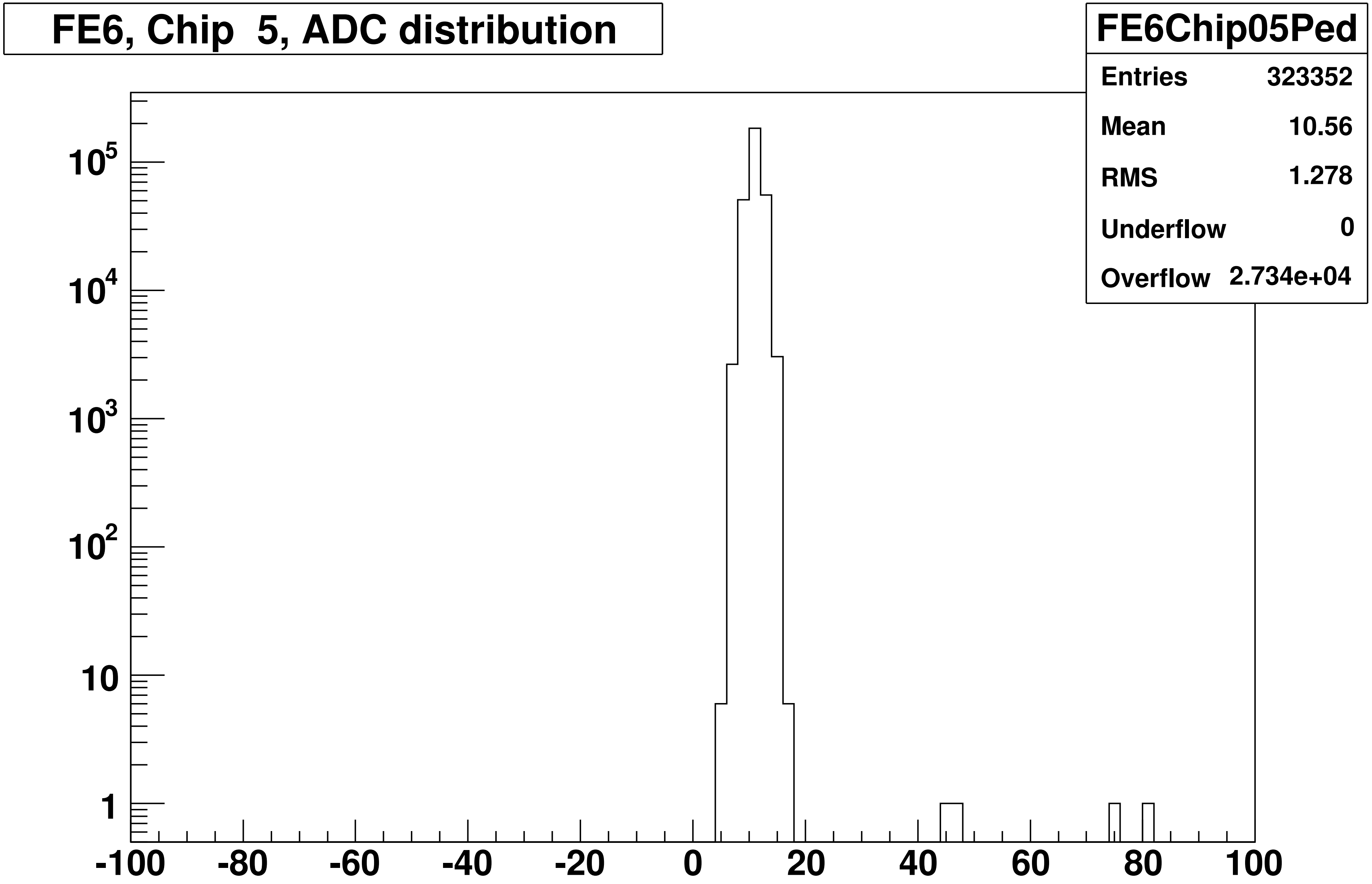

FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 323352
Mean 1582
RMS 5189
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



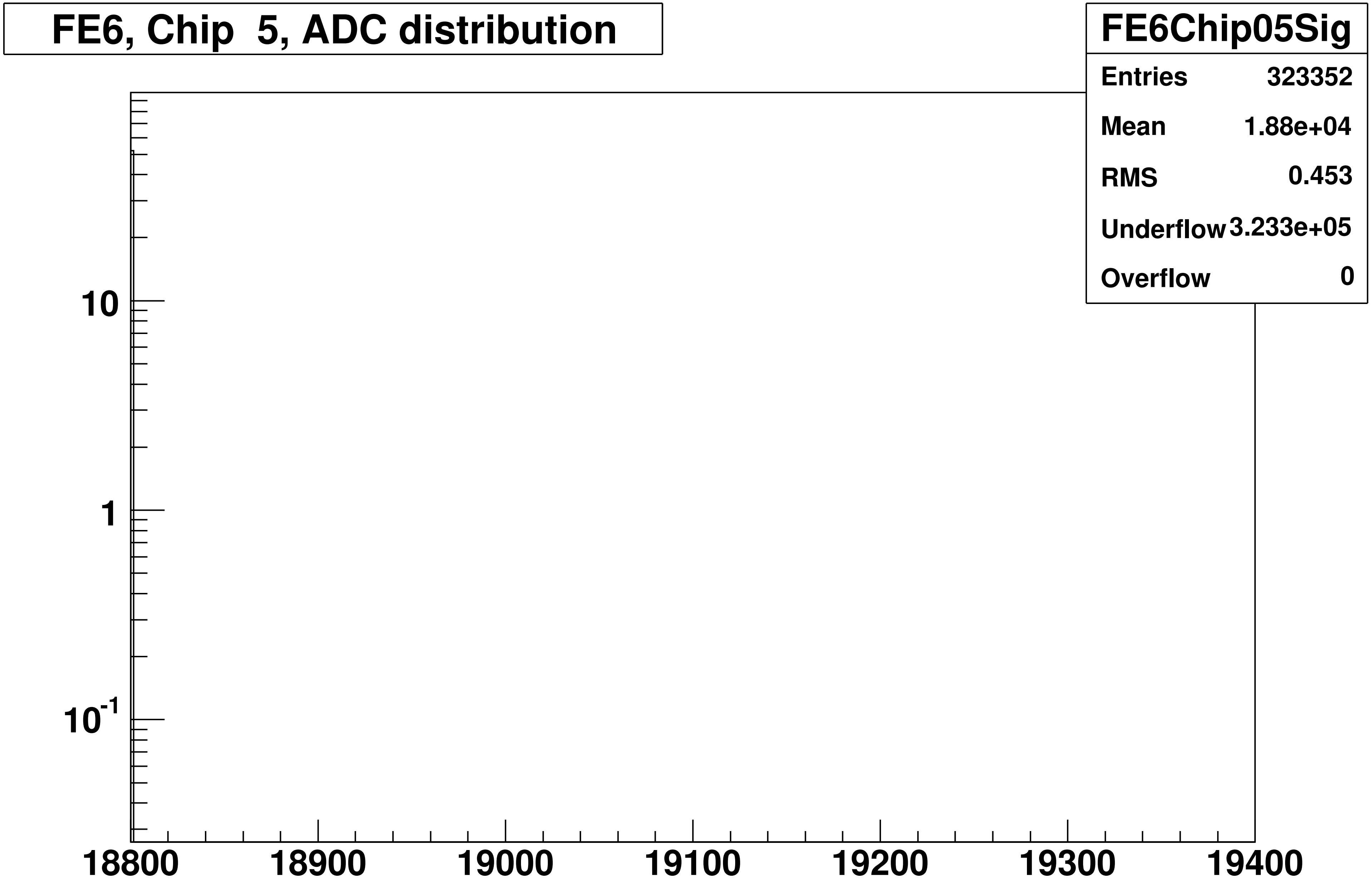
FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 323352
Mean 9.734
RMS 1.301
Underflow 0
Overflow 2.743e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

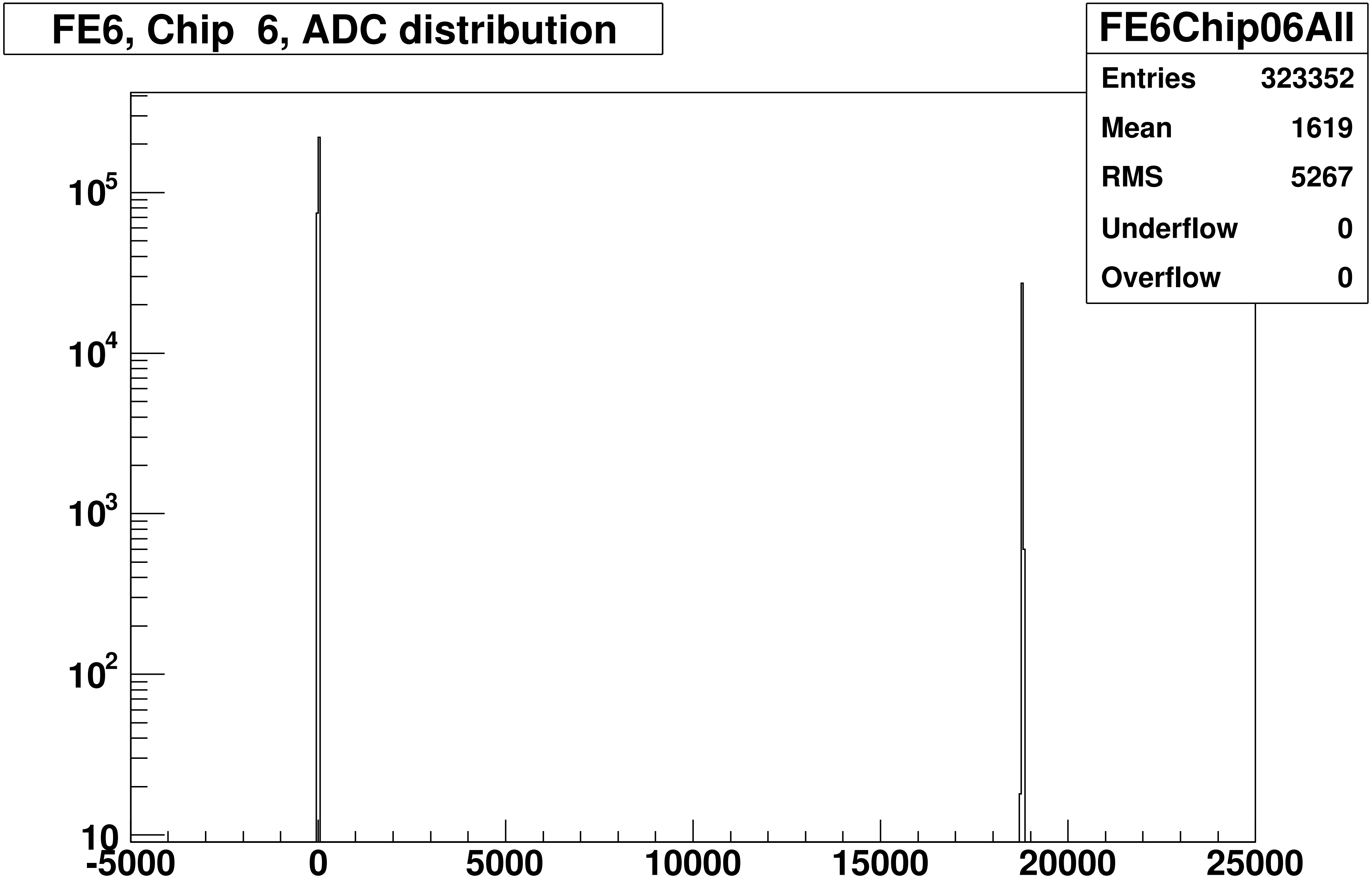


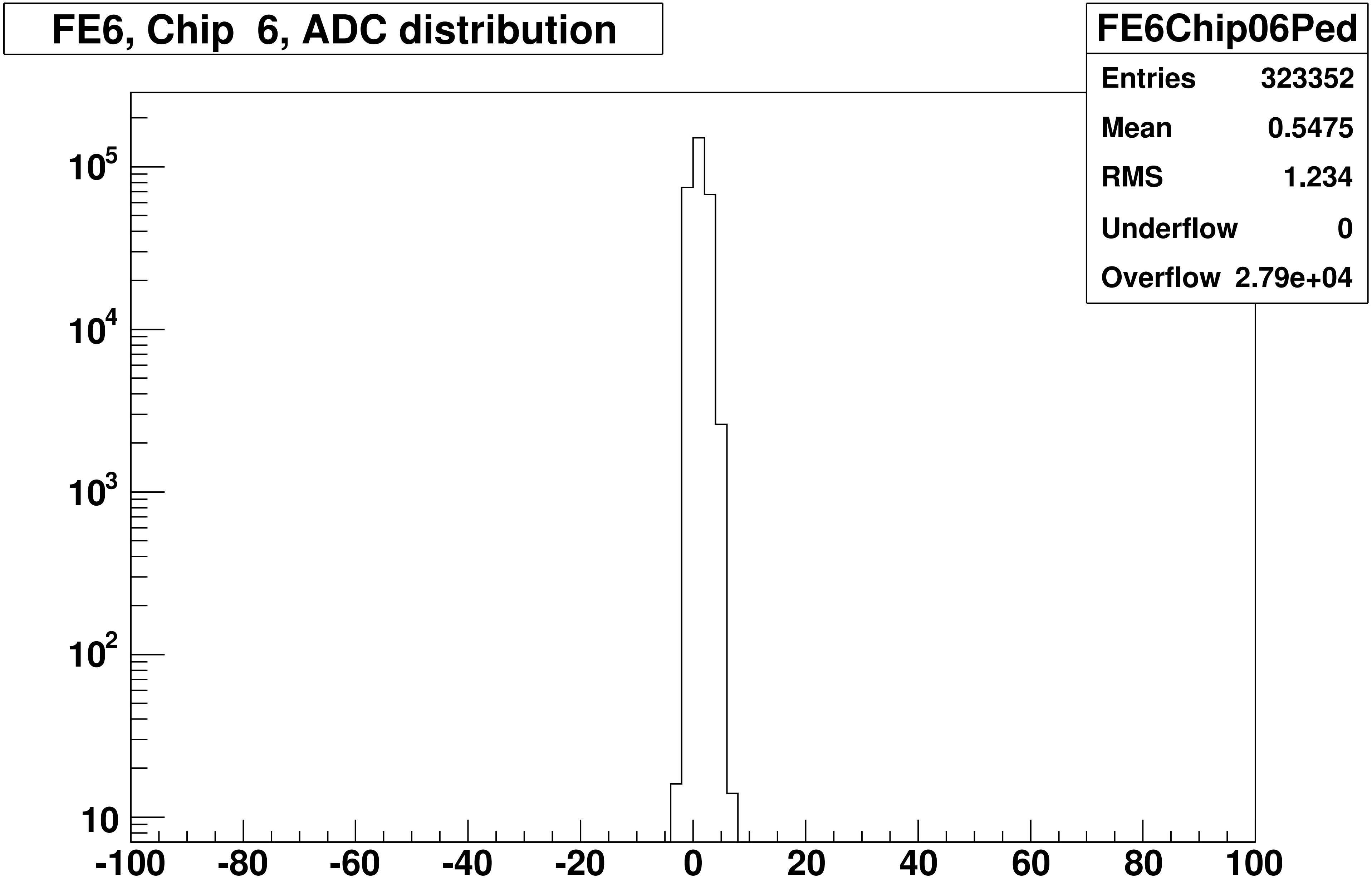
FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 323352
Mean 1596
RMS 5219
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

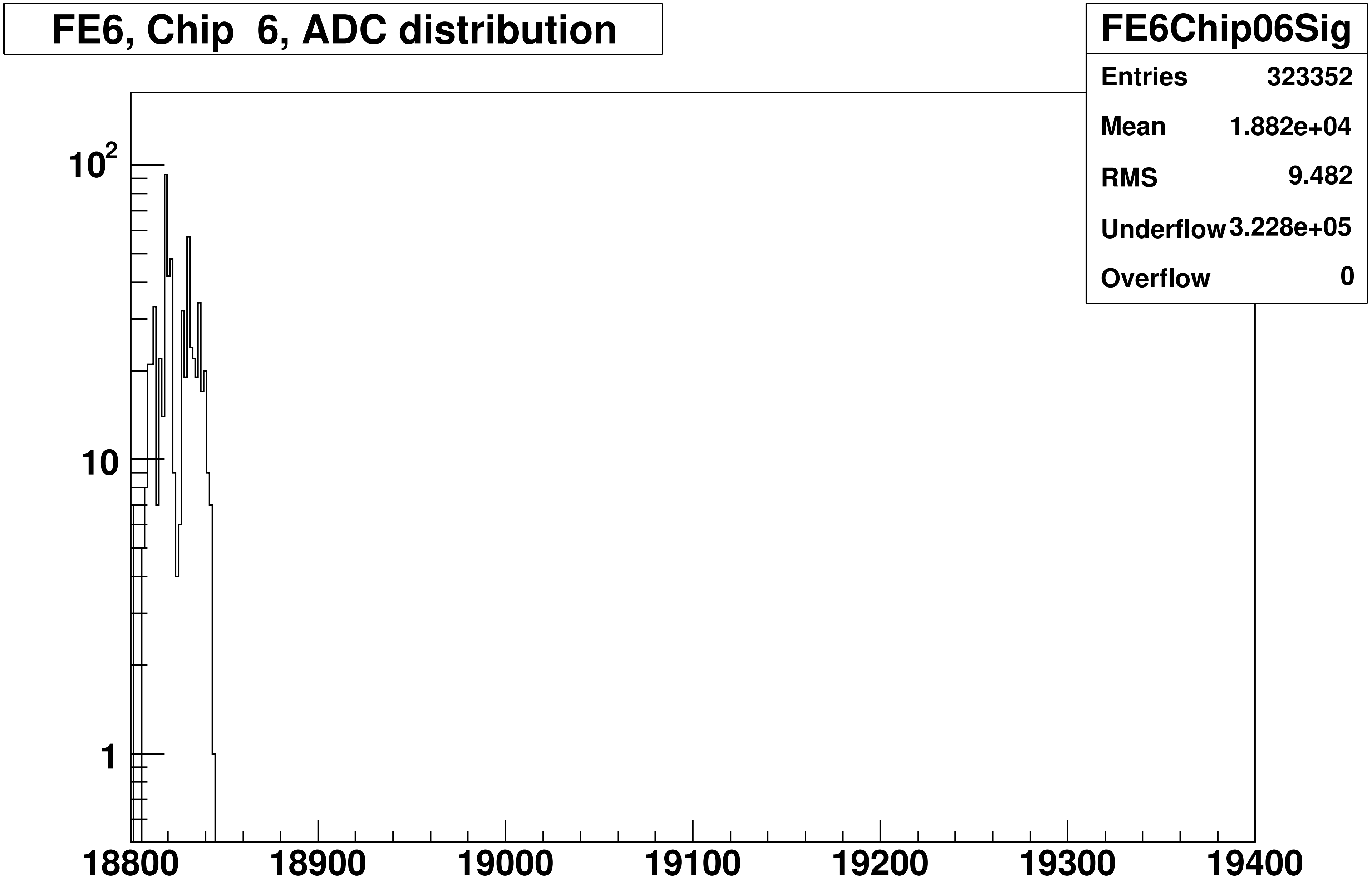


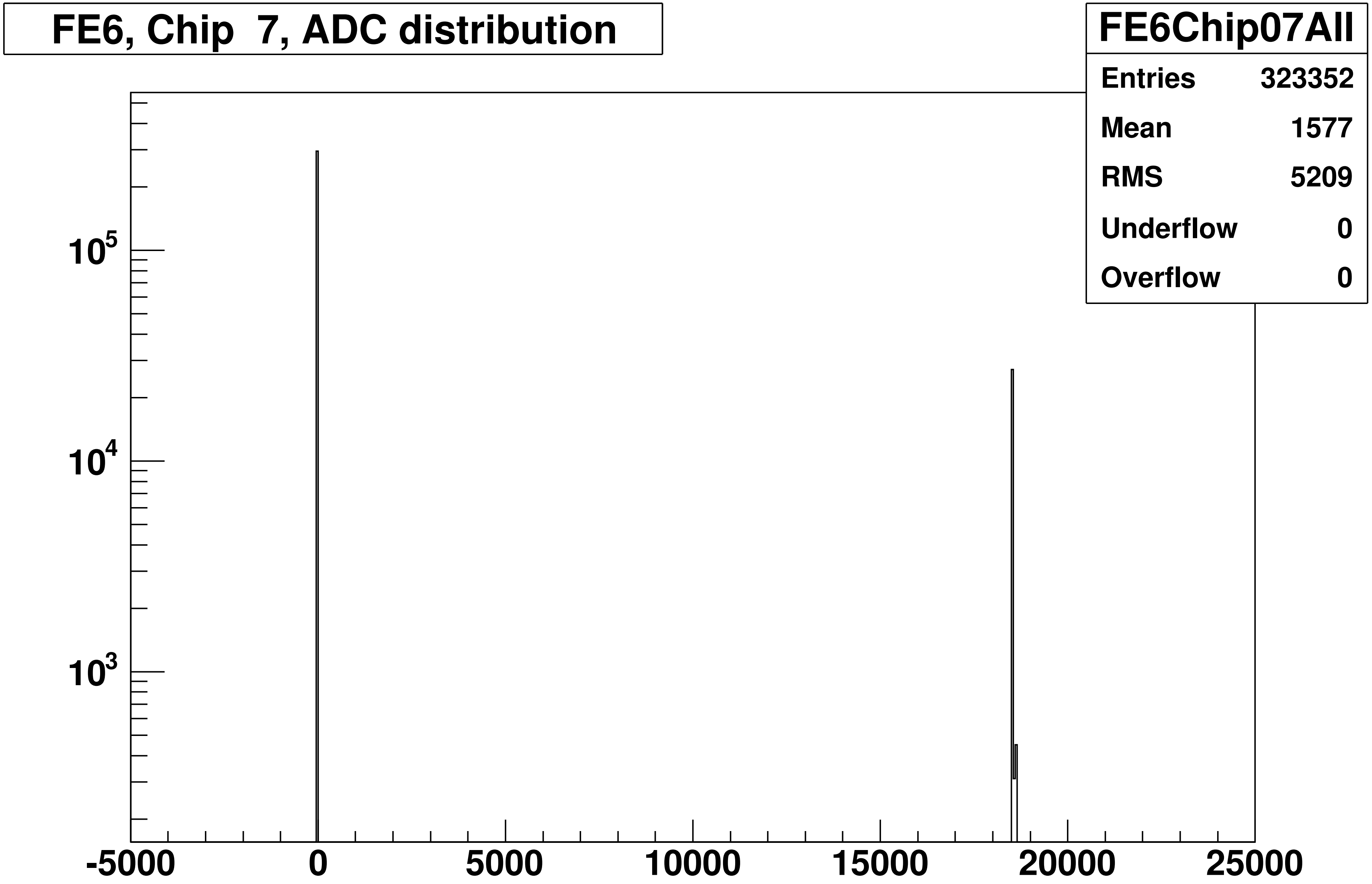
FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 323352
Mean 10.56
RMS 1.278
Underflow 0
Overflow 2.734e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

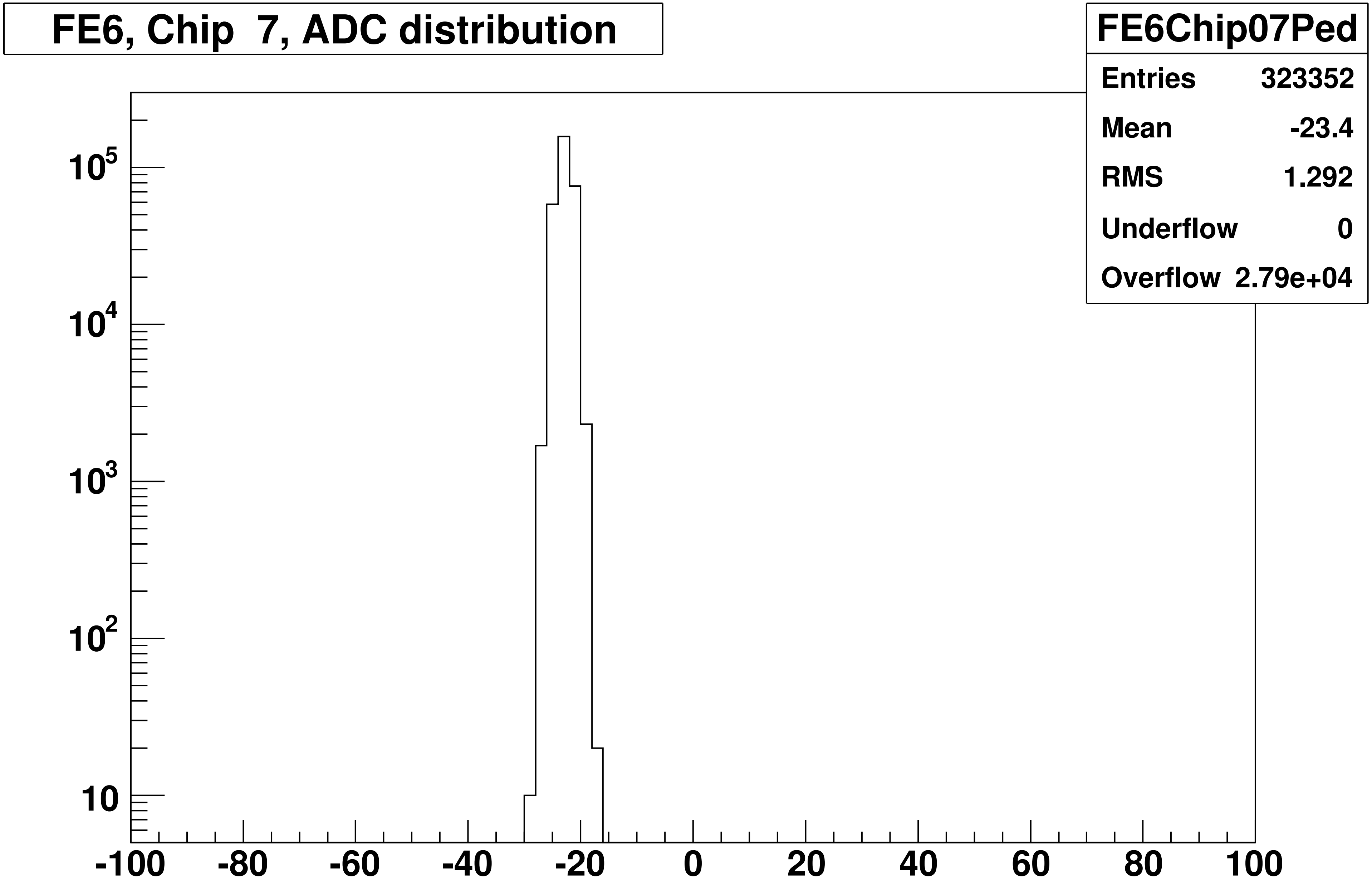

FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 323352
Mean 1.88e+04
RMS 0.453
Underflow 3.233e+05
Overflow 0
10
1
10^-1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 323352
Mean 1619
RMS 5267
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

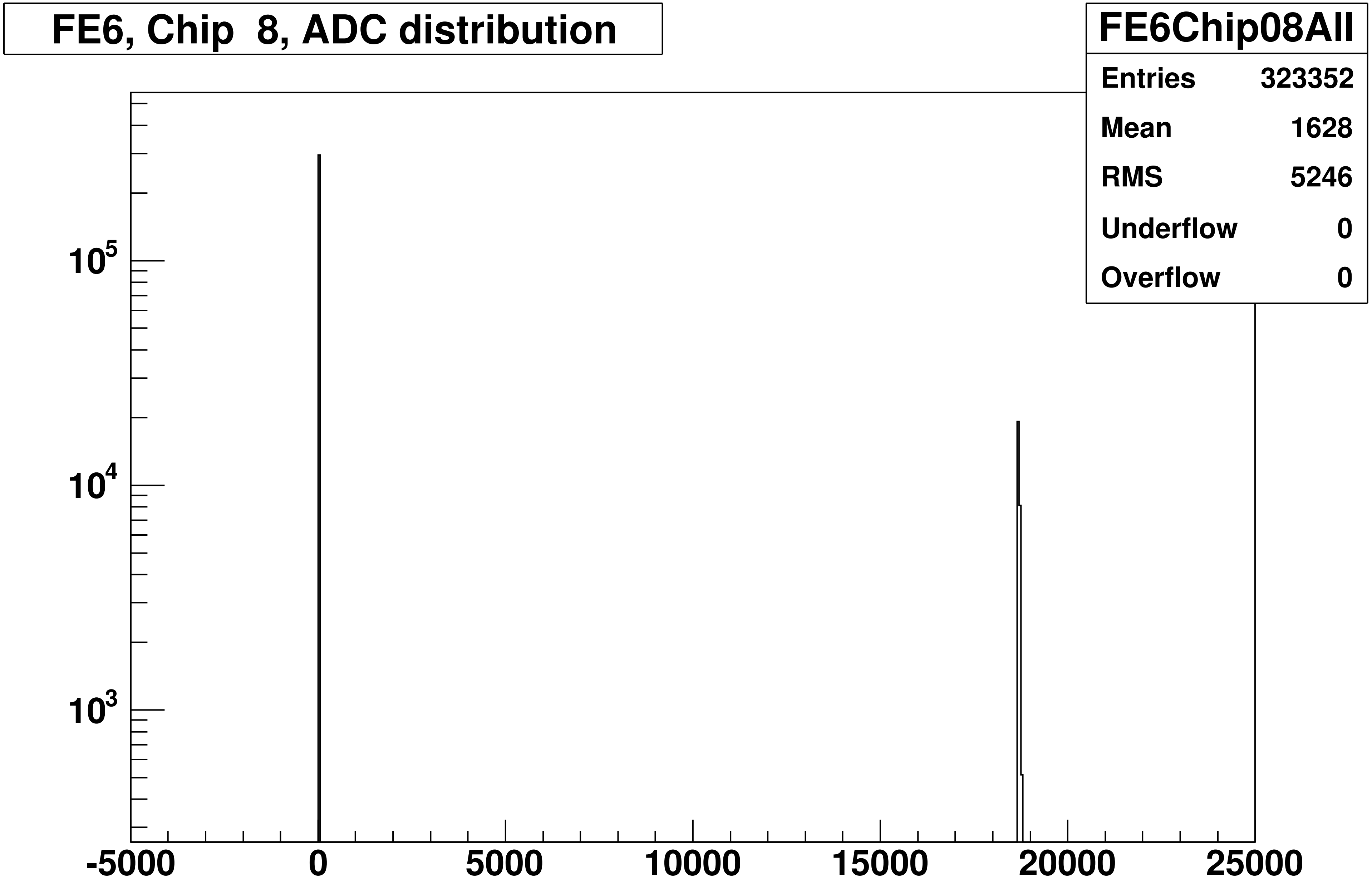

FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 323352
Mean 0.5475
RMS 1.234
Underflow 0
Overflow 2.79e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

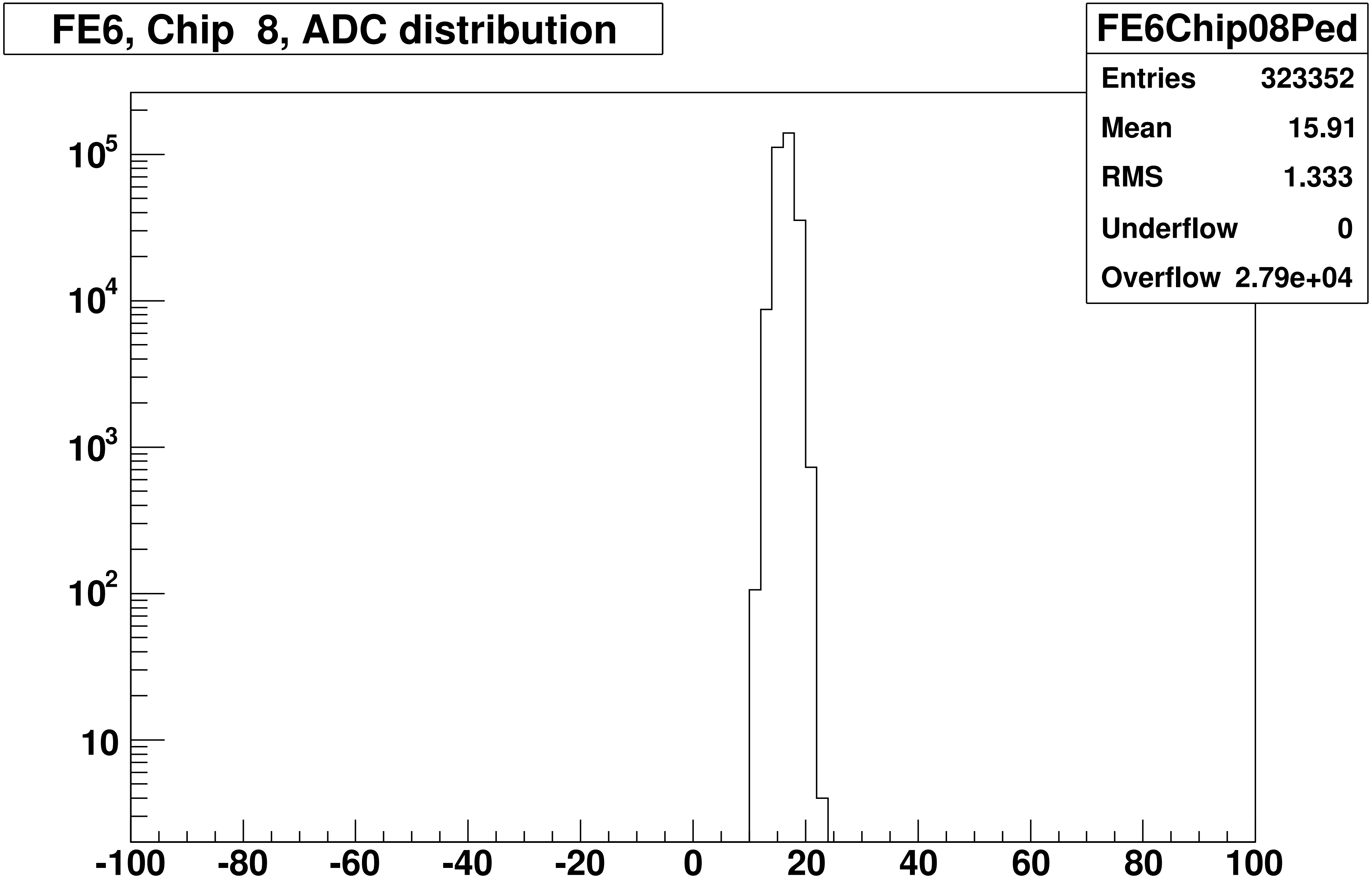

FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 323352
Mean 1.882e+04
RMS 9.482
Underflow 3.228e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

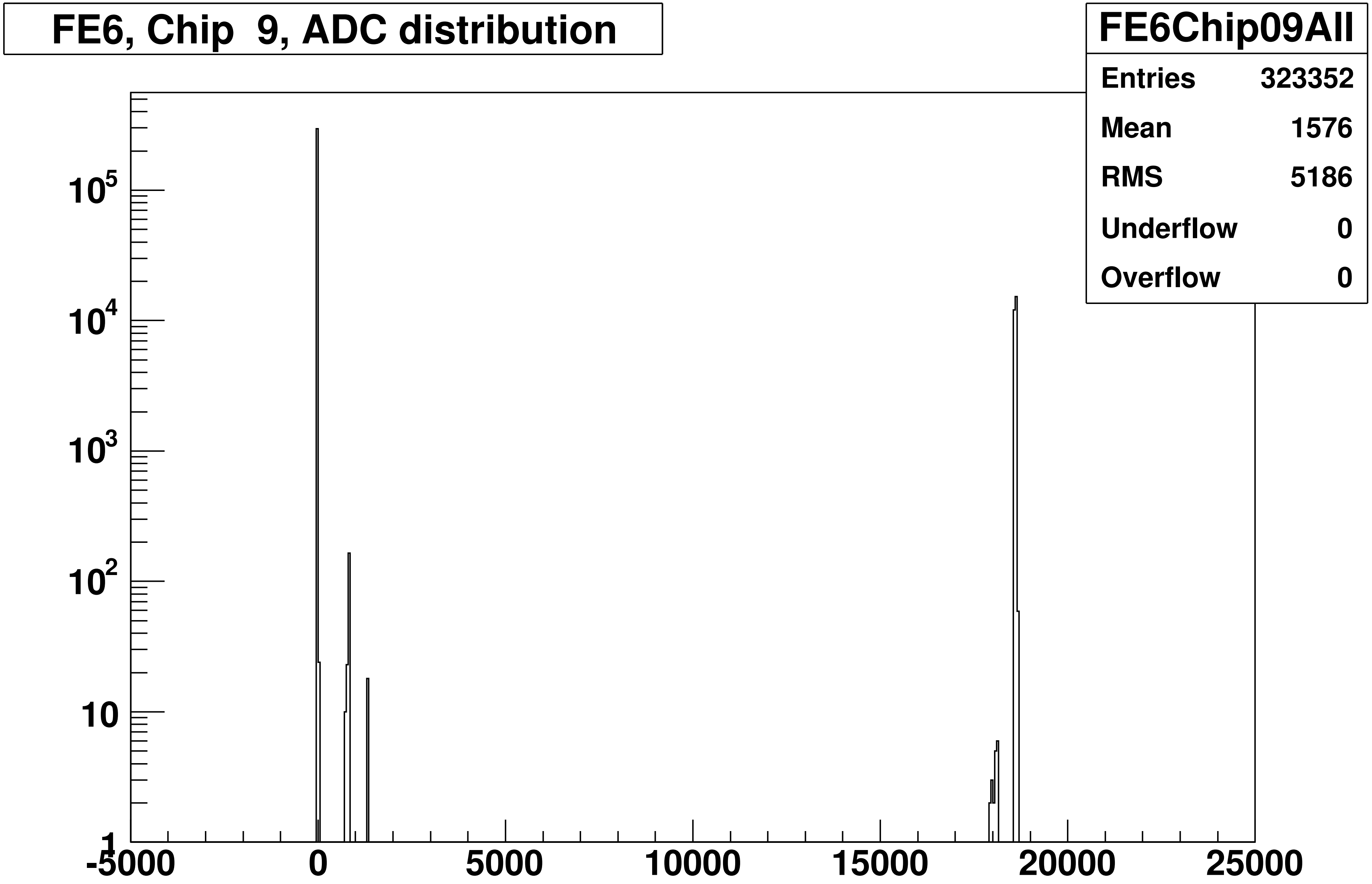

FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 323352
Mean 1577
RMS 5209
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

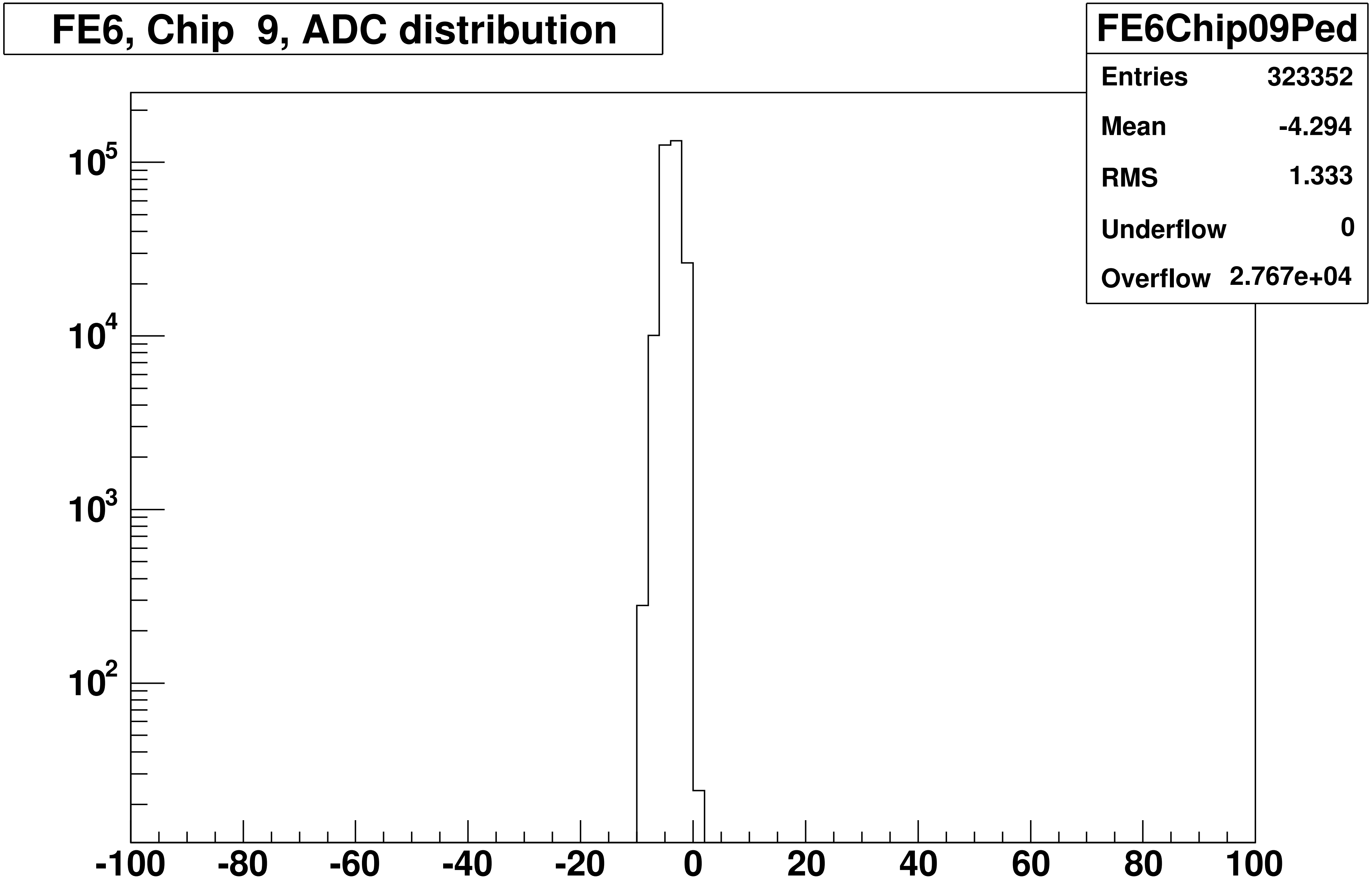


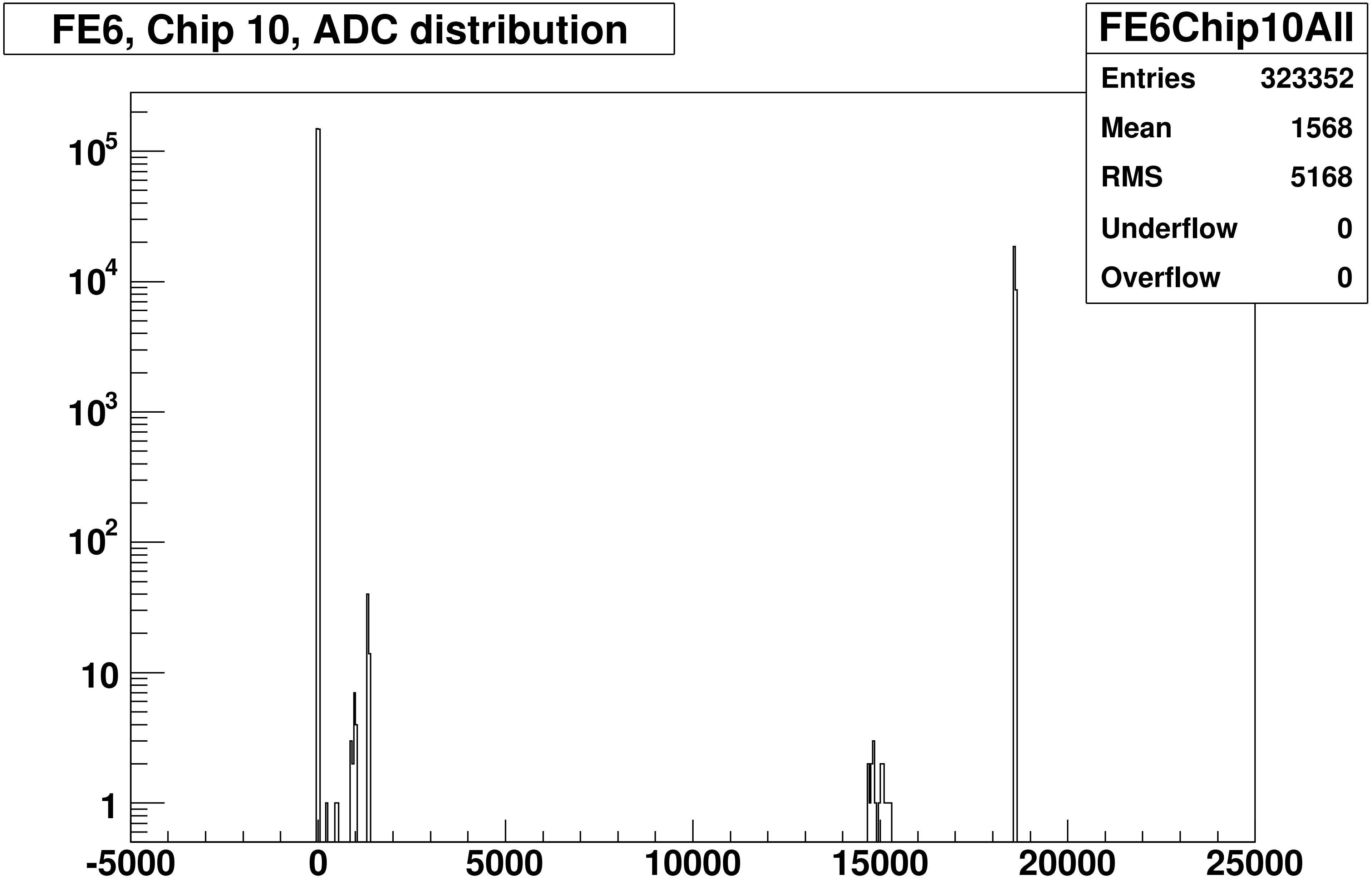
FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 323352
Mean -23.4
RMS 1.292
Underflow 0
Overflow 2.79e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 323352
Mean 1628
RMS 5246
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

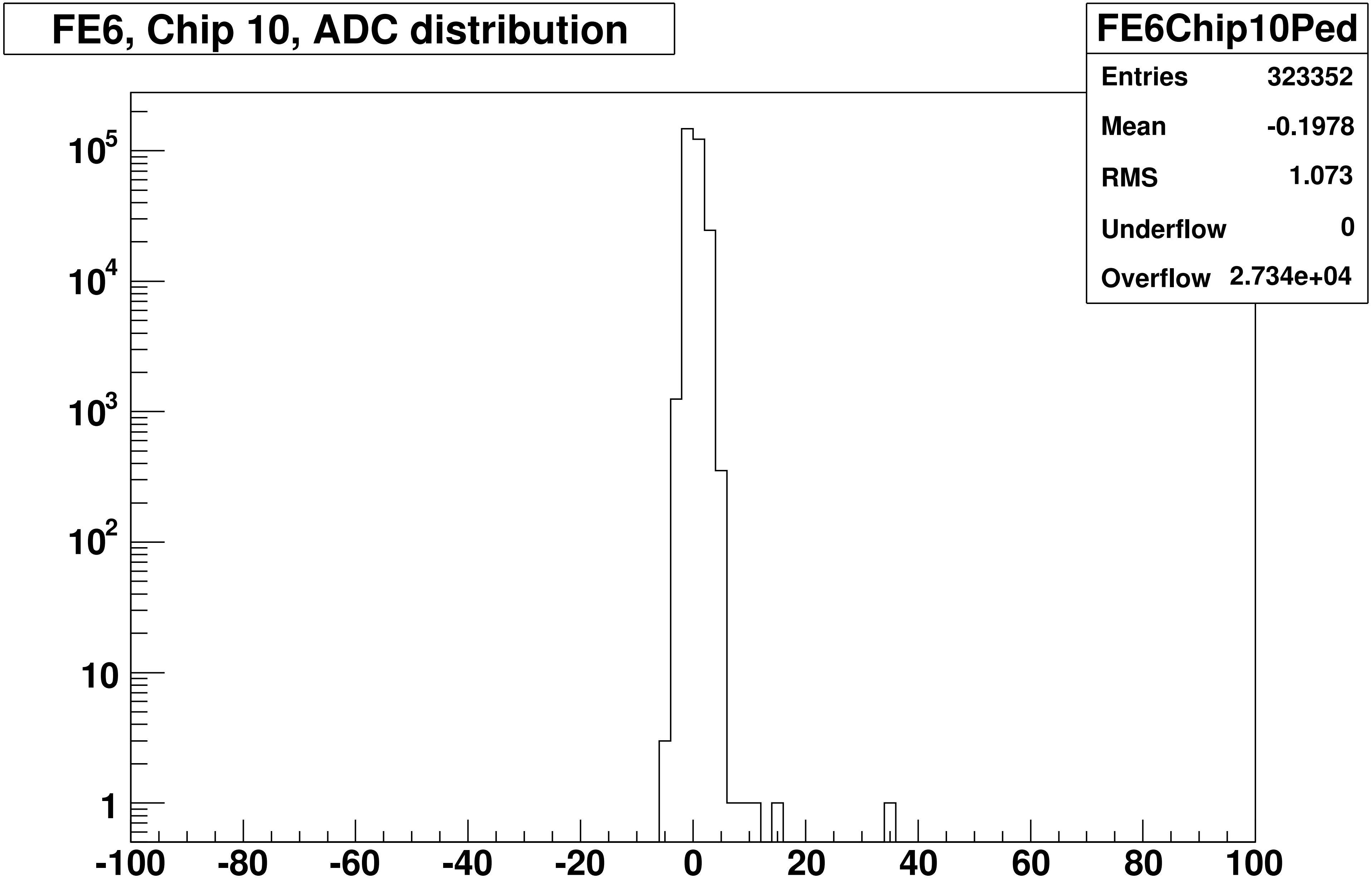

FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 323352
Mean 15.91
RMS 1.333
Underflow 0
Overflow 2.79e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

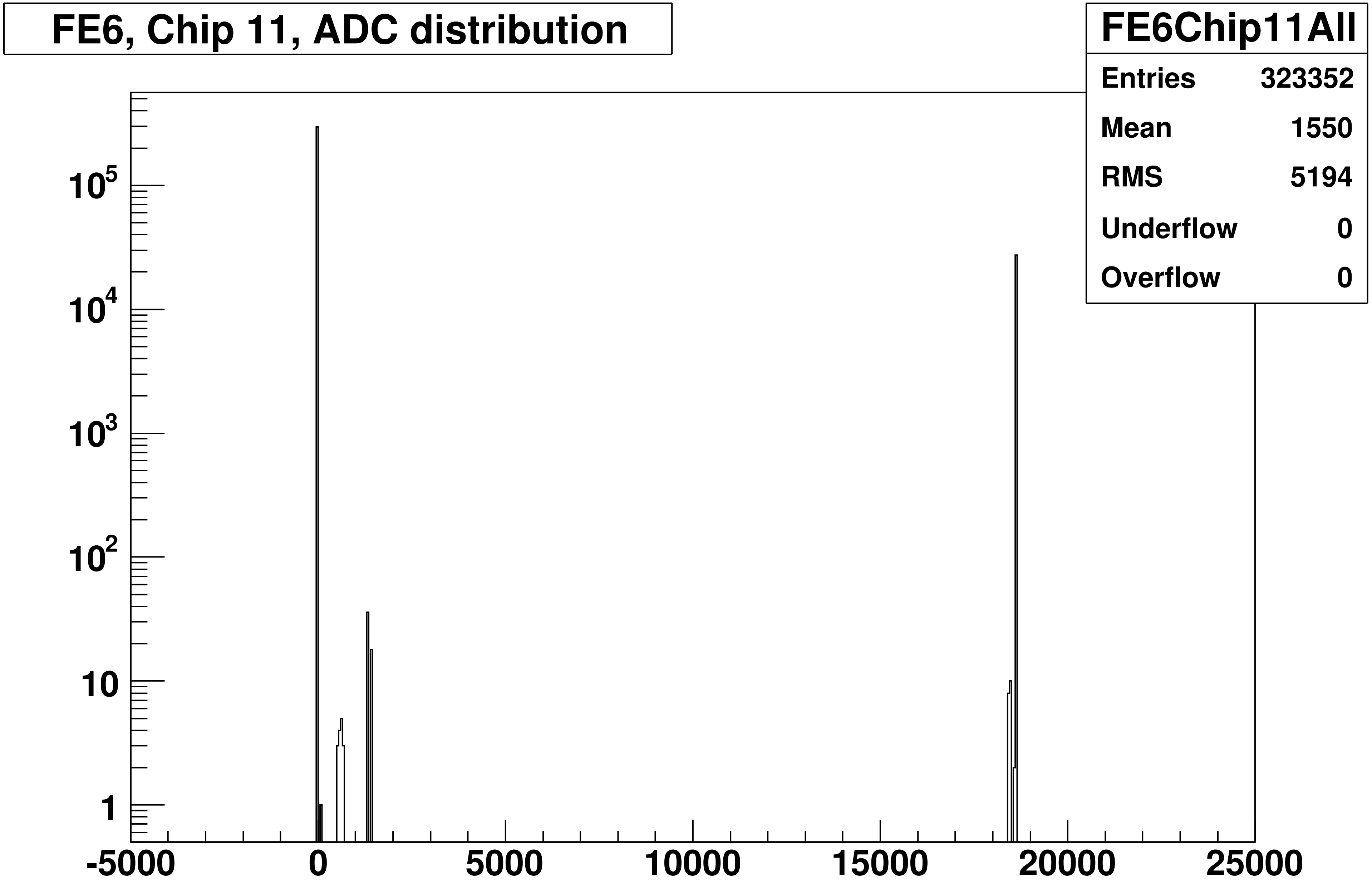

FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 323352
Mean 1576
RMS 5186
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 323352
Mean -4.294
RMS 1.333
Underflow 0
Overflow 2.767e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

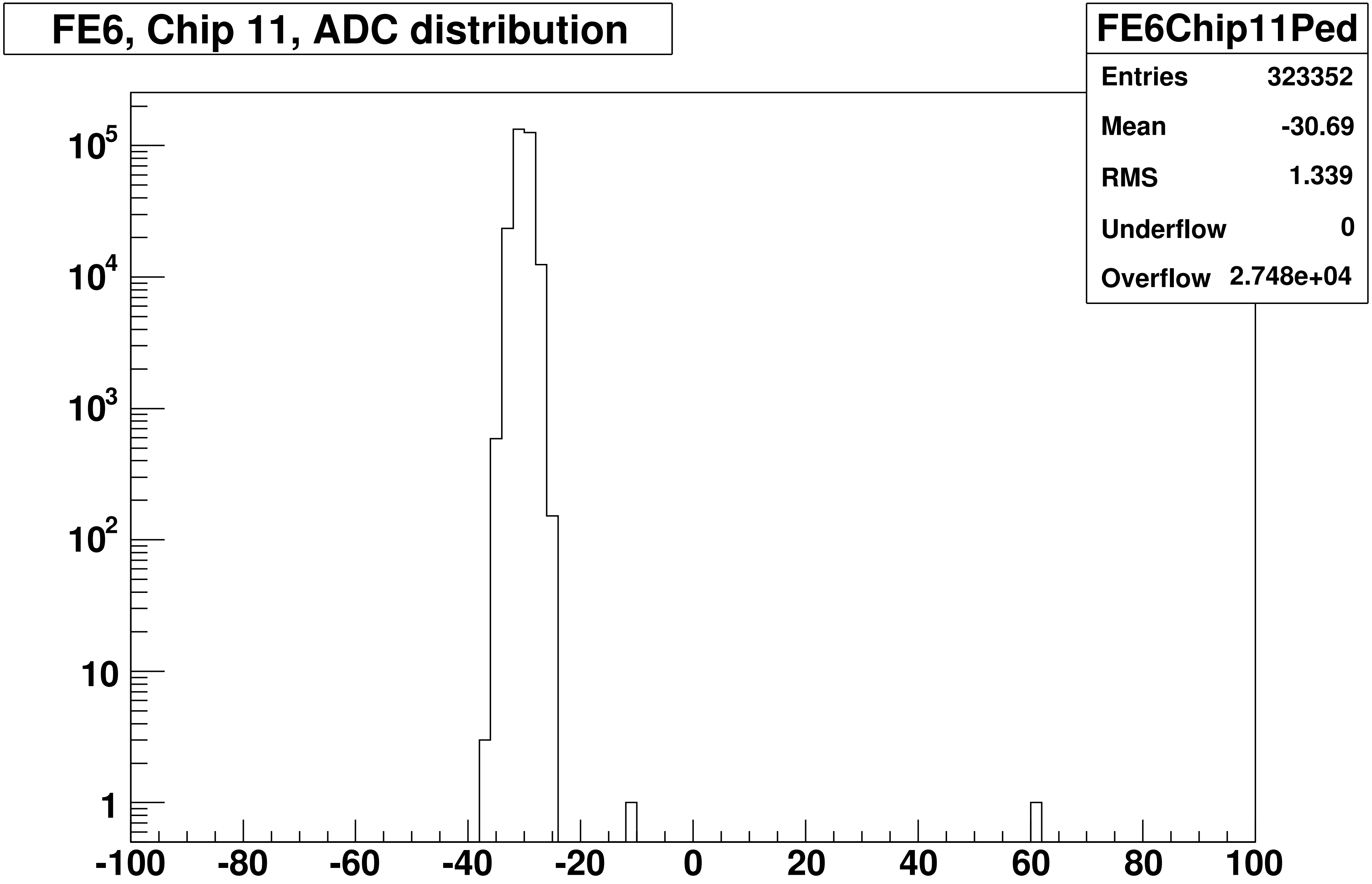


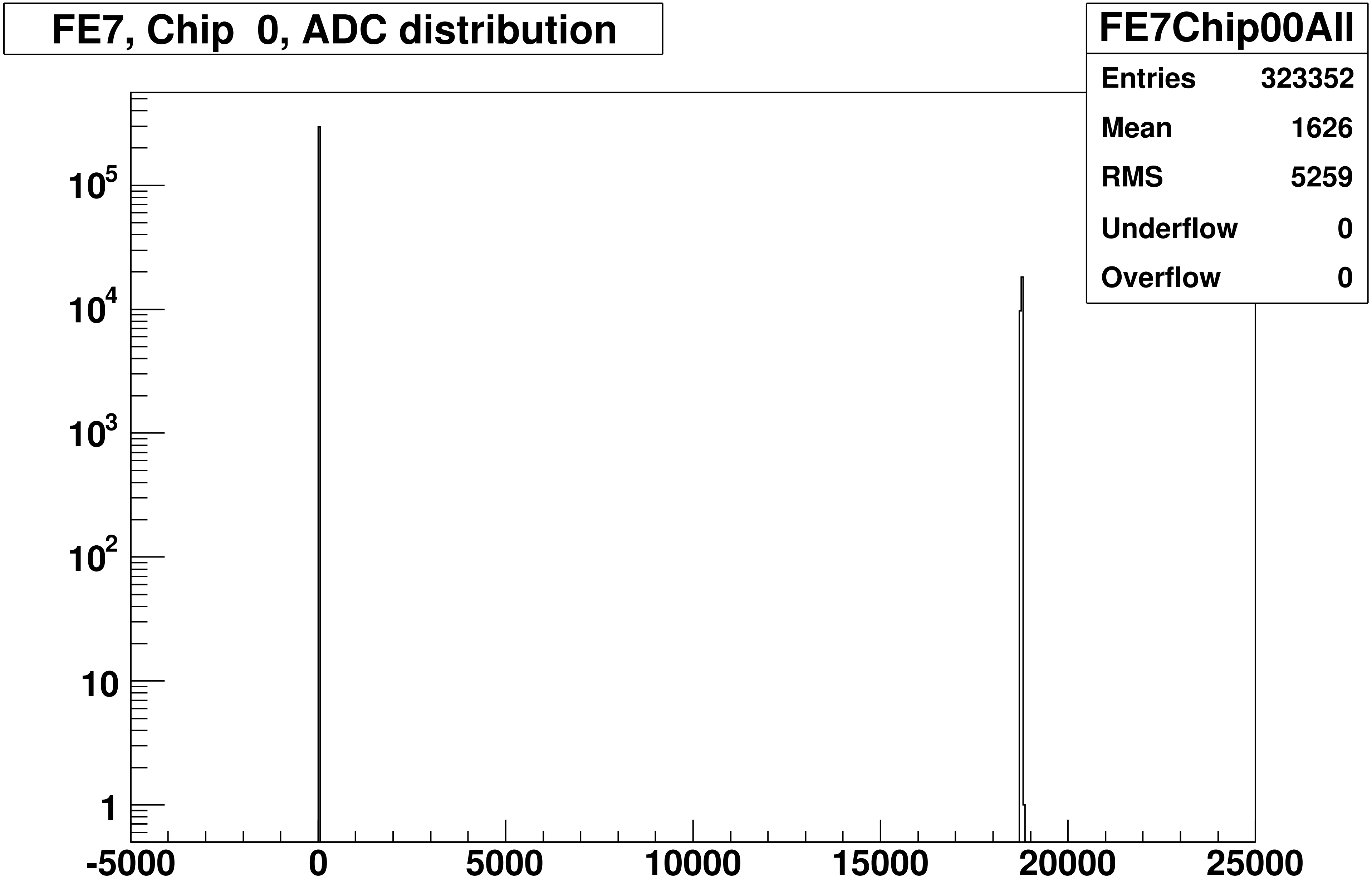
FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 323352
Mean 1568
RMS 5168
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 323352
Mean -0.1978
RMS 1.073
Underflow 0
Overflow 2.734e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

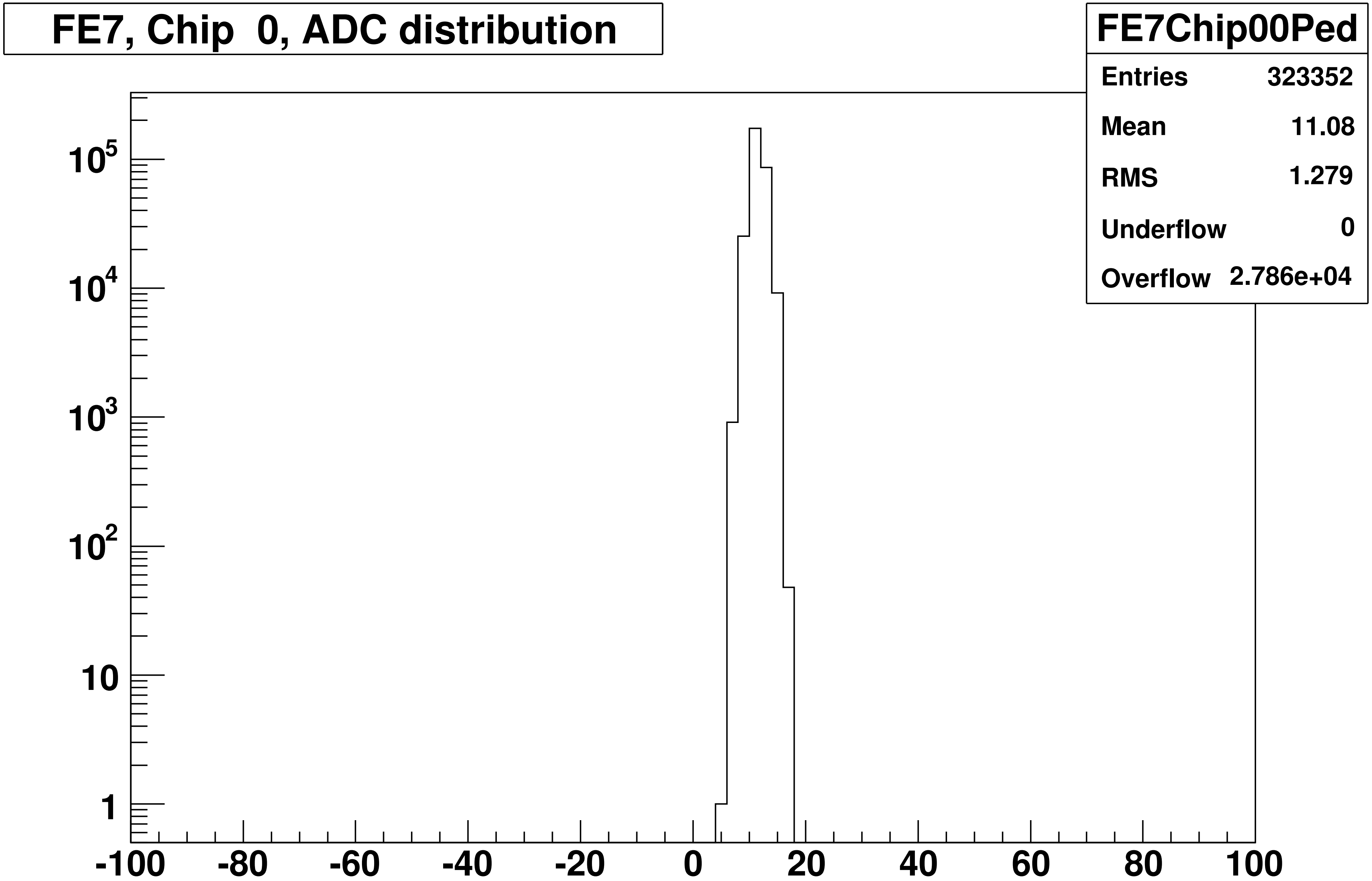


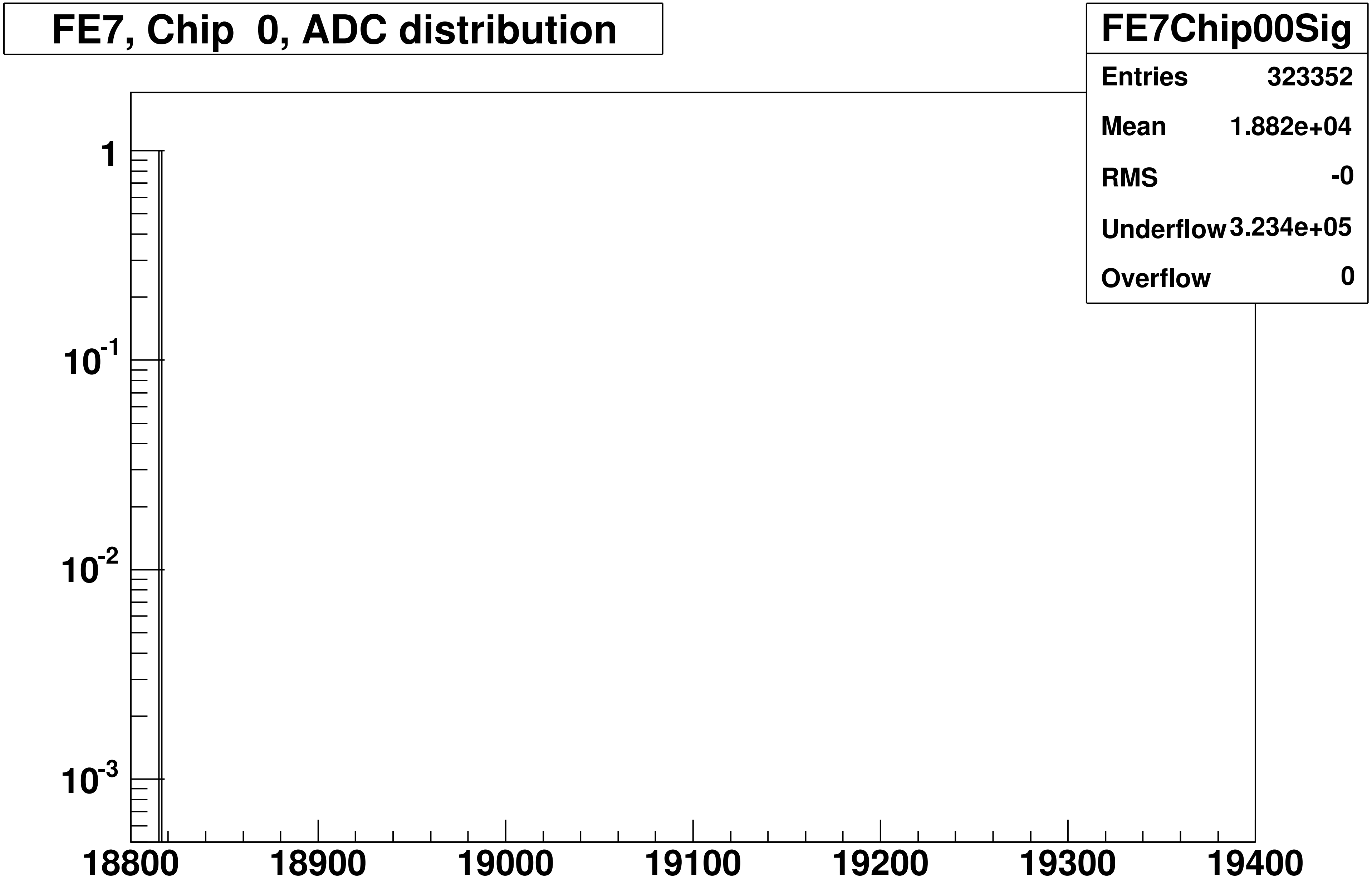
FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 323352
Mean 1550
RMS 5194
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 323352
Mean -30.69
RMS 1.339
Underflow 0
Overflow 2.748e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

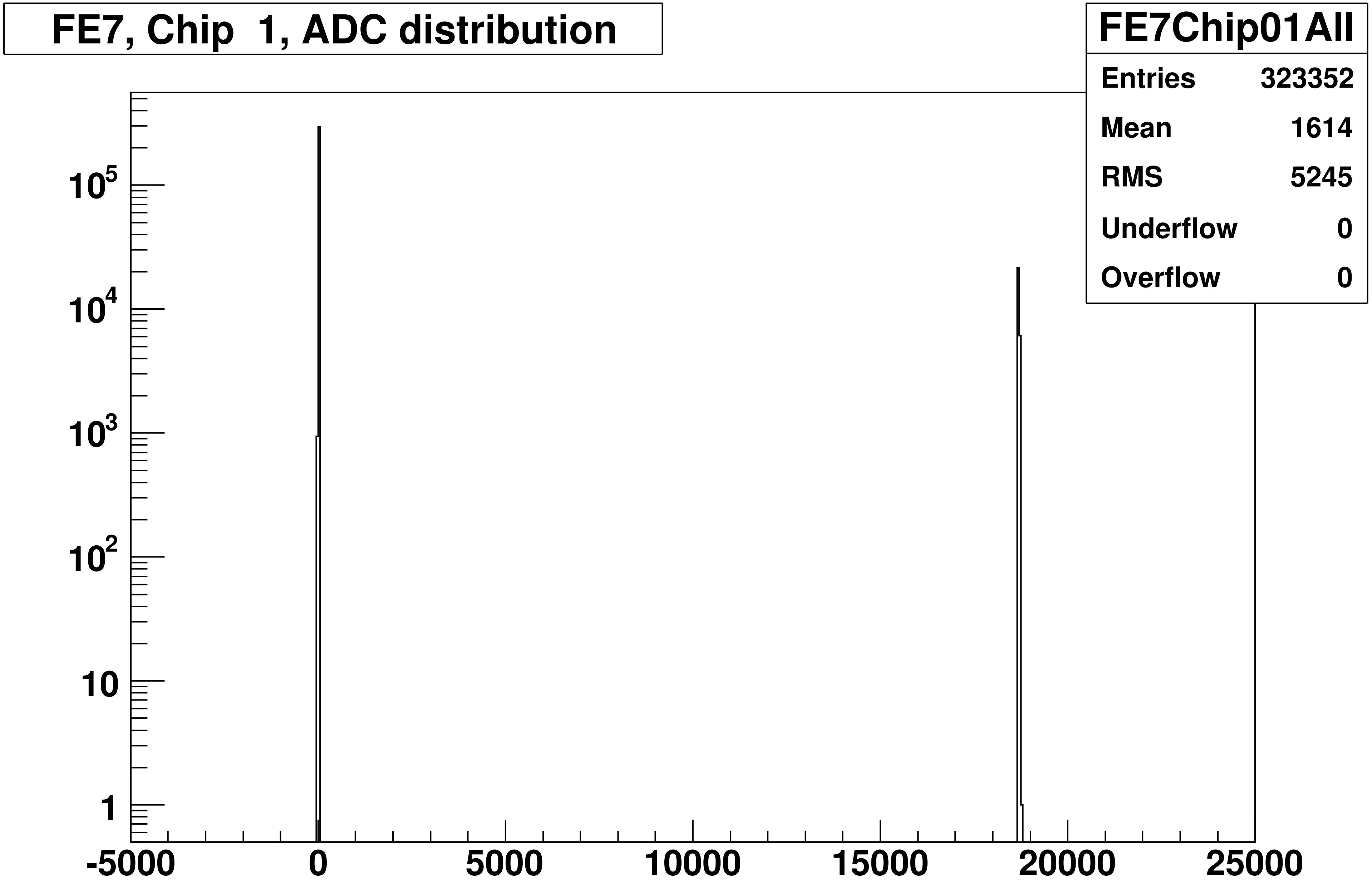


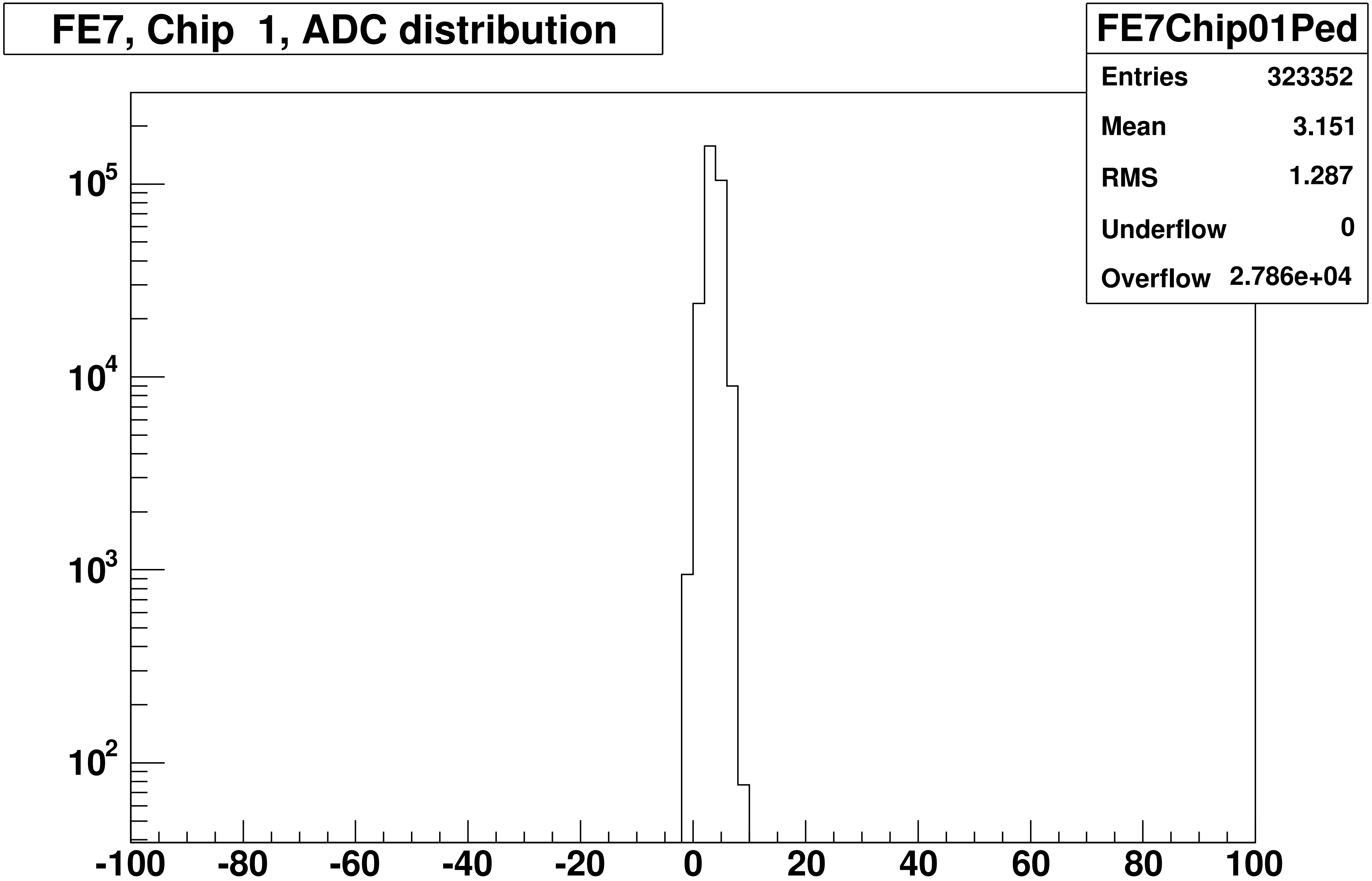
FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 323352
Mean 1626
RMS 5259
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

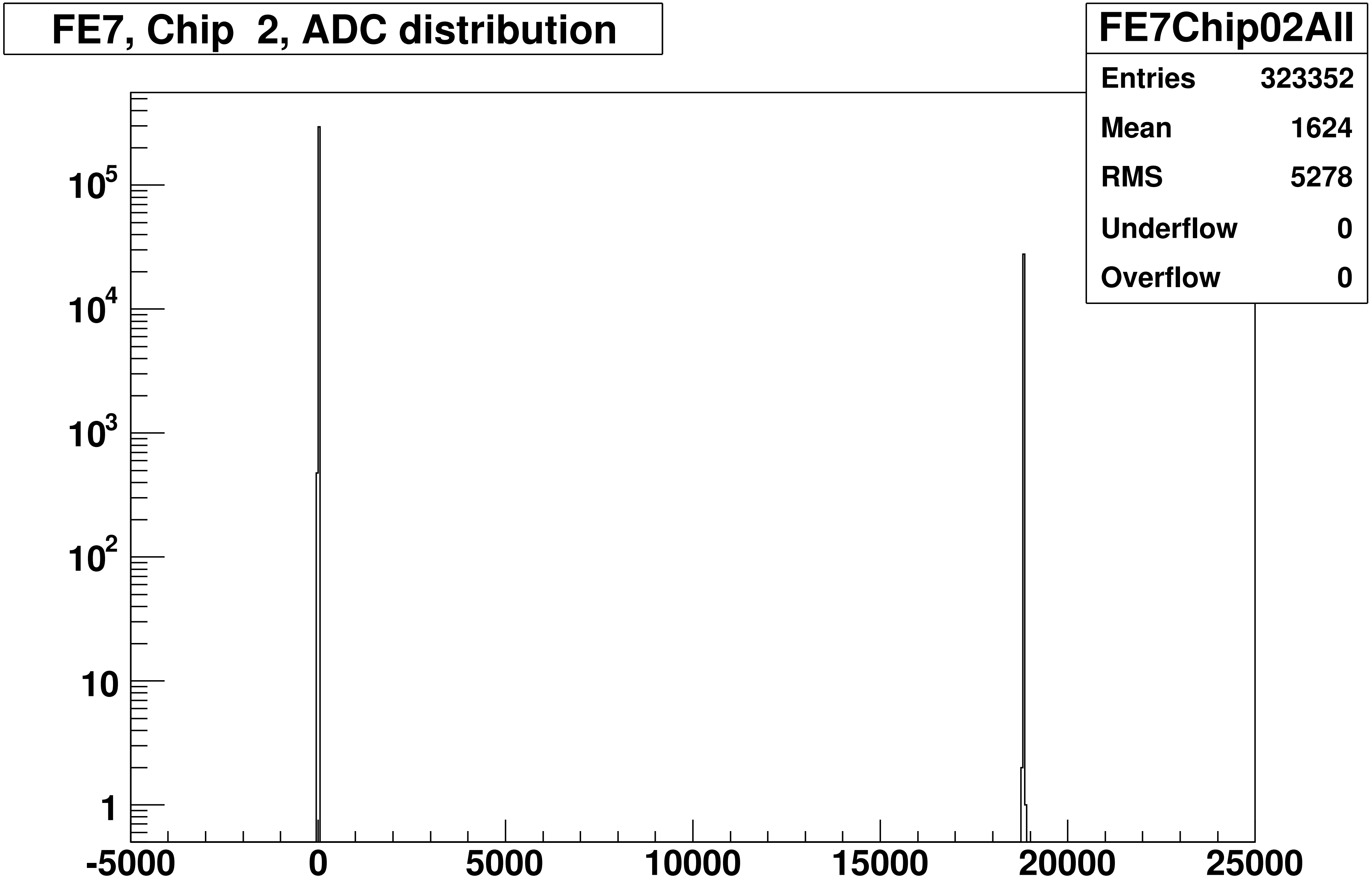

FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 323352
Mean 11.08
RMS 1.279
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

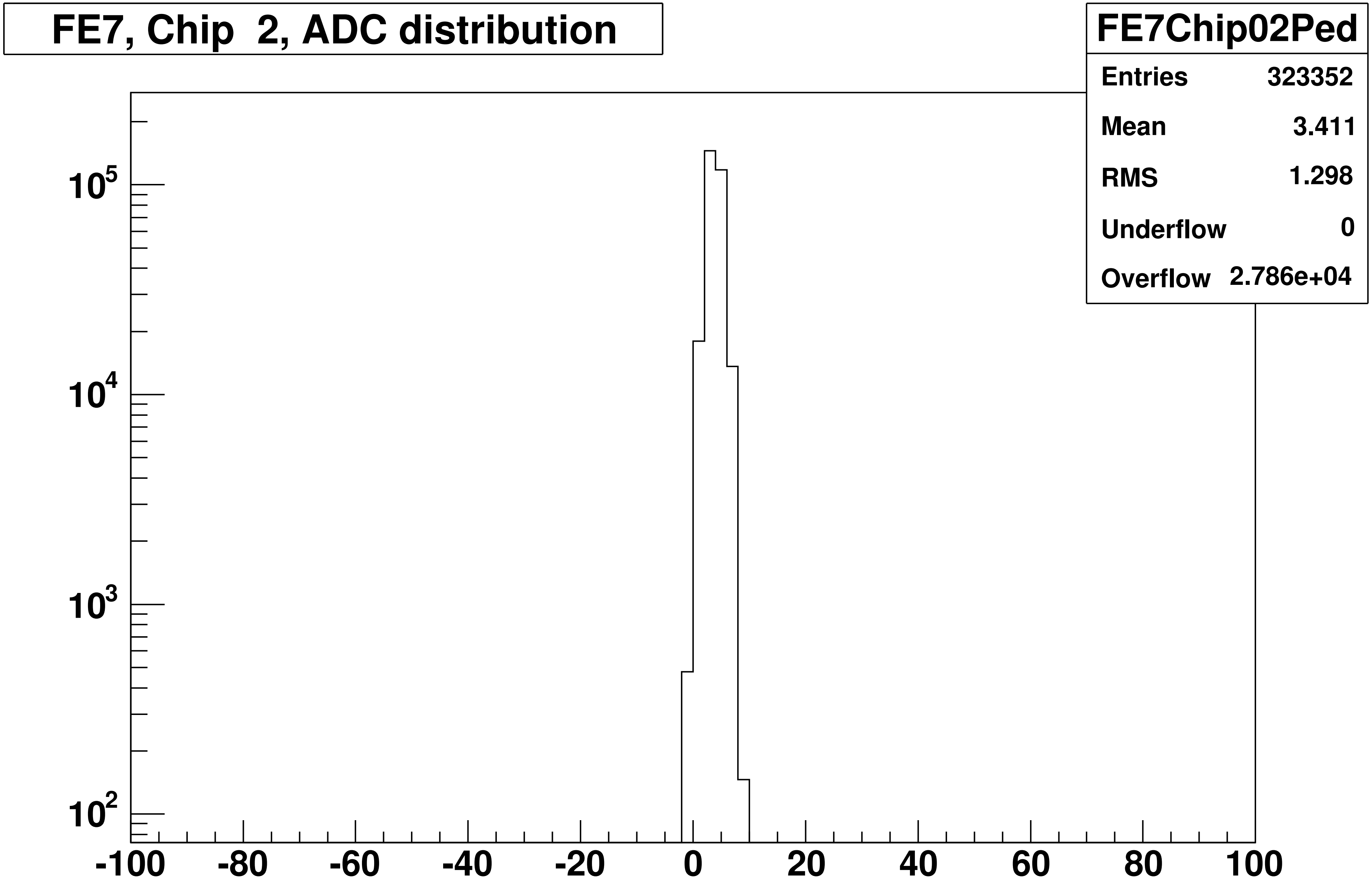


FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 323352
Mean 1.882e+04
RMS -0
Underflow 3.234e+05
Overflow 0
1
10^{-1}
10^{-2}
10^{-3}
18800
18900
19000
19100
19200
19300
19400

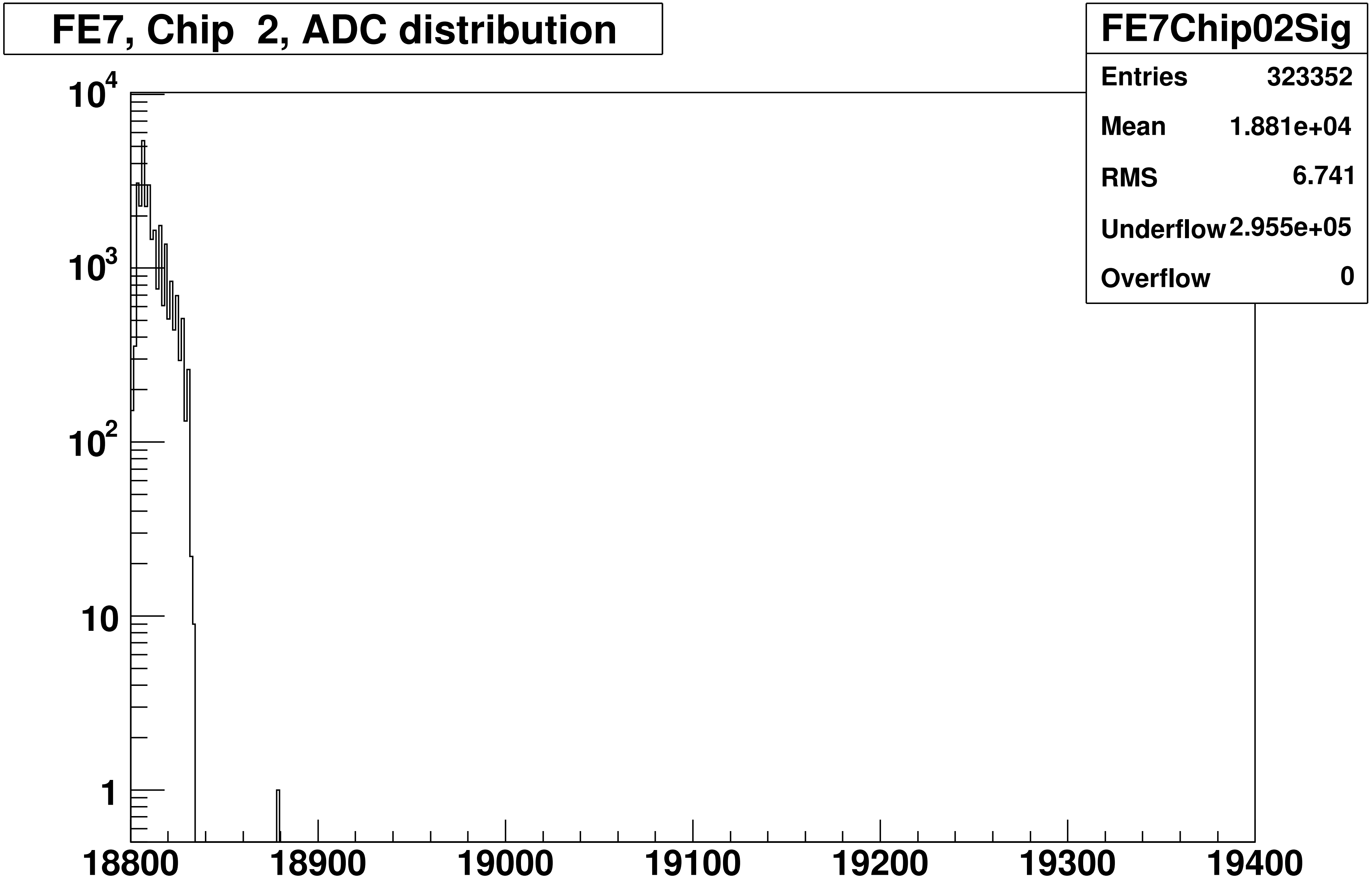

FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 323352
Mean 1614
RMS 5245
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

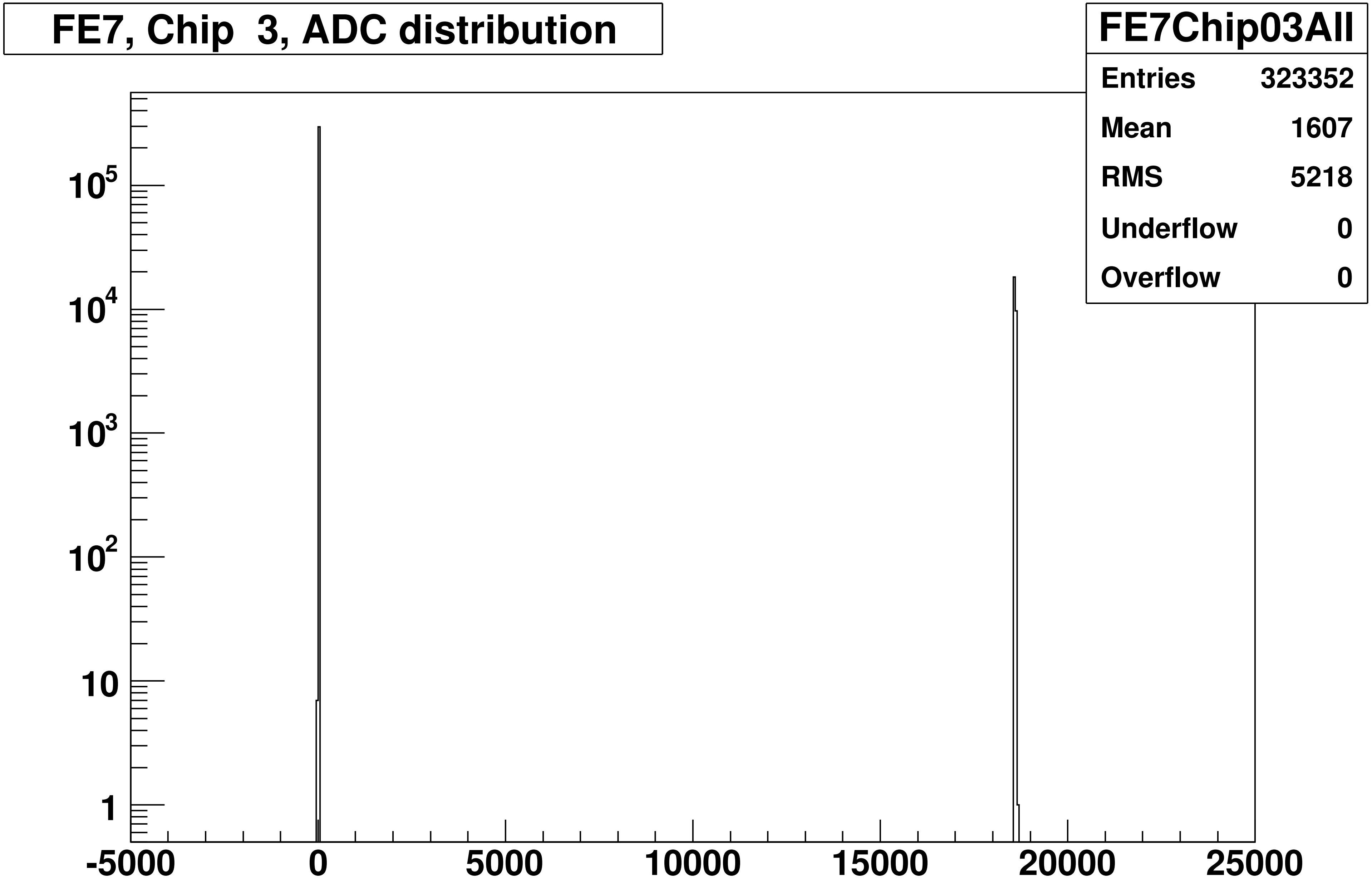

FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 323352
Mean 3.151
RMS 1.287
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

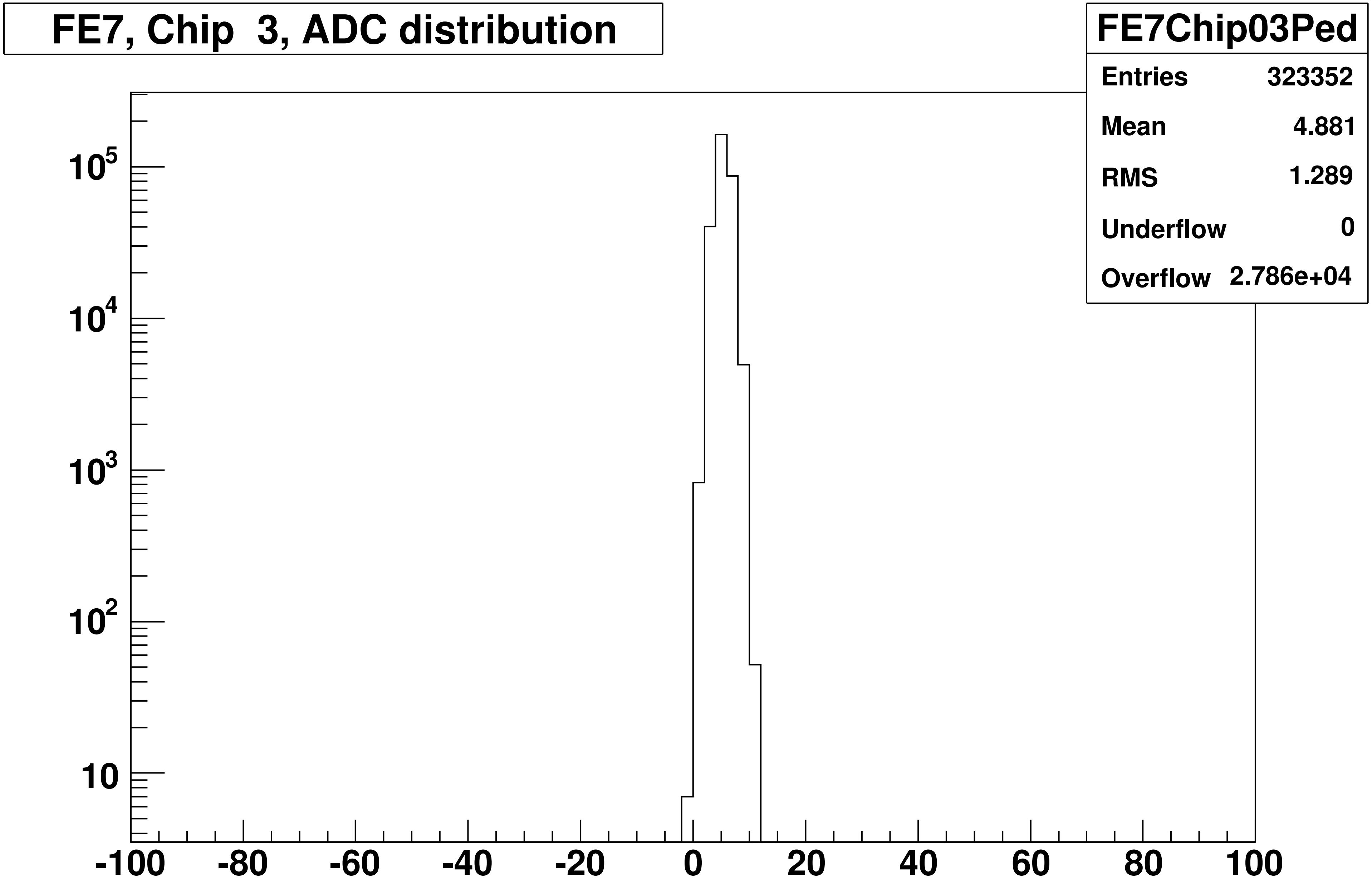

FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 323352
Mean 1624
RMS 5278
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

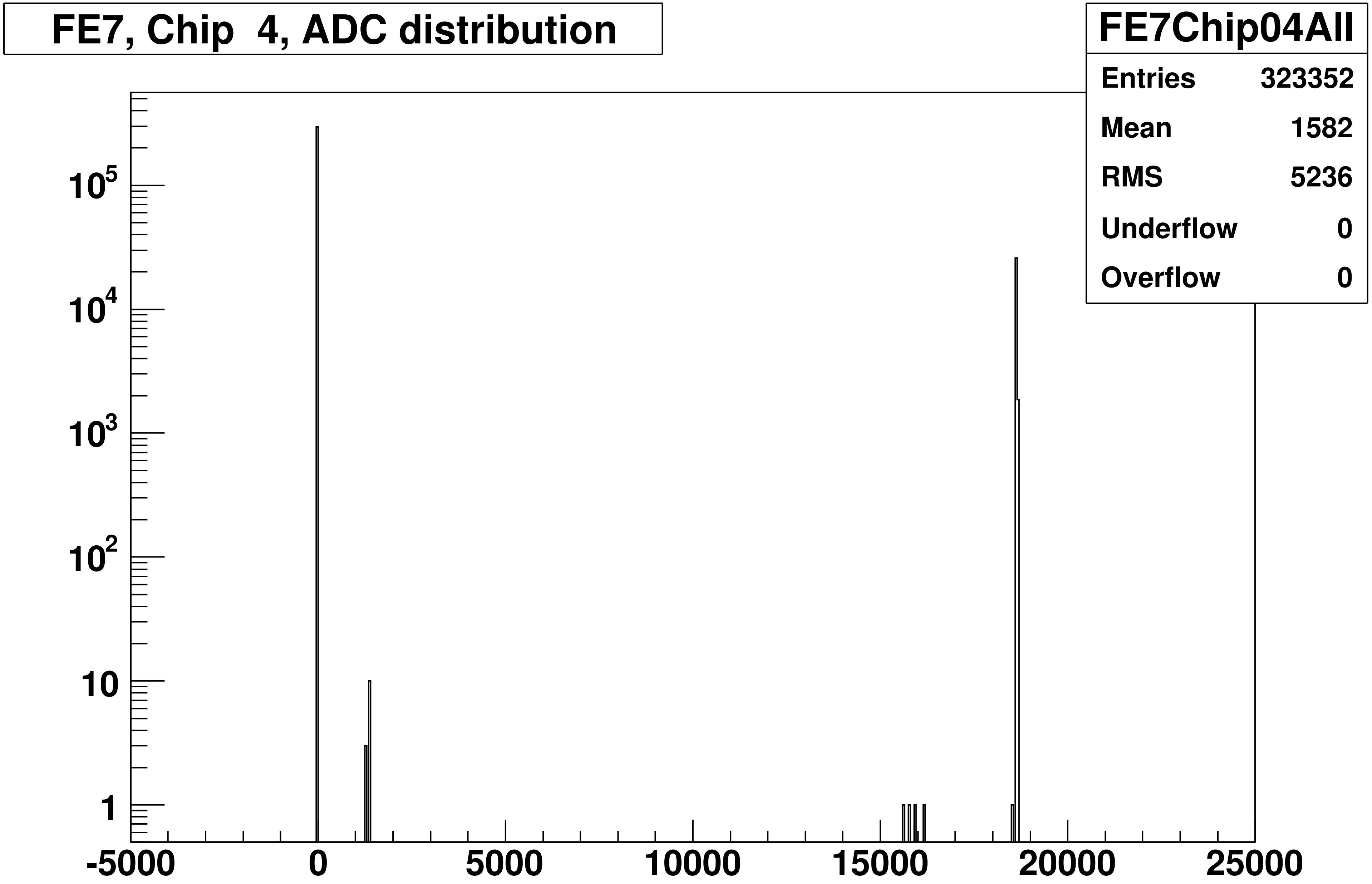


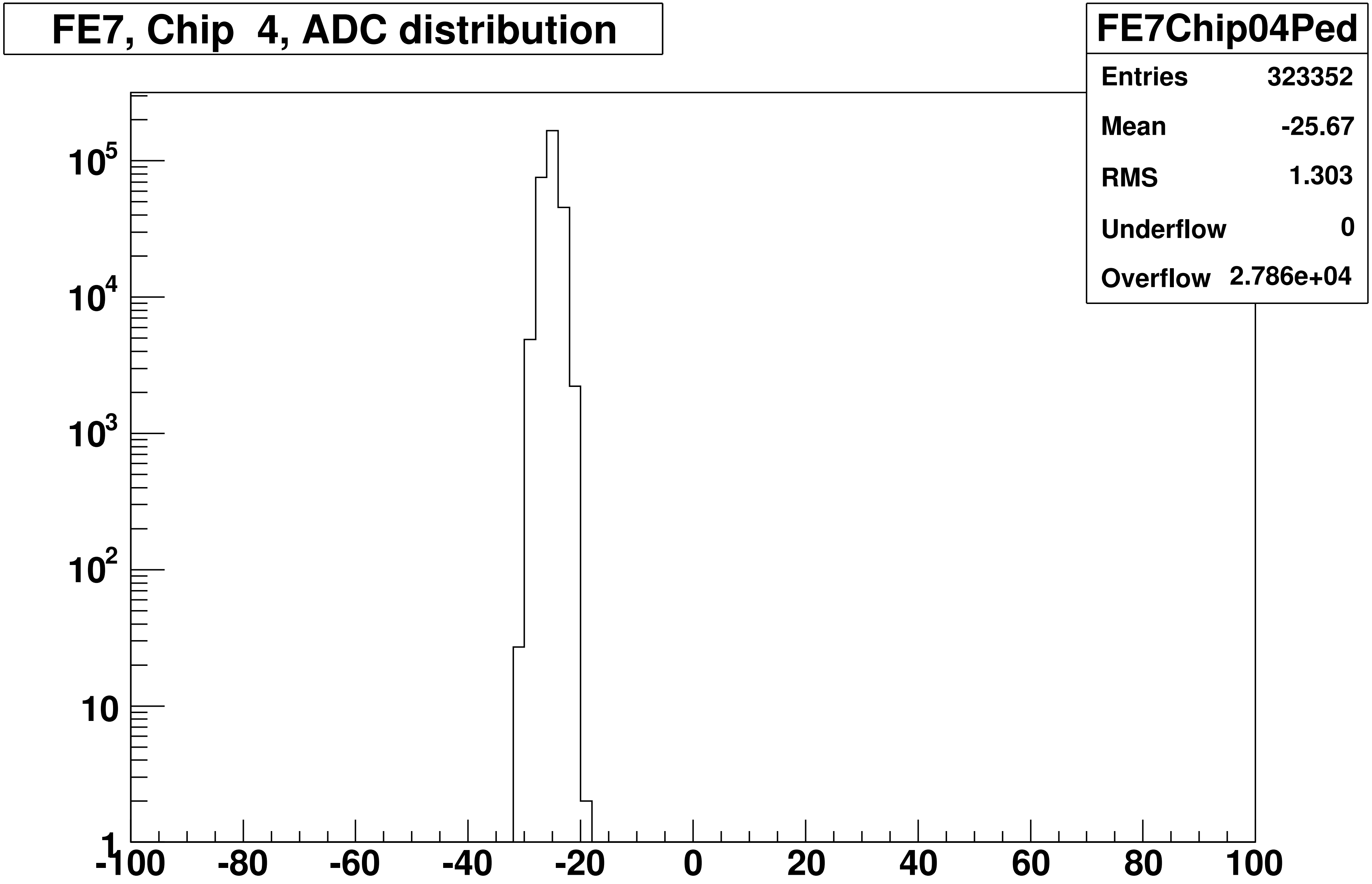
FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 323352
Mean 3.411
RMS 1.298
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

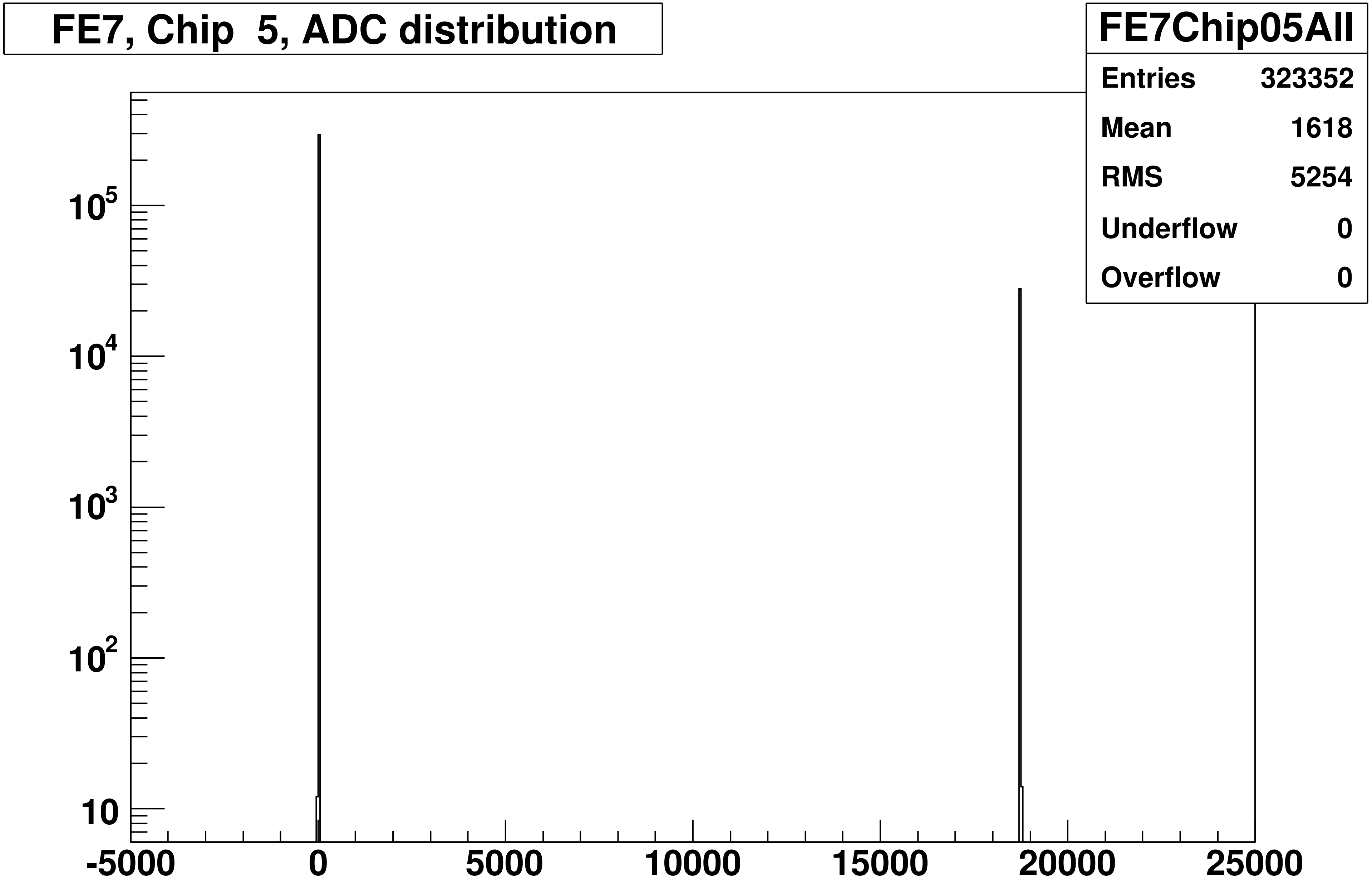

FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 323352
Mean 1.881e+04
RMS 6.741
Underflow 2.955e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 323352
Mean 1607
RMS 5218
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

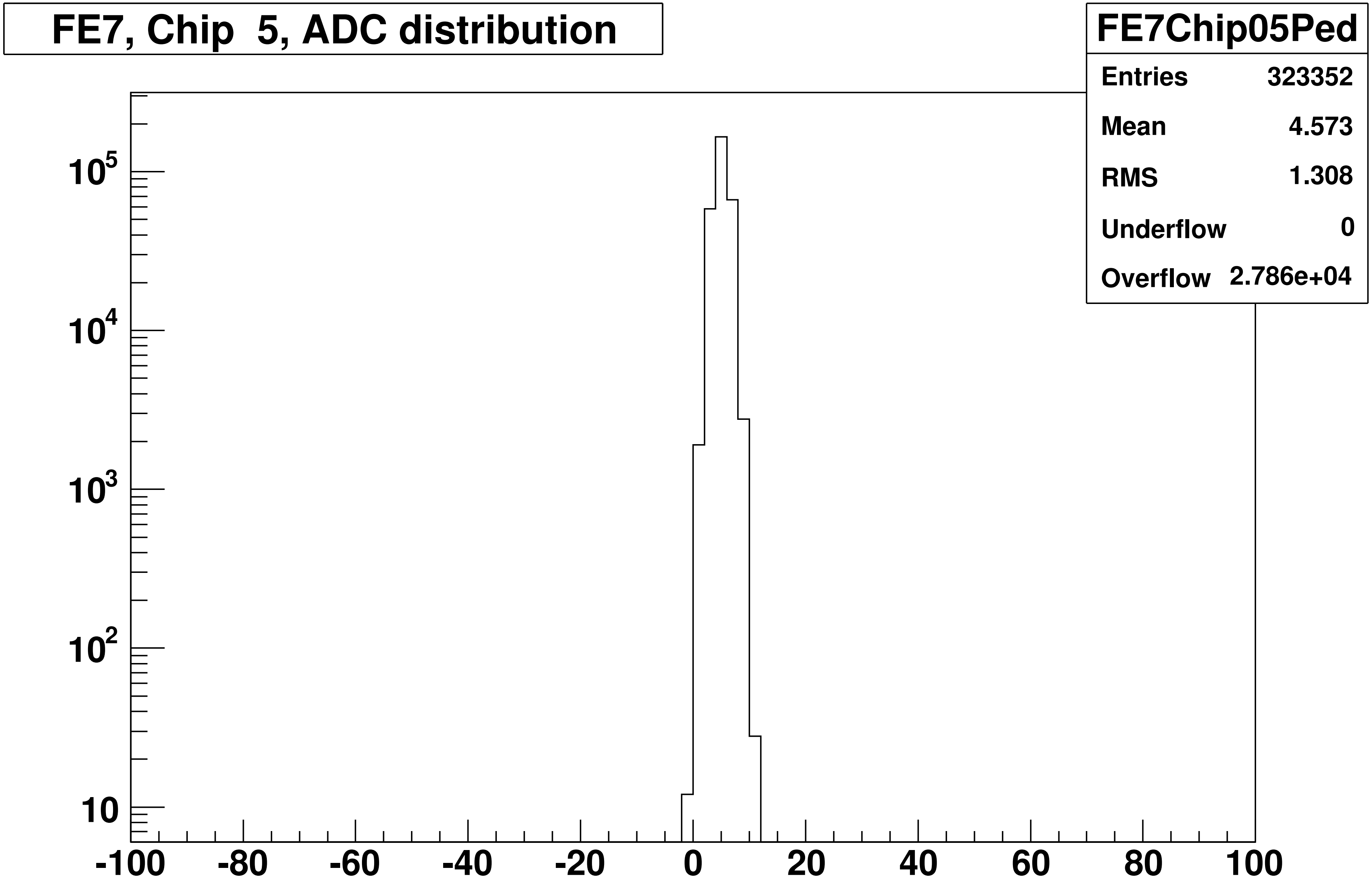

FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 323352
Mean 4.881
RMS 1.289
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 323352
Mean 1582
RMS 5236
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

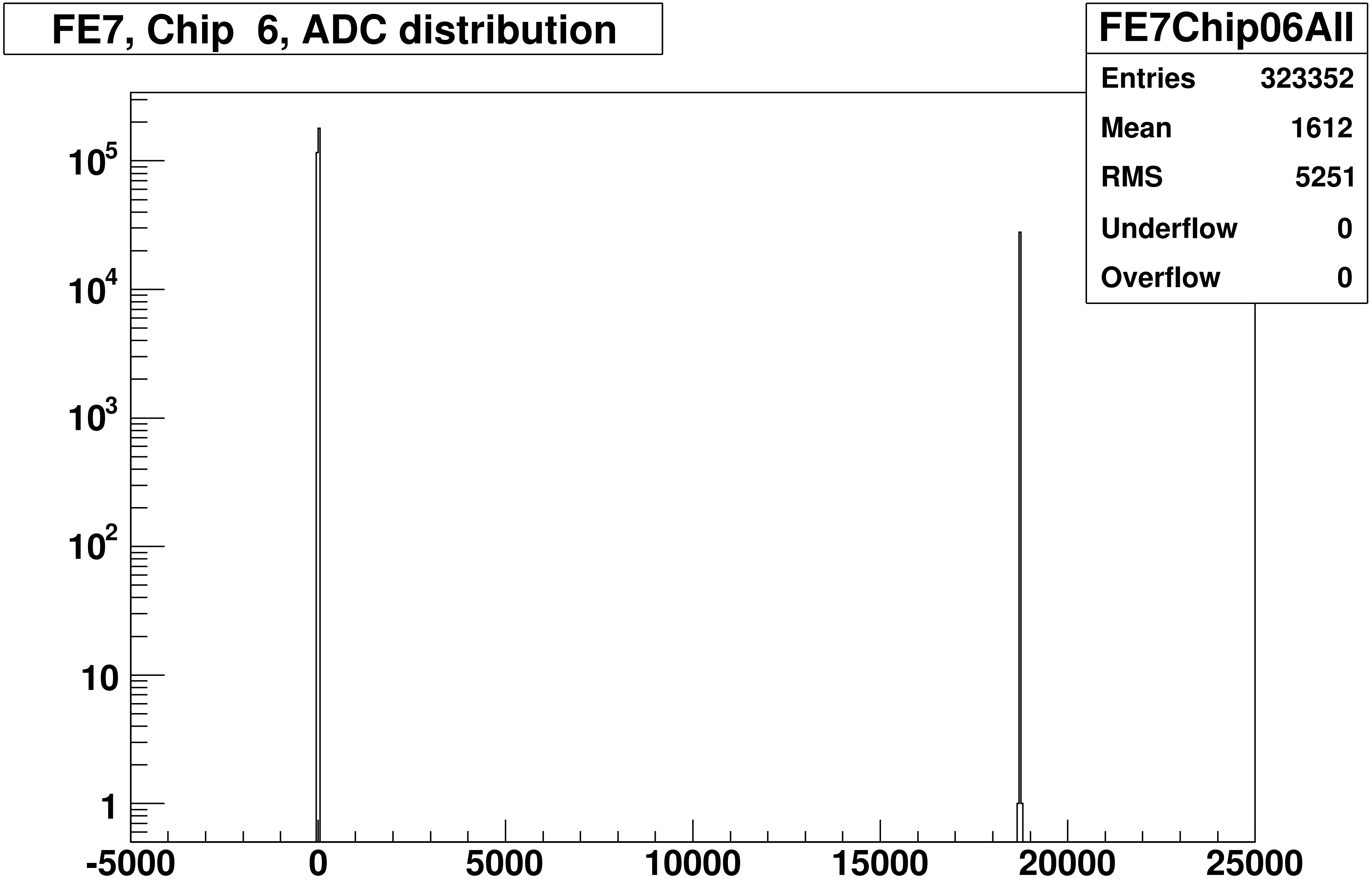

FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 323352
Mean -25.67
RMS 1.303
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

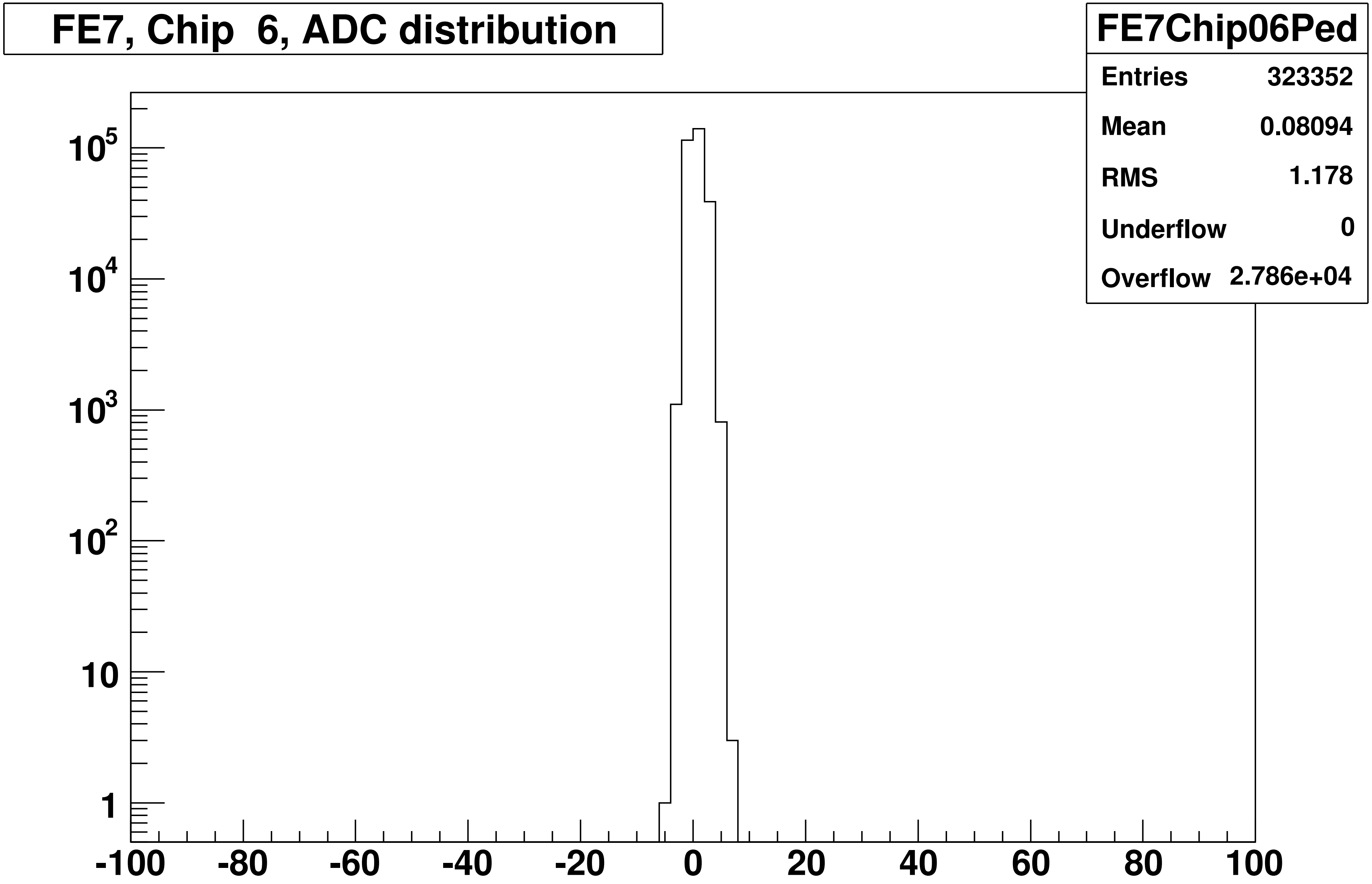


FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 323352
Mean 1618
RMS 5254
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

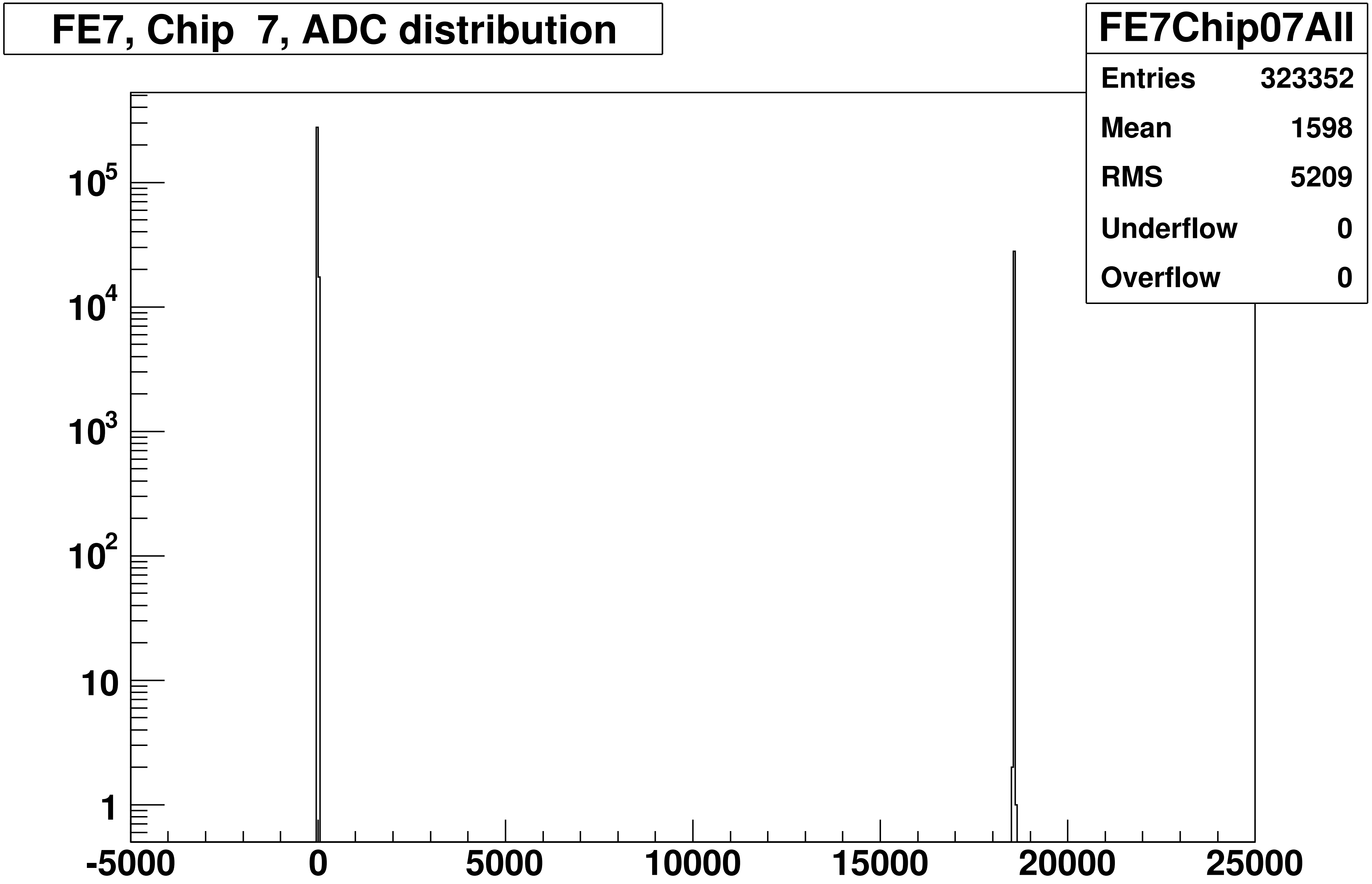


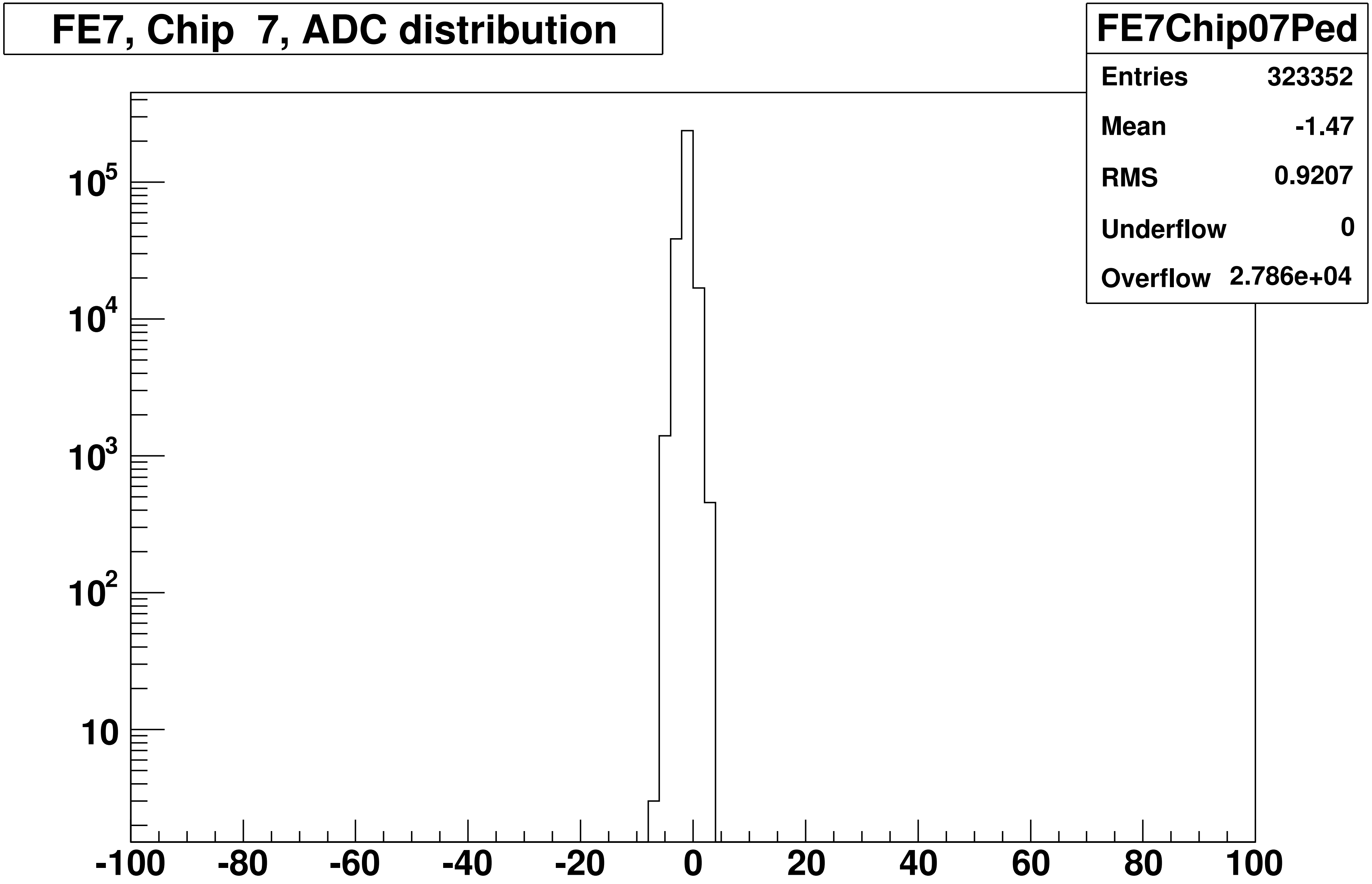
FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 323352
Mean 4.573
RMS 1.308
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

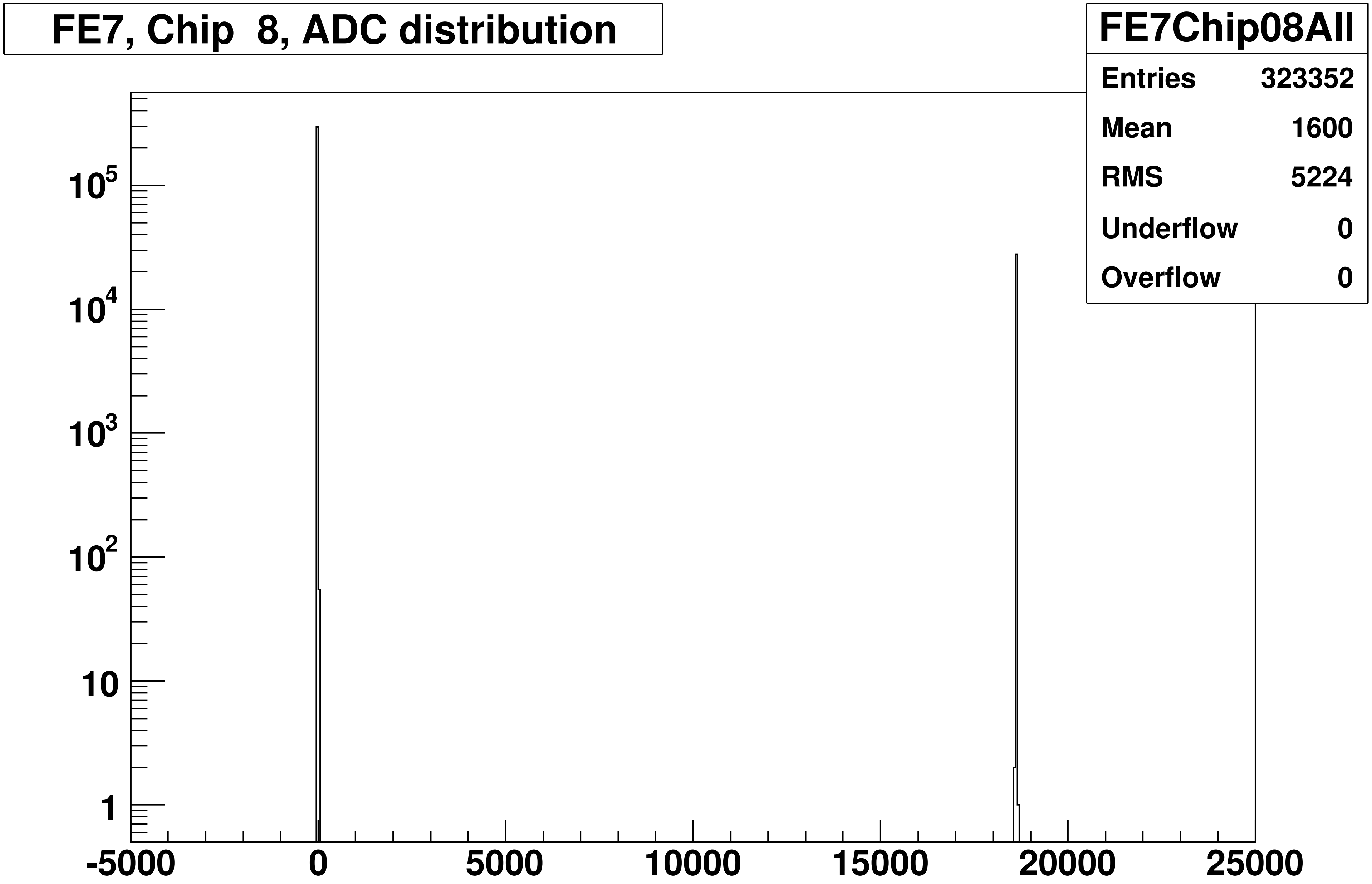

FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 323352
Mean 1612
RMS 5251
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



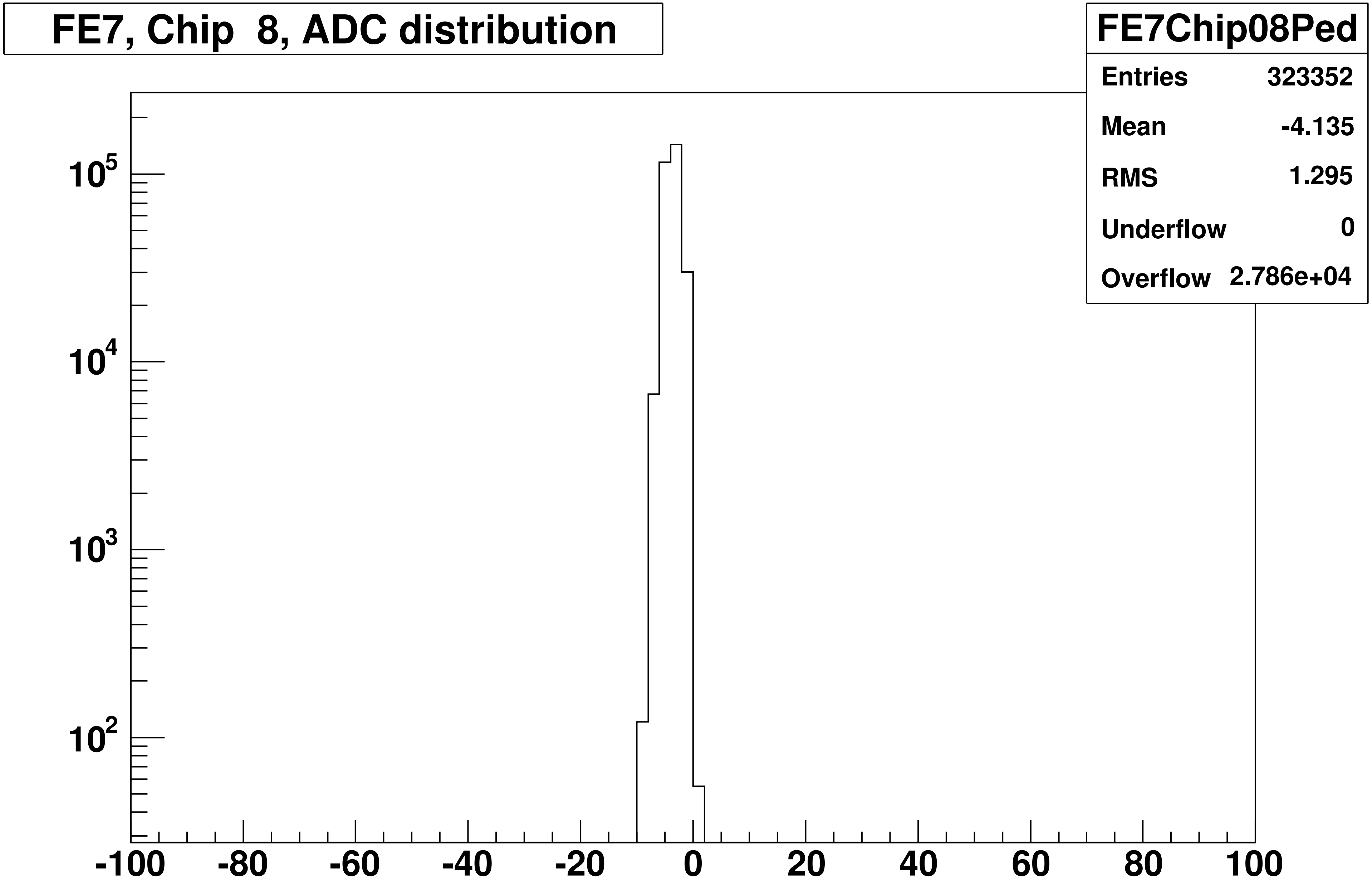
FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 323352
Mean 0.08094
RMS 1.178
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

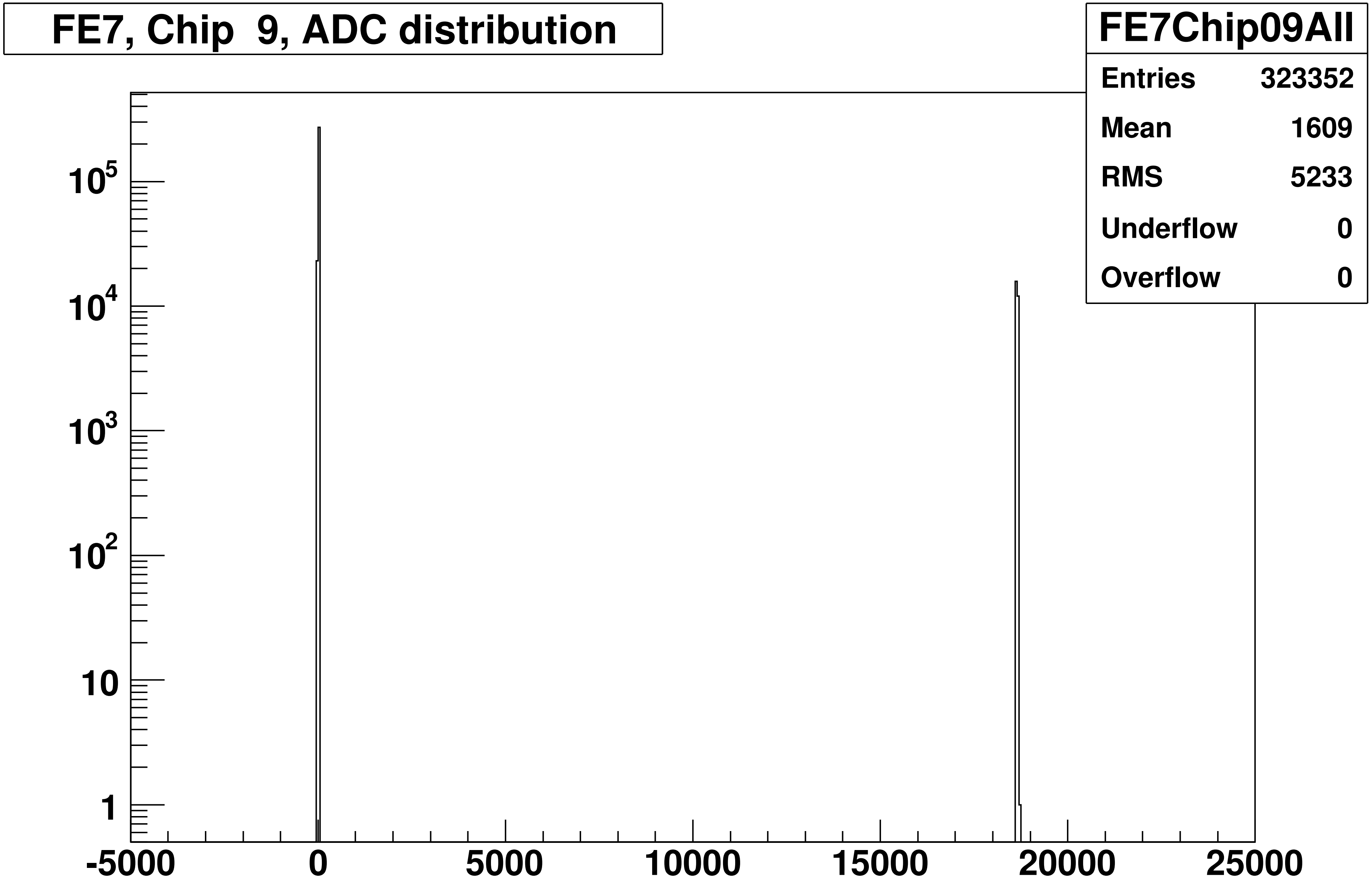

FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 323352
Mean 1598
RMS 5209
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 323352
Mean -1.47
RMS 0.9207
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

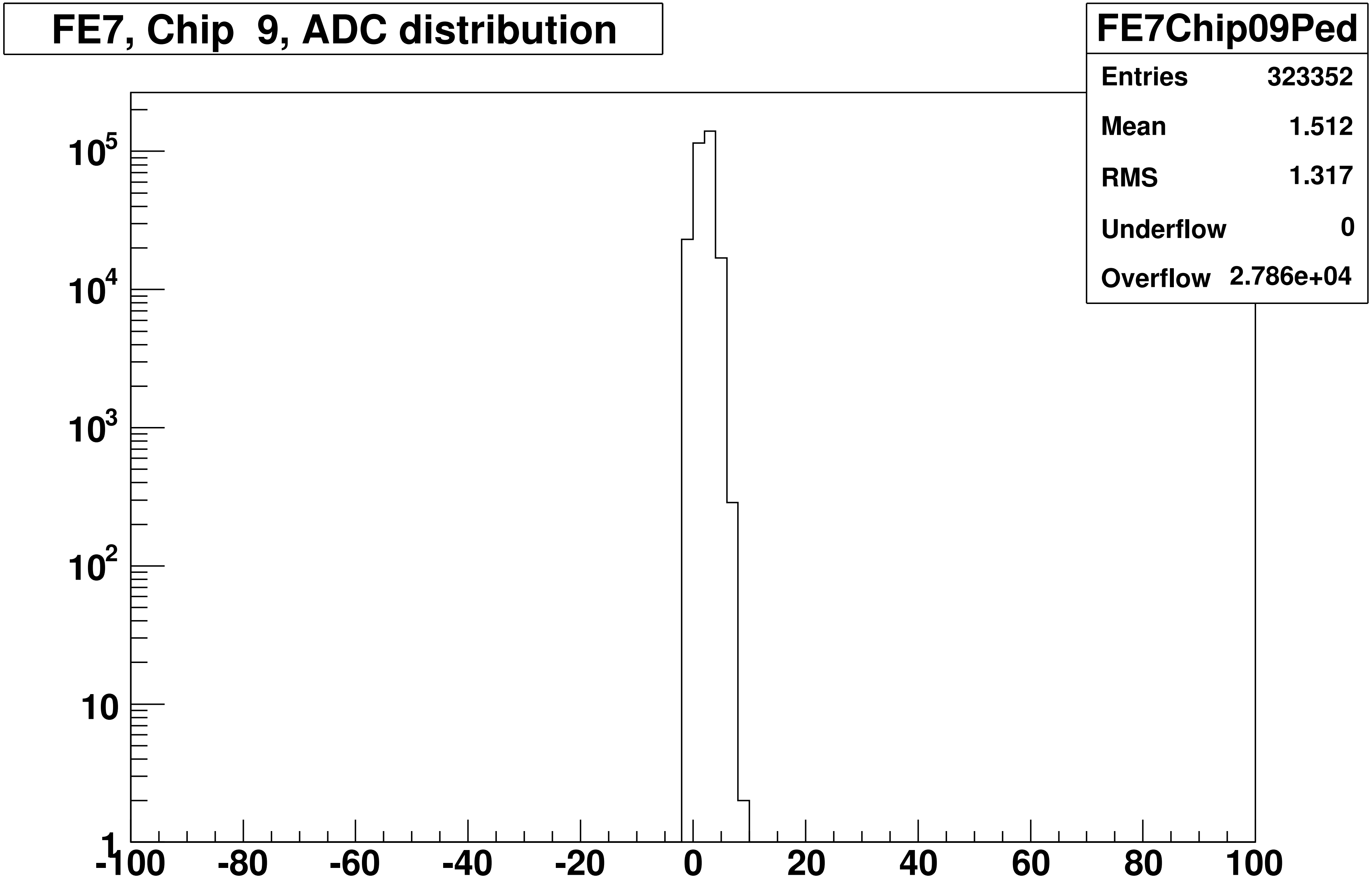


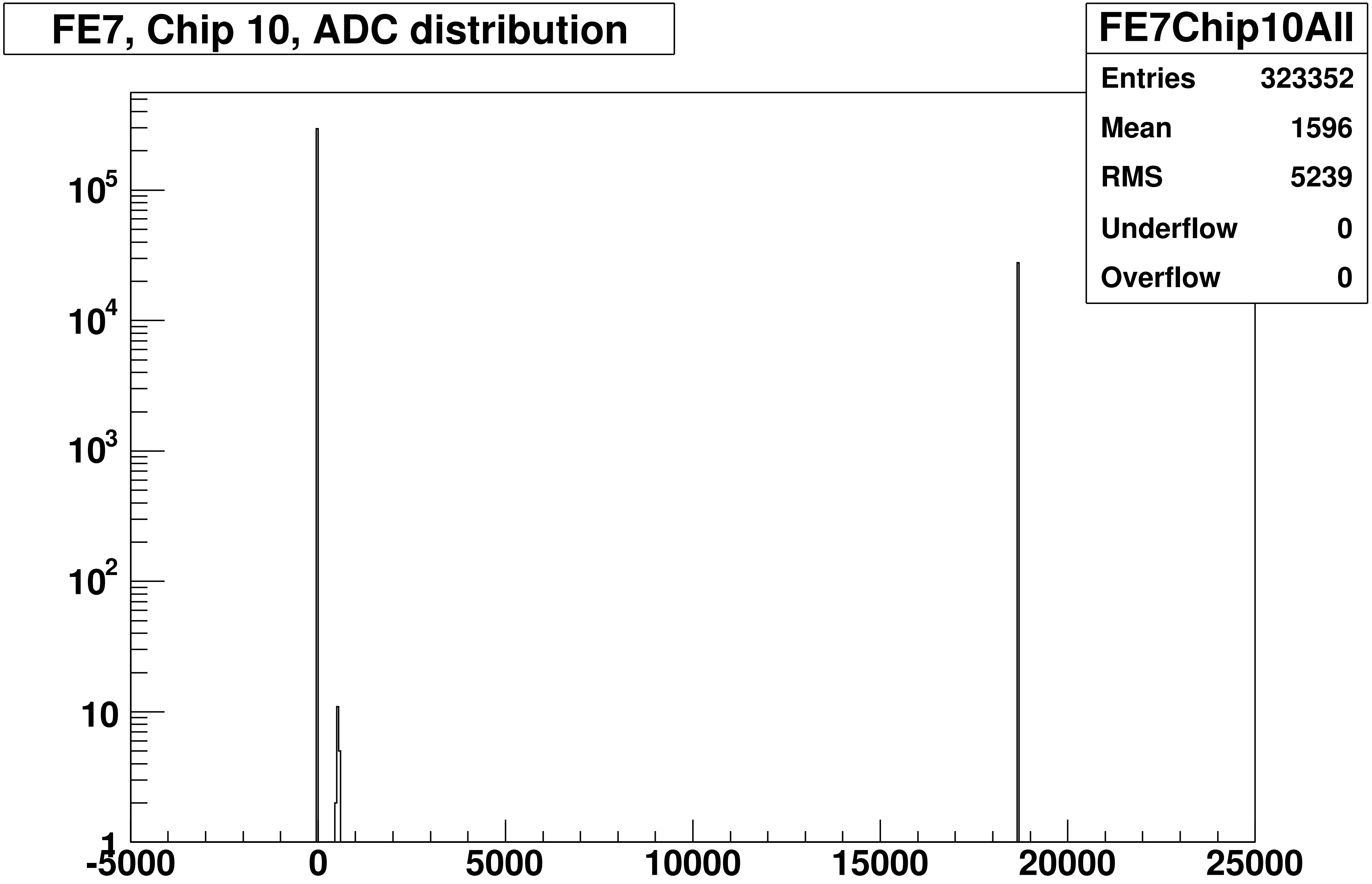
FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 323352
Mean 1600
RMS 5224
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 323352
Mean -4.135
RMS 1.295
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

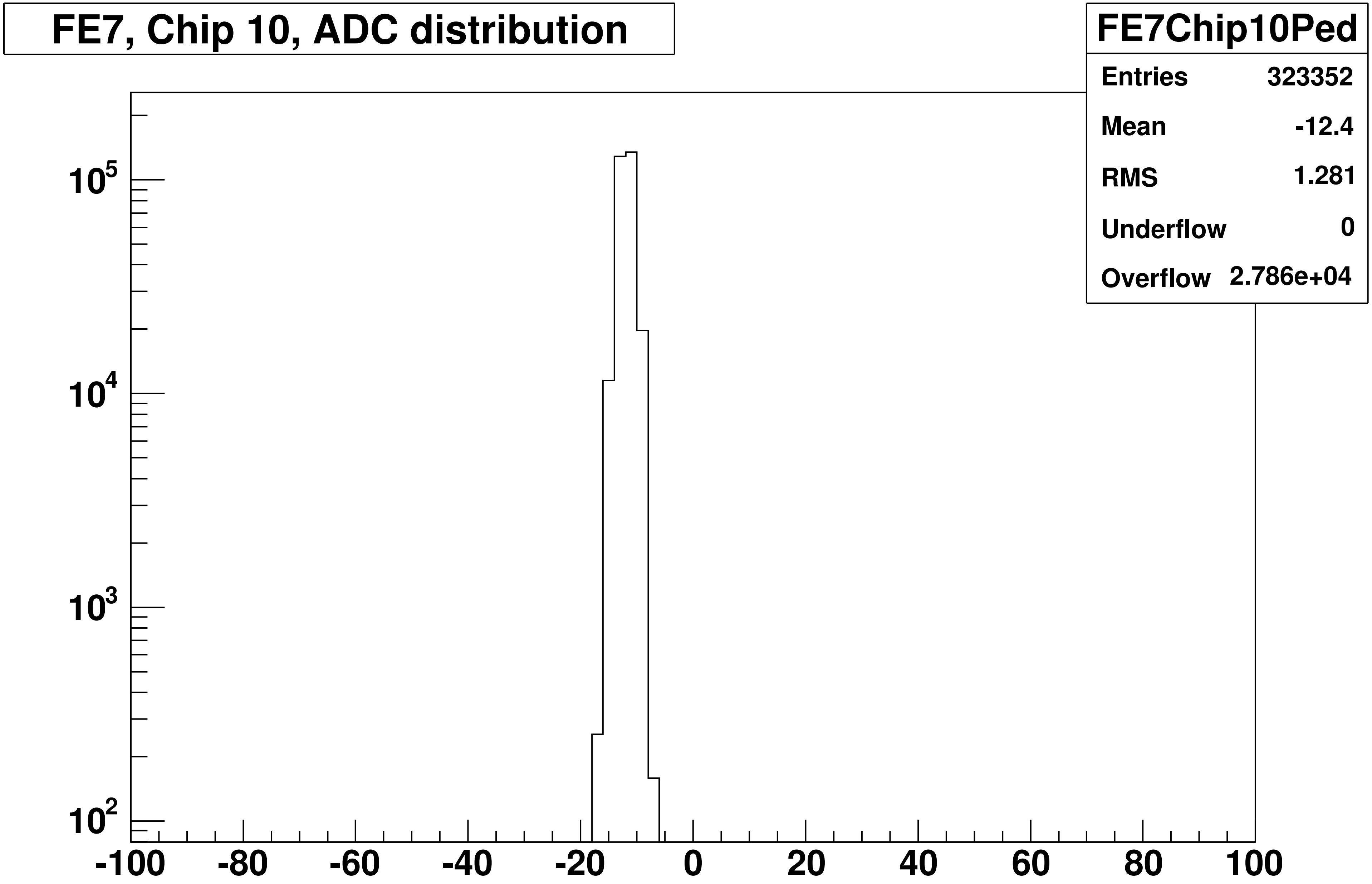


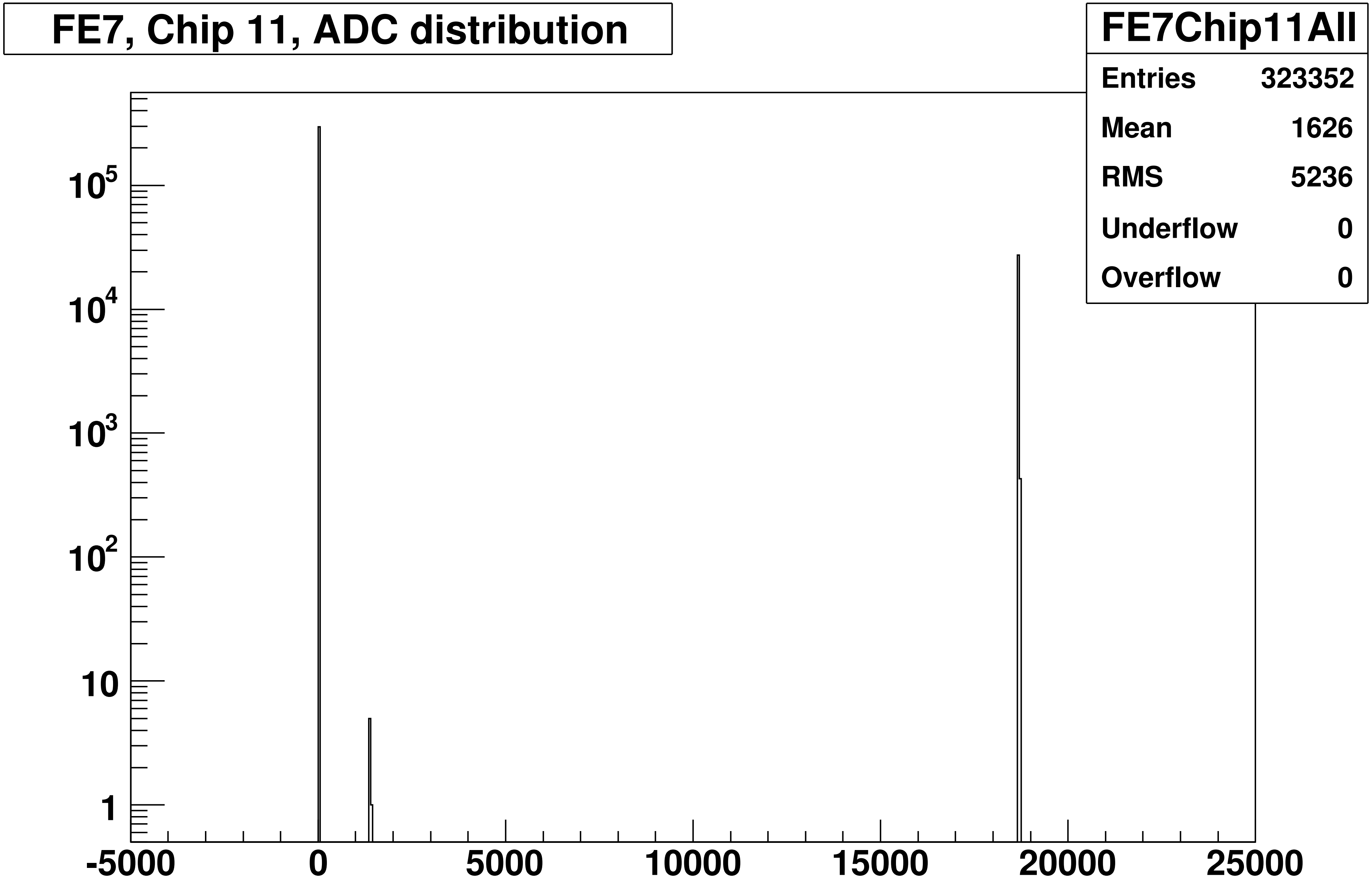
FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 323352
Mean 1609
RMS 5233
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 323352
Mean 1.512
RMS 1.317
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

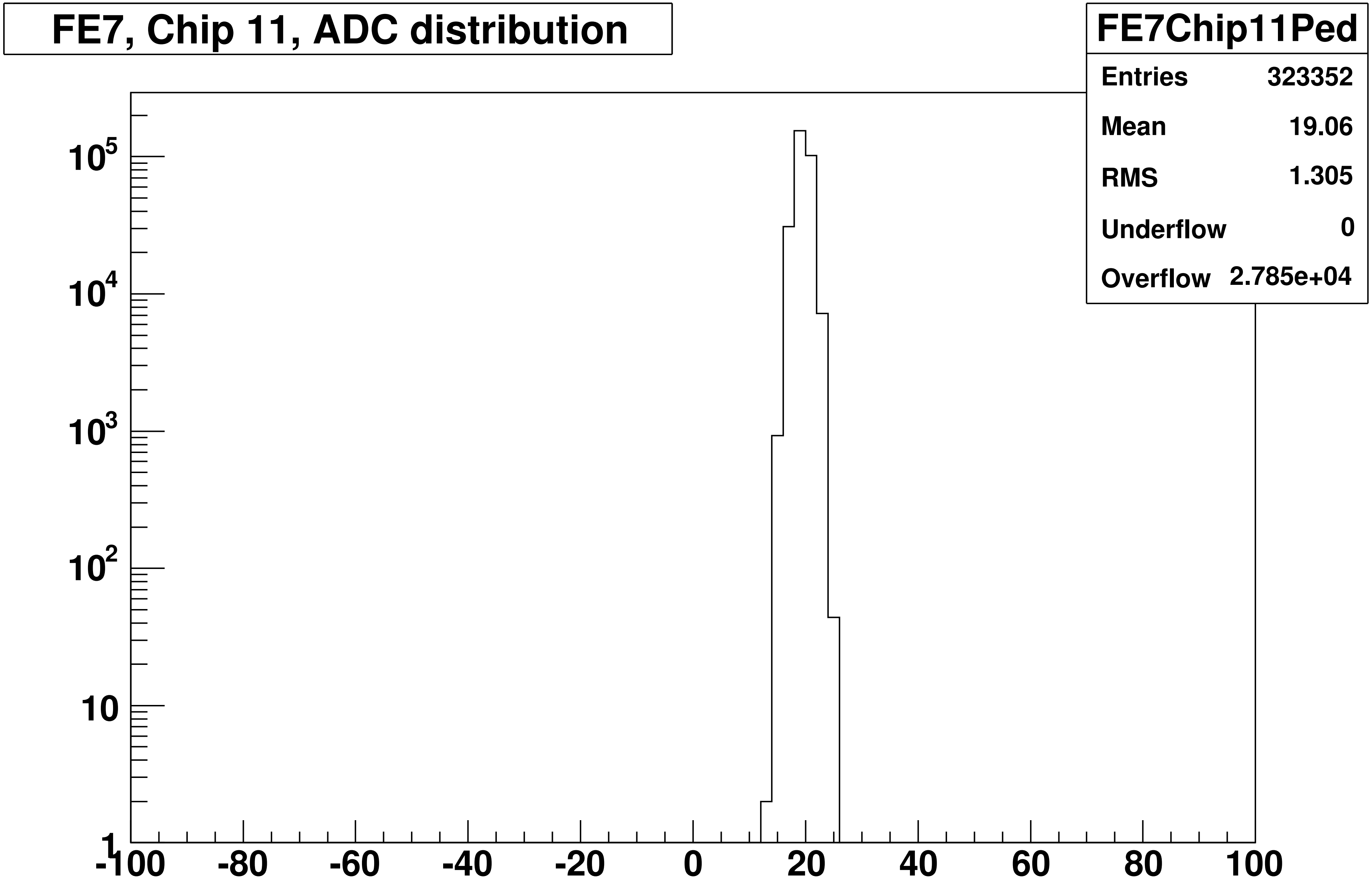


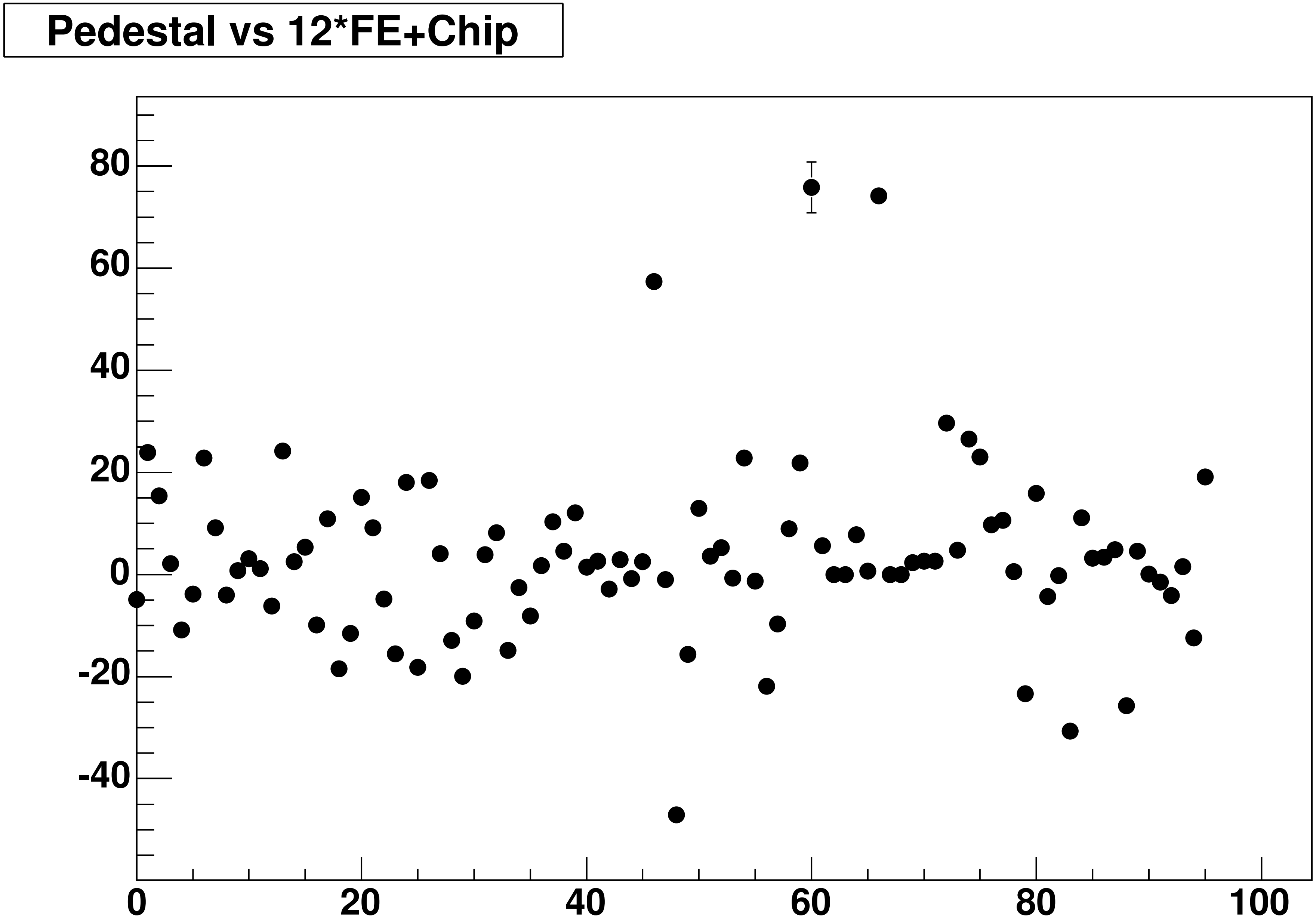
FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 323352
Mean 1596
RMS 5239
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 323352
Mean -12.4
RMS 1.281
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

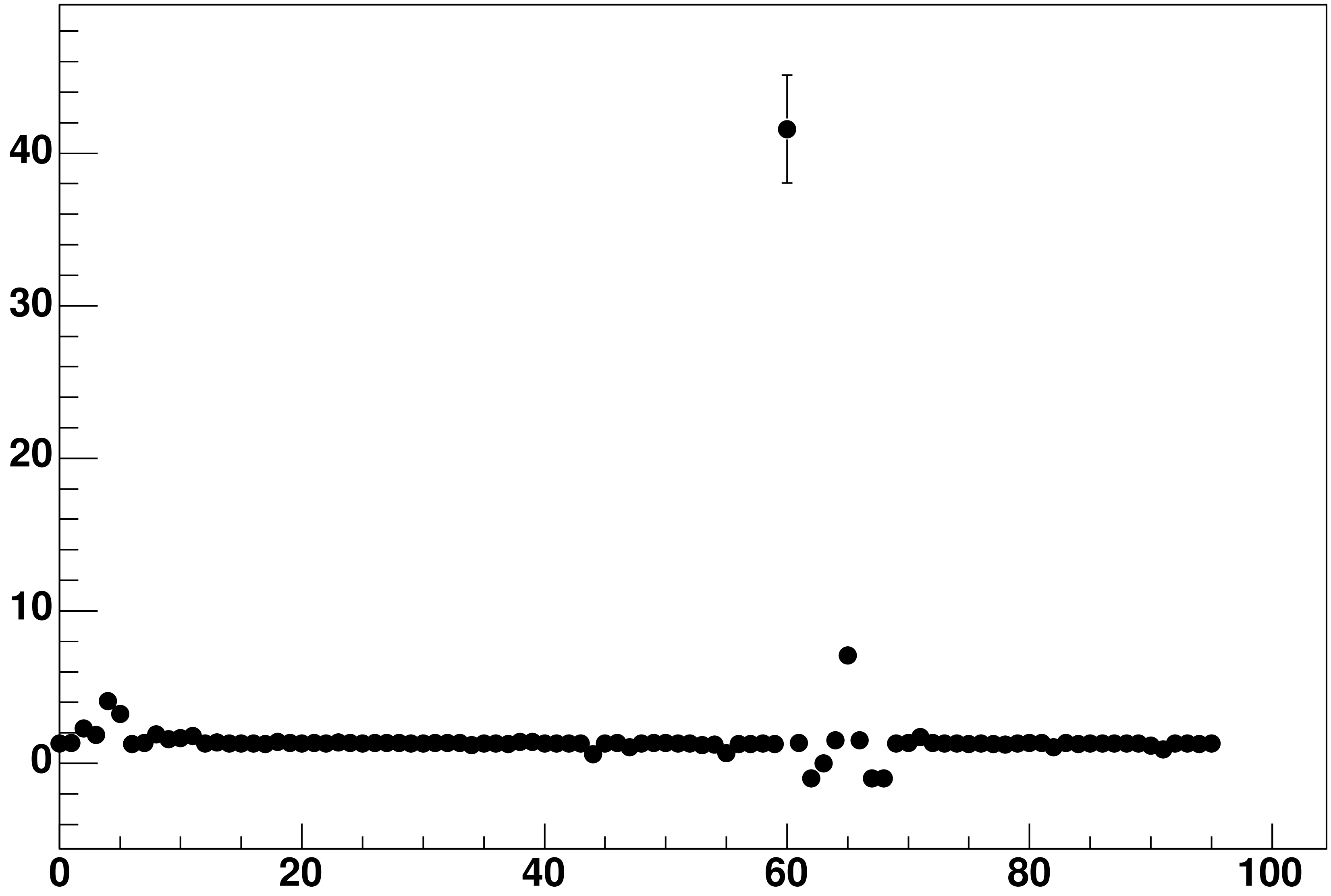


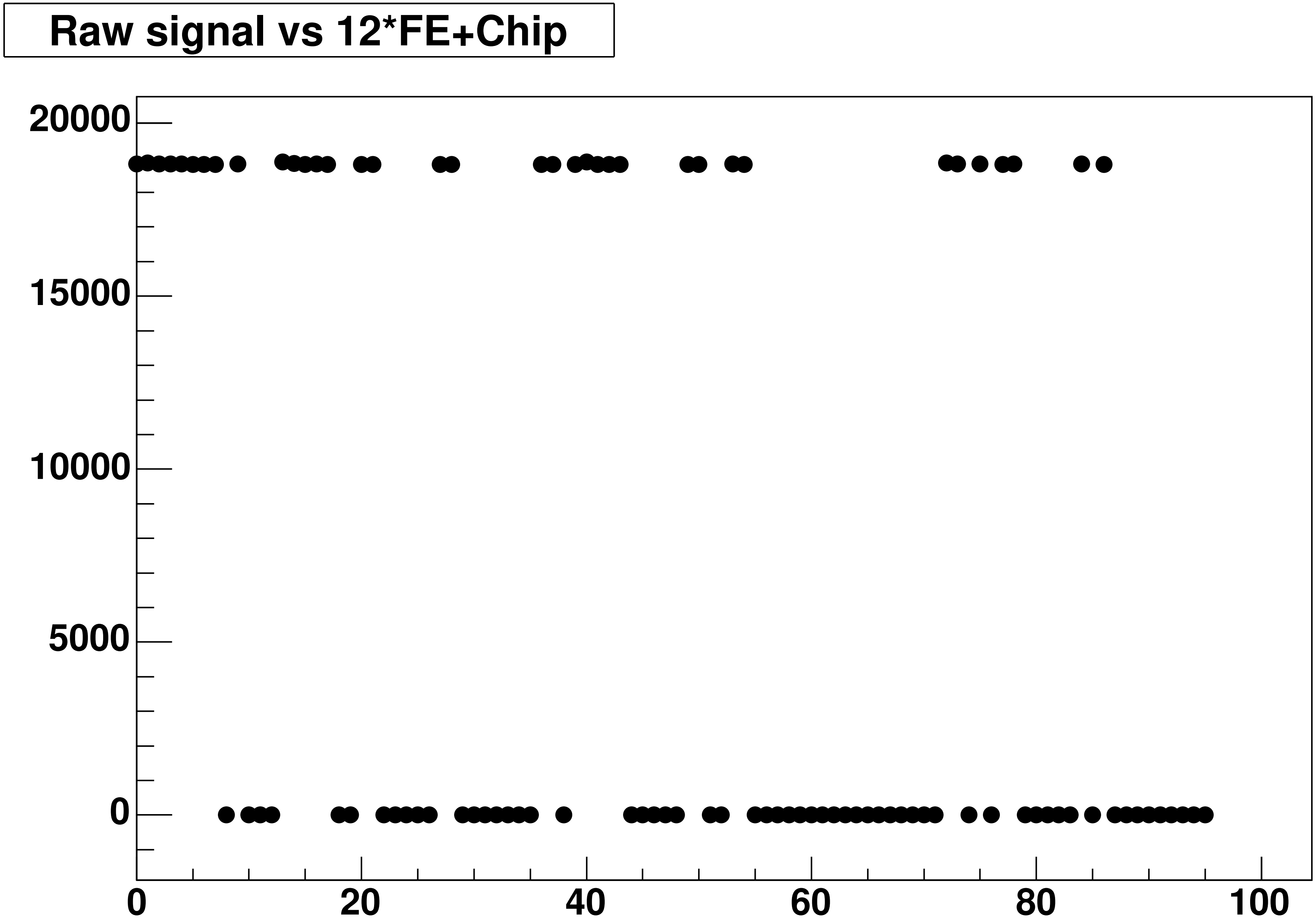
FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 323352
Mean 1626
RMS 5236
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

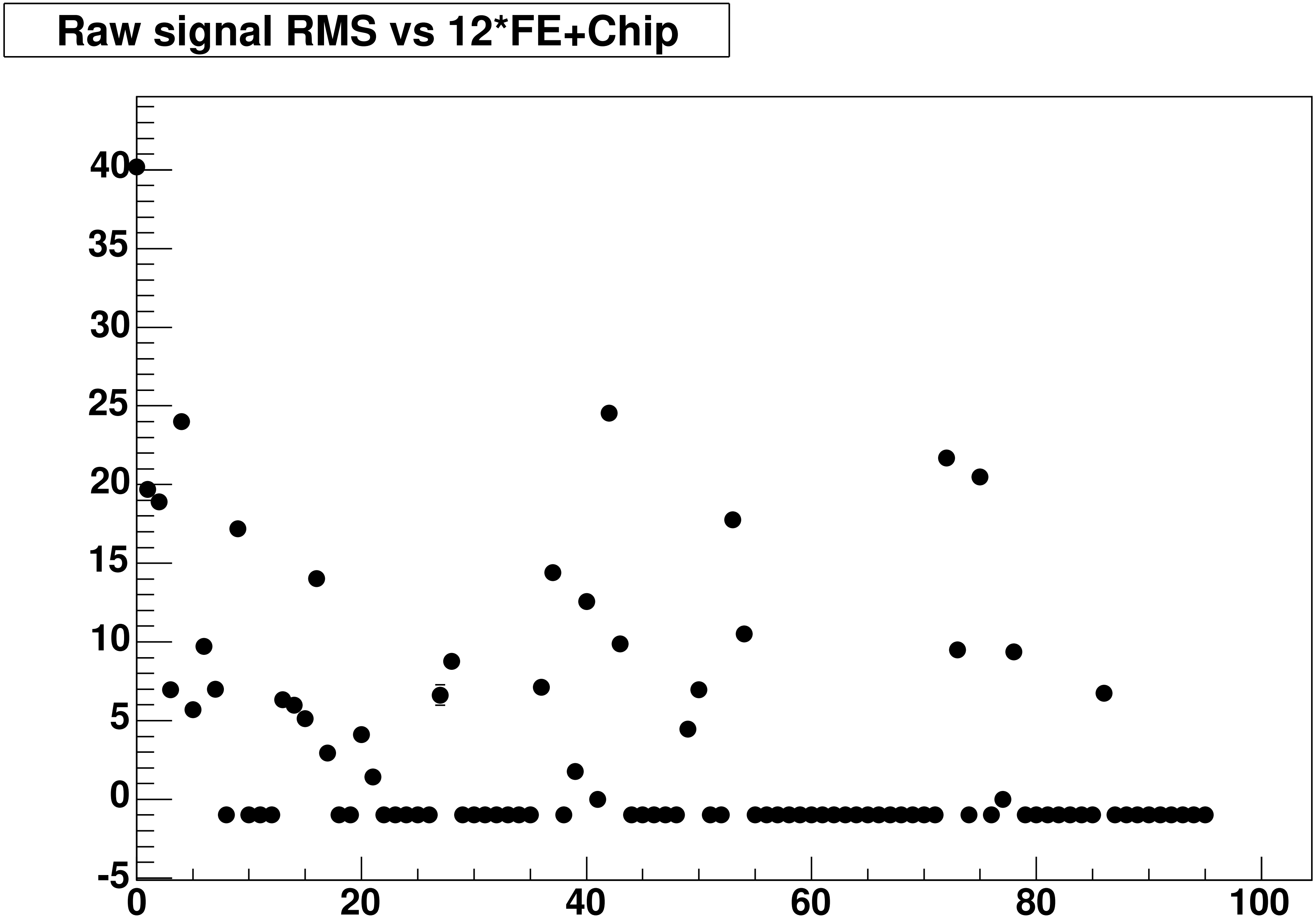

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 323352
Mean 19.06
RMS 1.305
Underflow 0
Overflow 2.785e+04
1
10
10^2
10^3
10^4
10^5
-100 -80 -60 -40 -20 0 20 40 60 80 100


Pedestal vs 12*FE+Chip
80
60
40
20
0
-20
-40
0
20
40
60
80
100

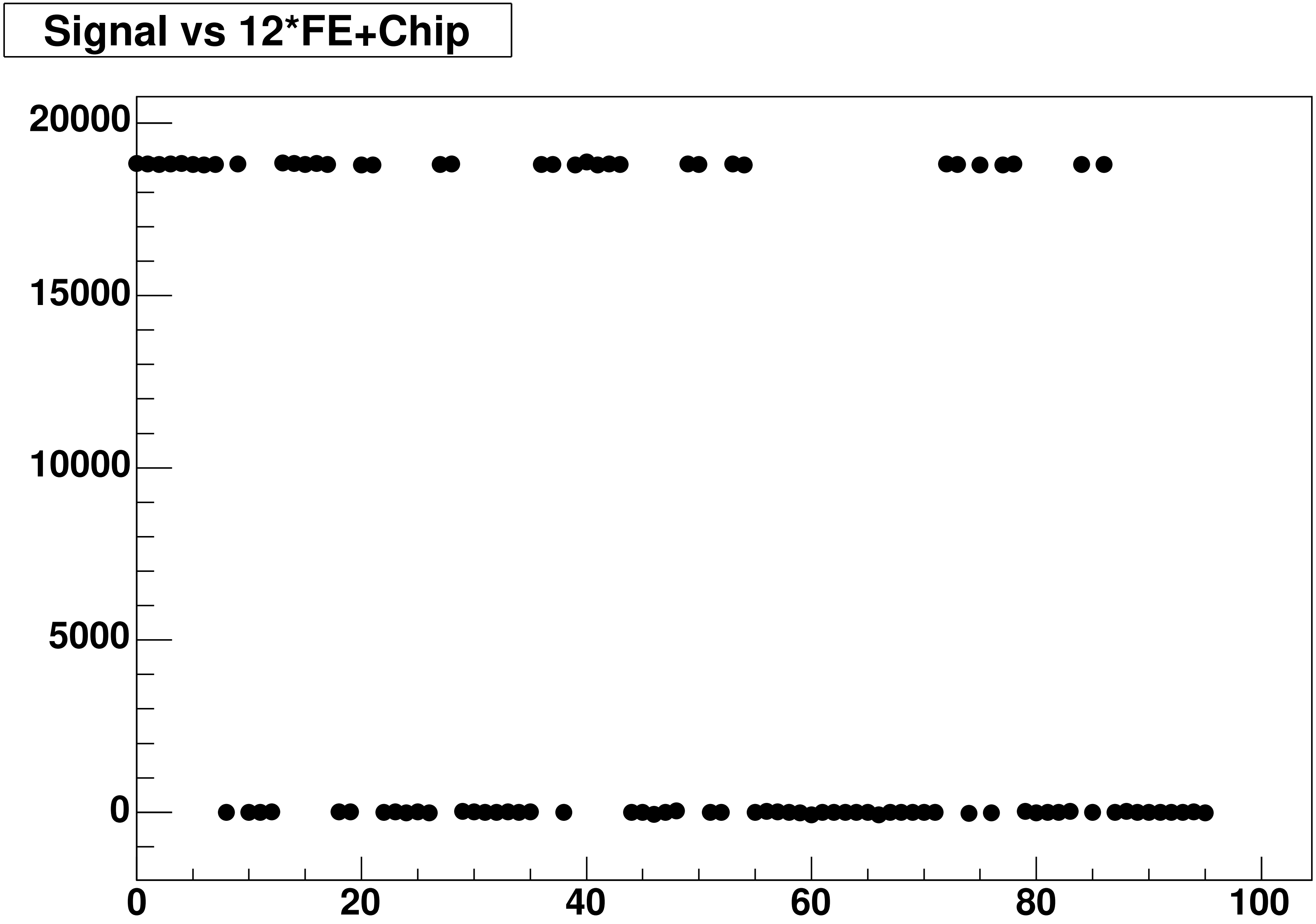
Pedestal RMS vs 12*FE+Chip



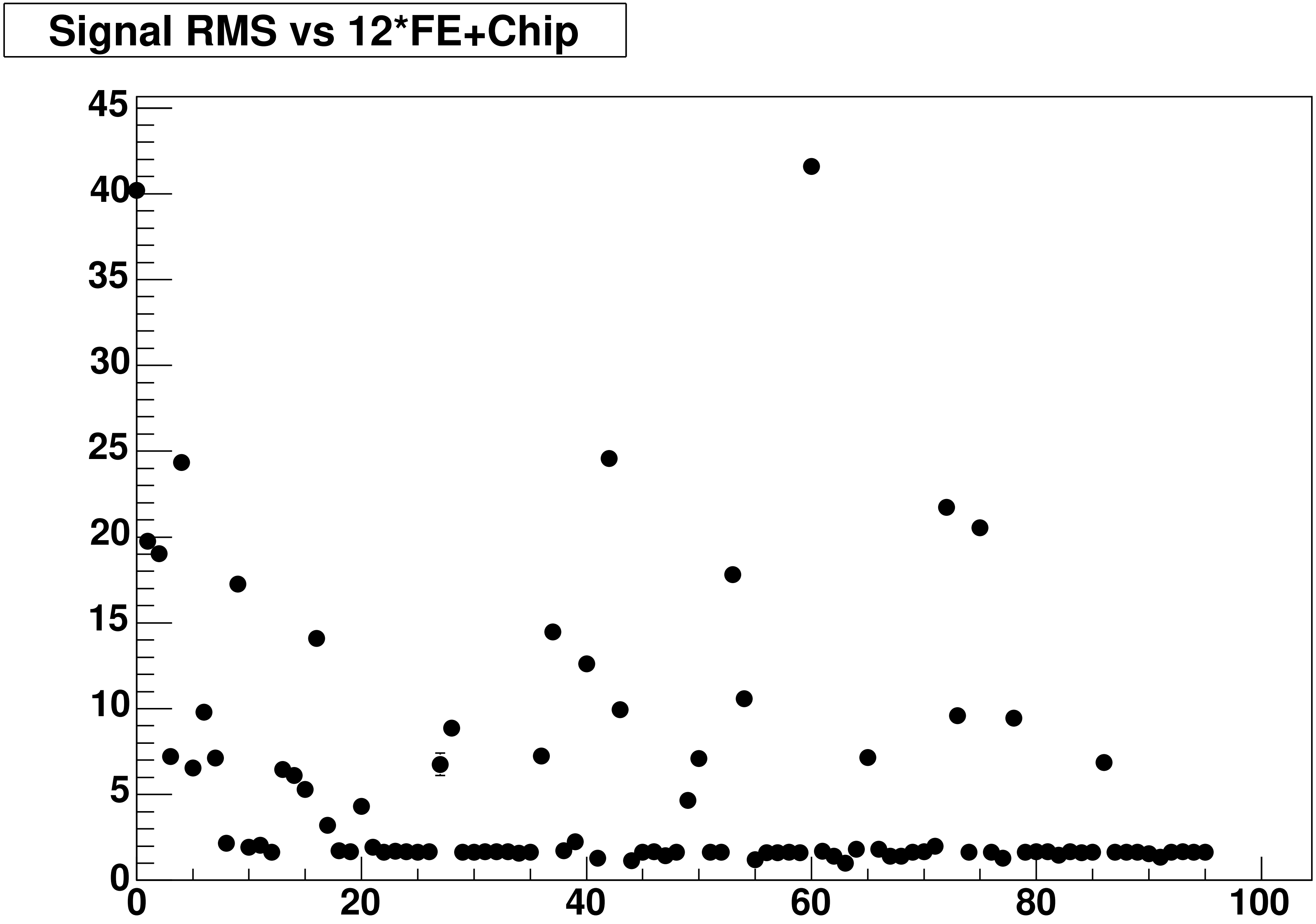

Raw signal vs 12*FE+Chip
20000
15000
10000
5000
0
0
20
40
60
80
100



Raw signal RMS vs 12*FE+Chip
40
35
30
25
20
15
10
5
0
-5
0
20
40
60
80
100



Signal vs 12*FE+Chip
20000
15000
10000
5000
0
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
45
40
35
30
25
20
15
10
5
0
0
20
40
60
80
100