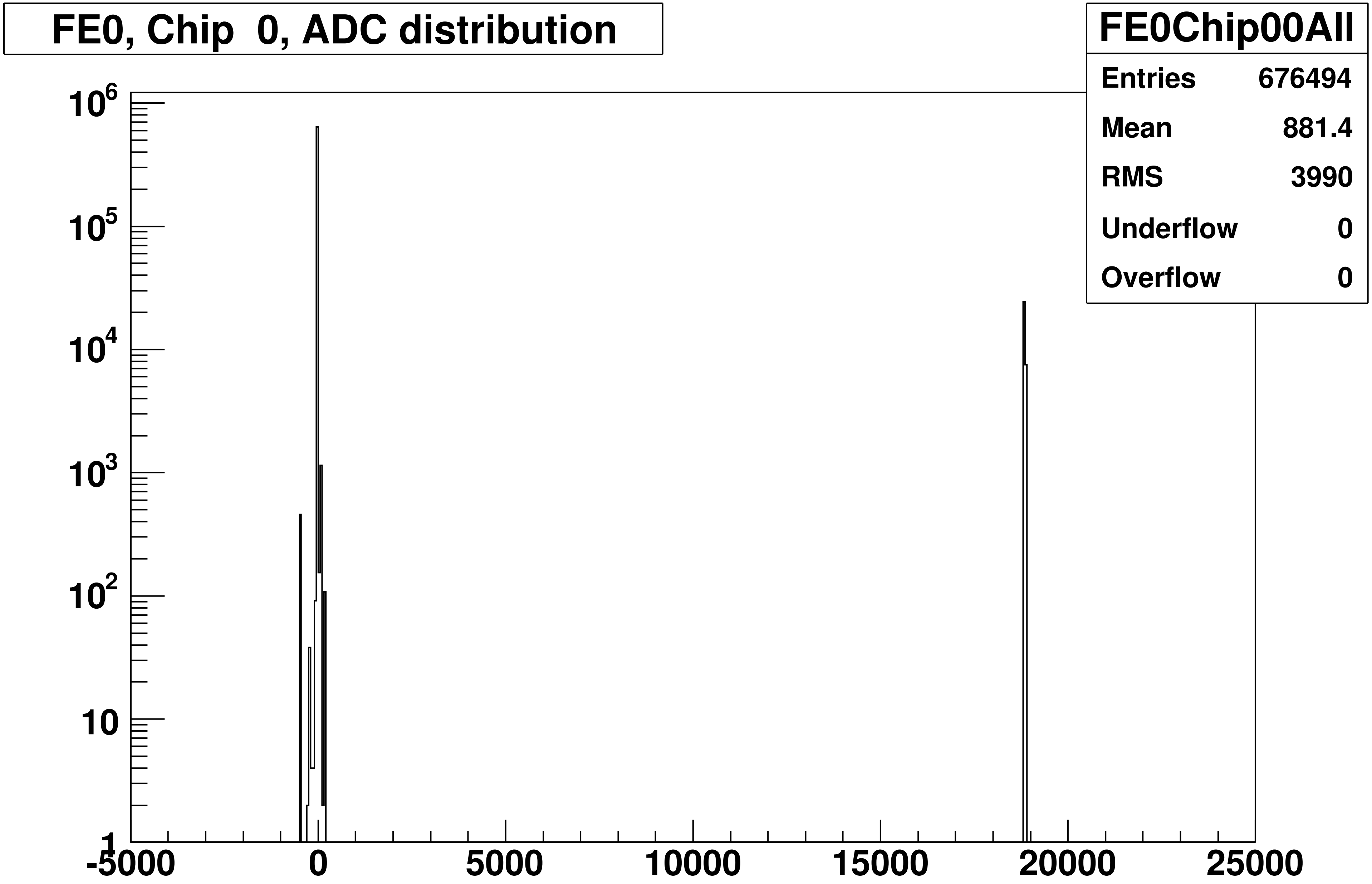

FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 676494
Mean 881.4
RMS 3990
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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10000
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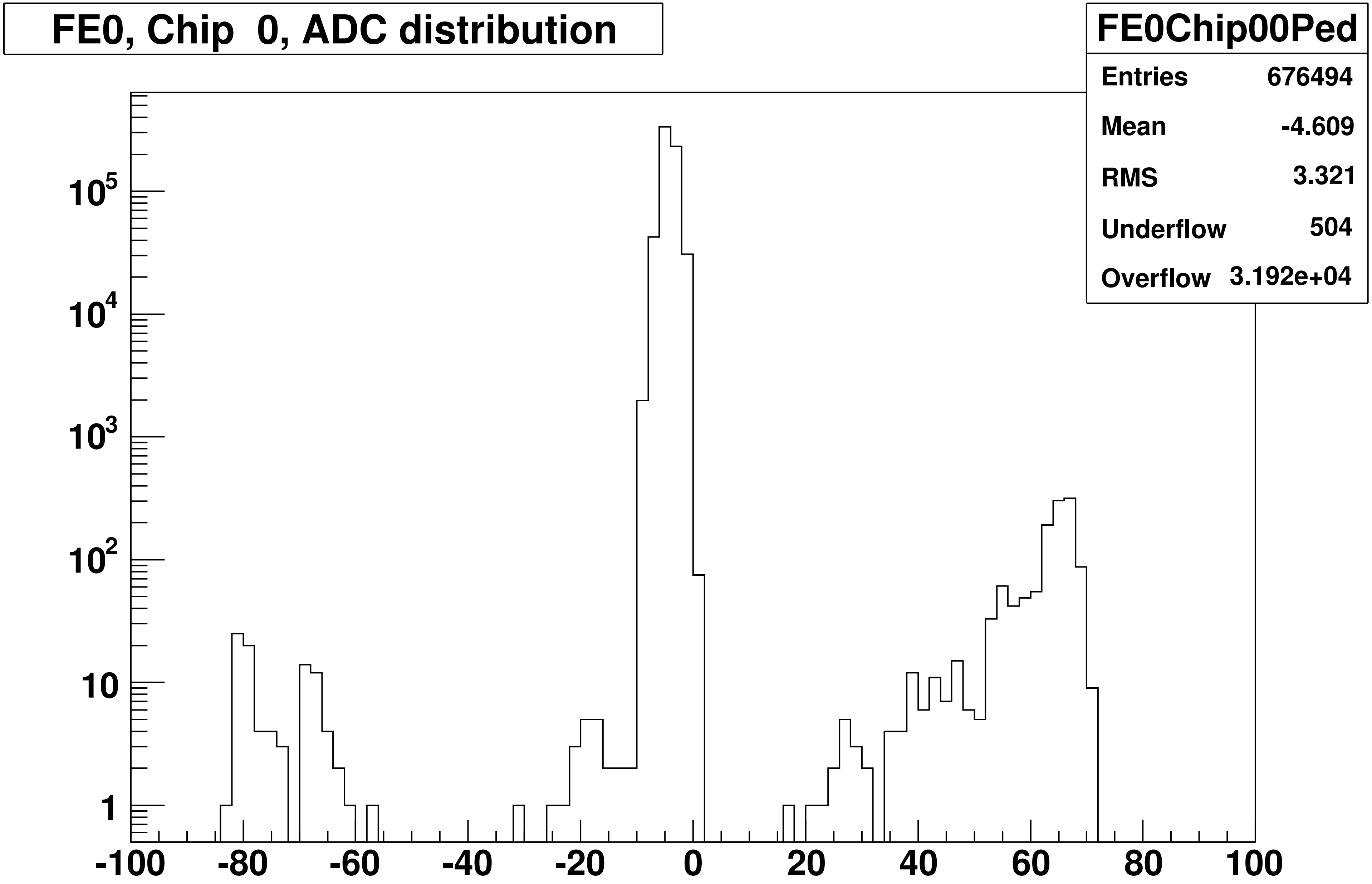
20000
25000


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 676494
Mean -4.609
RMS 3.321
Underflow 504
Overflow 3.192e+04
10^5
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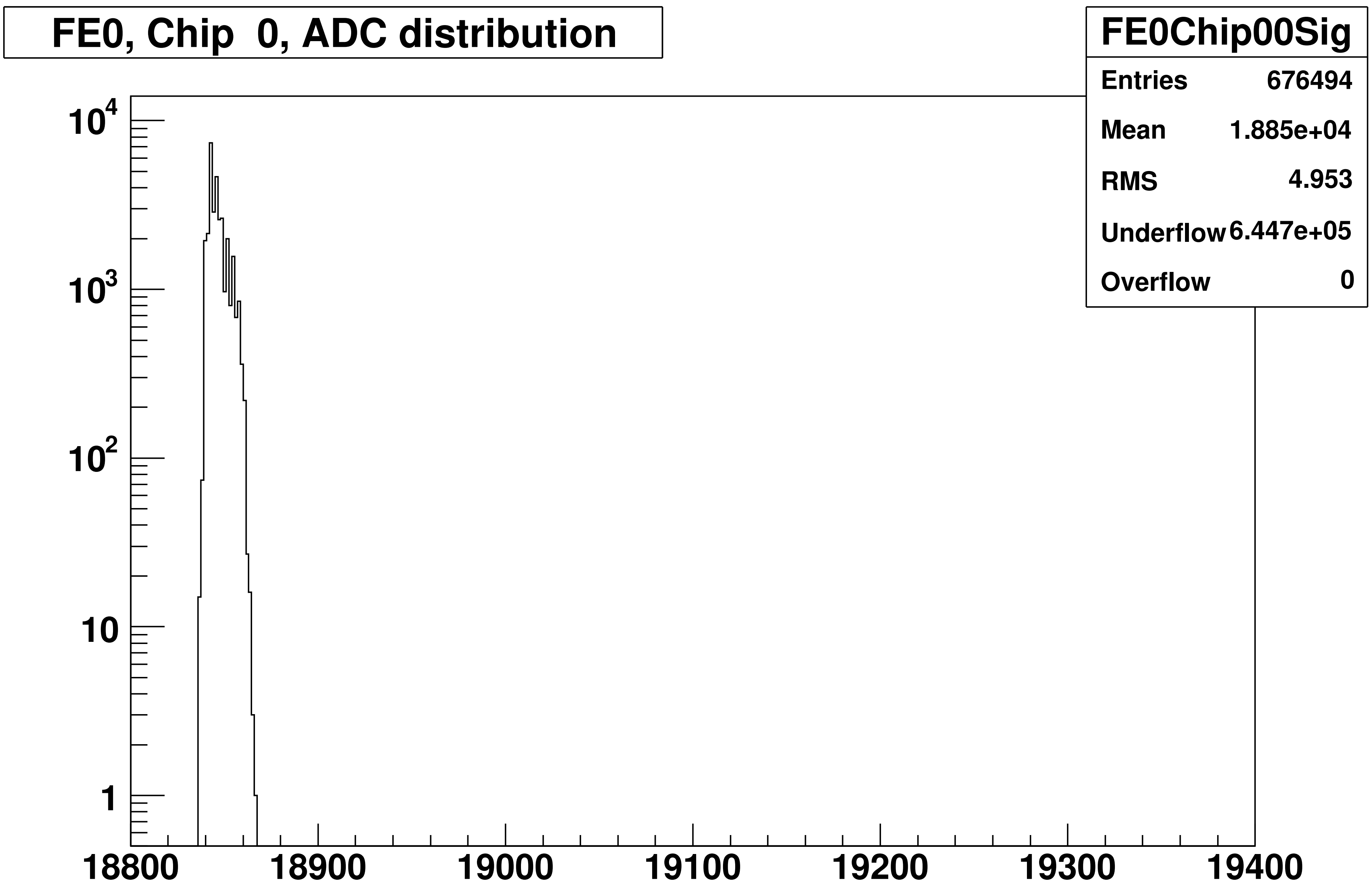

FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 676494
Mean 1.885e+04
RMS 4.953
Underflow 6.447e+05
Overflow 0
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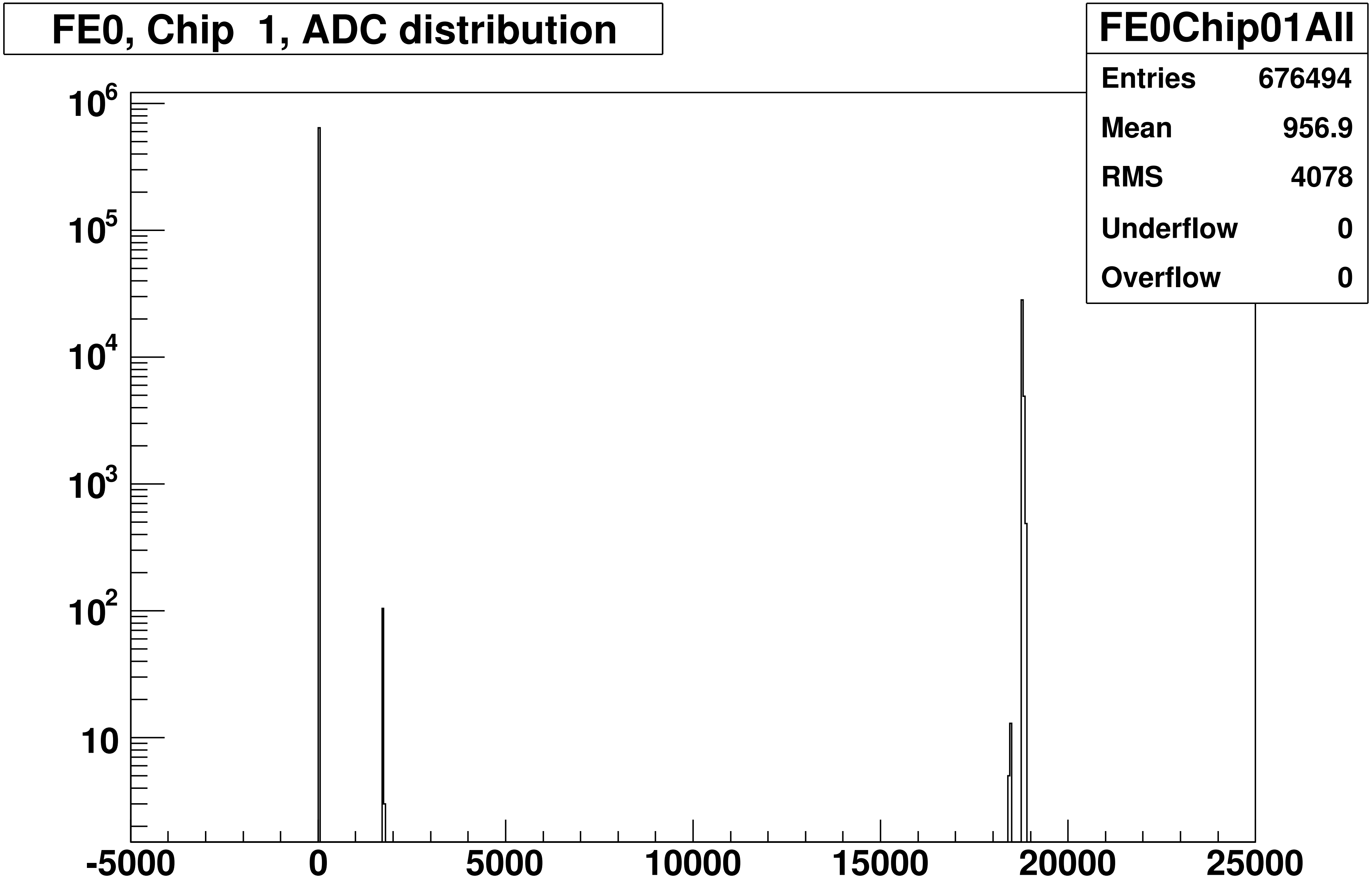
FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 676494
Mean 956.9
RMS 4078
Underflow 0
Overflow 0
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10^5
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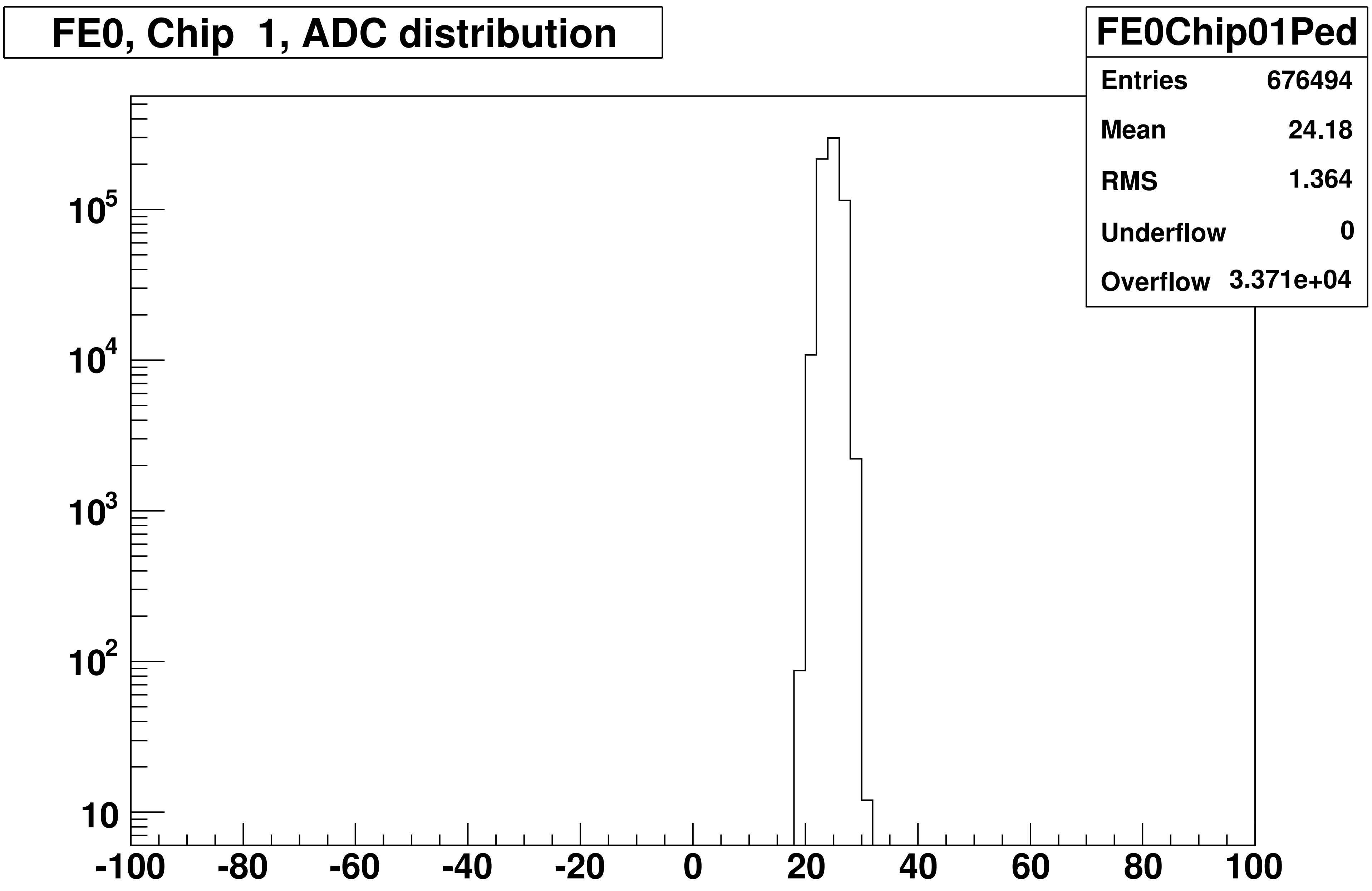
FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 676494
Mean 24.18
RMS 1.364
Underflow 0
Overflow 3.371e+04
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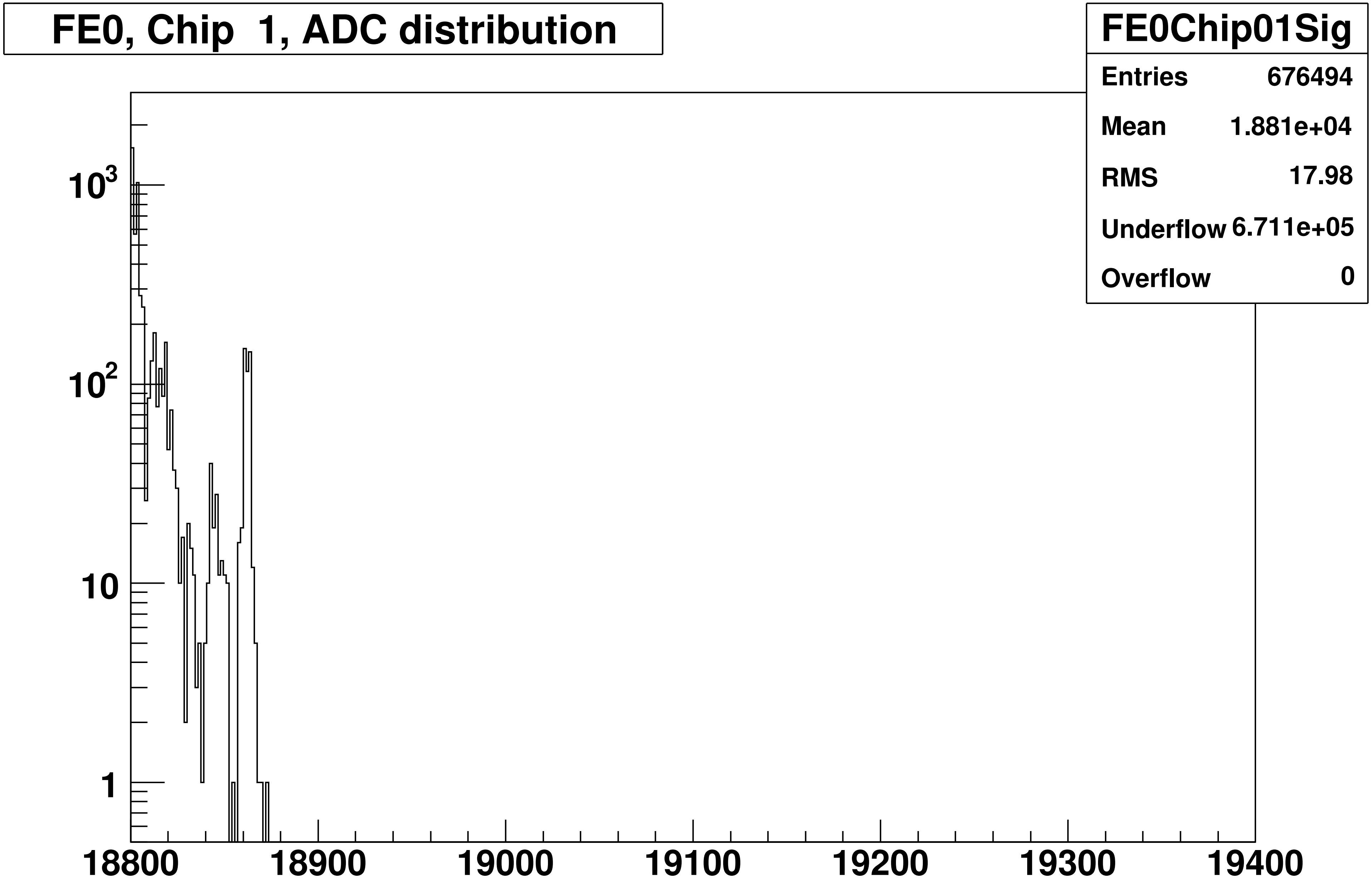
FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 676494
Mean 1.881e+04
RMS 17.98
Underflow 6.711e+05
Overflow 0
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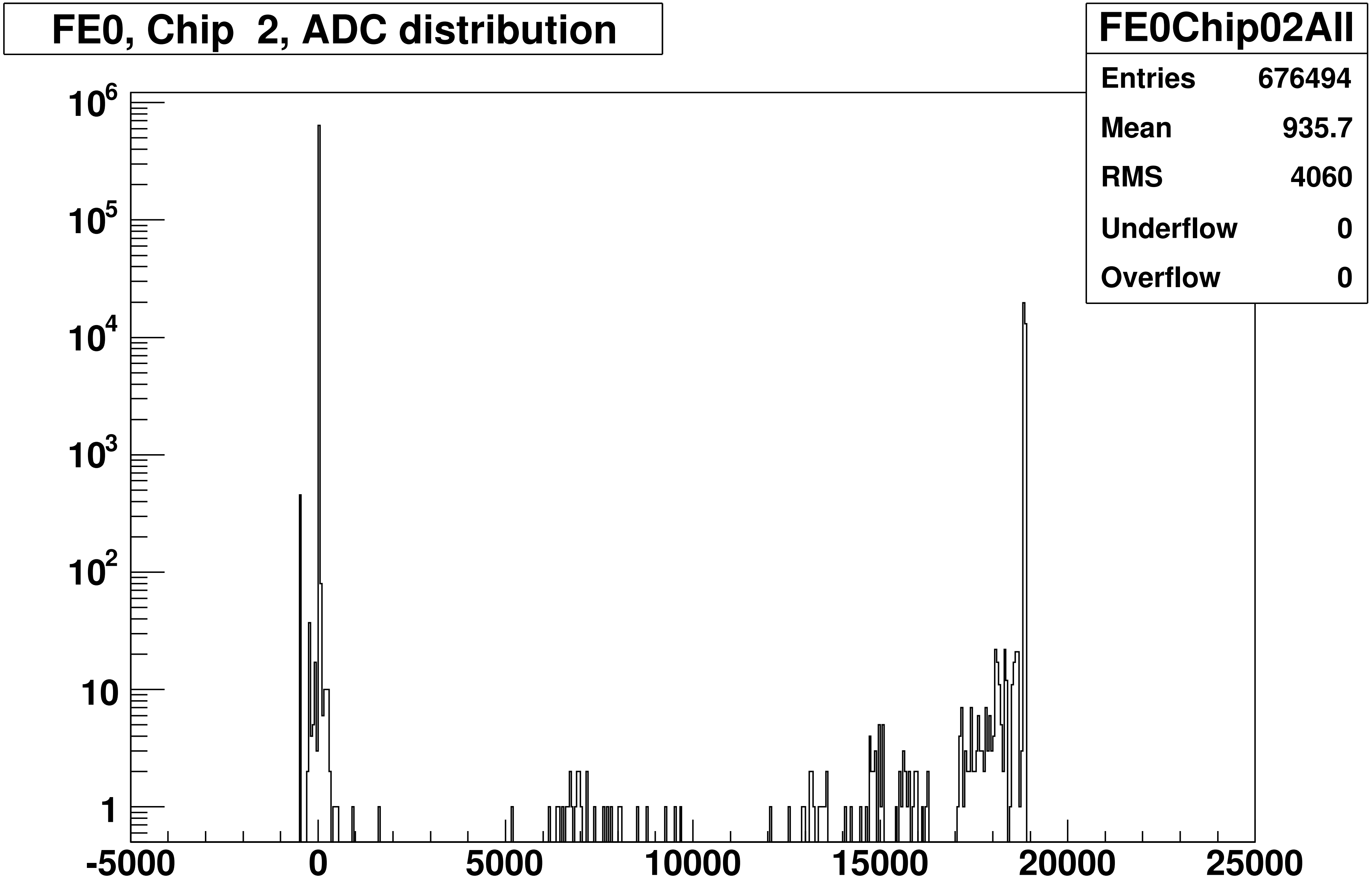
FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 676494
Mean 935.7
RMS 4060
Underflow 0
Overflow 0
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10^4
10^3
10^2
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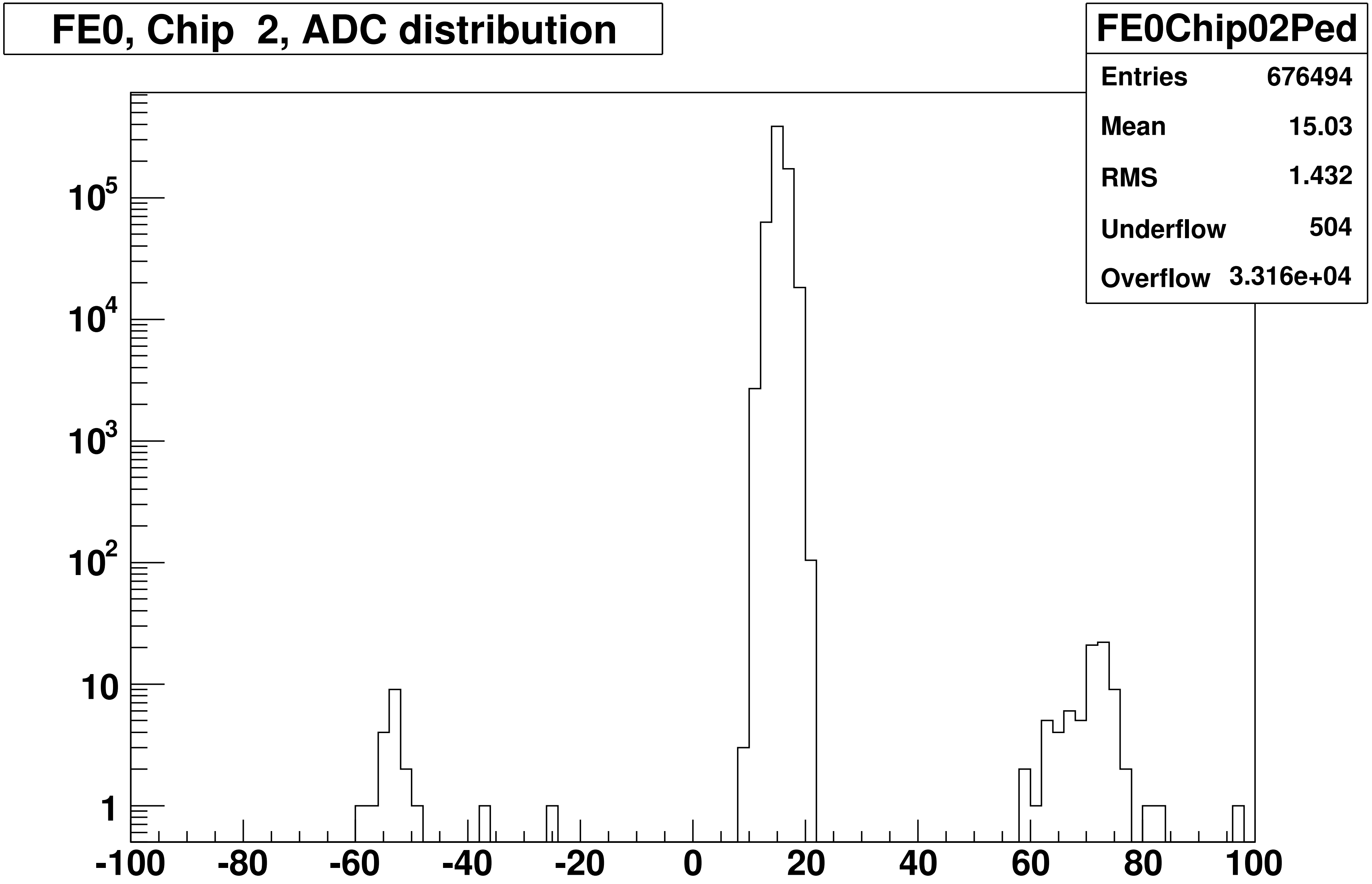

FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 676494
Mean 15.03
RMS 1.432
Underflow 504
Overflow 3.316e+04
10^5
10^4
10^3
10^2
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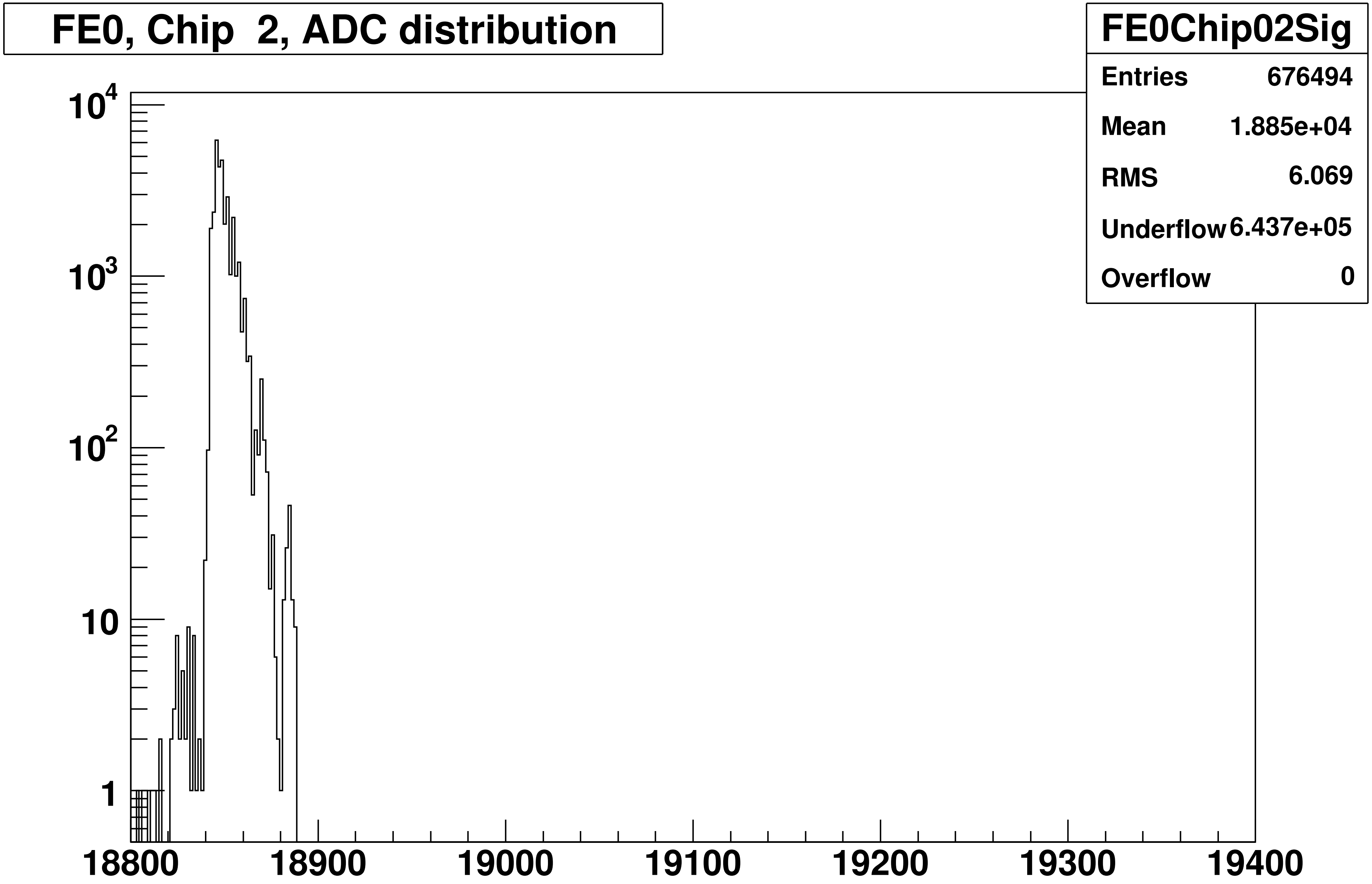
FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 676494
Mean 1.885e+04
RMS 6.069
Underflow 6.437e+05
Overflow 0
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10^3
10^2
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18900
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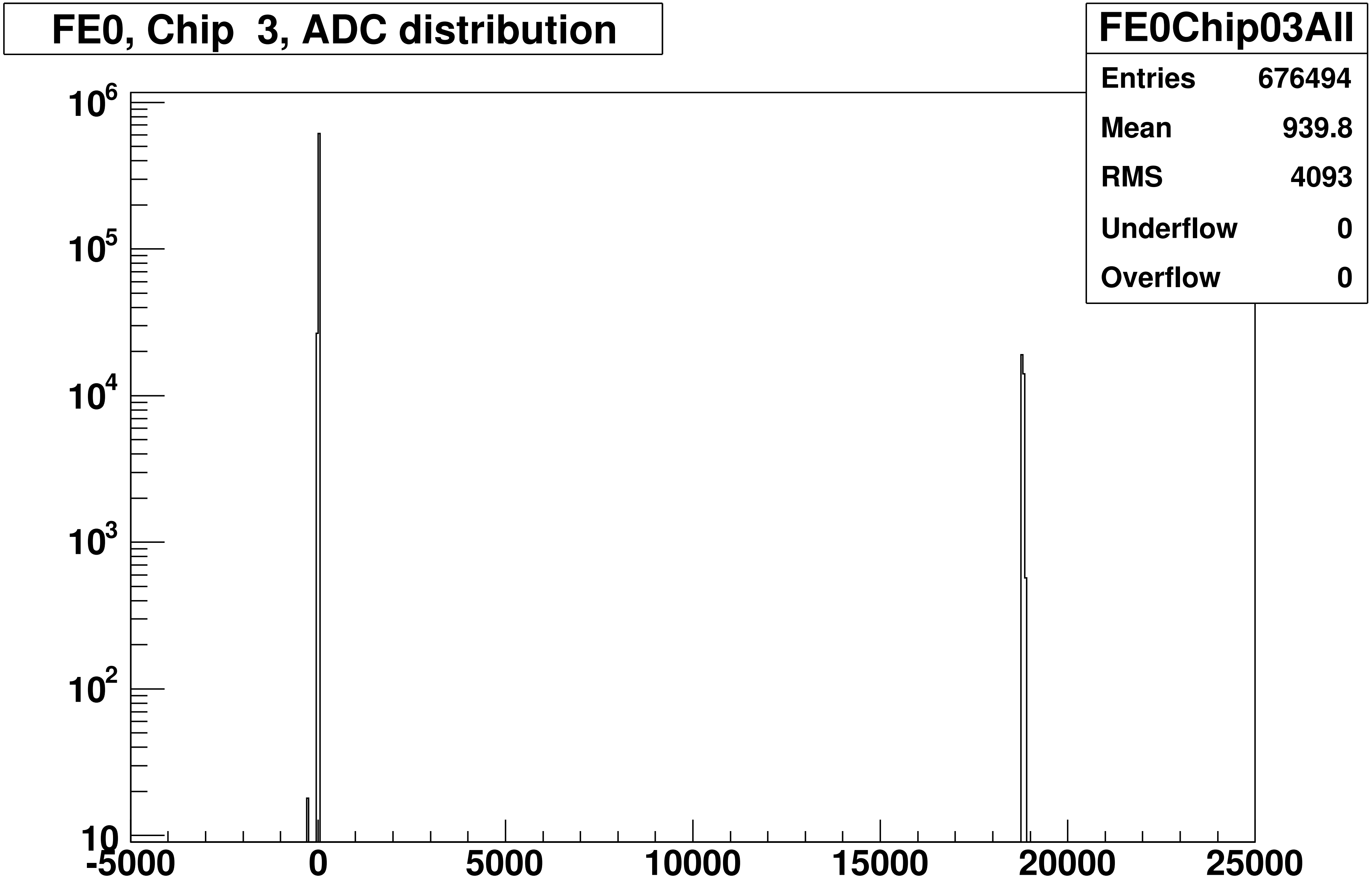

FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 676494
Mean 939.8
RMS 4093
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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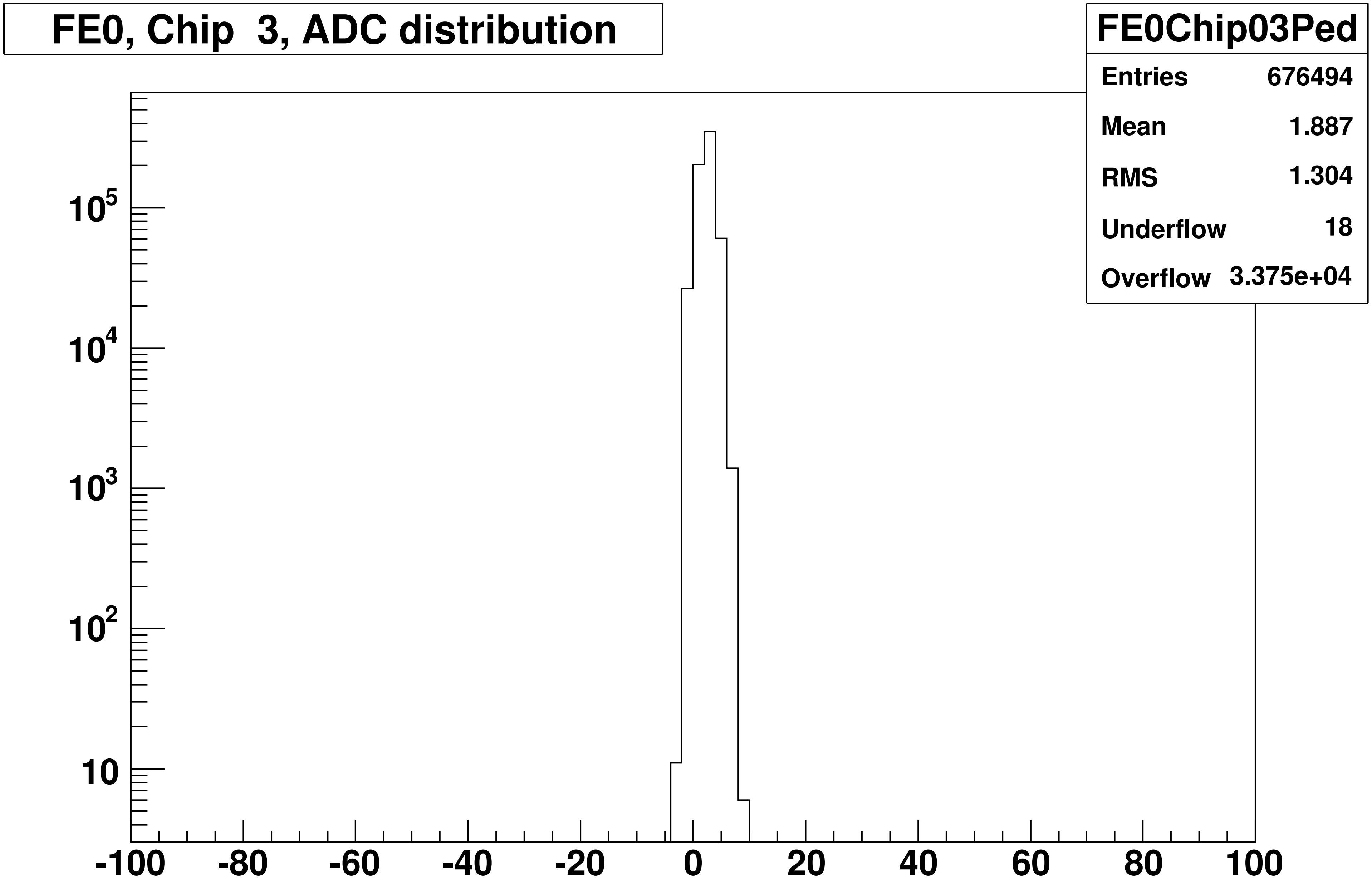
FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 676494
Mean 1.887
RMS 1.304
Underflow 18
Overflow 3.375e+04
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10^3
10^2
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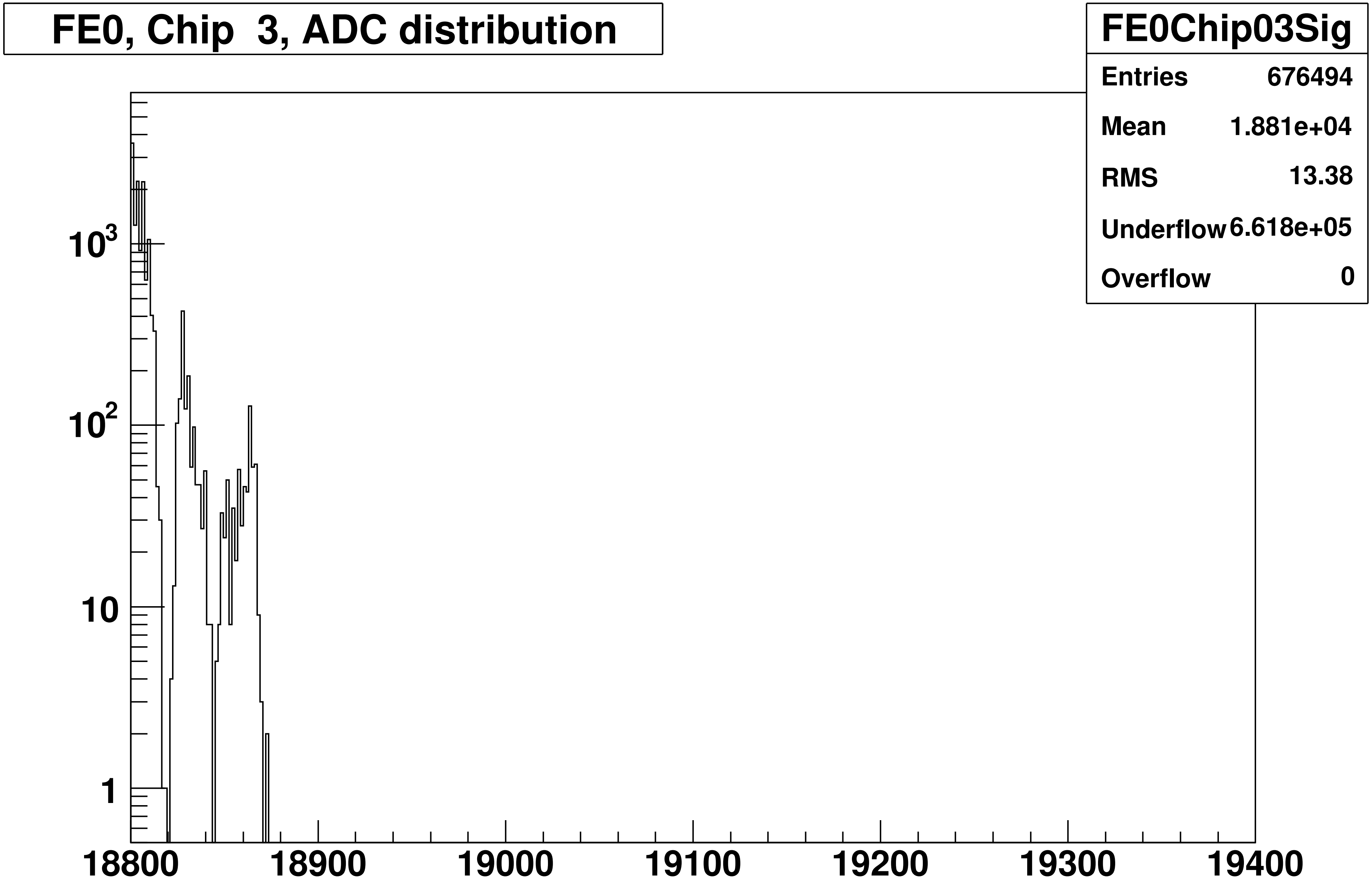
FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 676494
Mean 1.881e+04
RMS 13.38
Underflow 6.618e+05
Overflow 0
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10^2
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18900
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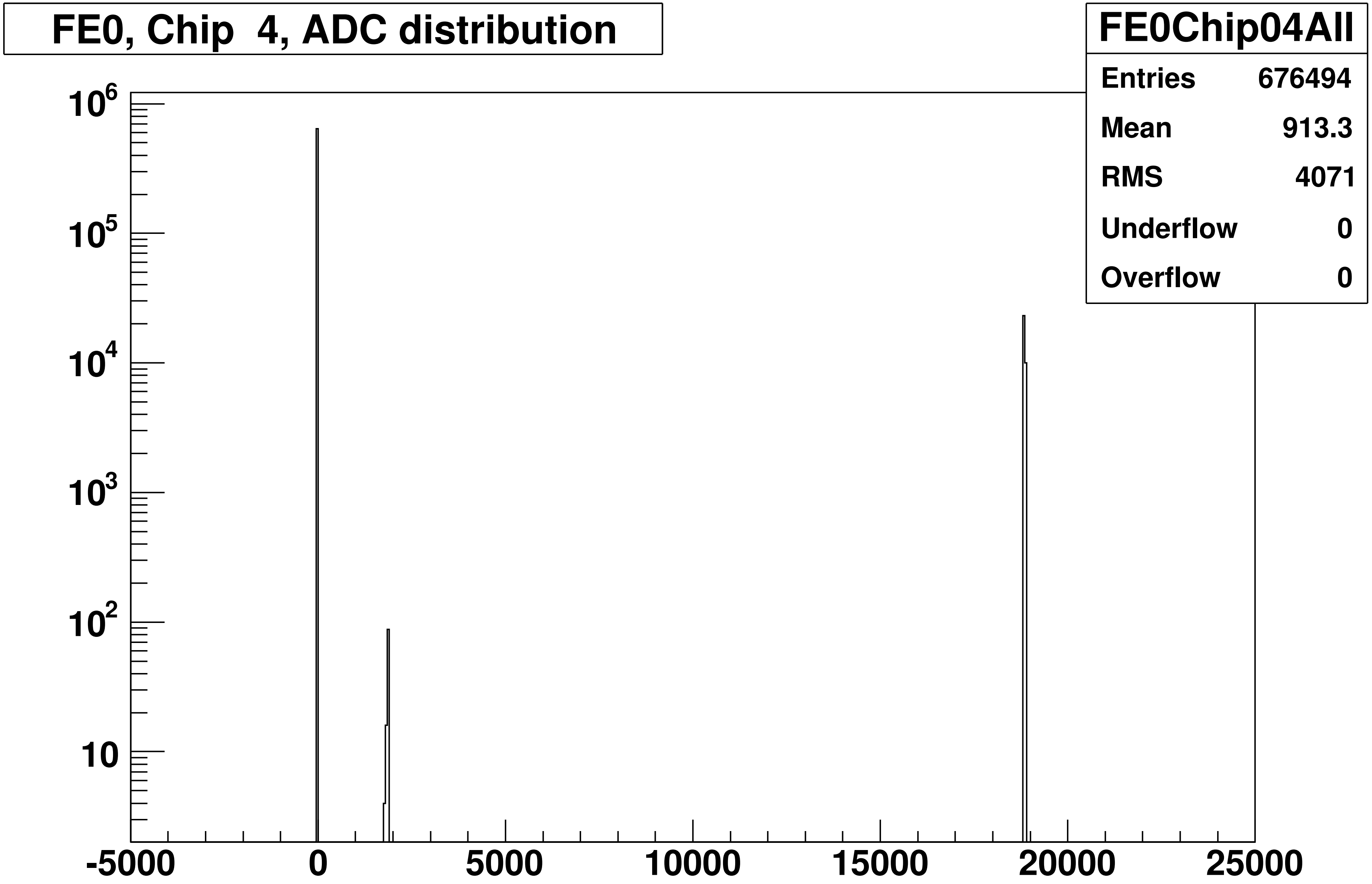

FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 676494
Mean 913.3
RMS 4071
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
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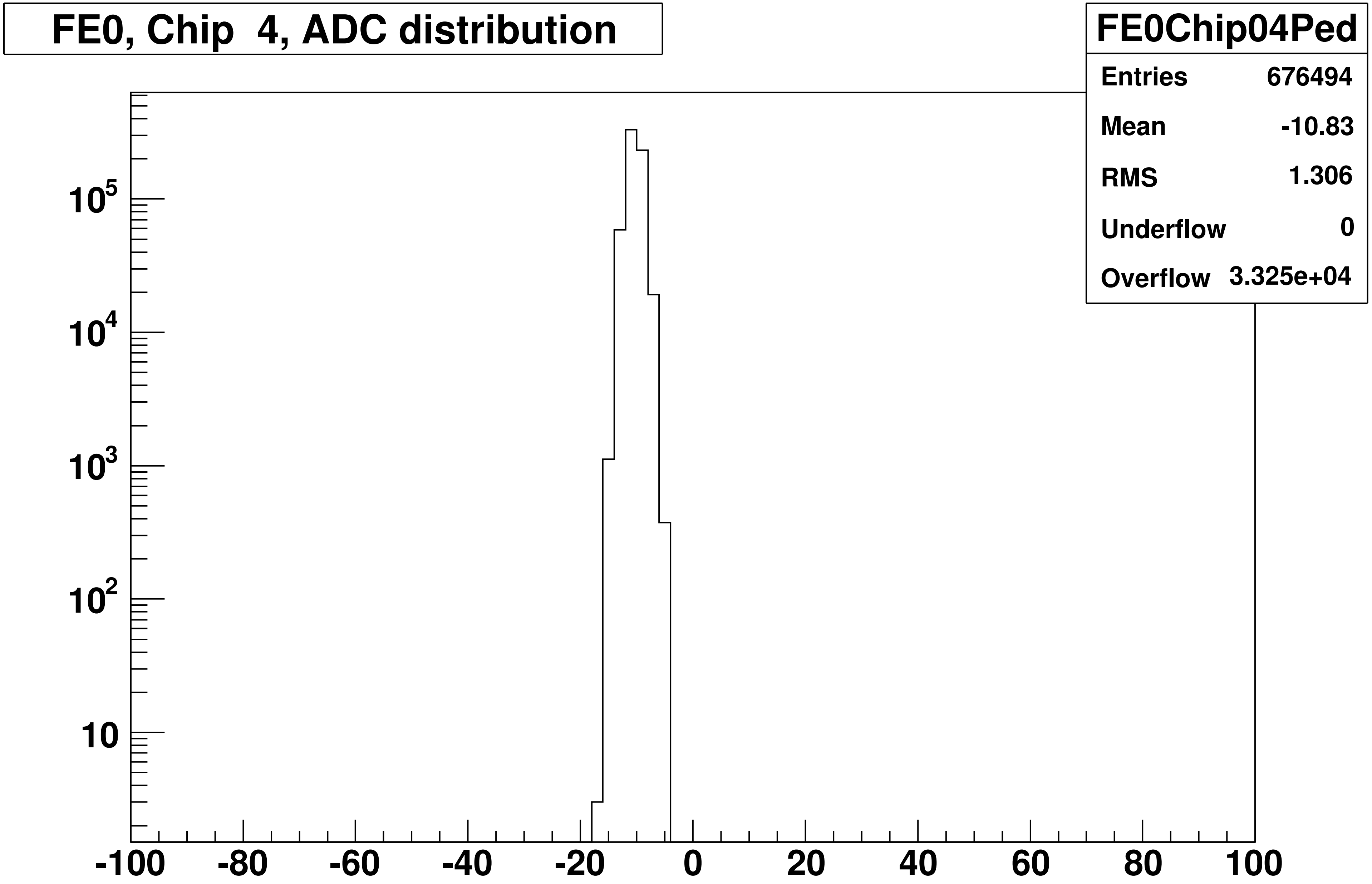
20000
25000


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 676494
Mean -10.83
RMS 1.306
Underflow 0
Overflow 3.325e+04
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10^4
10^3
10^2
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-100
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80
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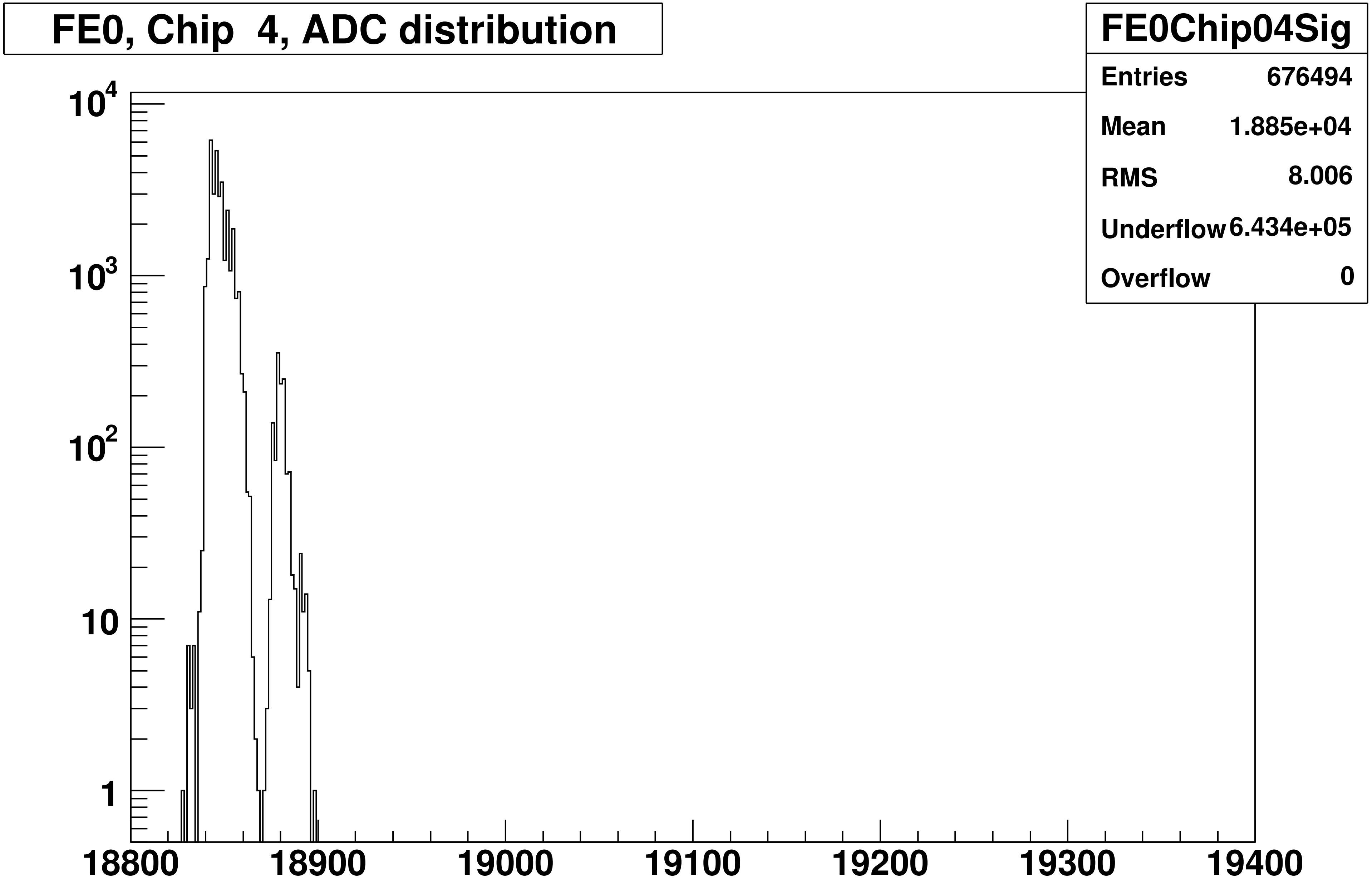
FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 676494
Mean 1.885e+04
RMS 8.006
Underflow 6.434e+05
Overflow 0
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10^3
10^2
10
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18900
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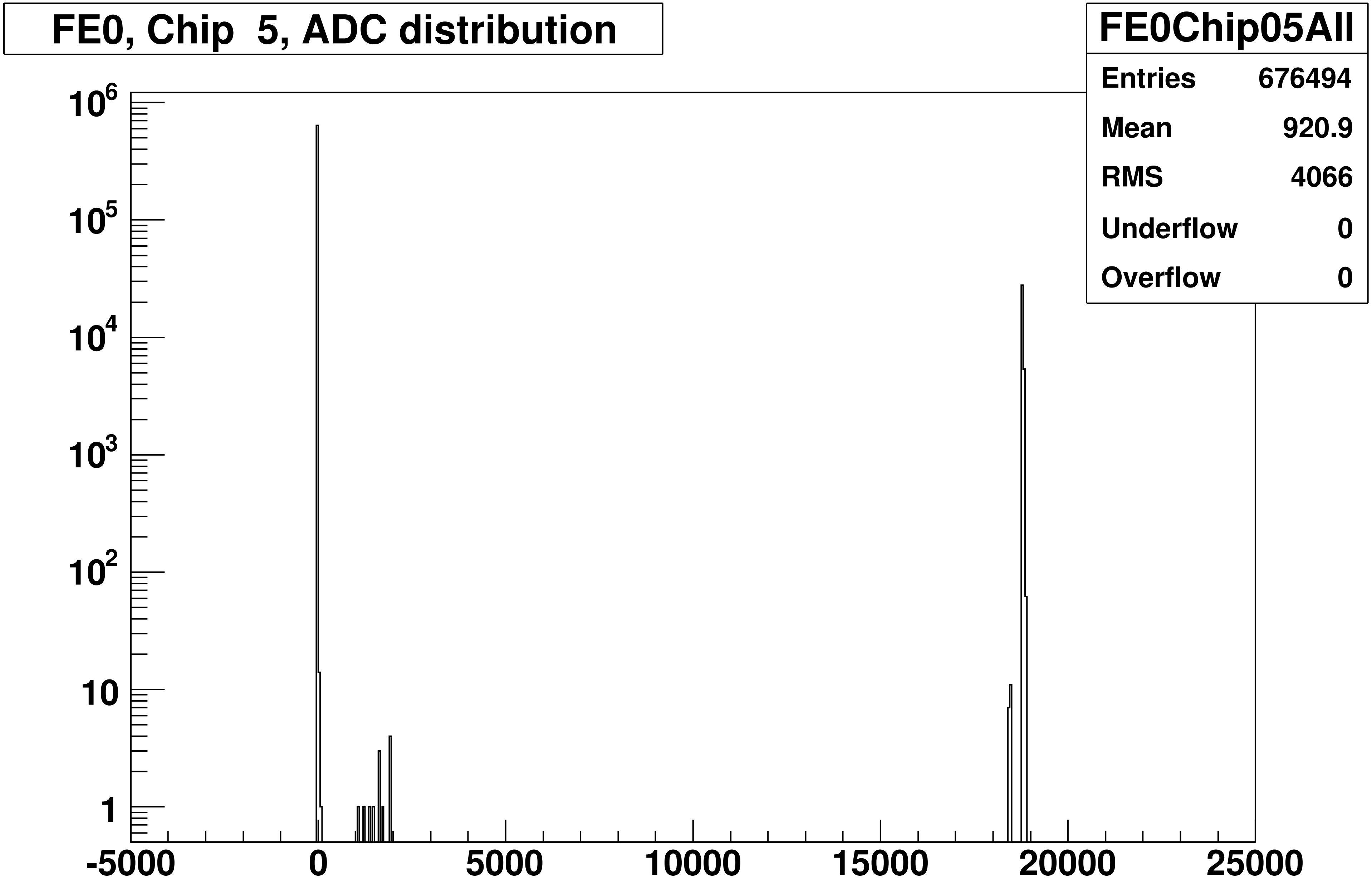
FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 676494
Mean 920.9
RMS 4066
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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-5000
0
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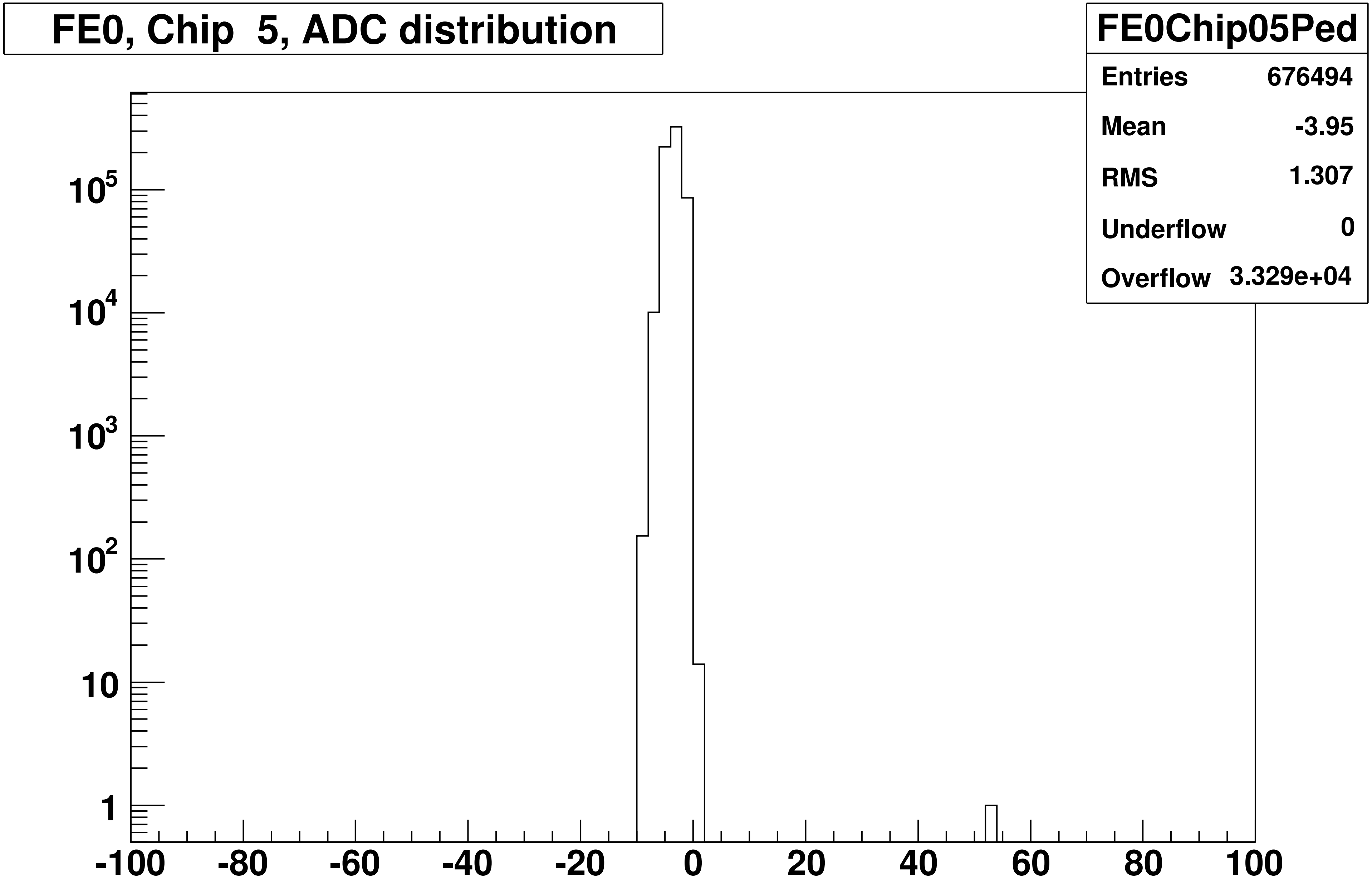
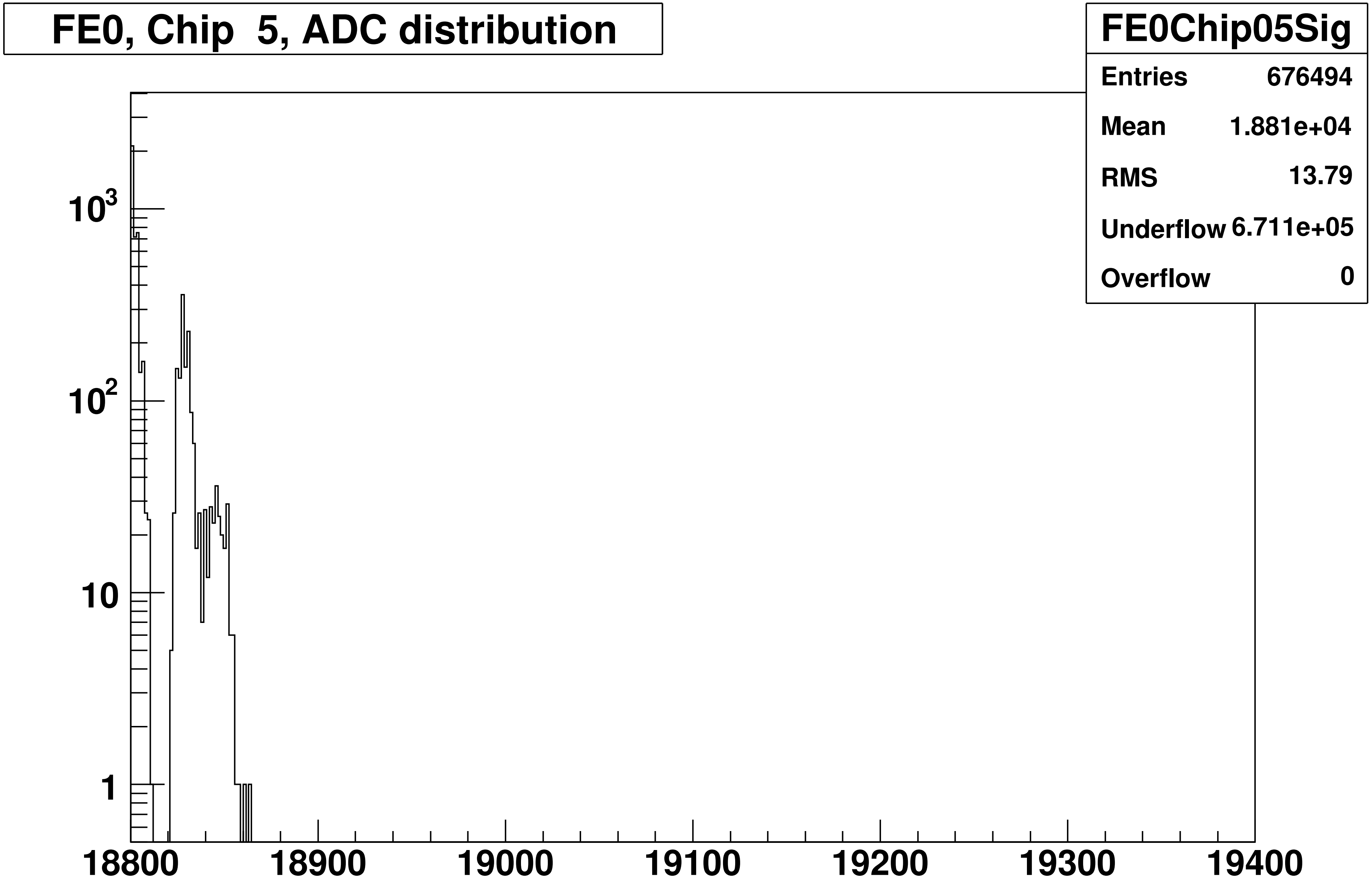
10000
15000
20000
25000


FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 676494
Mean -3.95
RMS 1.307
Underflow 0
Overflow 3.329e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 676494
Mean 1.881e+04
RMS 13.79
Underflow 6.711e+05
Overflow 0
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10^2
10
1
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18900
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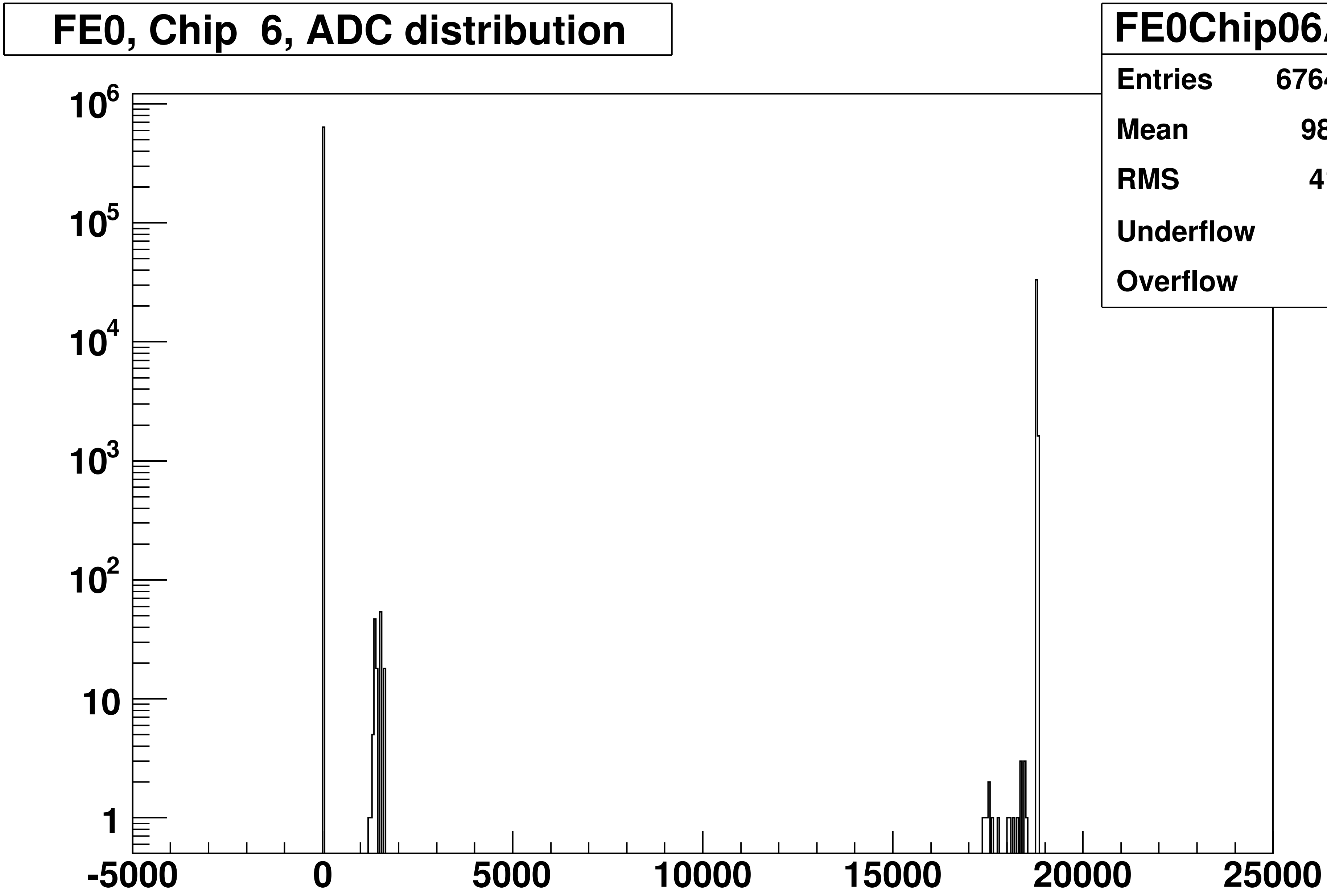
FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 676494
Mean 988.2
RMS 4145
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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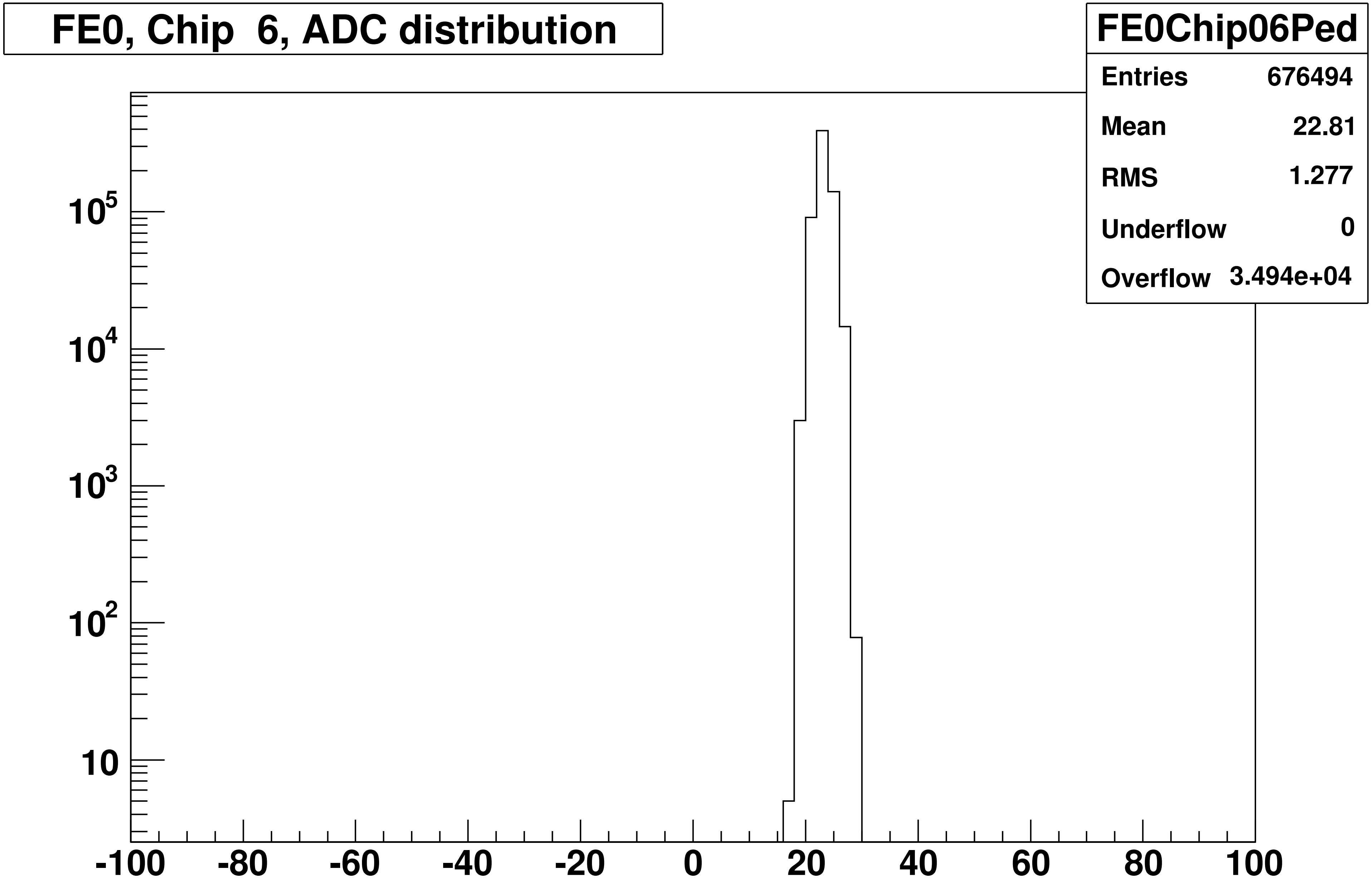
15000
20000
25000



FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 676494
Mean 22.81
RMS 1.277
Underflow 0
Overflow 3.494e+04
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10^3
10^2
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-20
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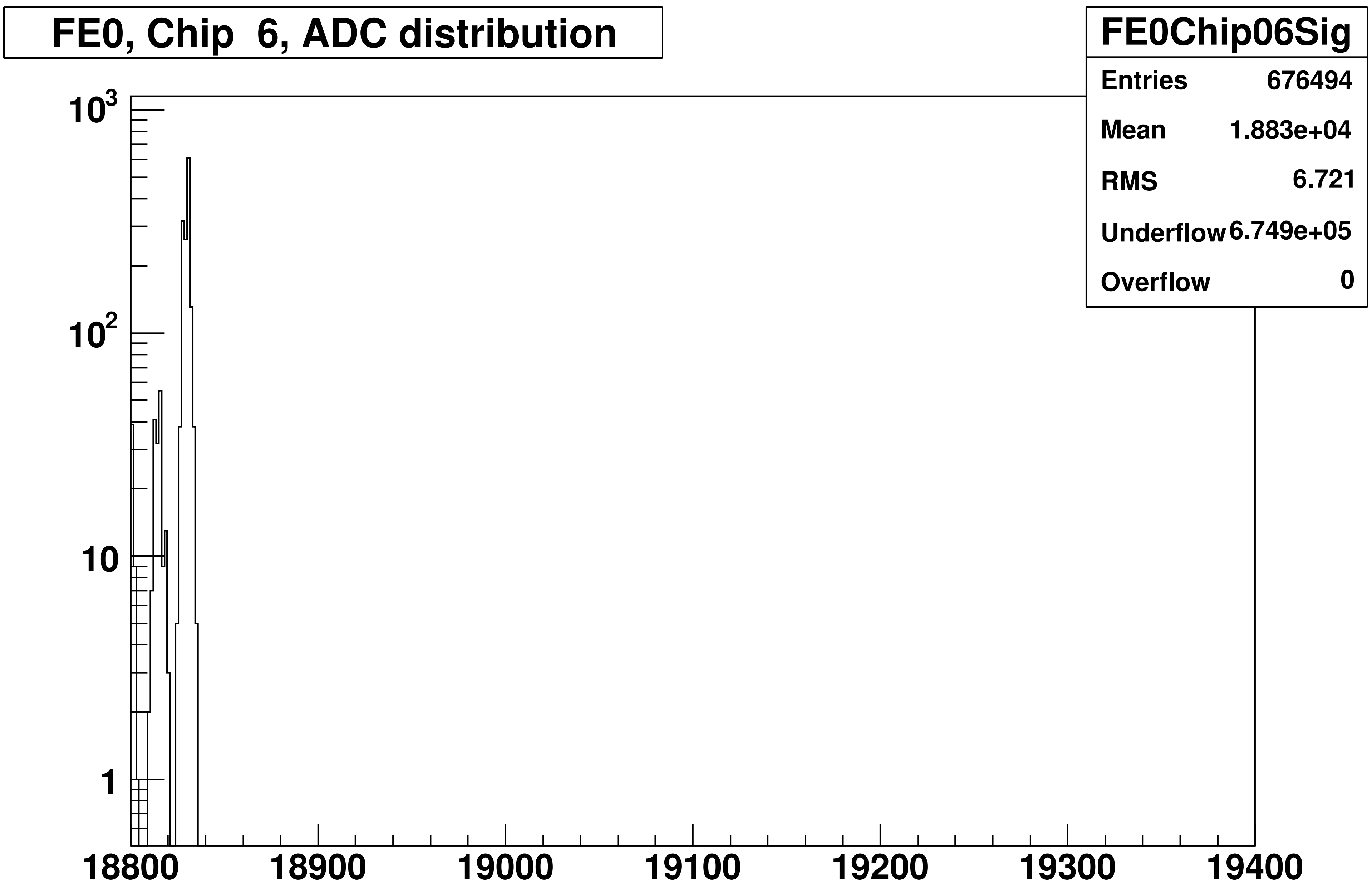
FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 676494
Mean 1.883e+04
RMS 6.721
Underflow 6.749e+05
Overflow 0
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10^2
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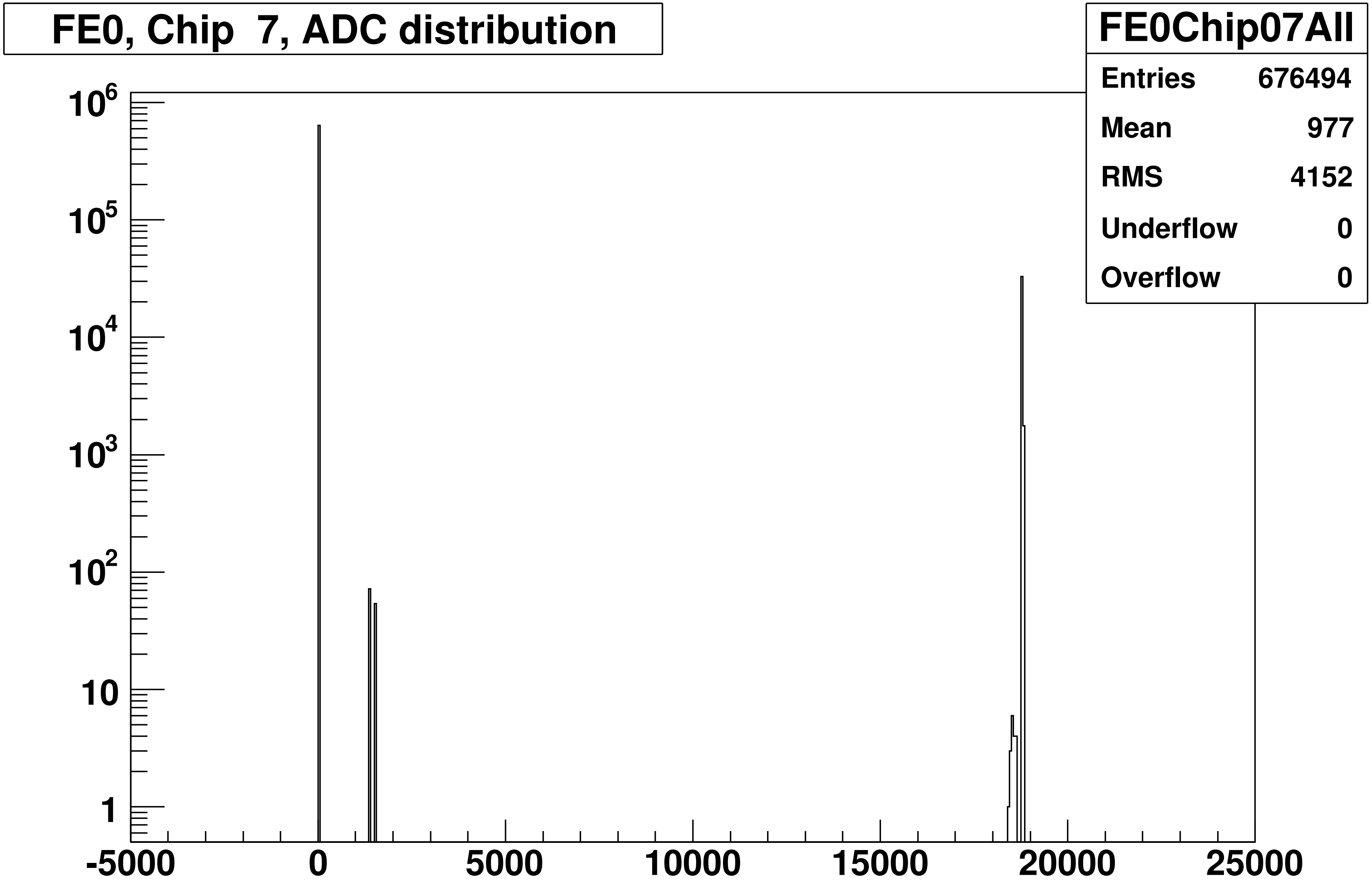
FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 676494
Mean 977
RMS 4152
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
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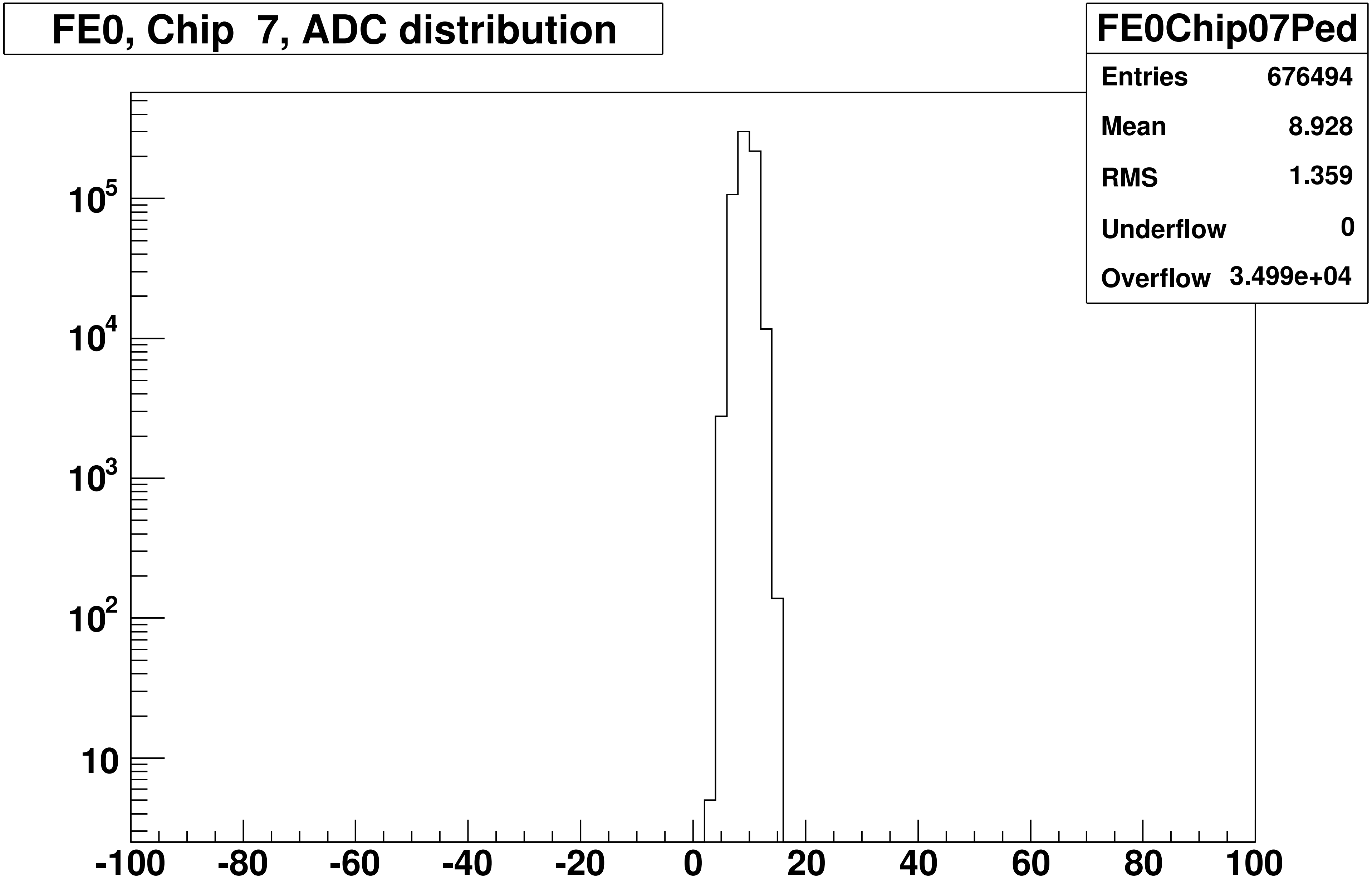
20000
25000



FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 676494
Mean 8.928
RMS 1.359
Underflow 0
Overflow 3.499e+04
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10^2
10^3
10^4
10^5
-100
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-60
-40
-20
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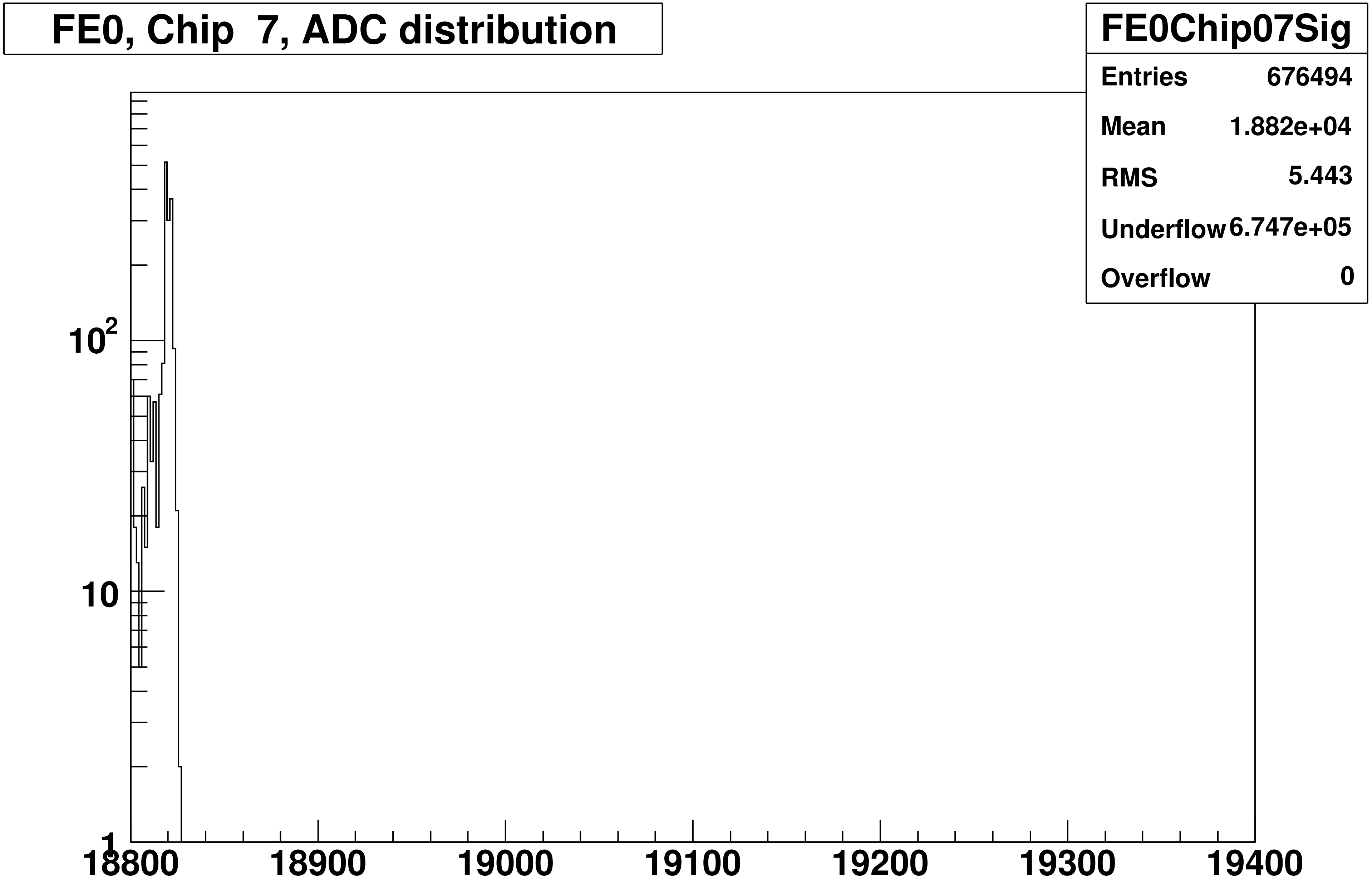
FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 676494
Mean 1.882e+04
RMS 5.443
Underflow 6.747e+05
Overflow 0
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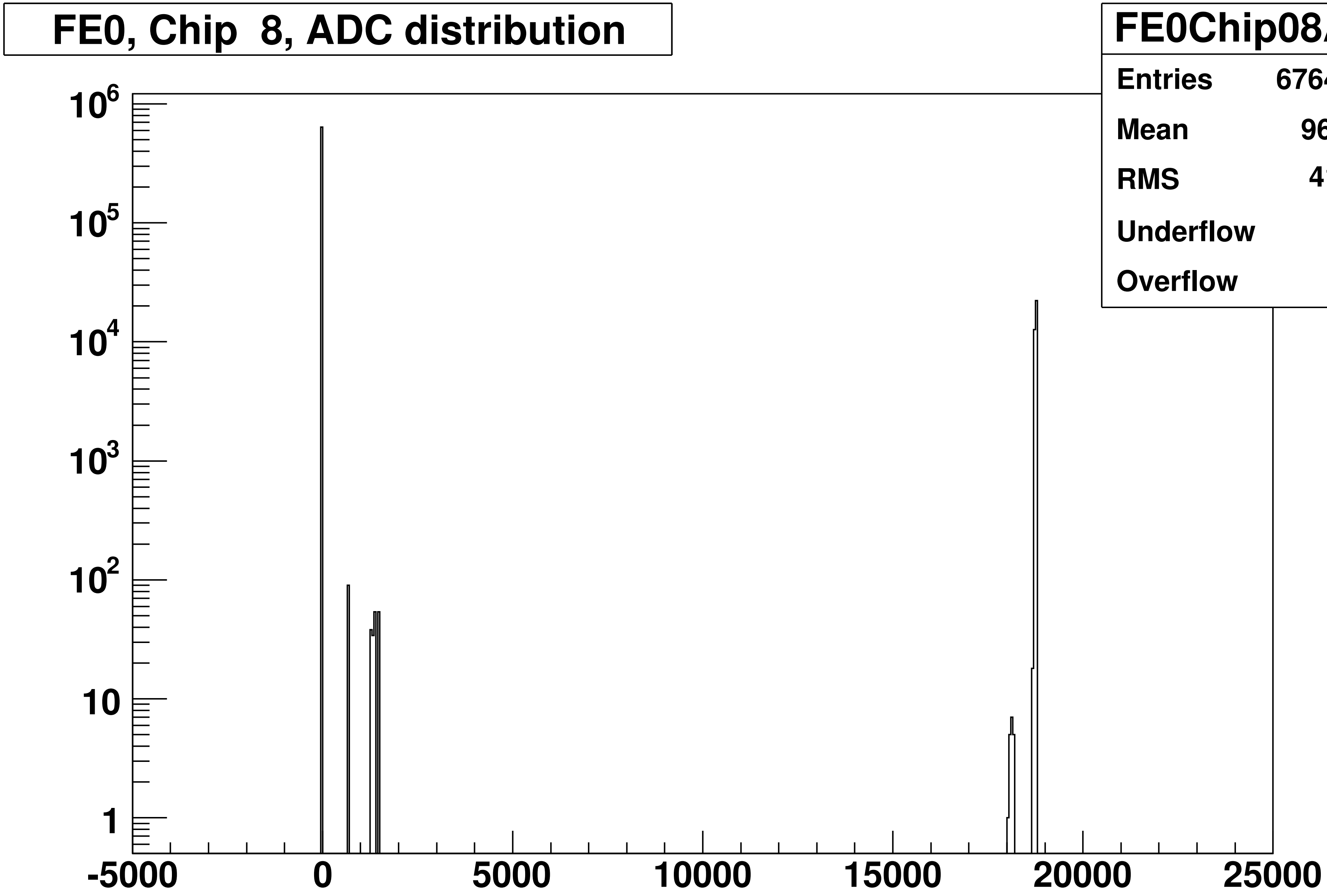
FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 676494
Mean 961.6
RMS 4144
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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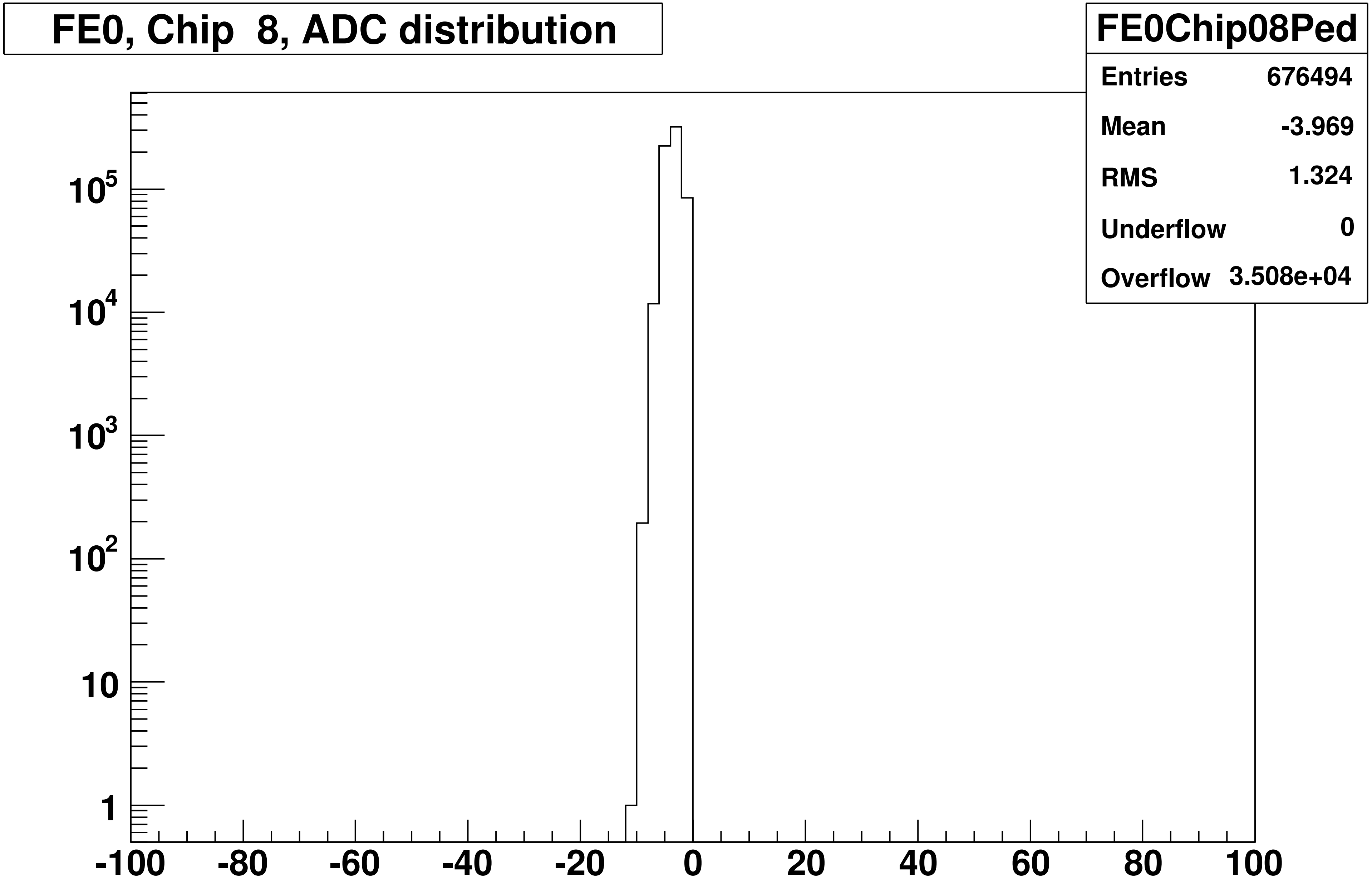
FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 676494
Mean -3.969
RMS 1.324
Underflow 0
Overflow 3.508e+04
10^5
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10^3
10^2
10
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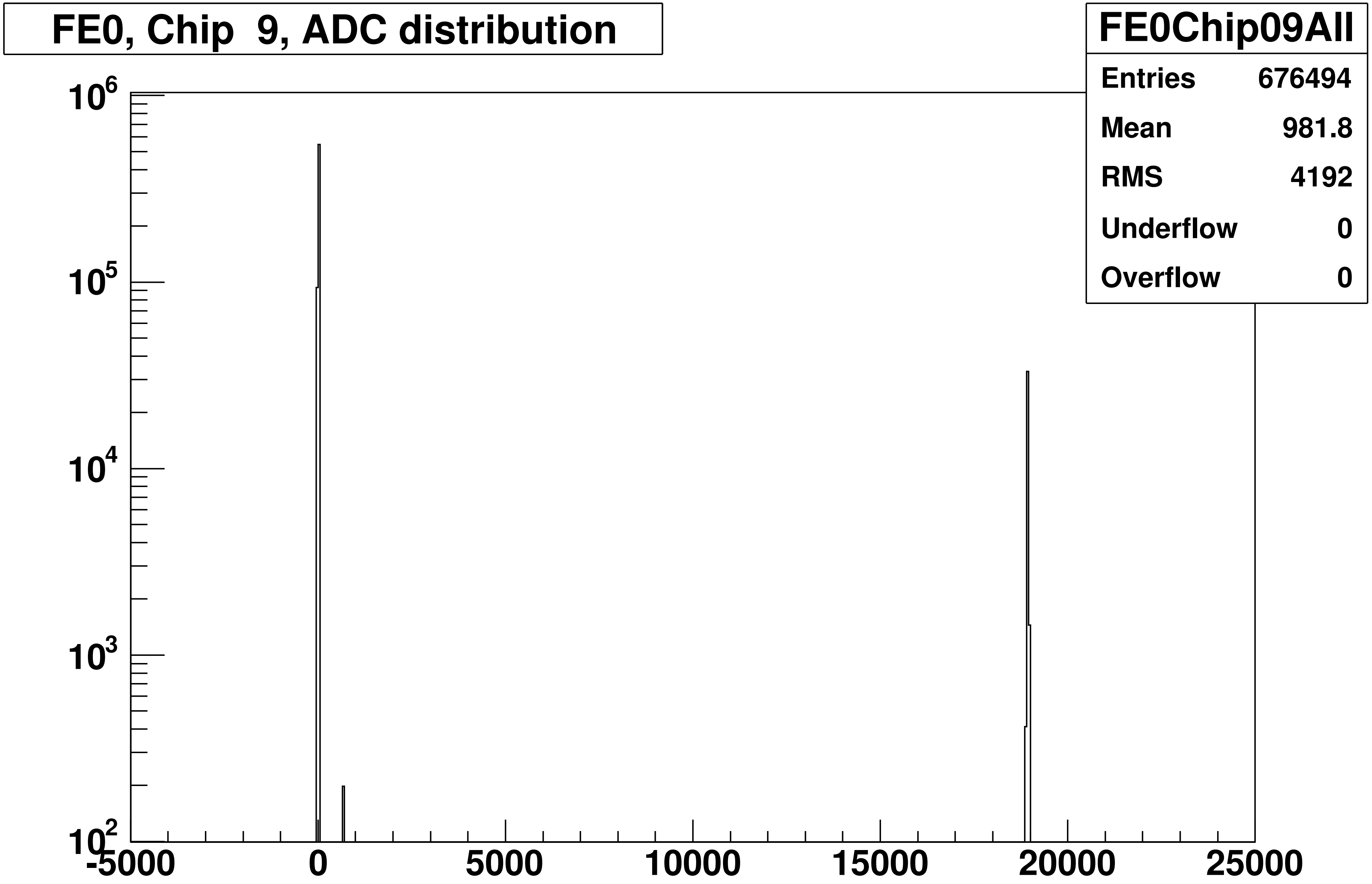
FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 676494
Mean 981.8
RMS 4192
Underflow 0
Overflow 0
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10^4
10^3
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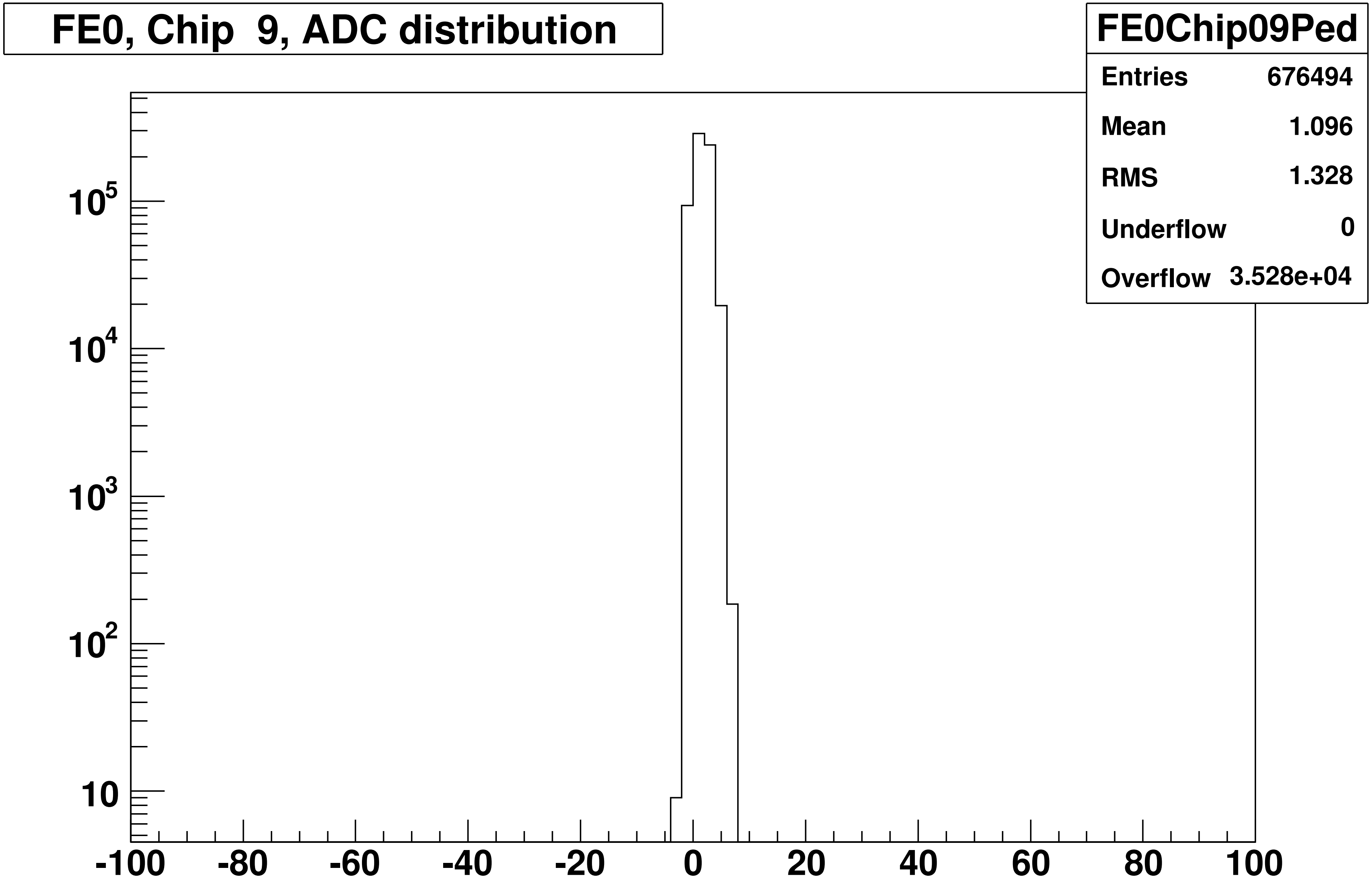

FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 676494
Mean 1.096
RMS 1.328
Underflow 0
Overflow 3.528e+04
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10^4
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10^2
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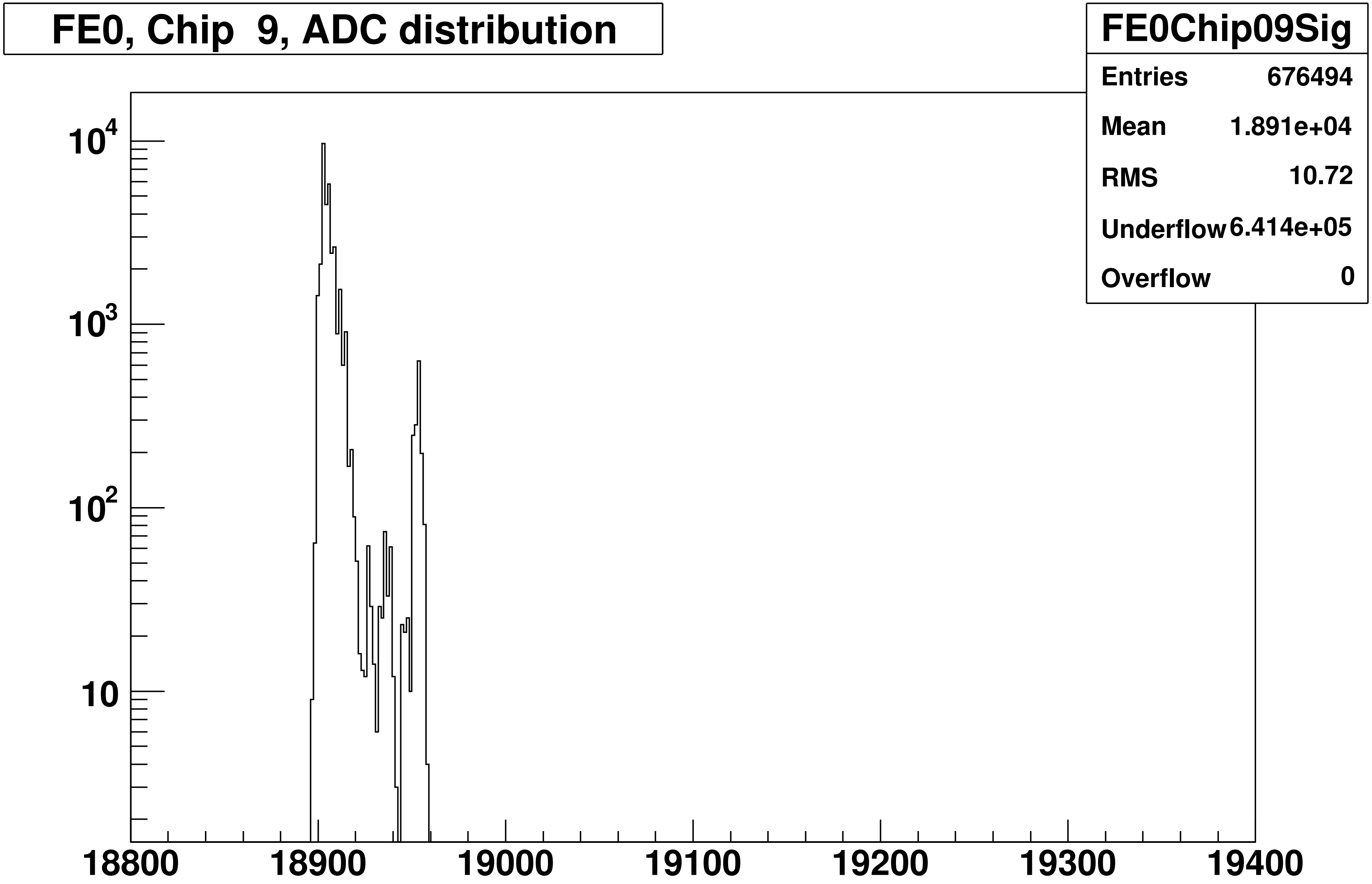
FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 676494
Mean 1.891e+04
RMS 10.72
Underflow 6.414e+05
Overflow 0
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10^2
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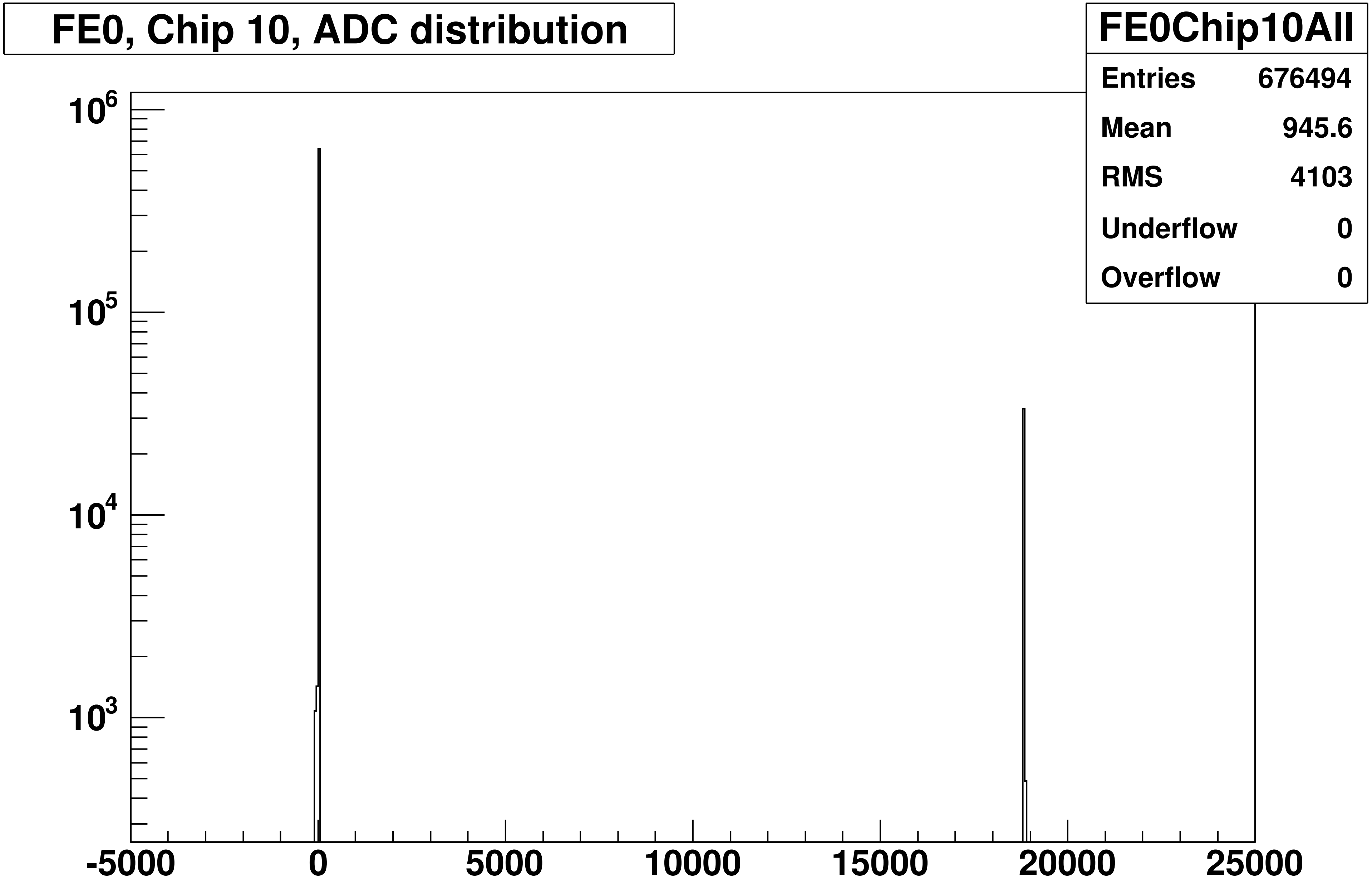
FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 676494
Mean 945.6
RMS 4103
Underflow 0
Overflow 0
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10^4
10^3
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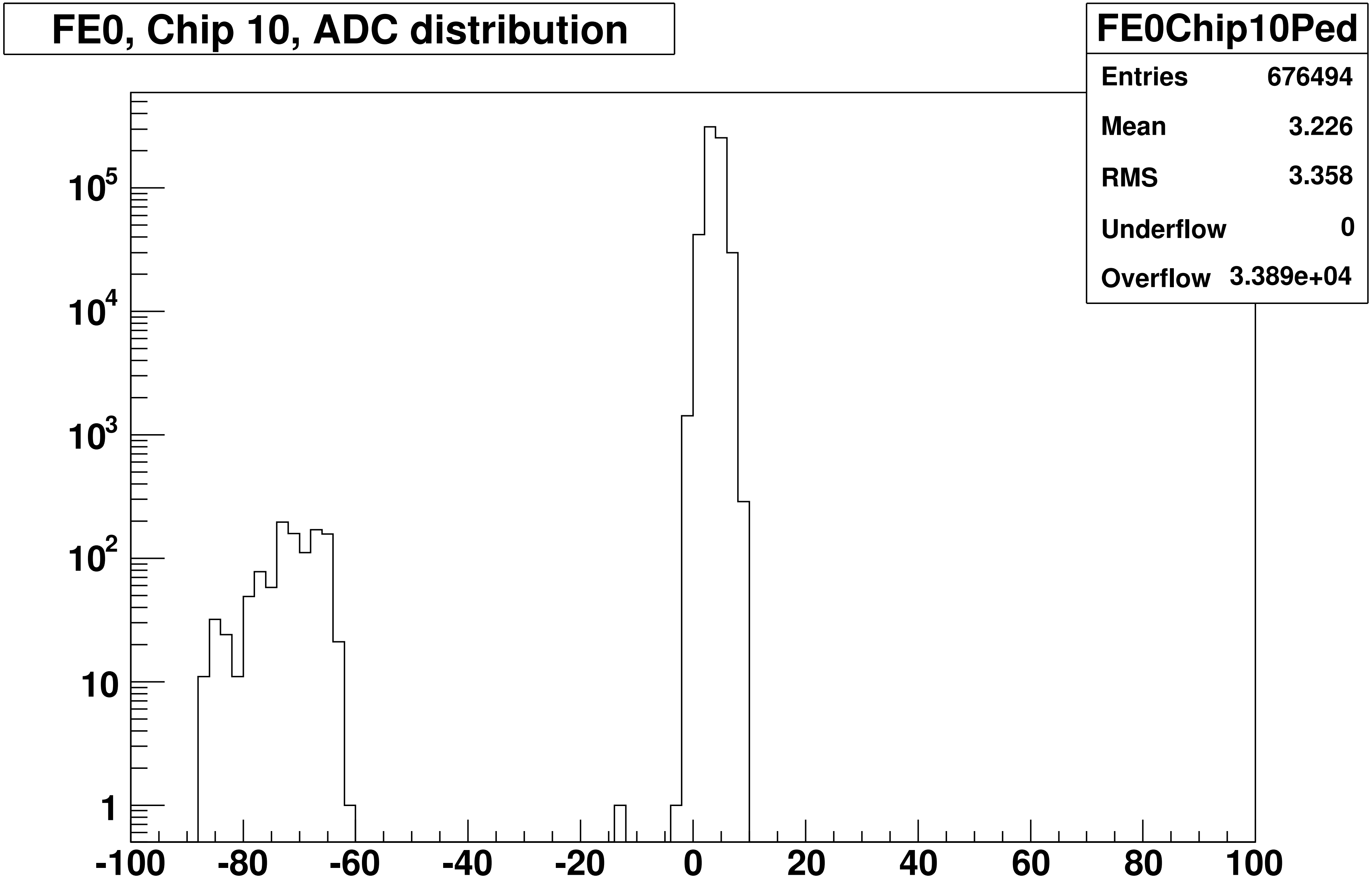
FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 676494
Mean 3.226
RMS 3.358
Underflow 0
Overflow 3.389e+04
10^5
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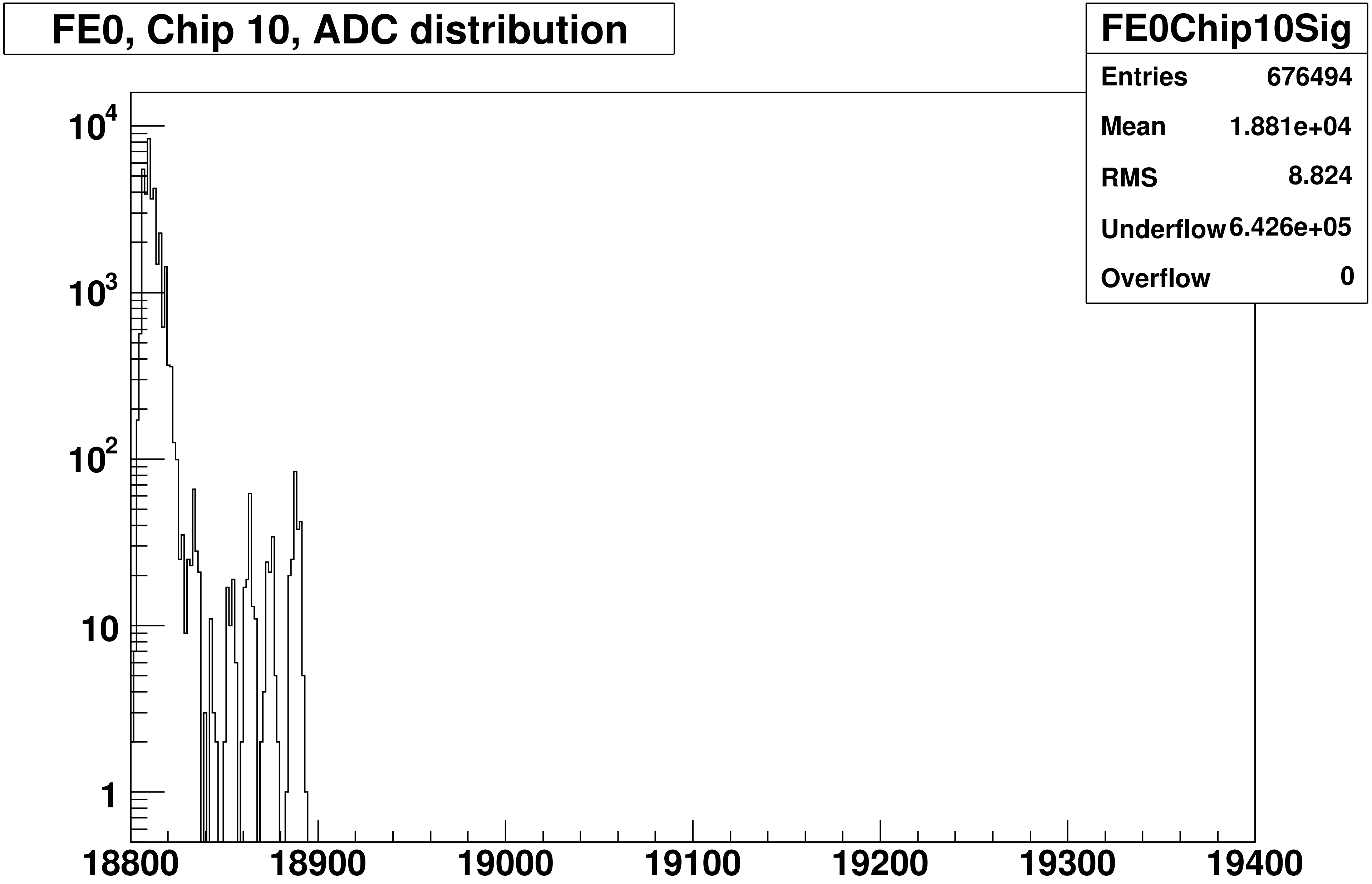
FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 676494
Mean 1.881e+04
RMS 8.824
Underflow 6.426e+05
Overflow 0
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10^3
10^2
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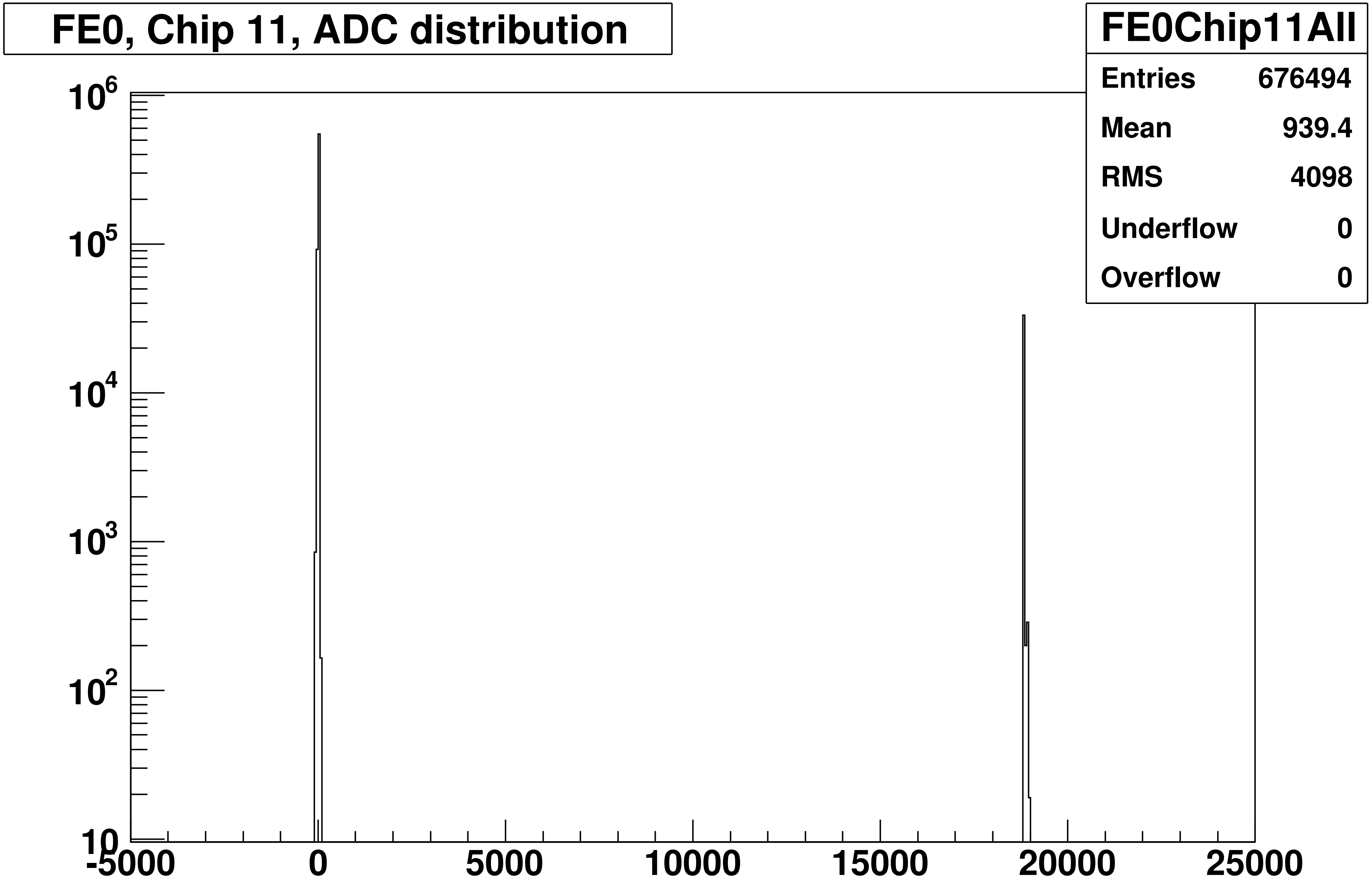
FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 676494
Mean 939.4
RMS 4098
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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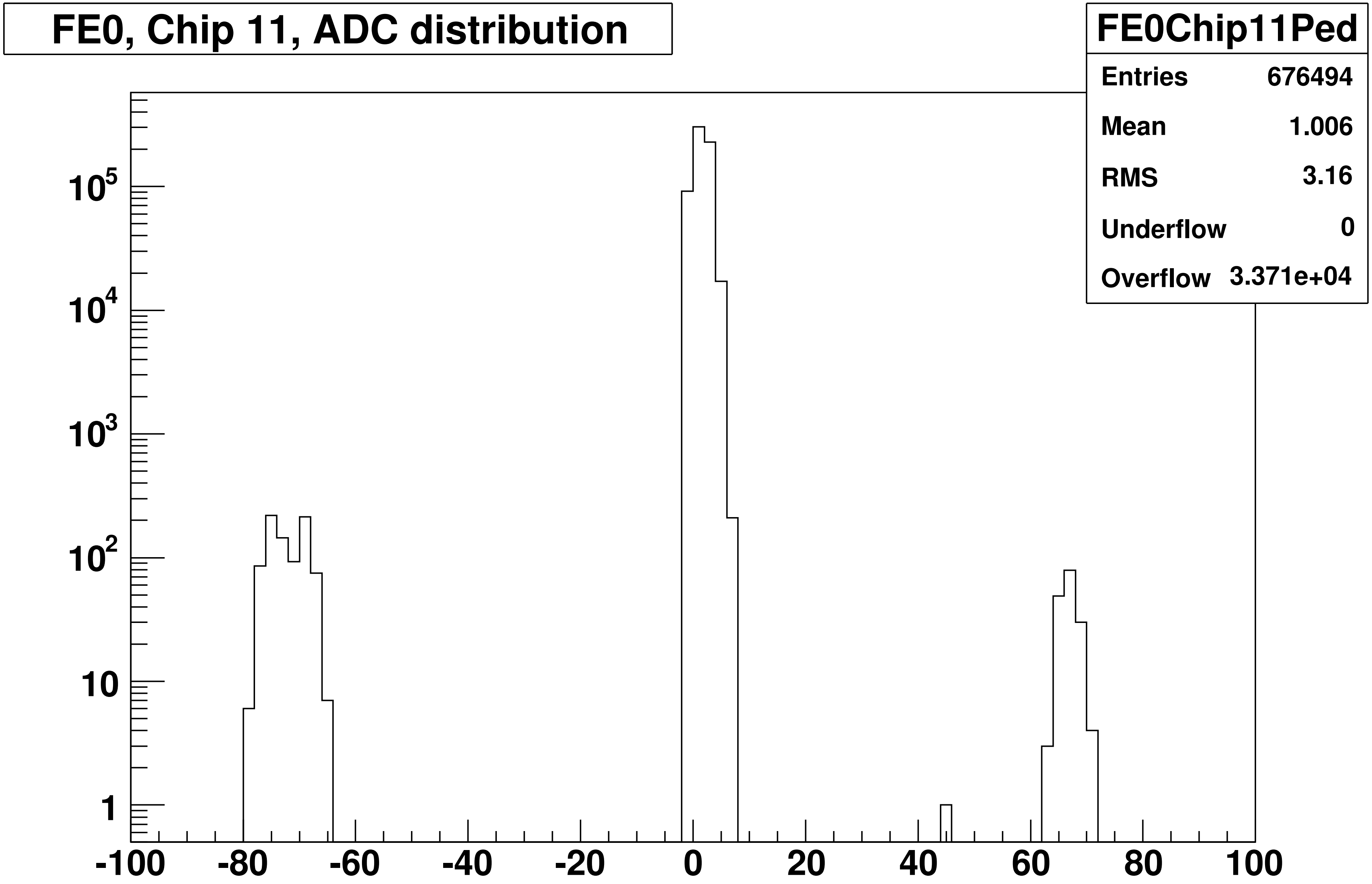
25000


FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 676494
Mean 1.006
RMS 3.16
Underflow 0
Overflow 3.371e+04
10^5
10^4
10^3
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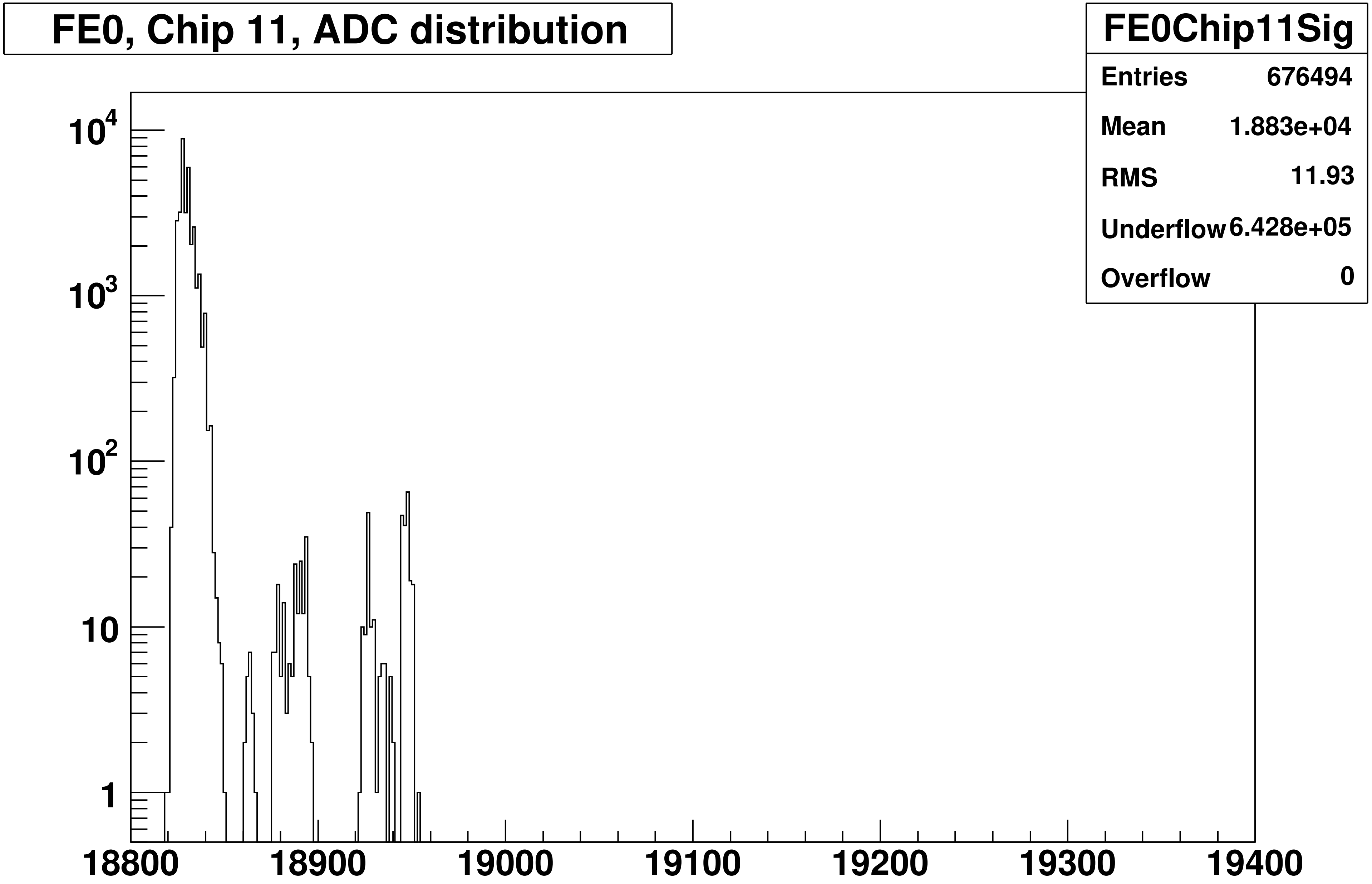
FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 676494
Mean 1.883e+04
RMS 11.93
Underflow 6.428e+05
Overflow 0
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10^3
10^2
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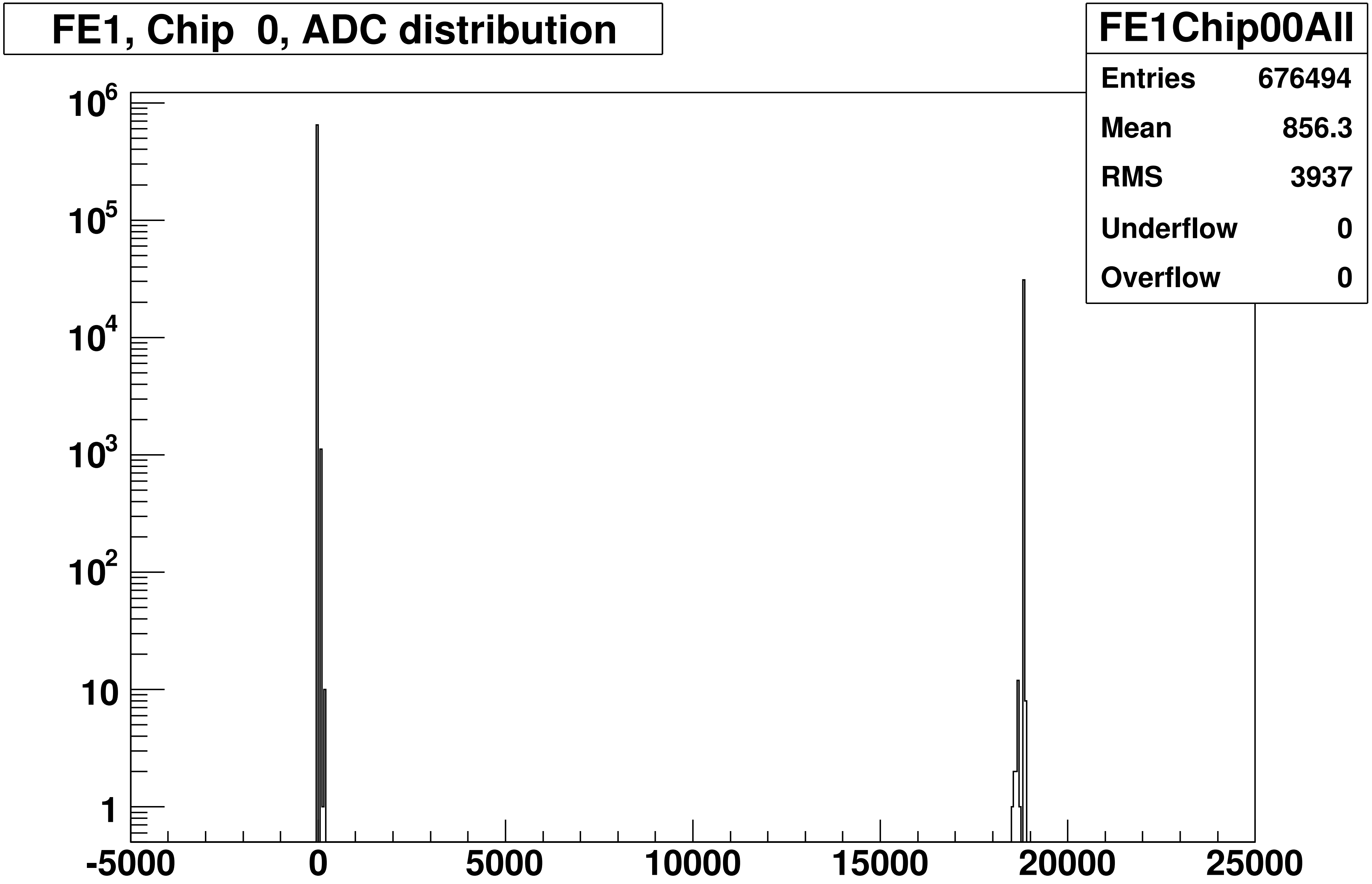
FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 676494
Mean 856.3
RMS 3937
Underflow 0
Overflow 0
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10^3
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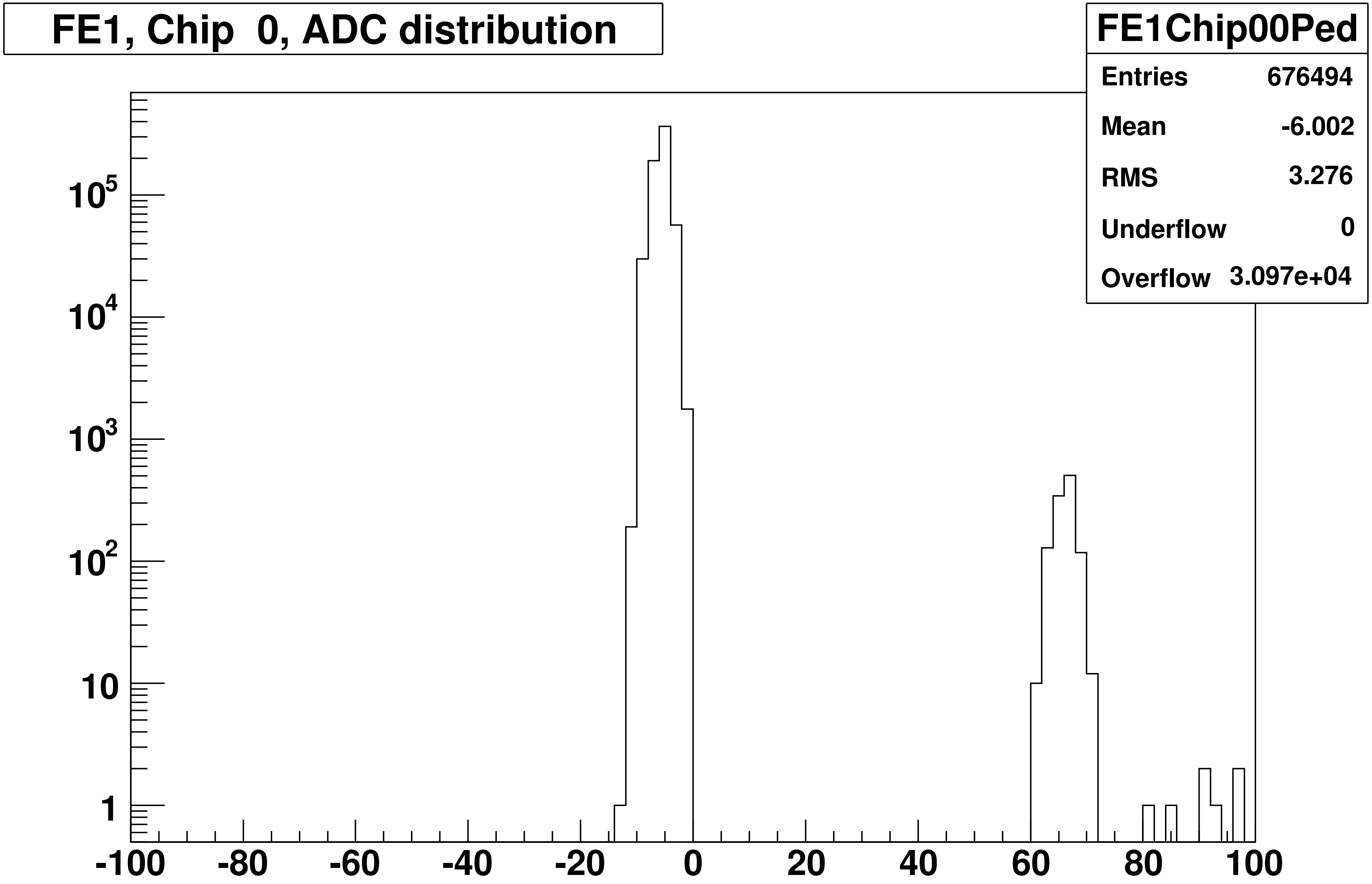

FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 676494
Mean -6.002
RMS 3.276
Underflow 0
Overflow 3.097e+04
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10^3
10^2
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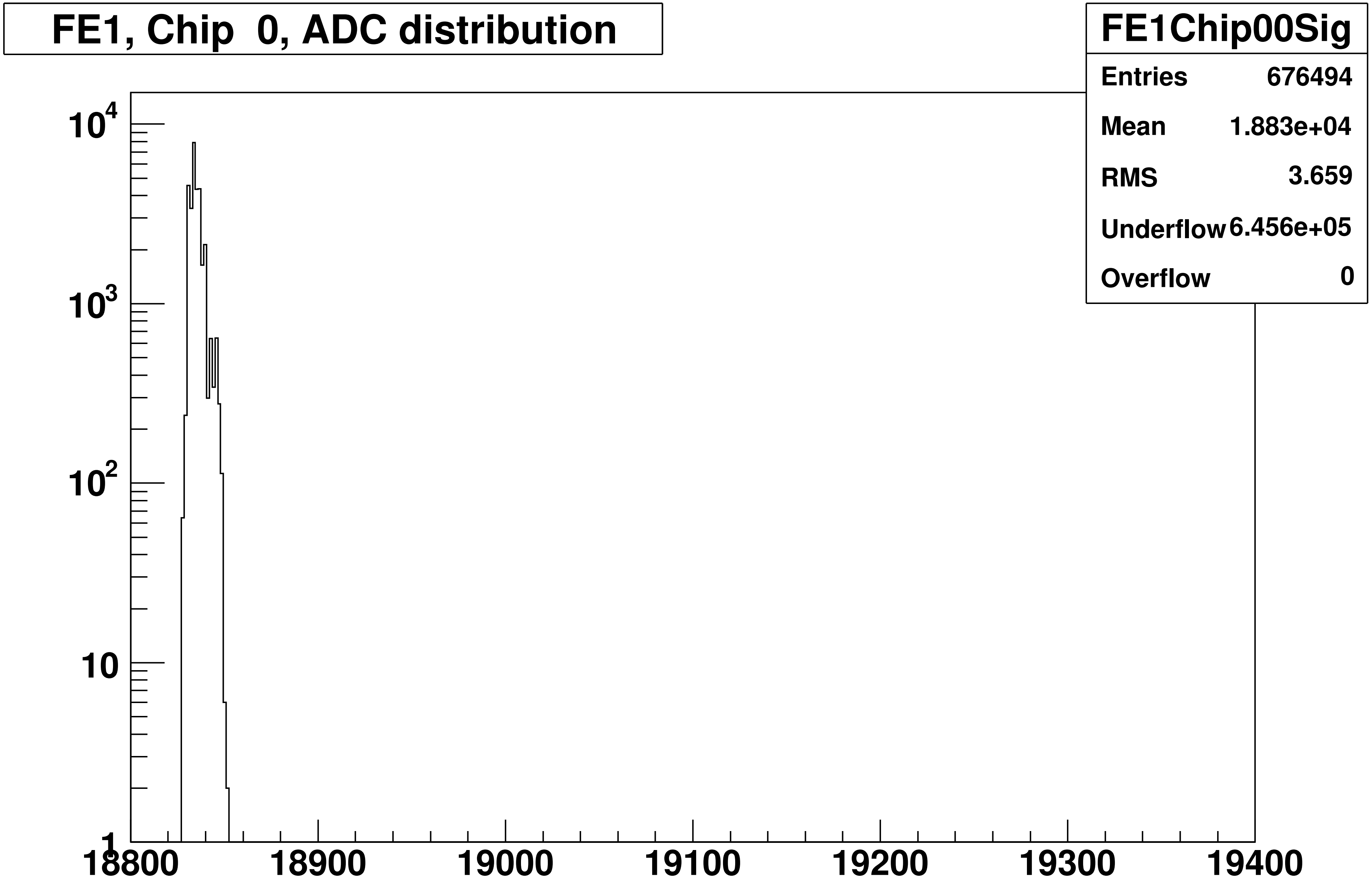
FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 676494
Mean 1.883e+04
RMS 3.659
Underflow 6.456e+05
Overflow 0
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10^3
10^2
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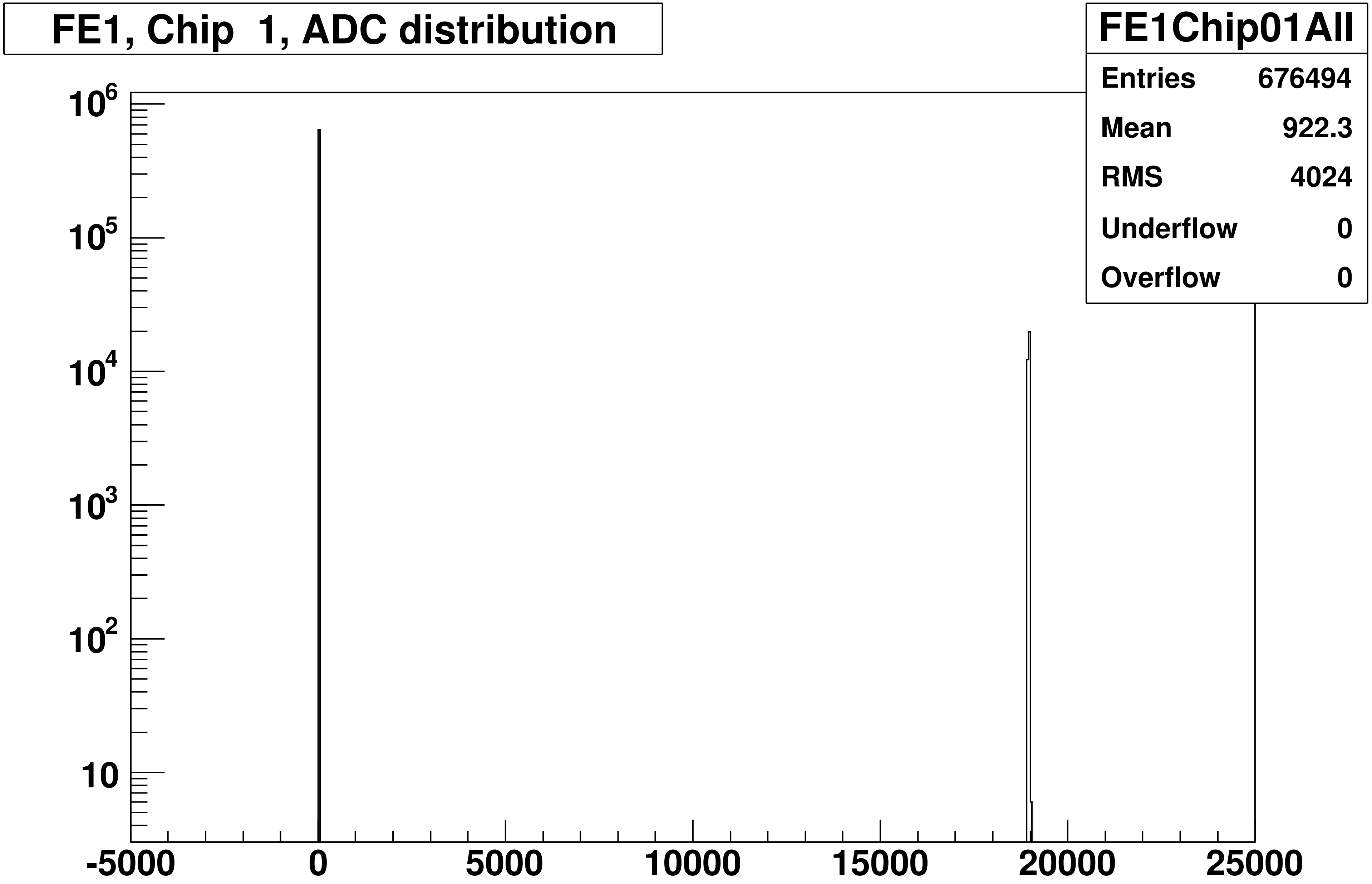
FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 676494
Mean 922.3
RMS 4024
Underflow 0
Overflow 0
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10^5
10^4
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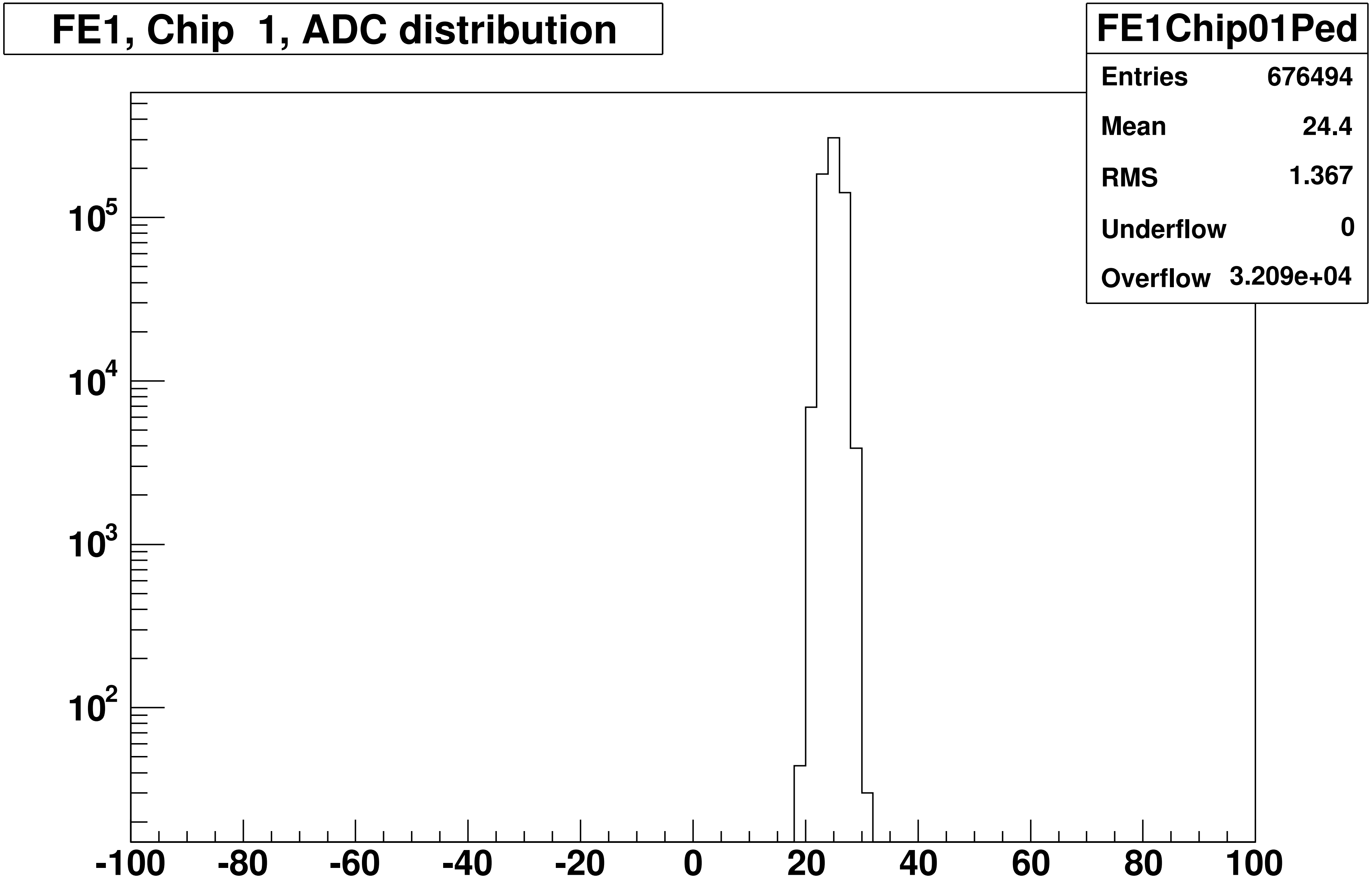
FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 676494
Mean 24.4
RMS 1.367
Underflow 0
Overflow 3.209e+04
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10^3
10^2
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-40
-20
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60
80
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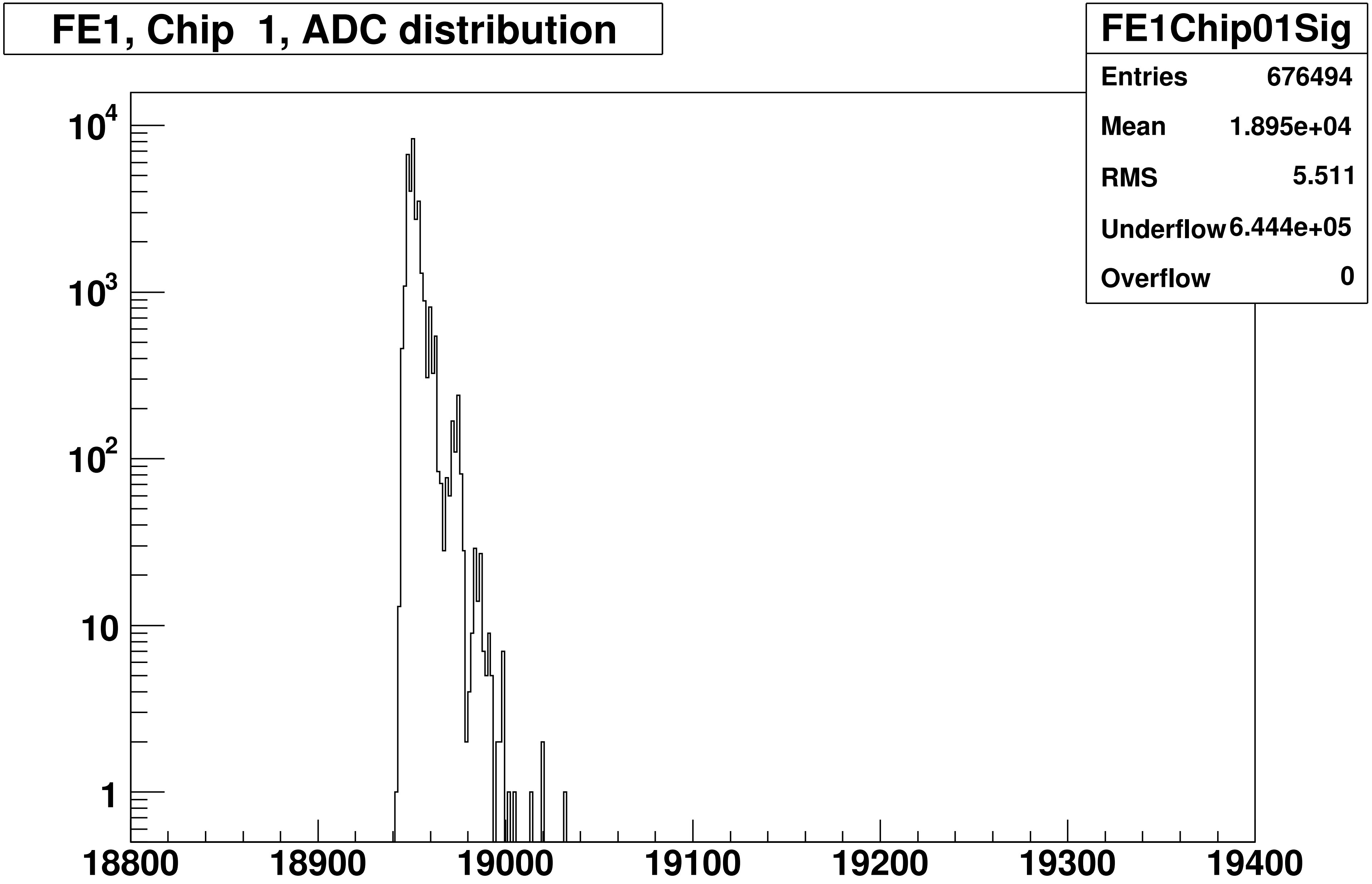
FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 676494
Mean 1.895e+04
RMS 5.511
Underflow 6.444e+05
Overflow 0
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10^3
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1
18800
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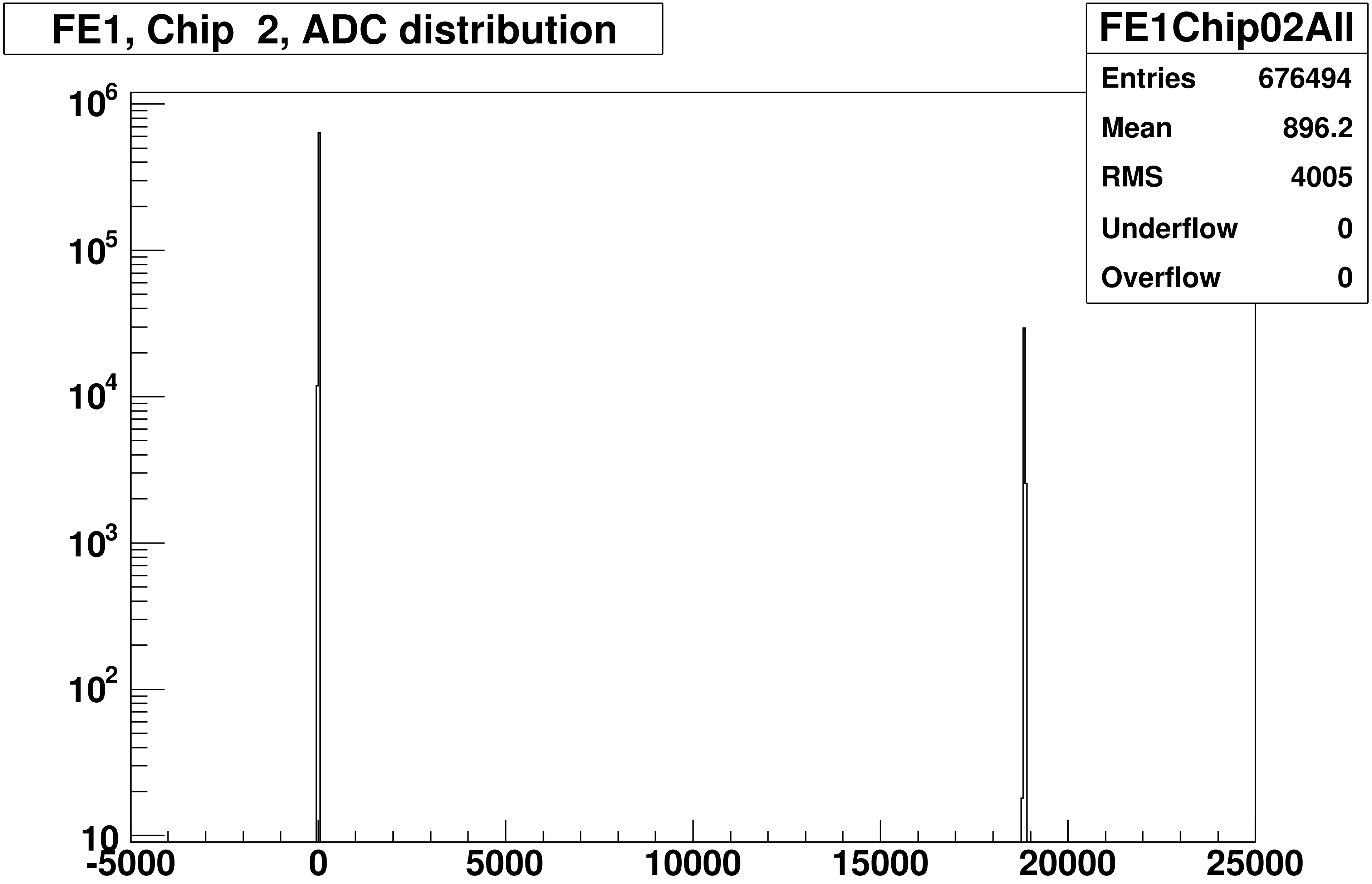
FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 676494
Mean 896.2
RMS 4005
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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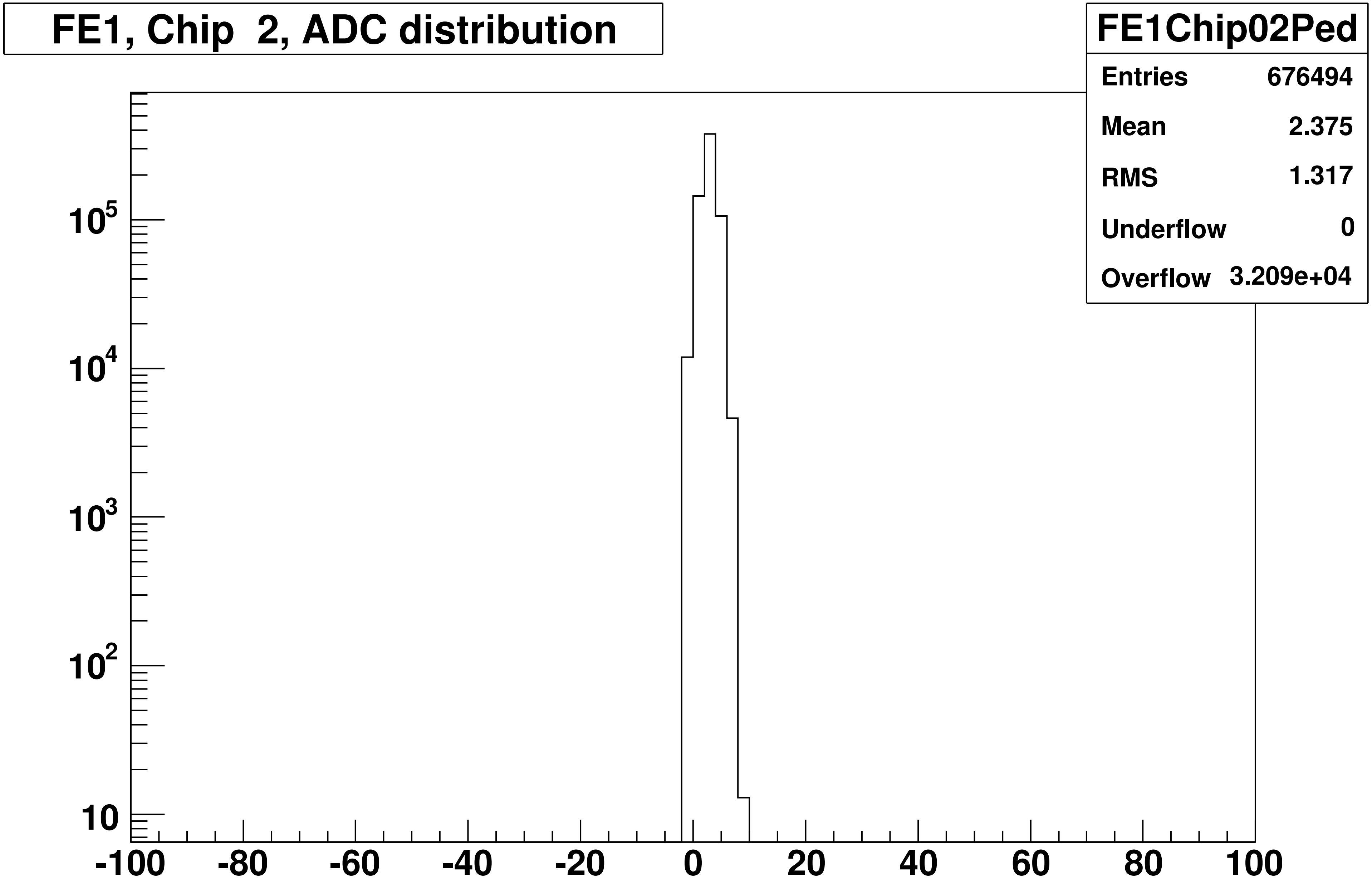
FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 676494
Mean 2.375
RMS 1.317
Underflow 0
Overflow 3.209e+04
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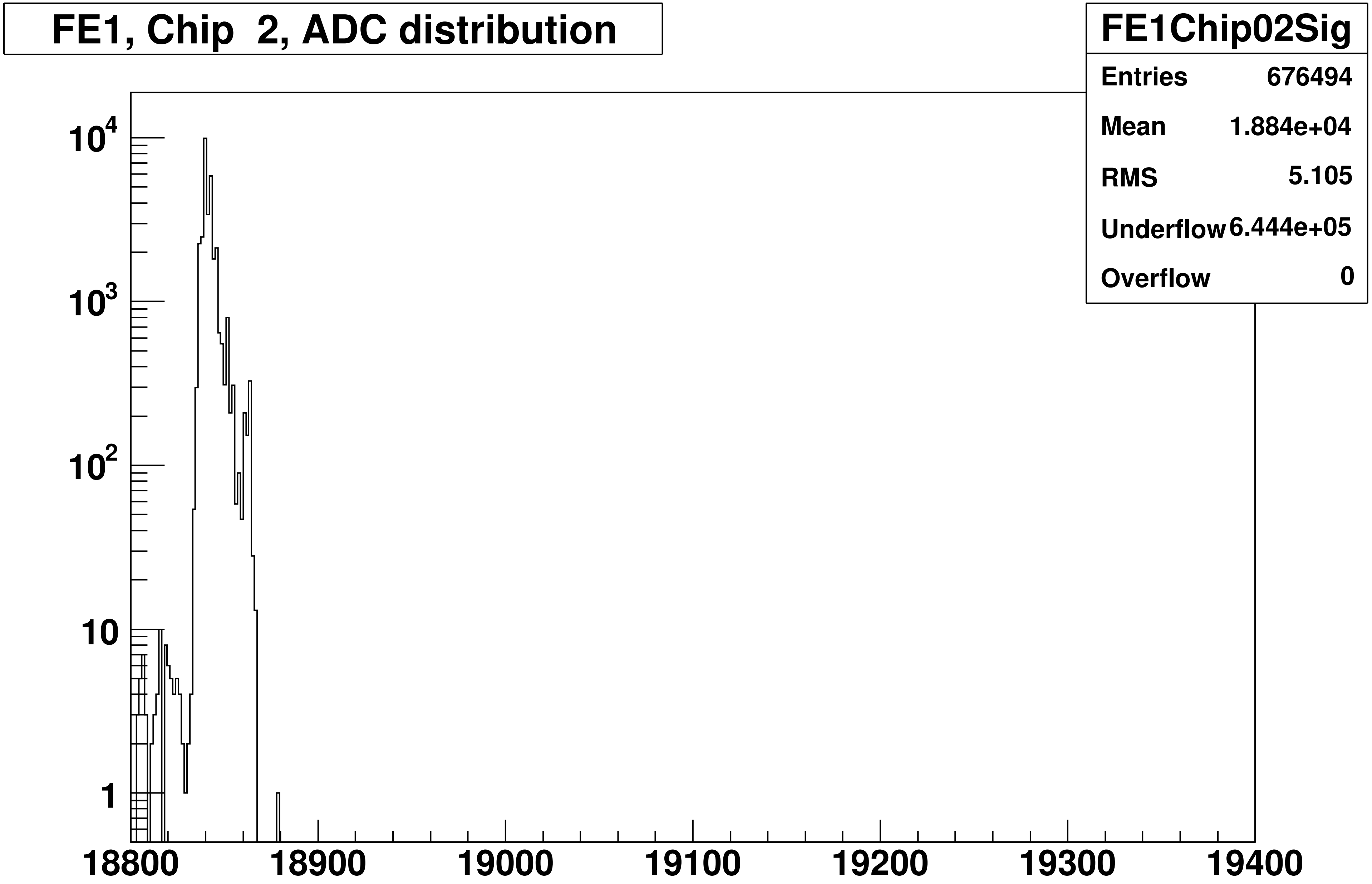

FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 676494
Mean 1.884e+04
RMS 5.105
Underflow 6.444e+05
Overflow 0
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10^3
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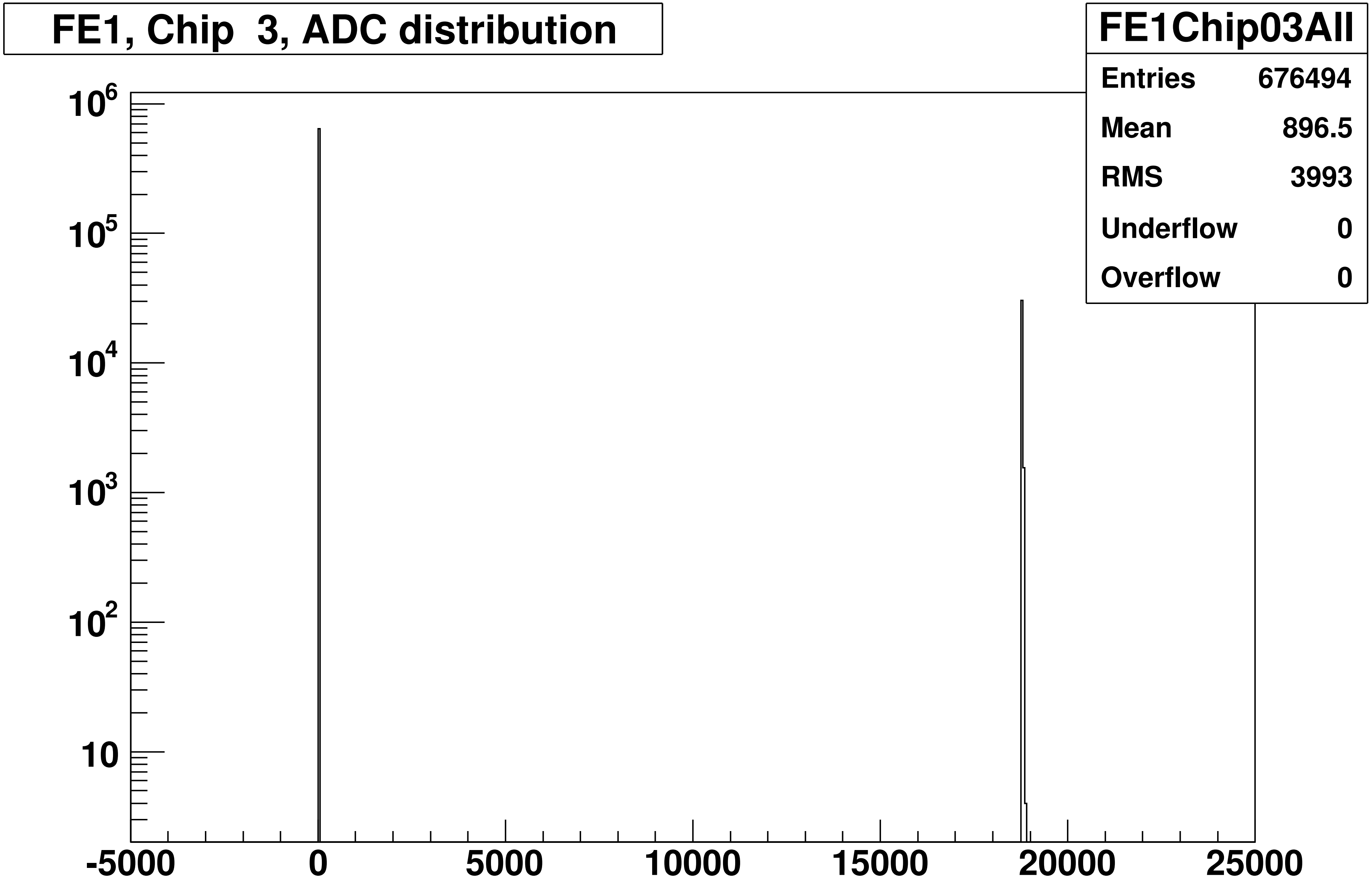
FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 676494
Mean 896.5
RMS 3993
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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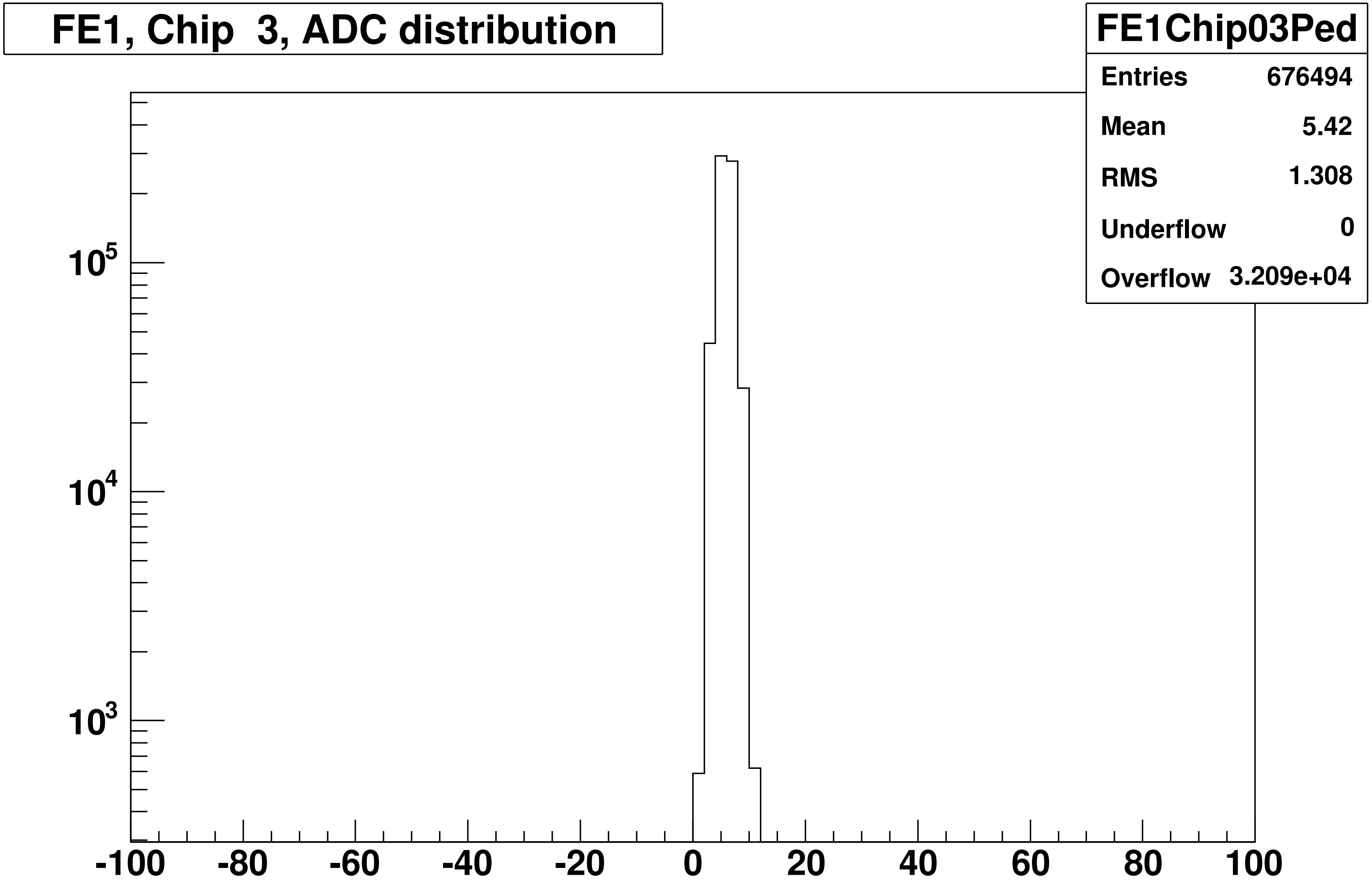

FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 676494
Mean 5.42
RMS 1.308
Underflow 0
Overflow 3.209e+04
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10^3
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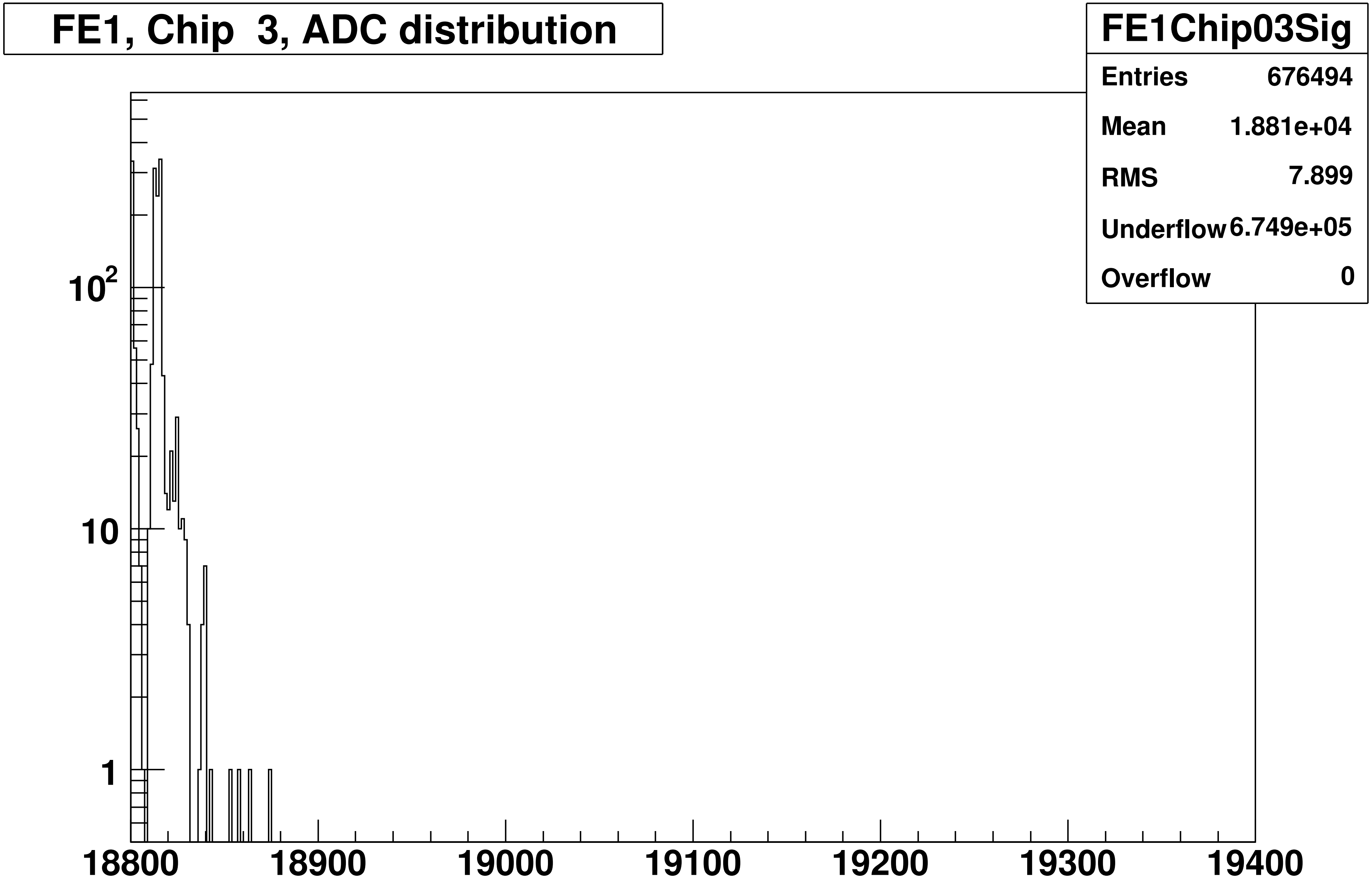
FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 676494
Mean 1.881e+04
RMS 7.899
Underflow 6.749e+05
Overflow 0
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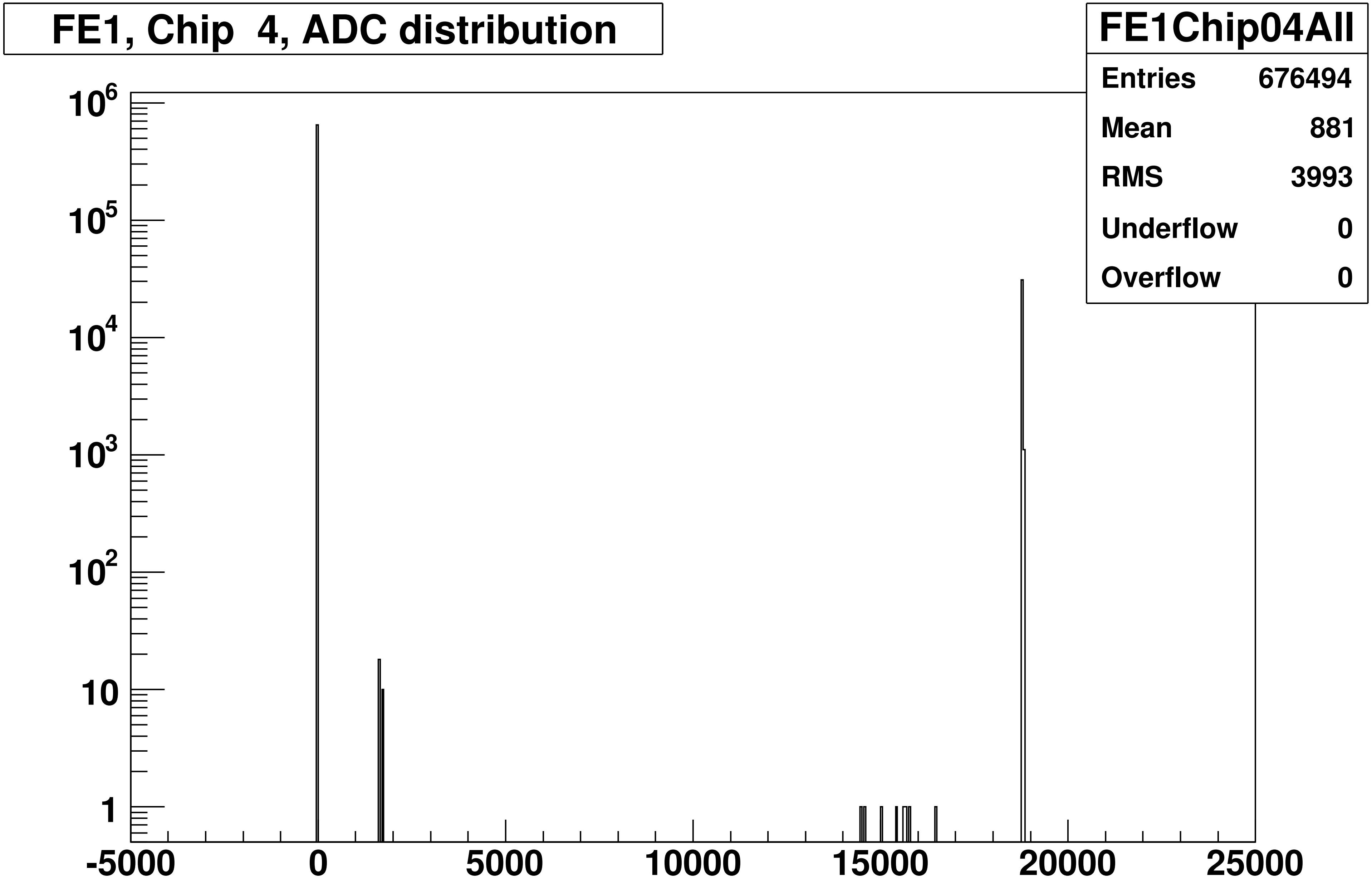
FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 676494
Mean 881
RMS 3993
Underflow 0
Overflow 0
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10^3
10^2
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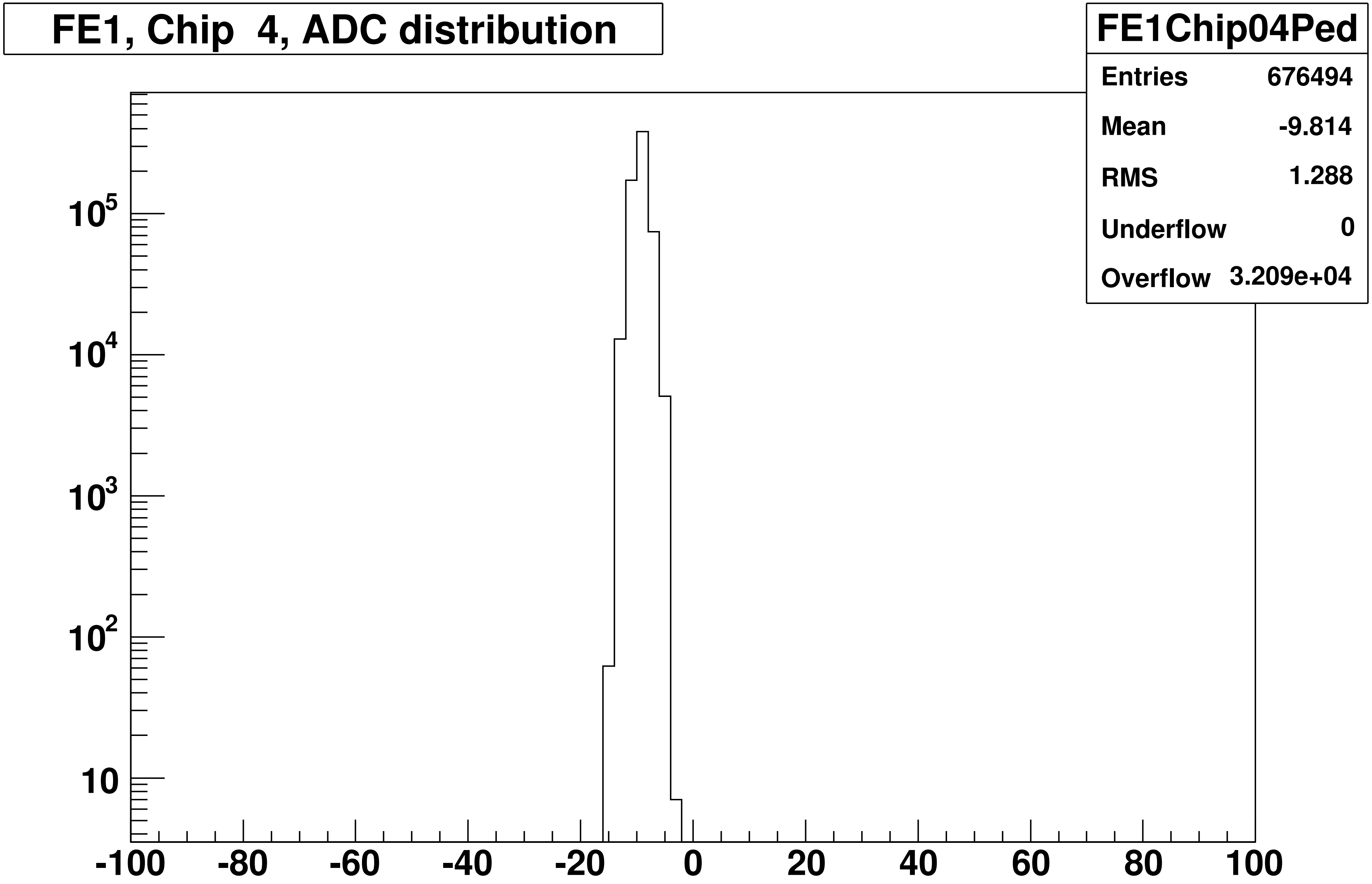
FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 676494
Mean -9.814
RMS 1.288
Underflow 0
Overflow 3.209e+04
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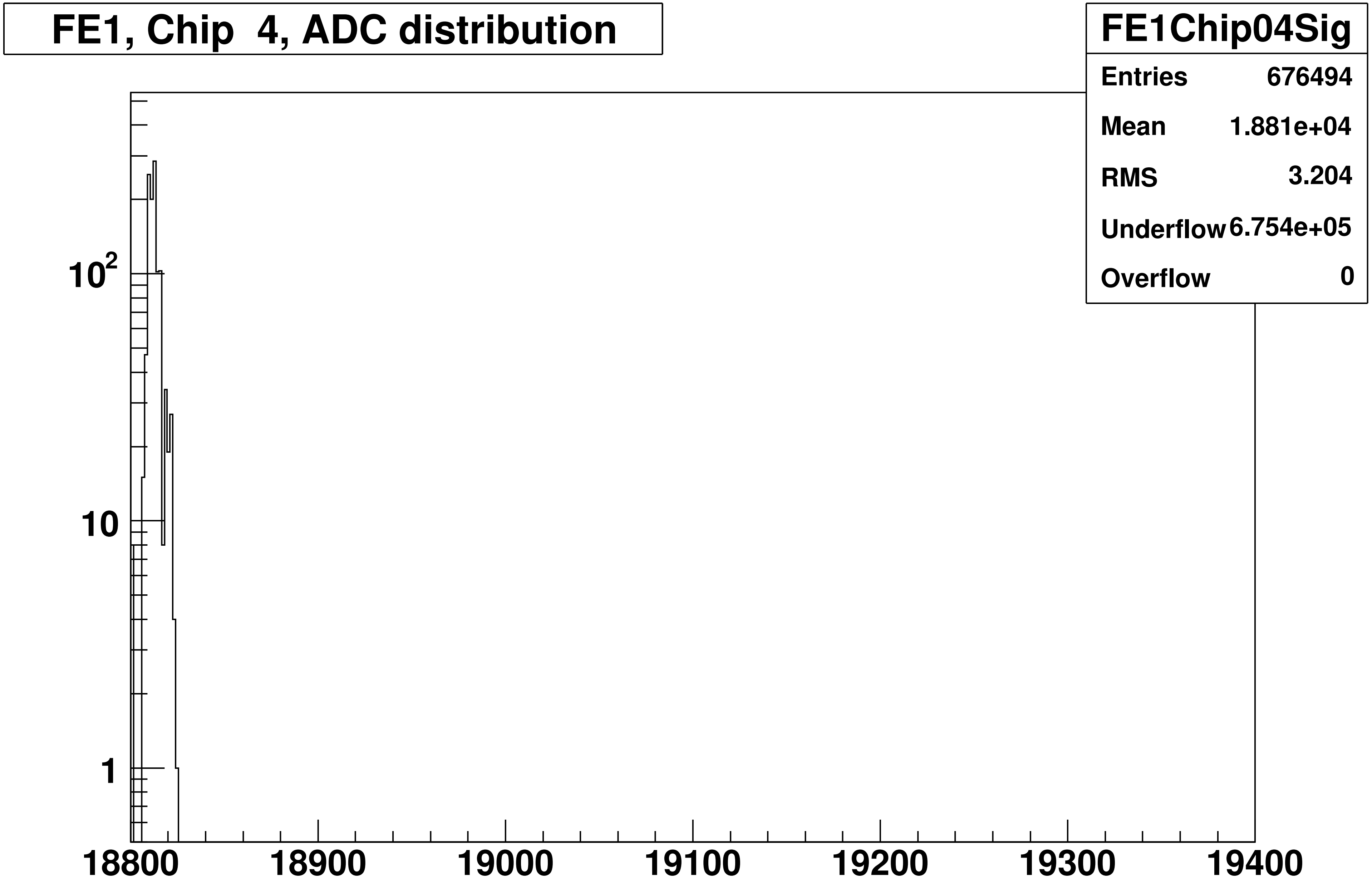

FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 676494
Mean 1.881e+04
RMS 3.204
Underflow 6.754e+05
Overflow 0
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18900
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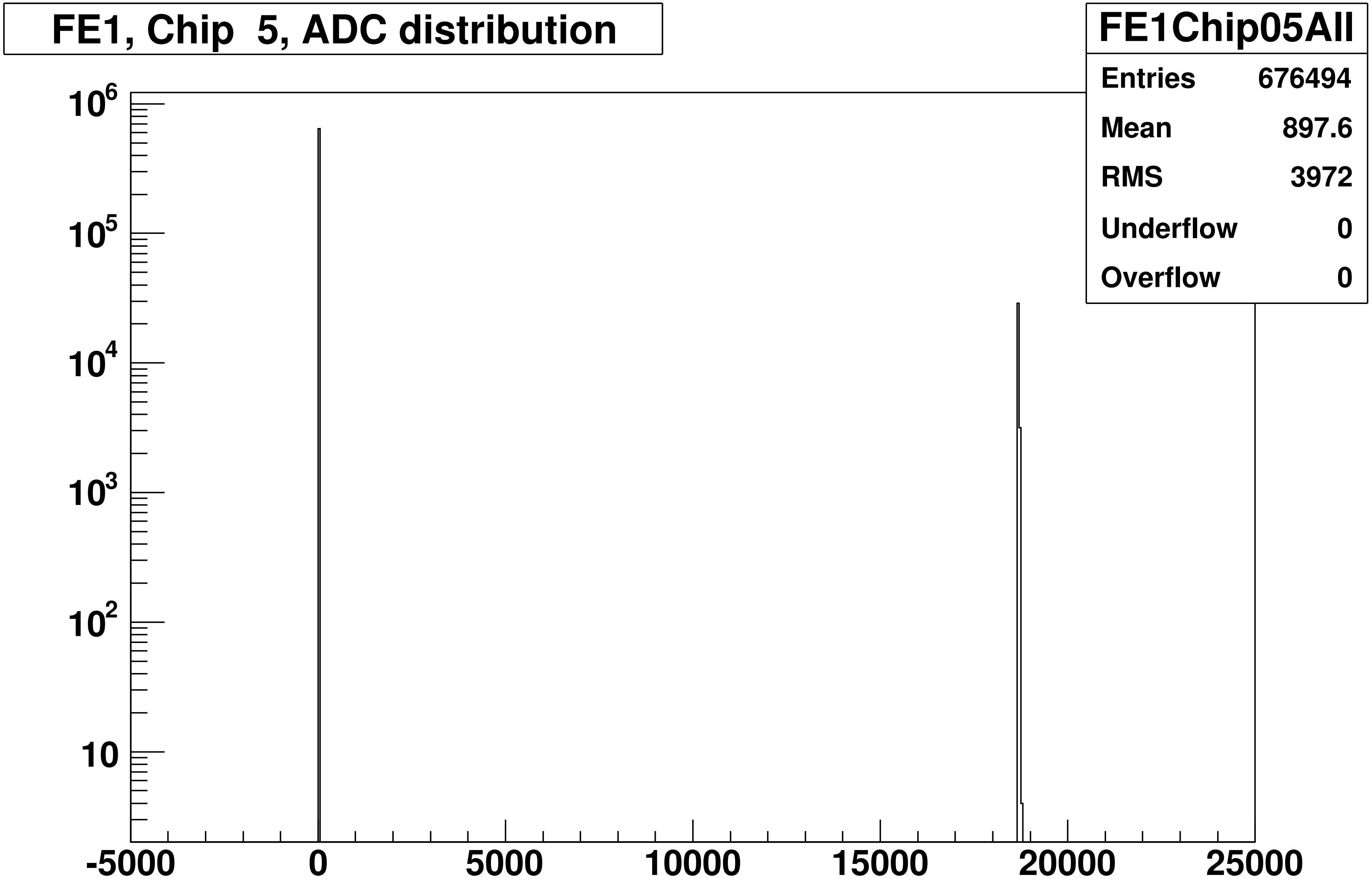
FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 676494
Mean 897.6
RMS 3972
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
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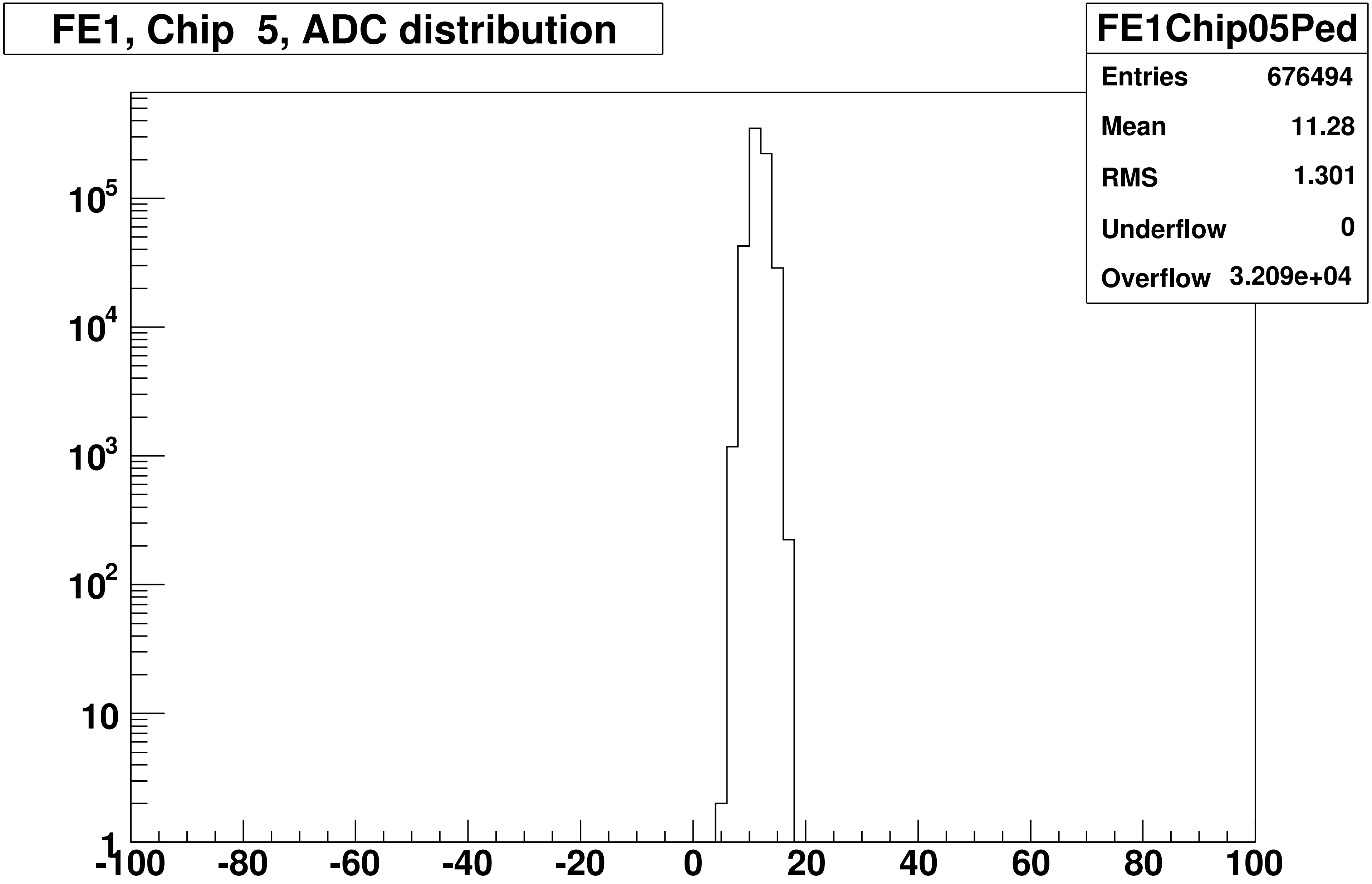
20000
25000


FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 676494
Mean 11.28
RMS 1.301
Underflow 0
Overflow 3.209e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
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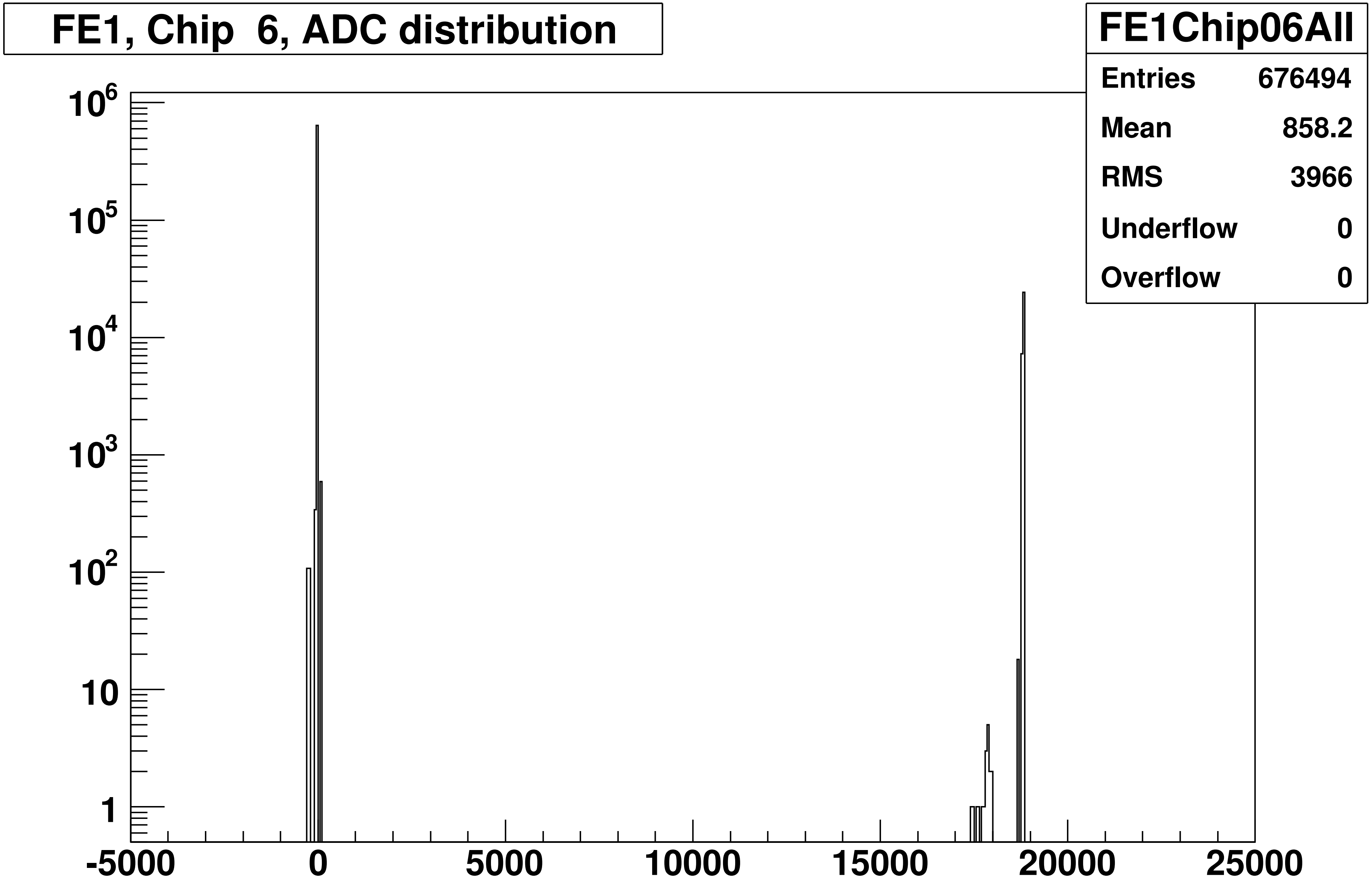
FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 676494
Mean 858.2
RMS 3966
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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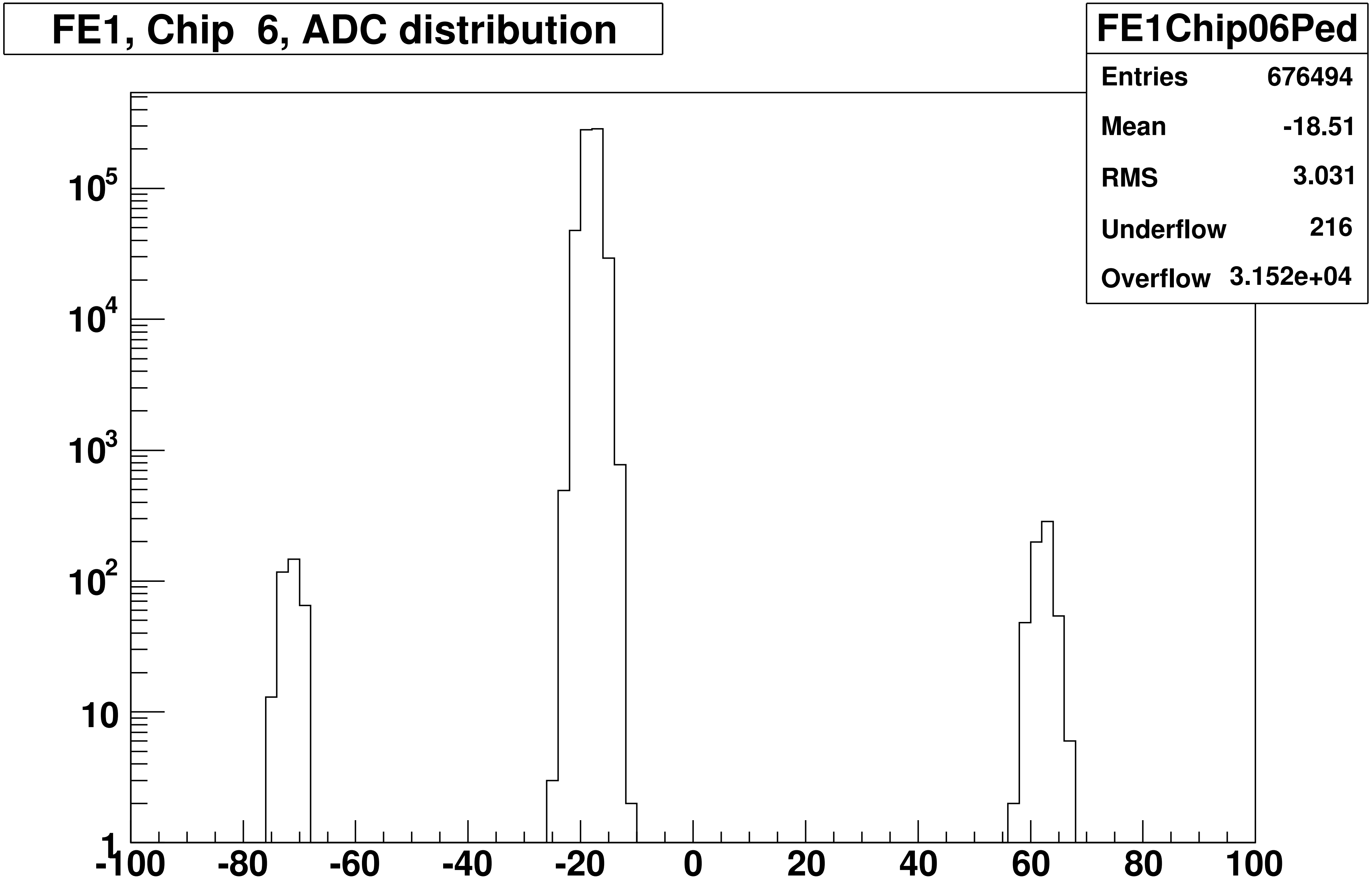
FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 676494
Mean -18.51
RMS 3.031
Underflow 216
Overflow 3.152e+04
10^5
10^4
10^3
10^2
10
1
-100
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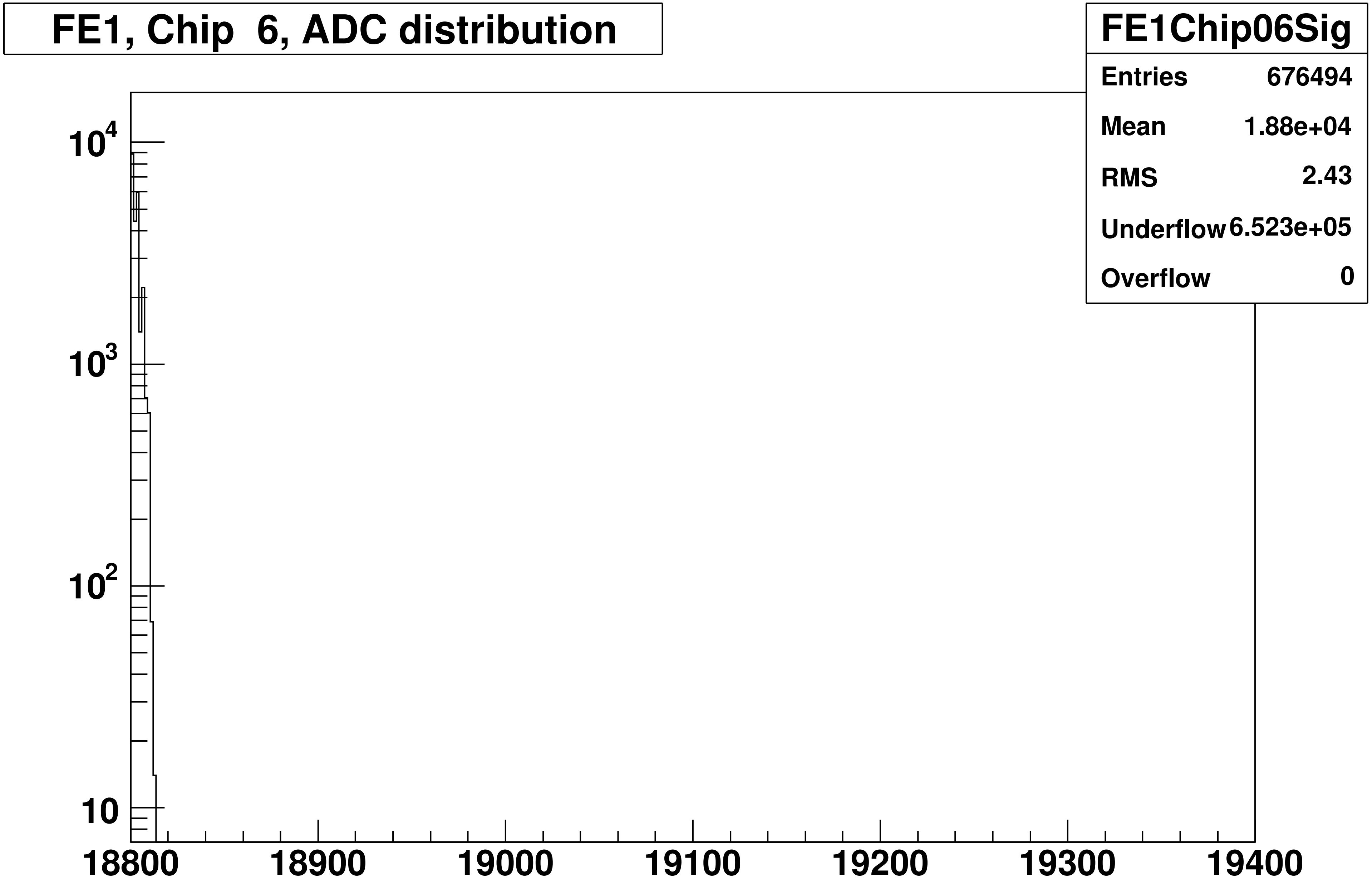
FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 676494
Mean 1.88e+04
RMS 2.43
Underflow 6.523e+05
Overflow 0
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10^3
10^2
10
18800
18900
19000
19100
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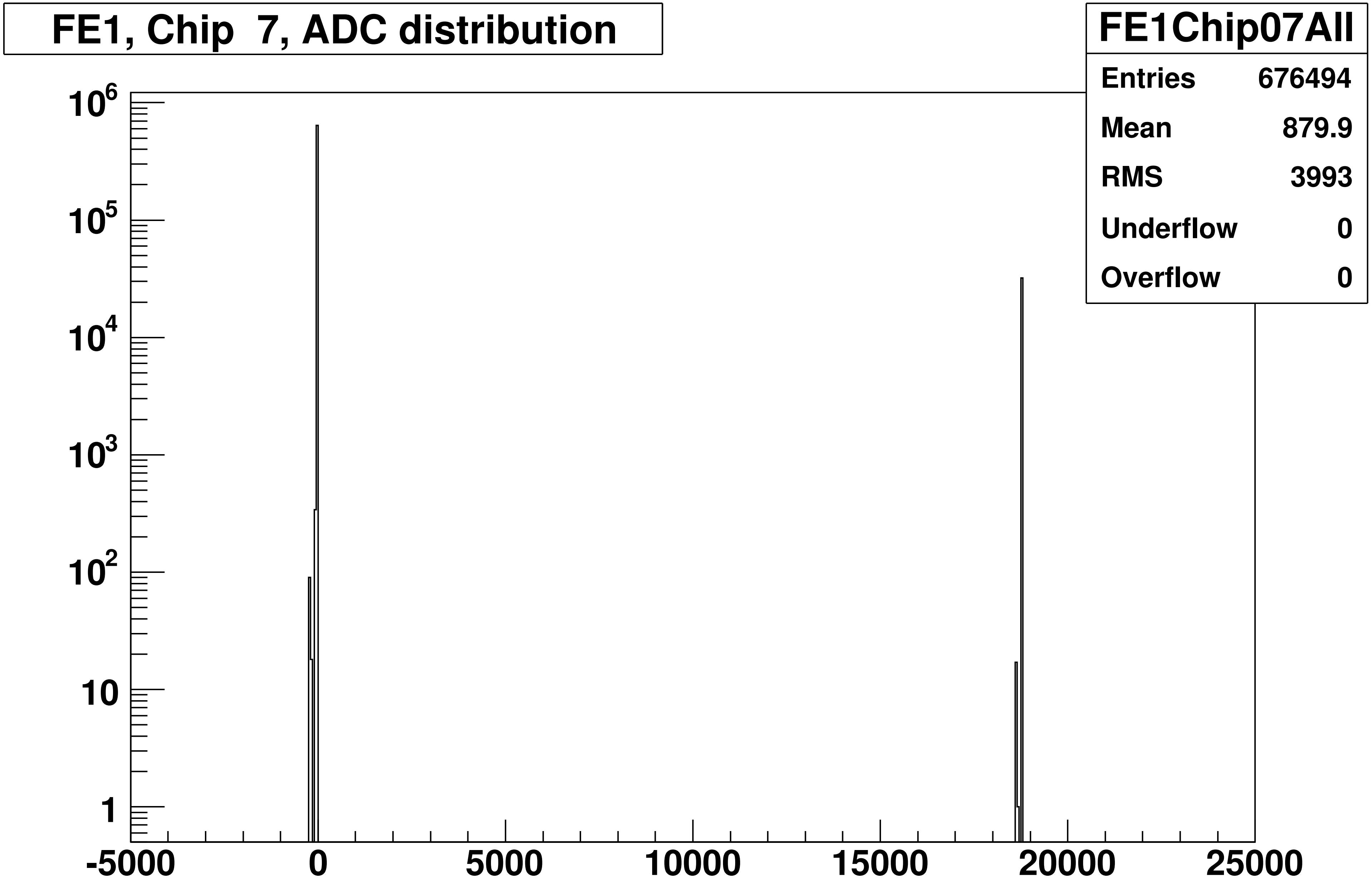
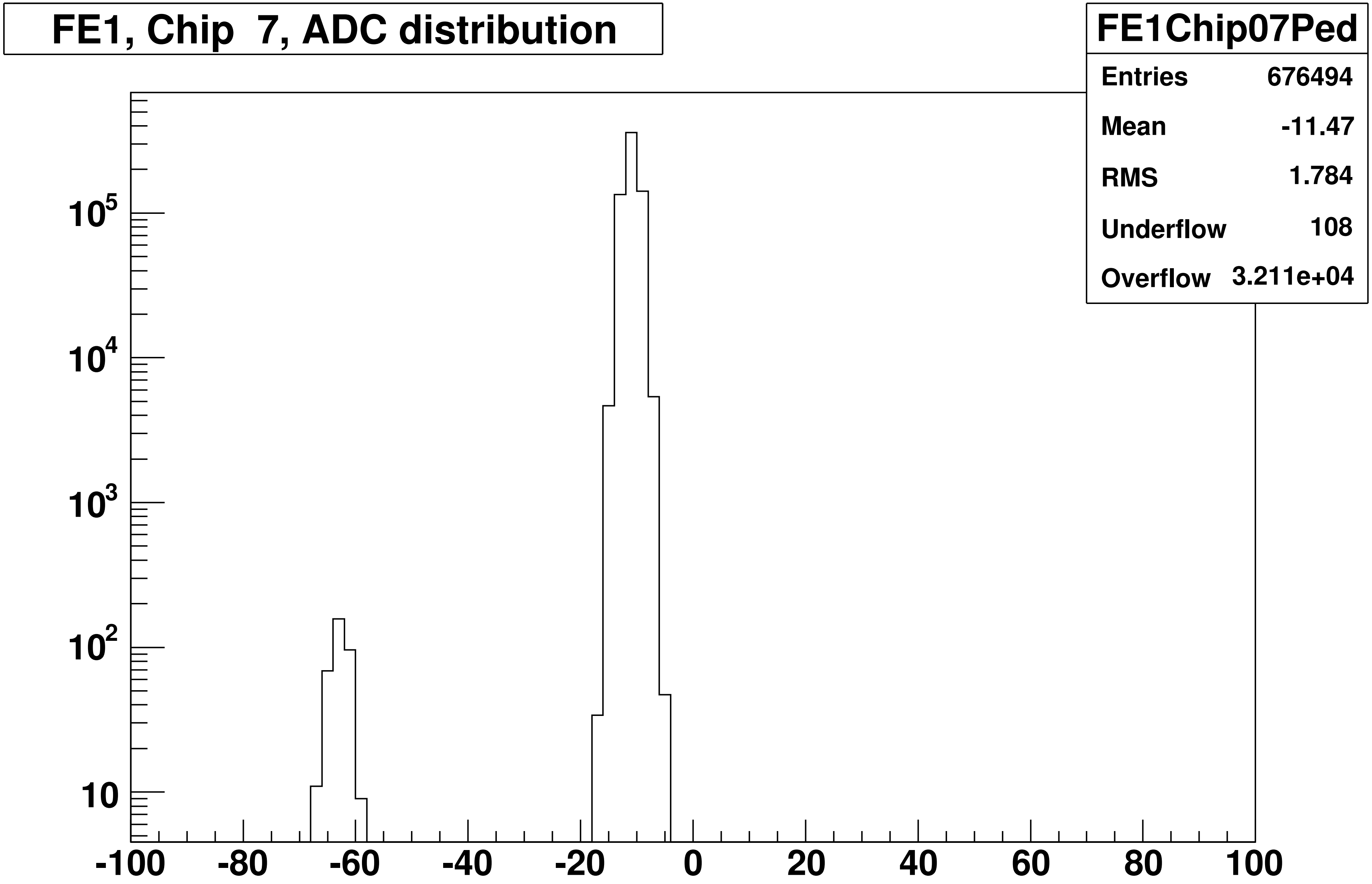
19300
19400


FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 676494
Mean 879.9
RMS 3993
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 676494
Mean -11.47
RMS 1.784
Underflow 108
Overflow 3.211e+04
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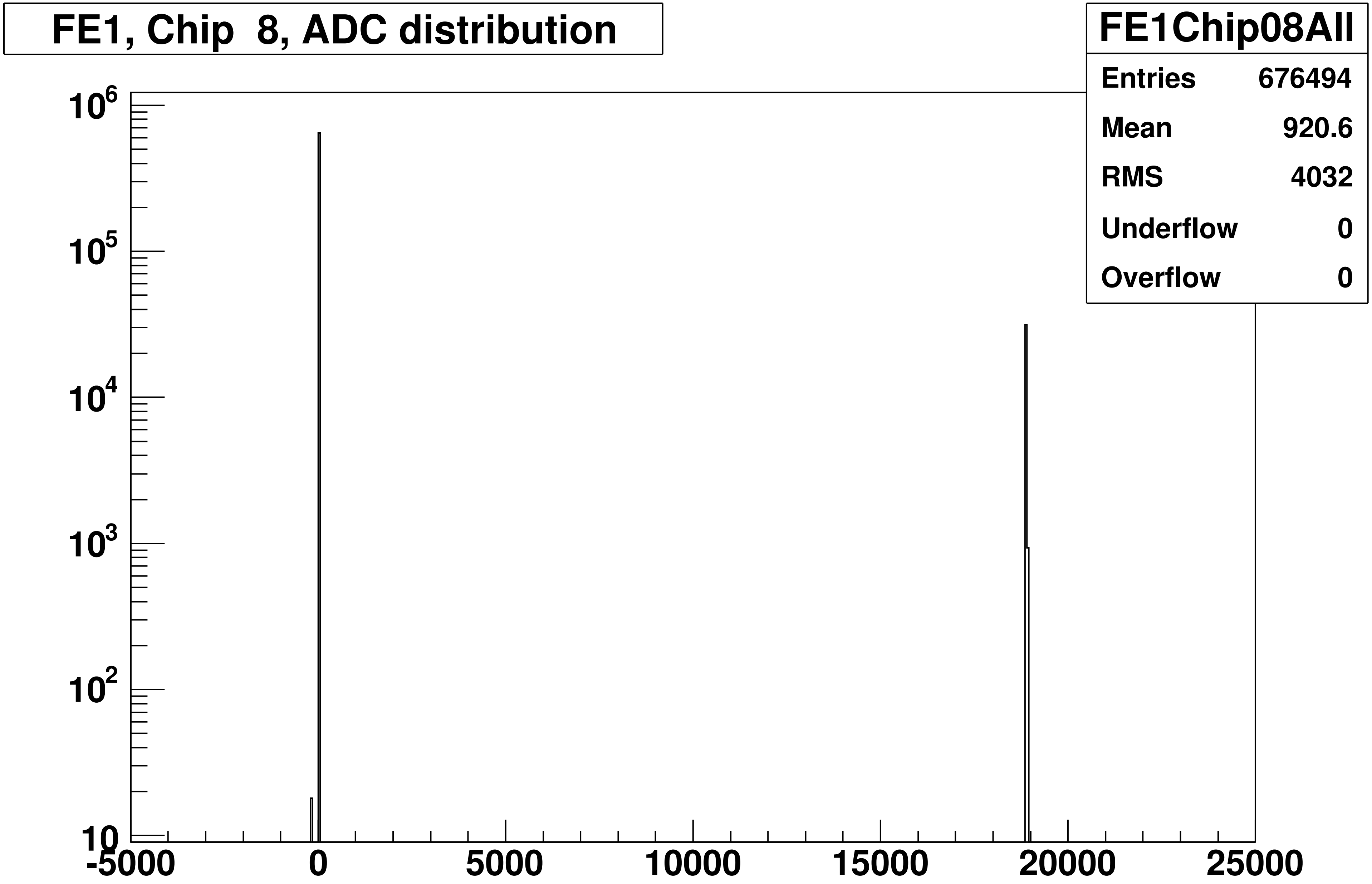
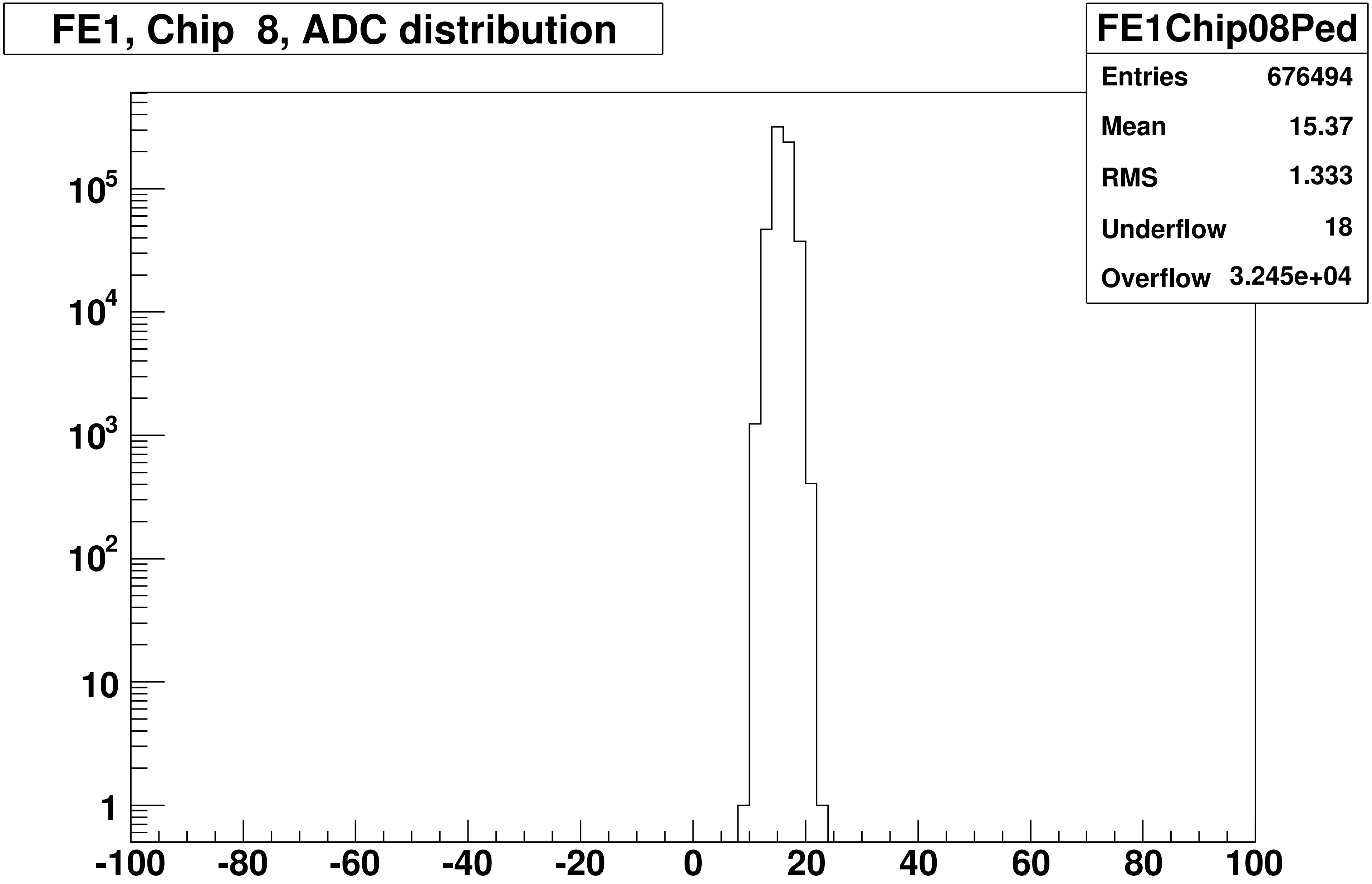
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 676494
Mean 920.6
RMS 4032
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 676494
Mean 15.37
RMS 1.333
Underflow 18
Overflow 3.245e+04
10^5
10^4
10^3
10^2
10
1
-100
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-40
-20
0
20
40
60
80
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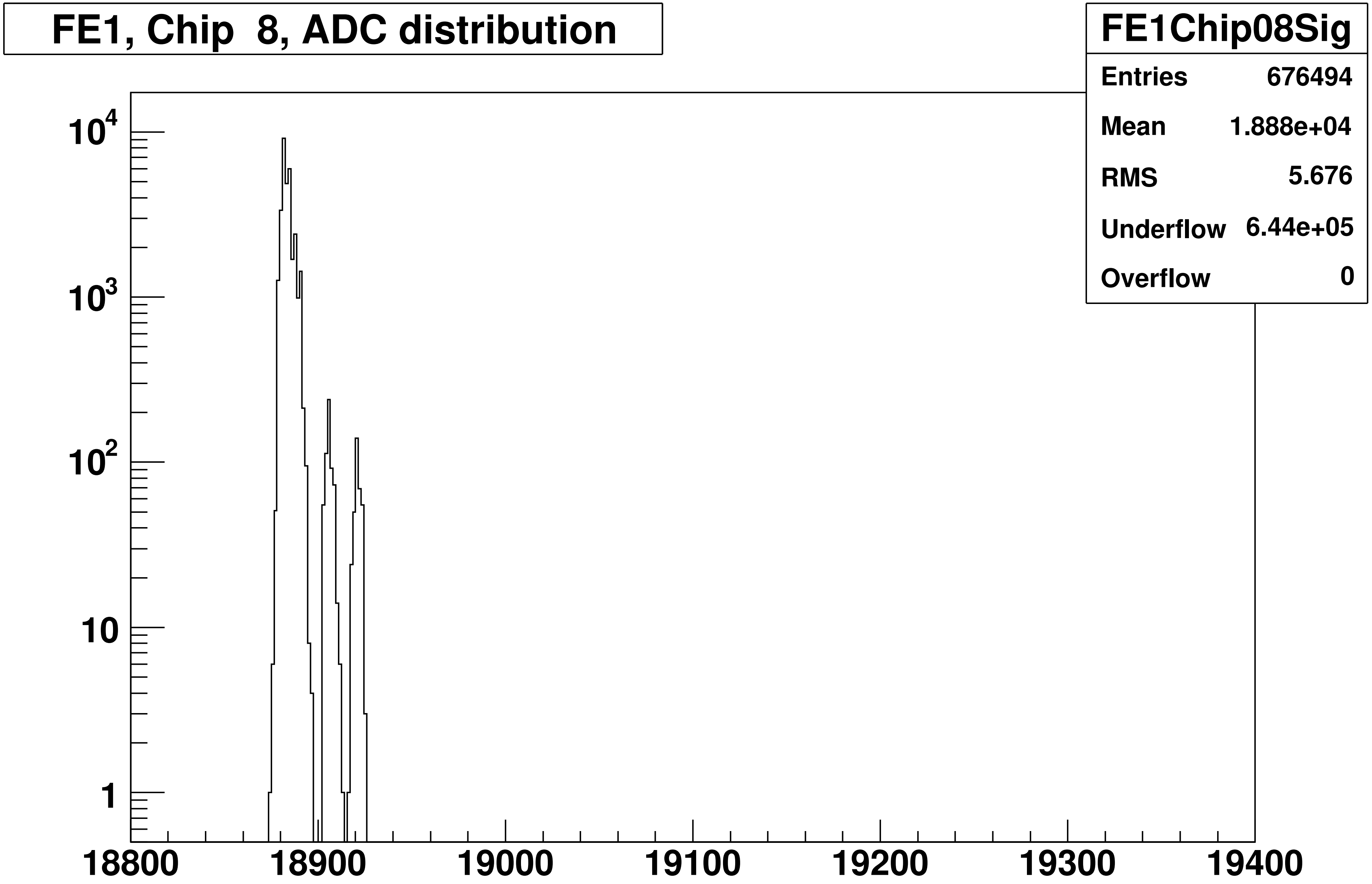

FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 676494
Mean 1.888e+04
RMS 5.676
Underflow 6.44e+05
Overflow 0
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10^3
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18800
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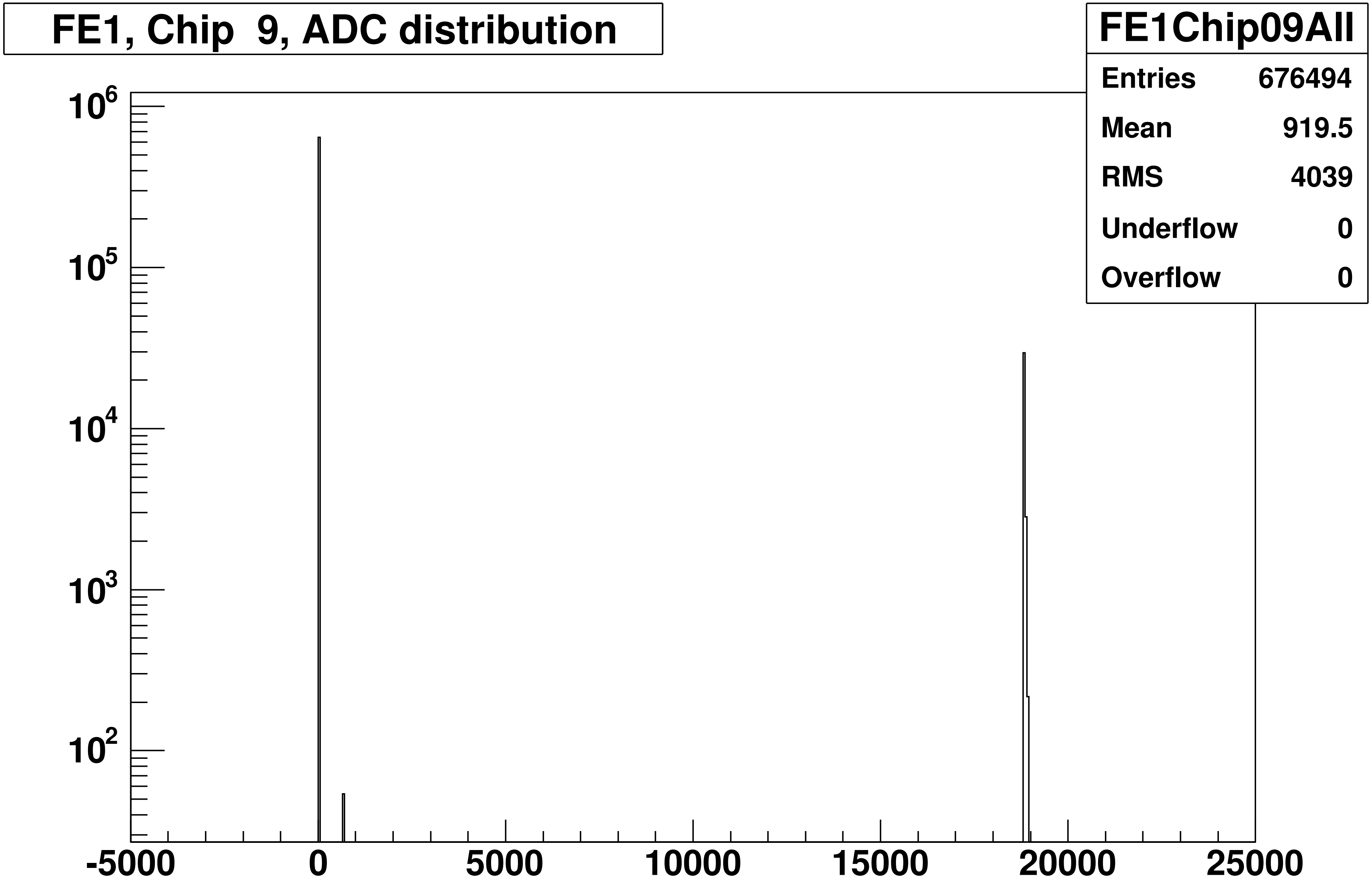
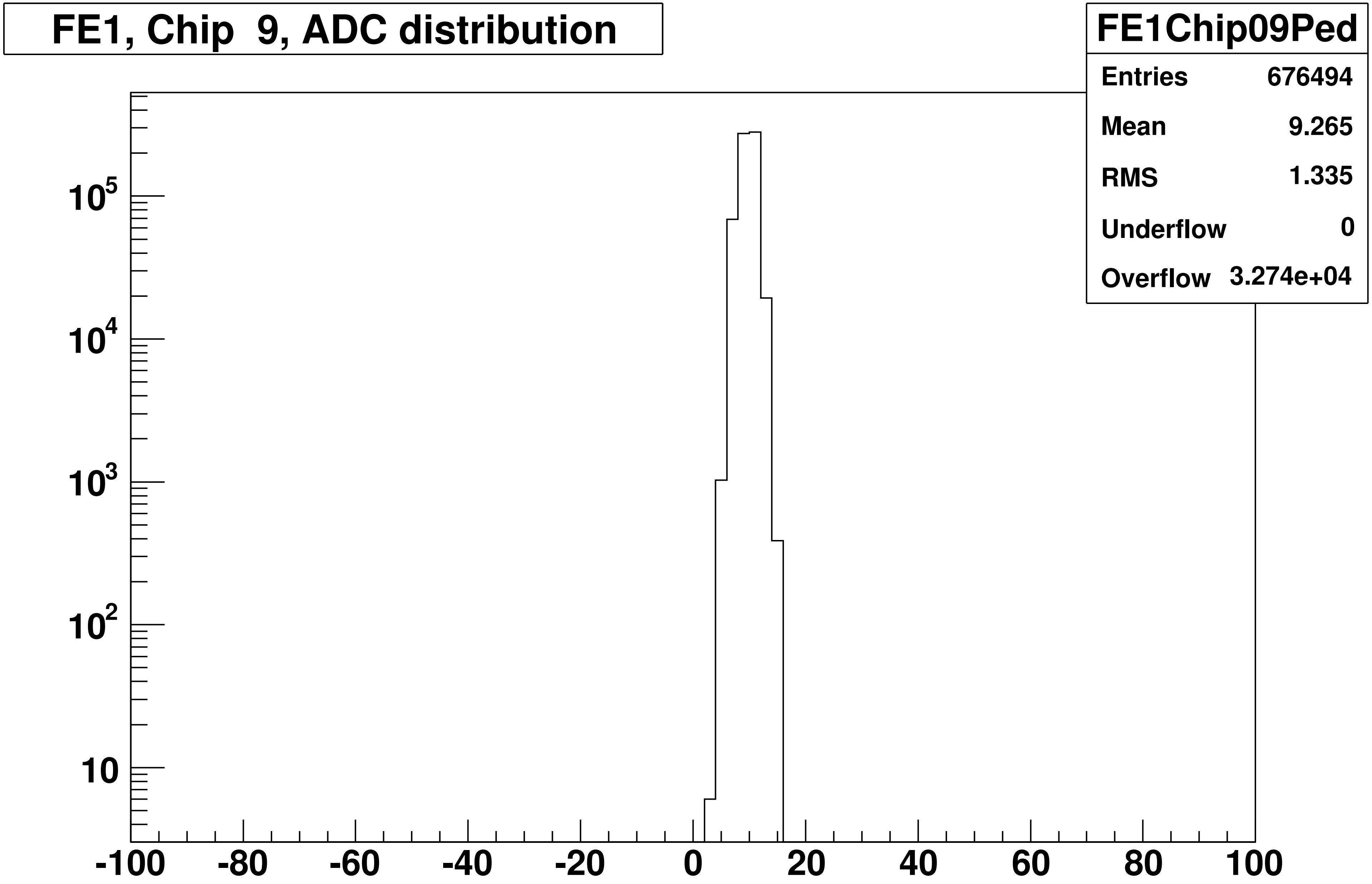

FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 676494
Mean 919.5
RMS 4039
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 676494
Mean 9.265
RMS 1.335
Underflow 0
Overflow 3.274e+04
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10^4
10^3
10^2
10
-100
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-40
-20
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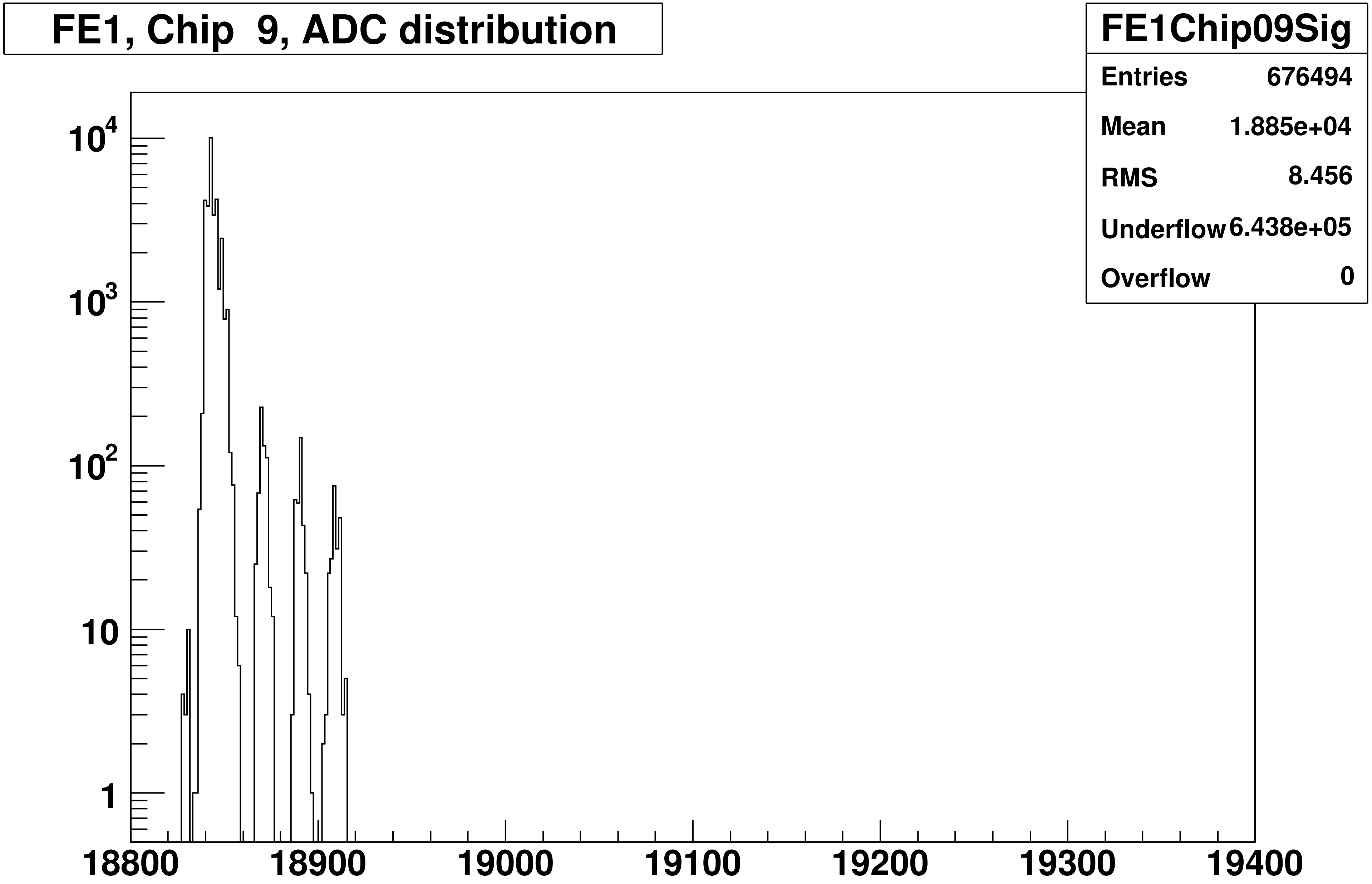
FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 676494
Mean 1.885e+04
RMS 8.456
Underflow 6.438e+05
Overflow 0
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10^3
10^2
10
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18800
18900
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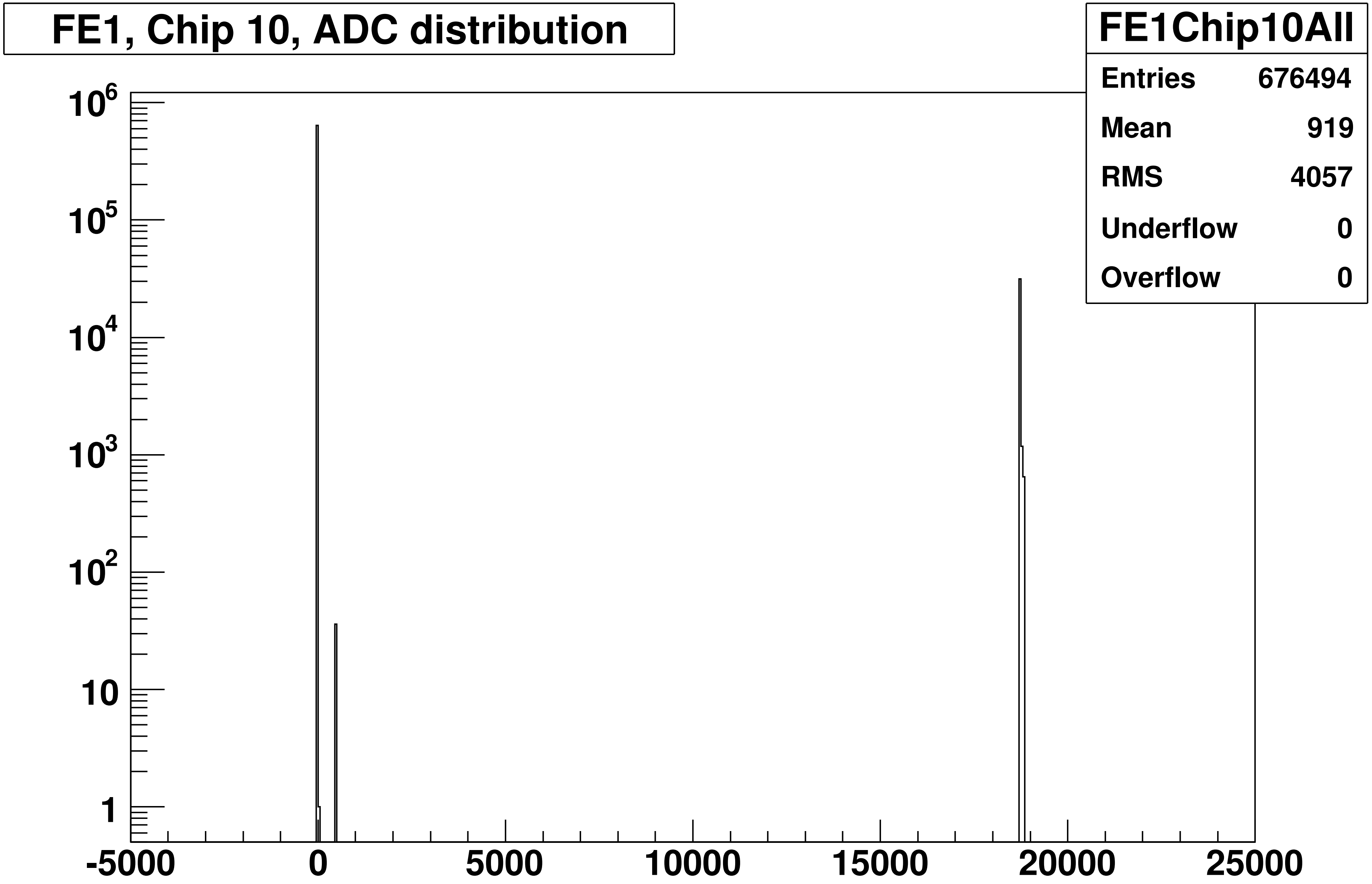
FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 676494
Mean 919
RMS 4057
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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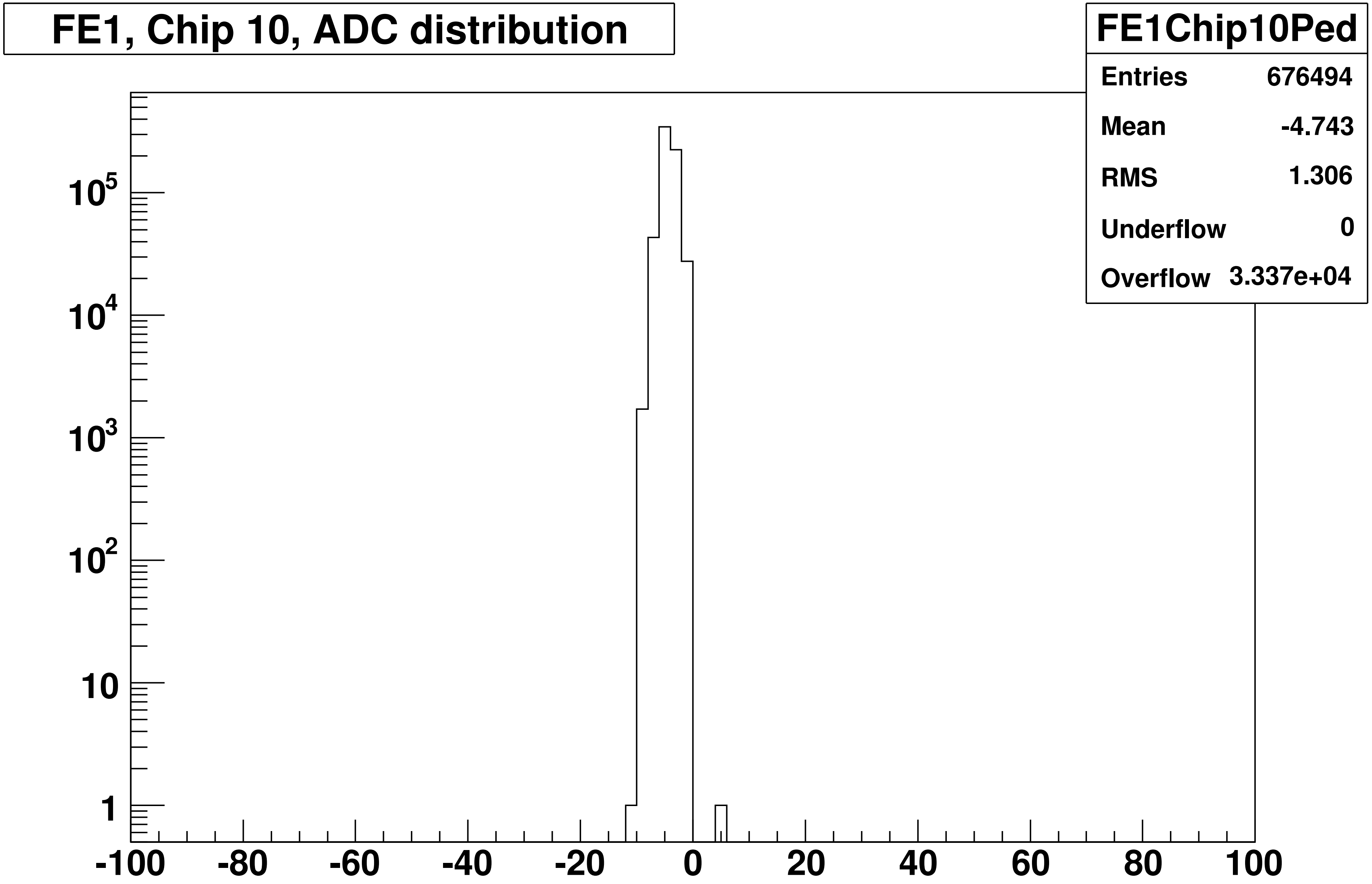
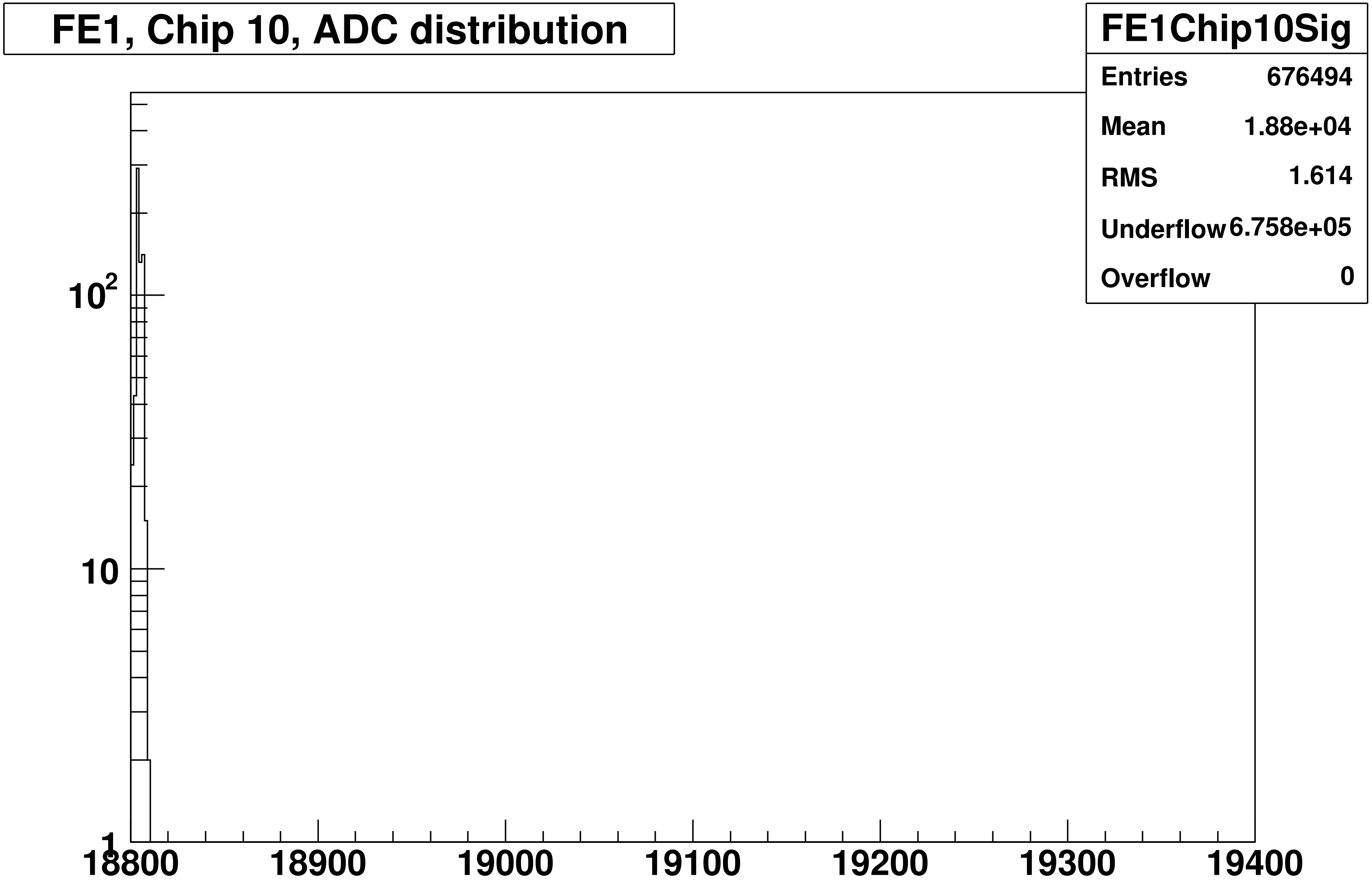


FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 676494
Mean -4.743
RMS 1.306
Underflow 0
Overflow 3.337e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 676494
Mean 1.88e+04
RMS 1.614
Underflow 6.758e+05
Overflow 0
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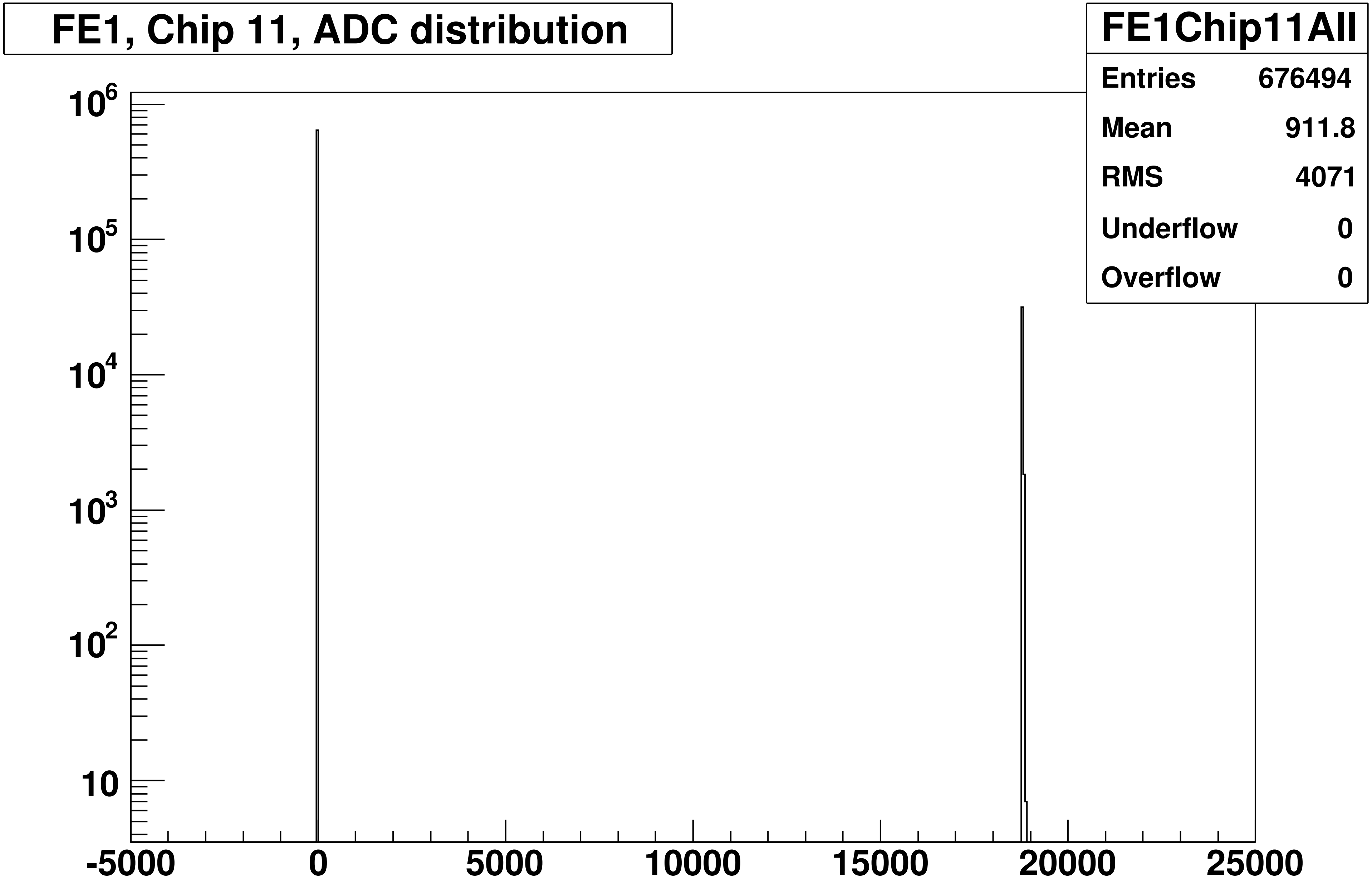

FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 676494
Mean 911.8
RMS 4071
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
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0
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10000
15000
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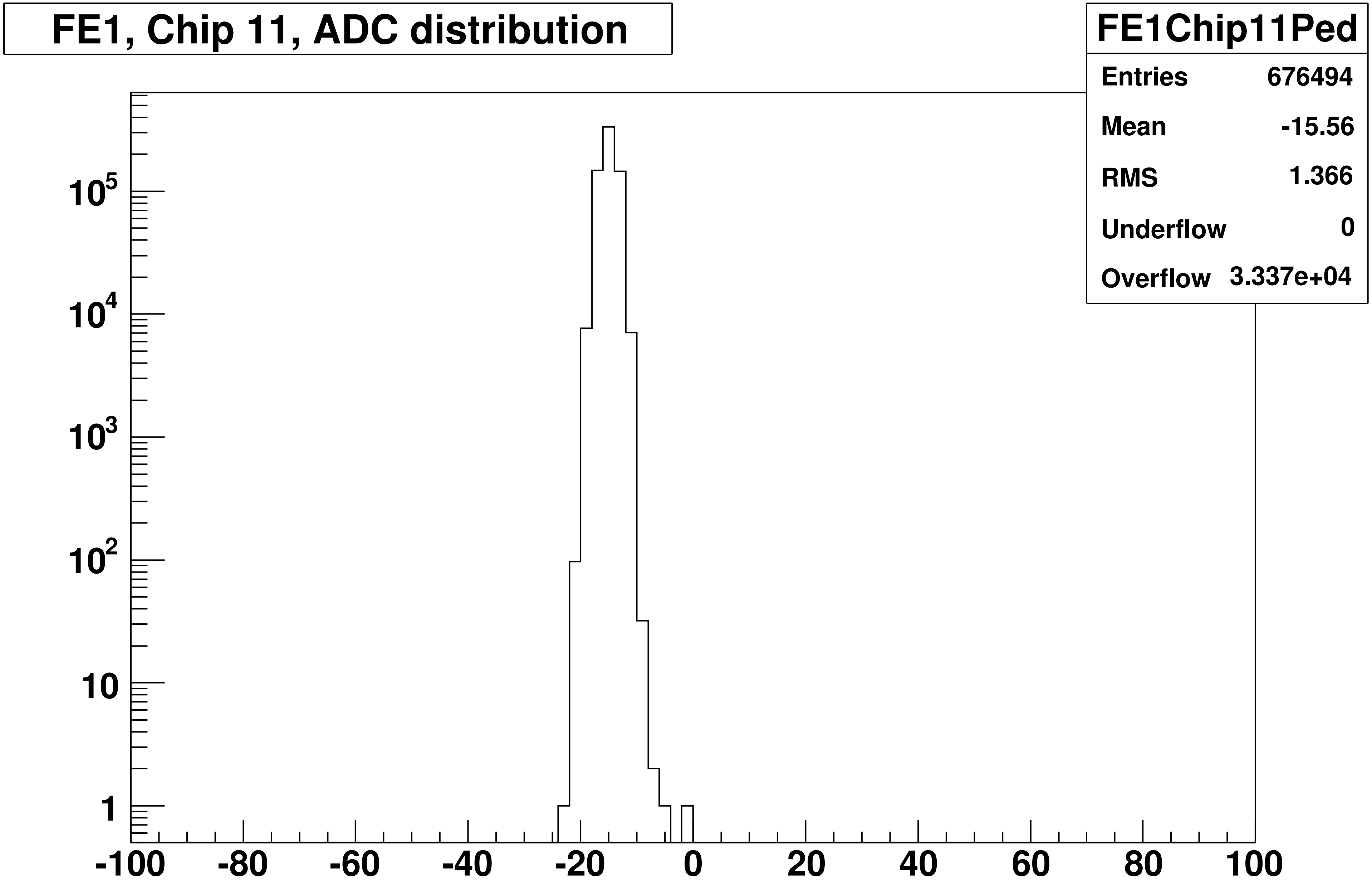

FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 676494
Mean -15.56
RMS 1.366
Underflow 0
Overflow 3.337e+04
10^5
10^4
10^3
10^2
10
1
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-20
0
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40
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80
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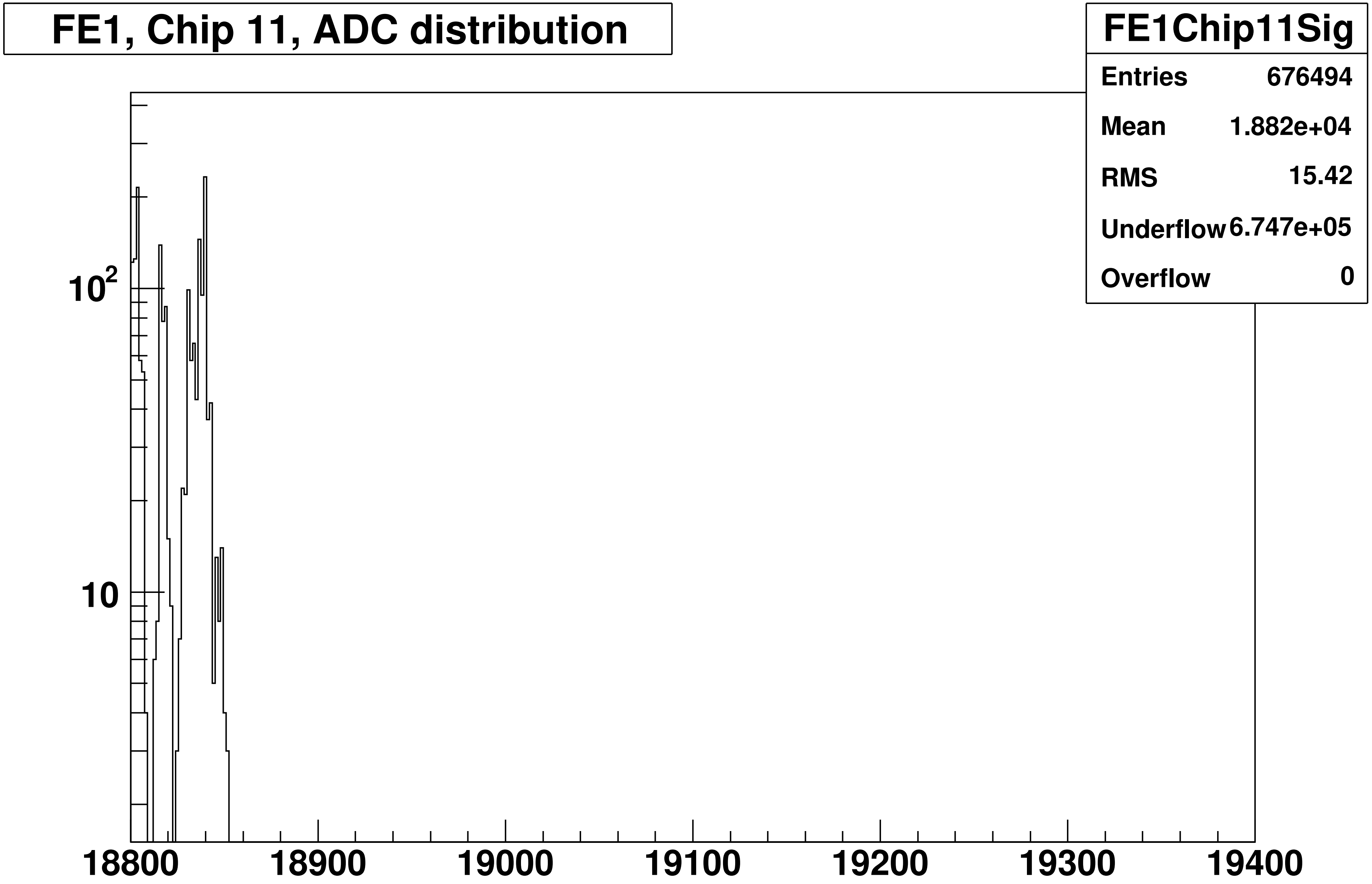

FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 676494
Mean 1.882e+04
RMS 15.42
Underflow 6.747e+05
Overflow 0
10^2
10
18800
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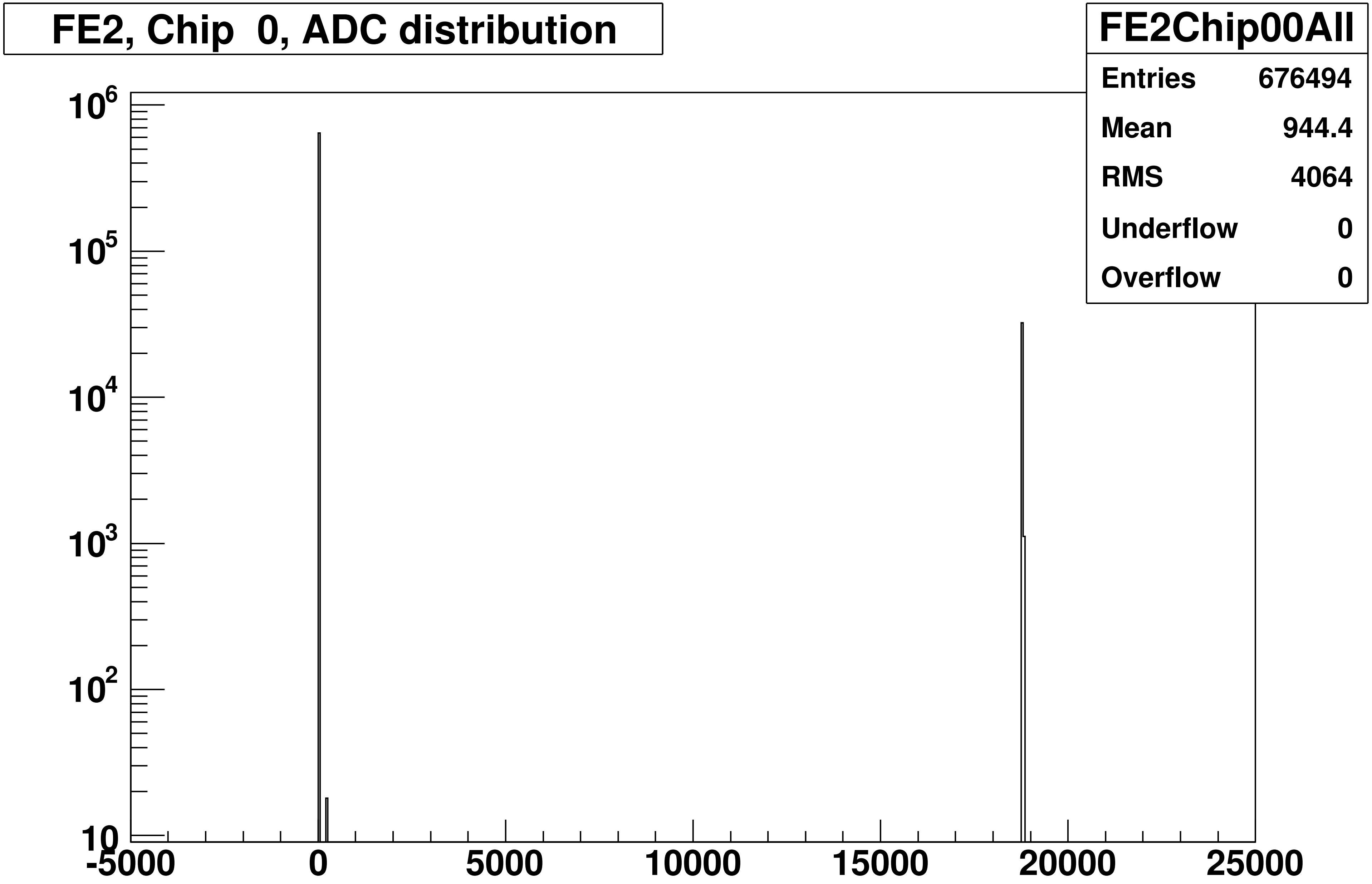

FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 676494
Mean 944.4
RMS 4064
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
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20000
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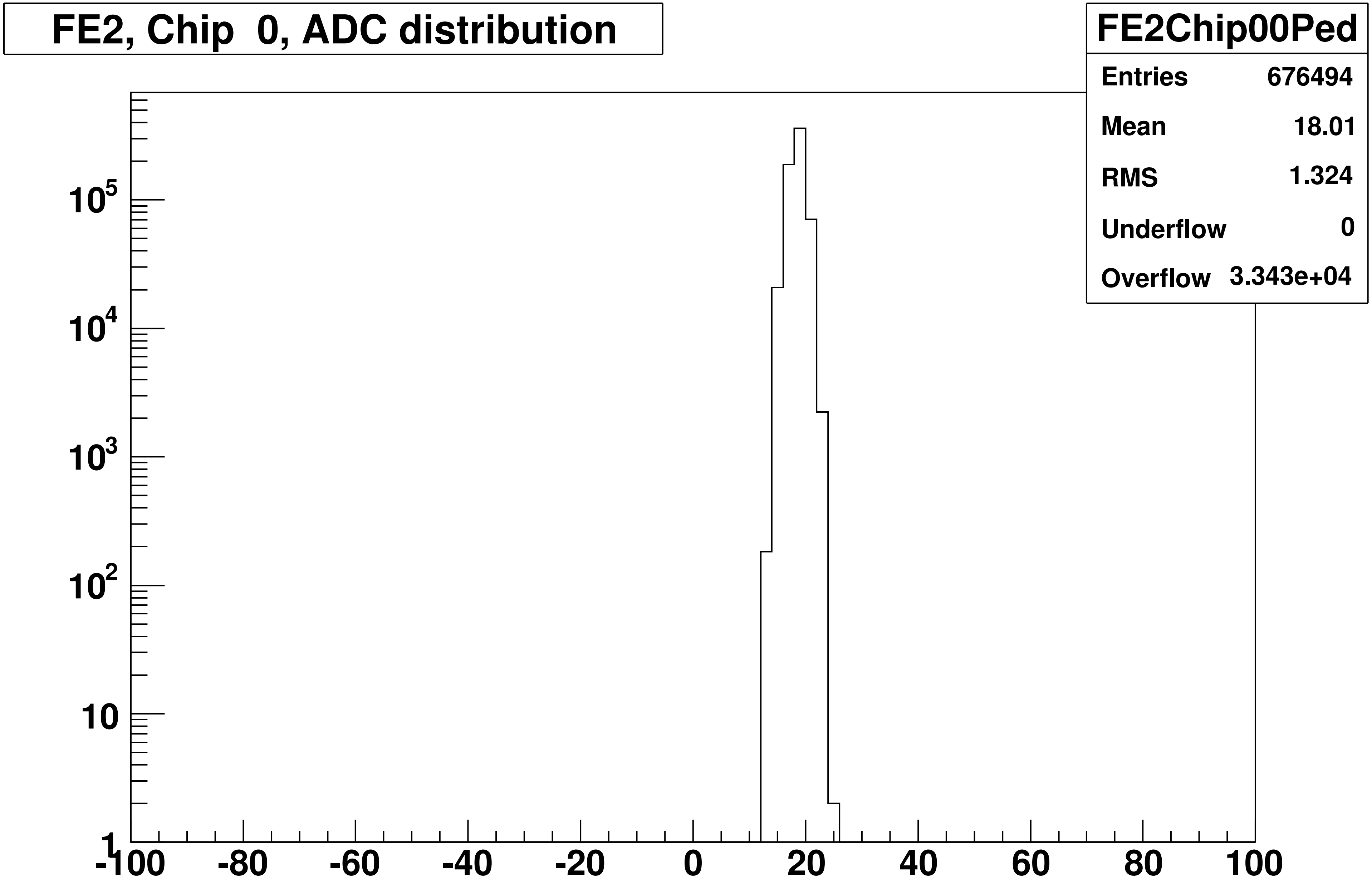
FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 676494
Mean 18.01
RMS 1.324
Underflow 0
Overflow 3.343e+04
1
10
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10^3
10^4
10^5
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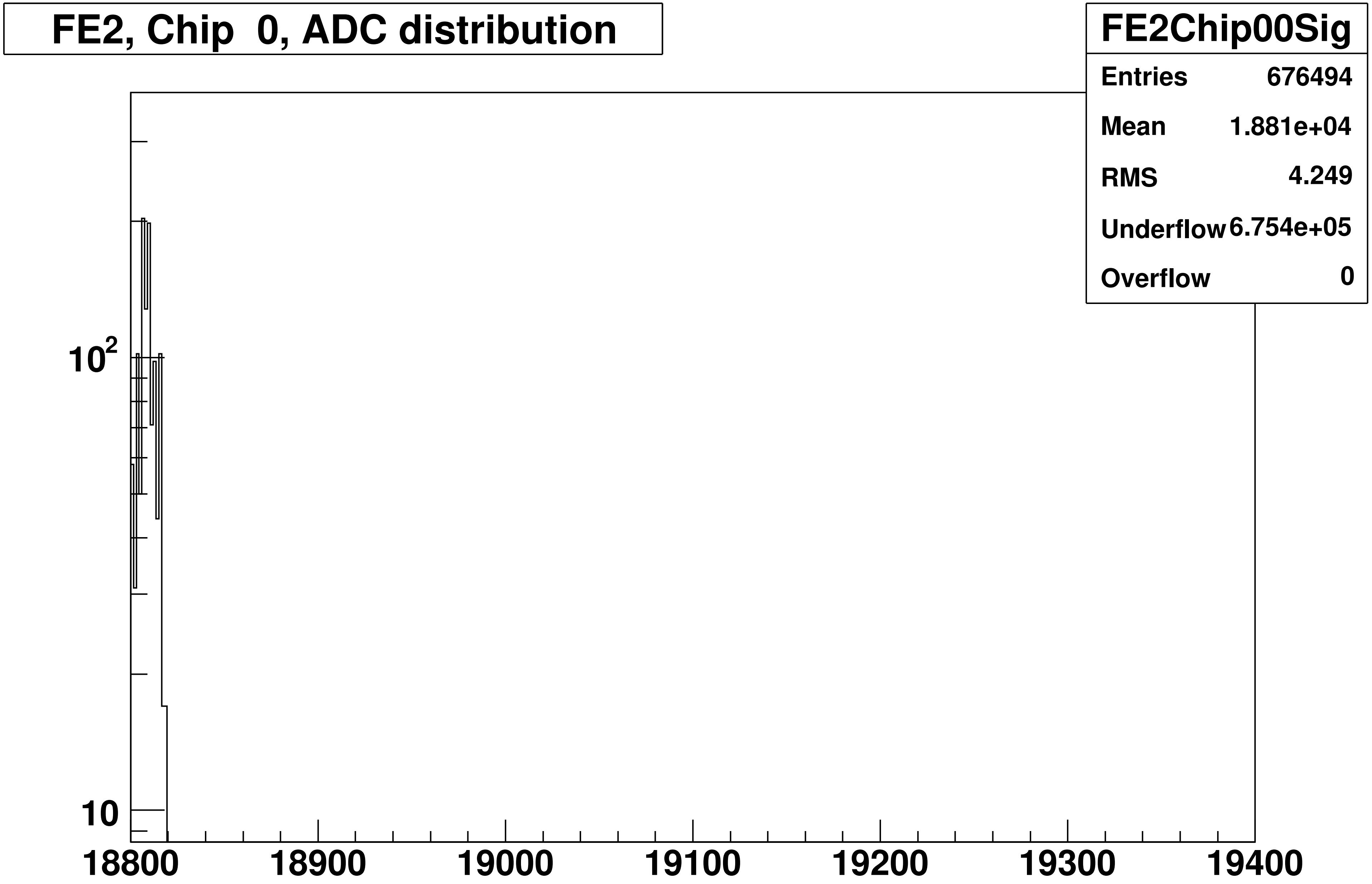

FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 676494
Mean 1.881e+04
RMS 4.249
Underflow 6.754e+05
Overflow 0
10^2
10
18800
18900
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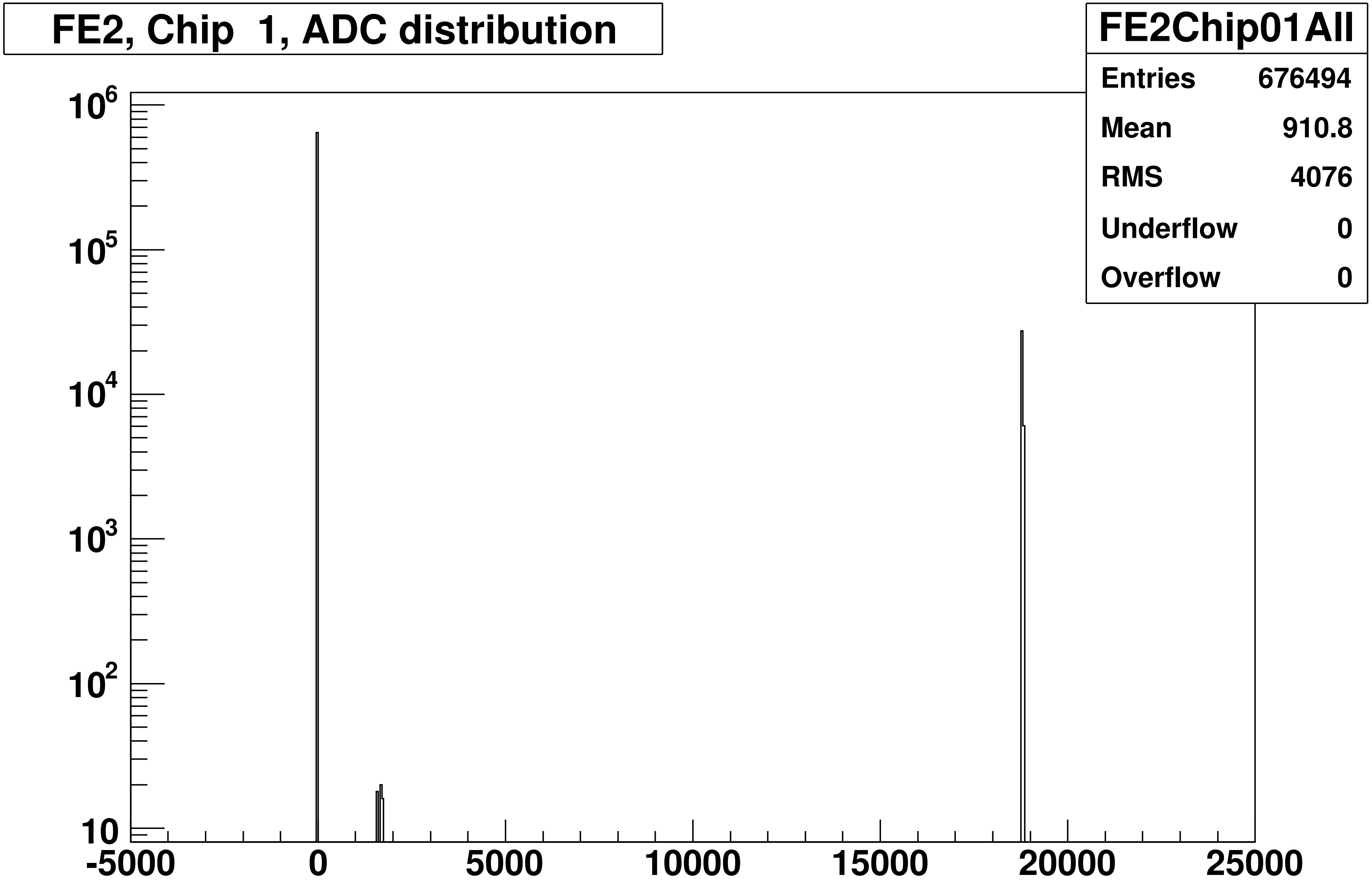
19100
19200
19300
19400


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 676494
Mean 910.8
RMS 4076
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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10000
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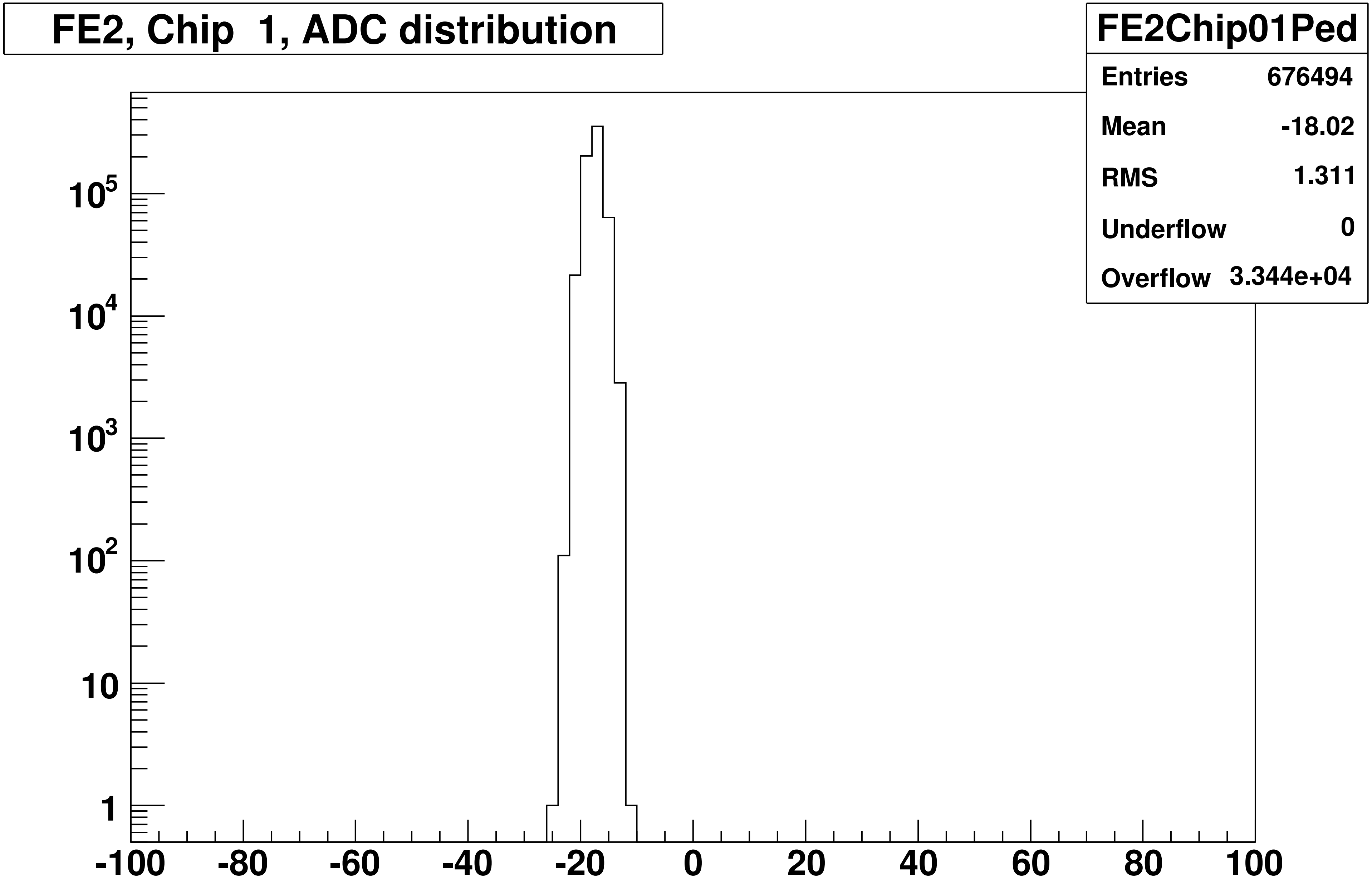
20000
25000


FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 676494
Mean -18.02
RMS 1.311
Underflow 0
Overflow 3.344e+04
10^5
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10^3
10^2
10
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-20
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20
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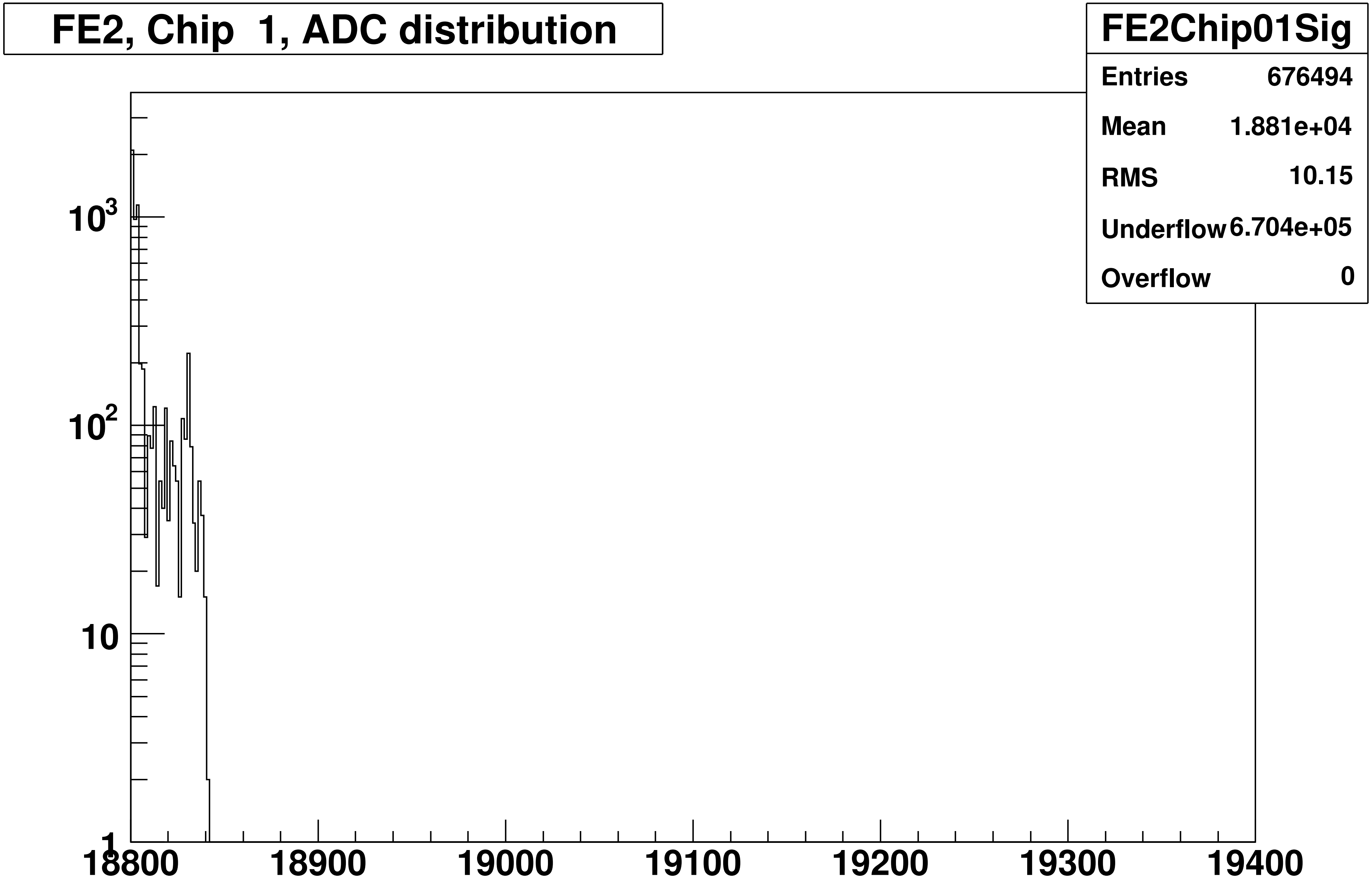

FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 676494
Mean 1.881e+04
RMS 10.15
Underflow 6.704e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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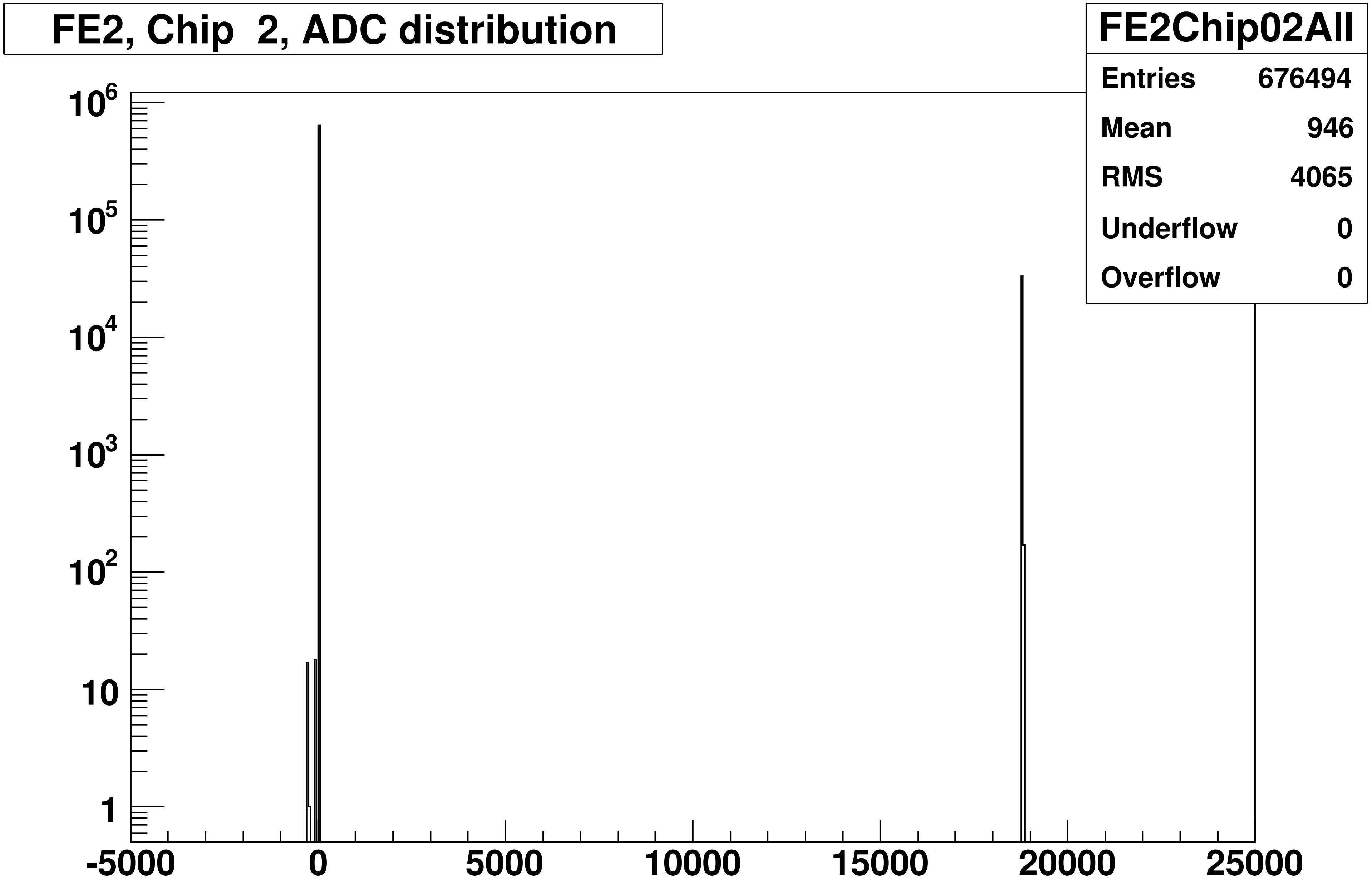
19100
19200
19300
19400


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 676494
Mean 946
RMS 4065
Underflow 0
Overflow 0
10^6
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10^4
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10^2
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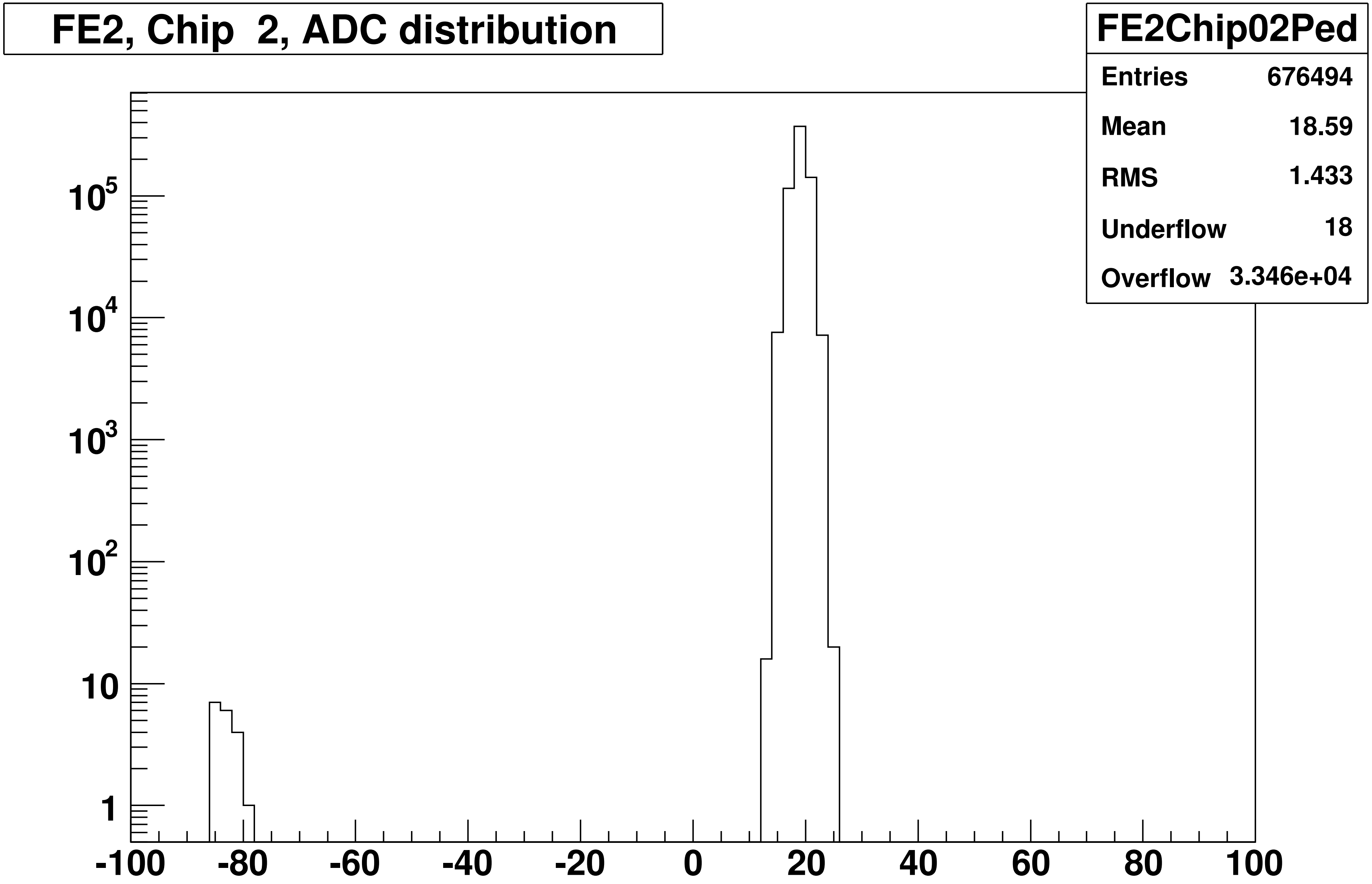
FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 676494
Mean 18.59
RMS 1.433
Underflow 18
Overflow 3.346e+04
10^5
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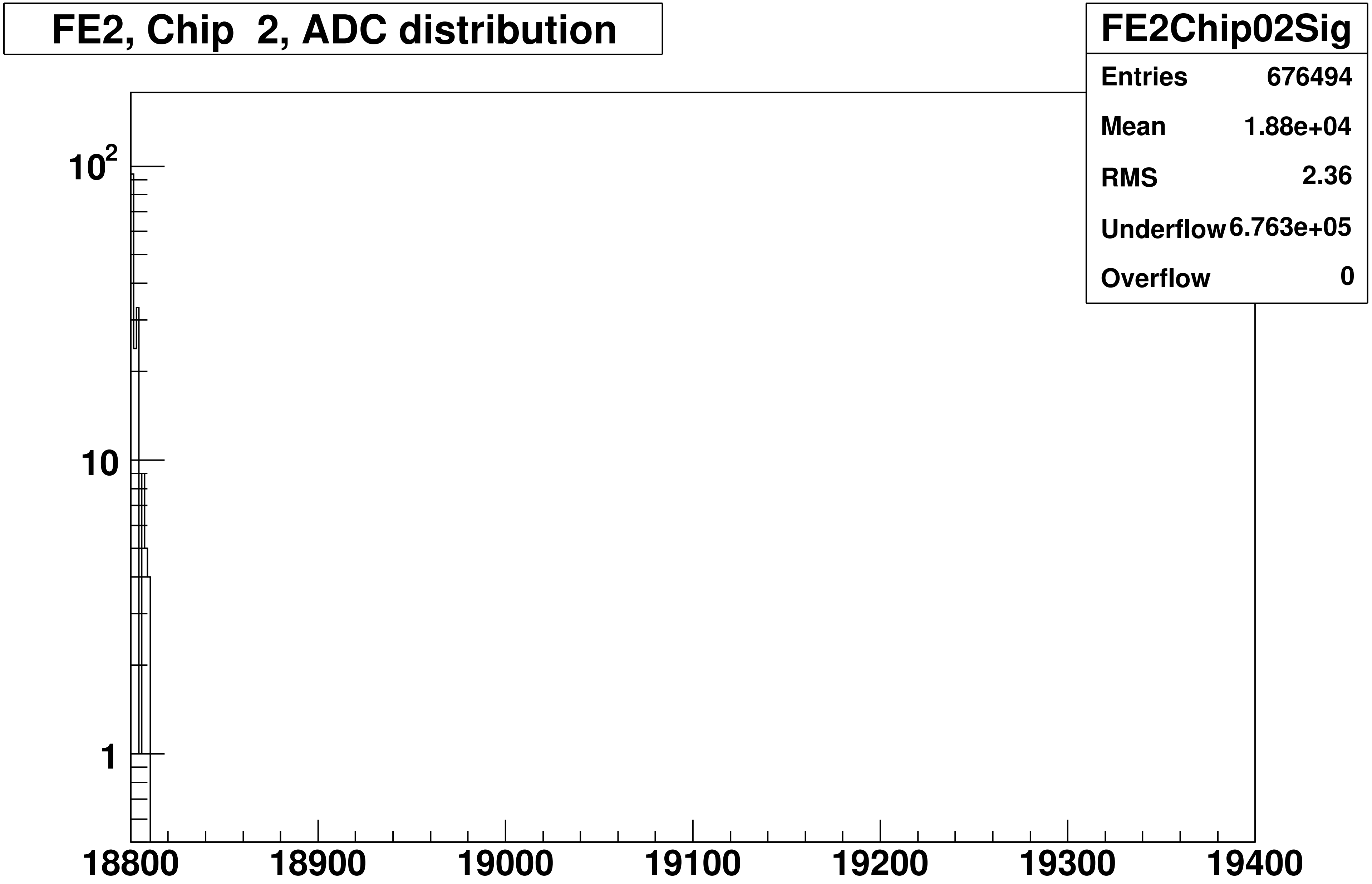
FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 676494
Mean 1.88e+04
RMS 2.36
Underflow 6.763e+05
Overflow 0
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18900
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19100
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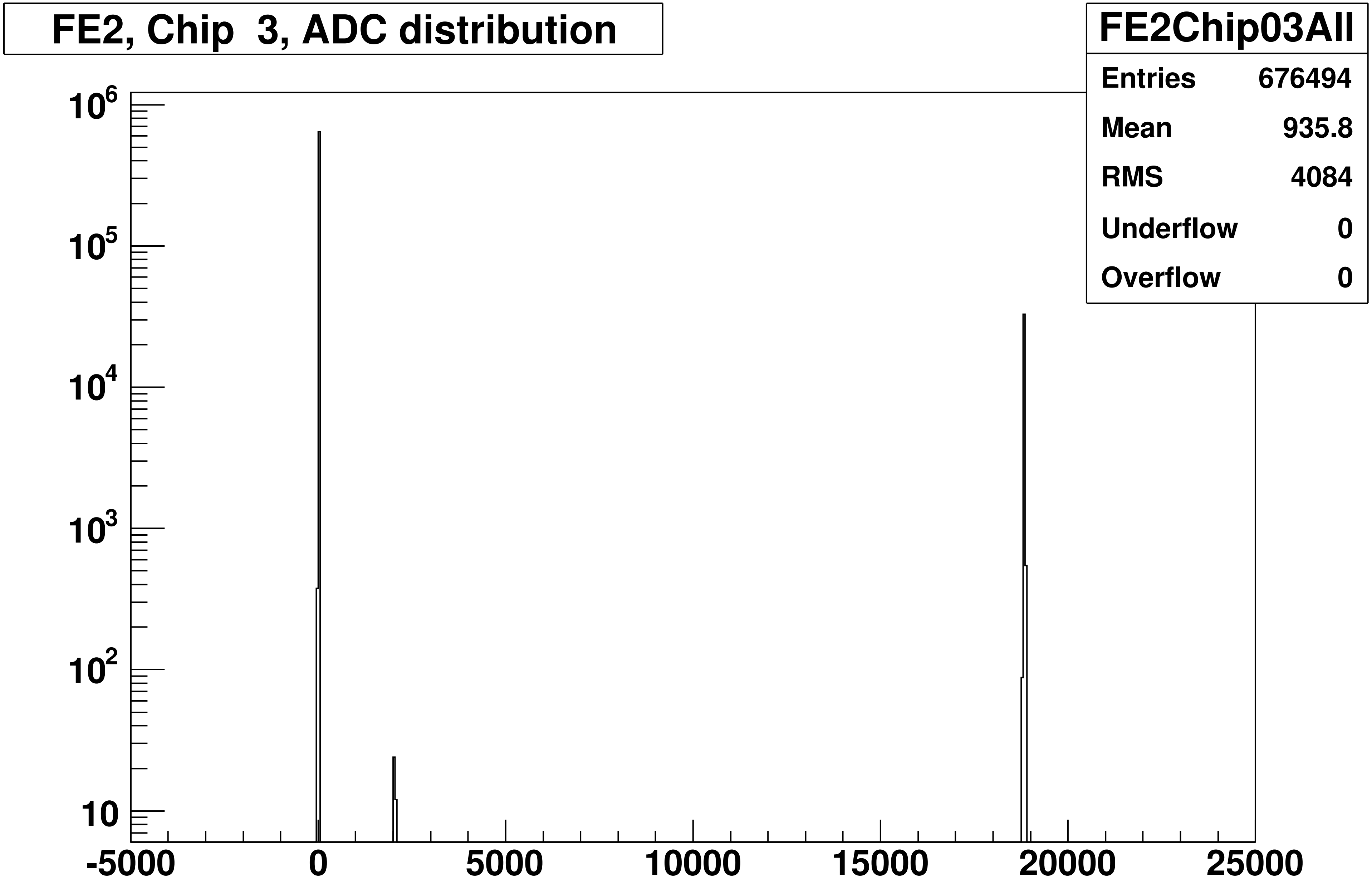
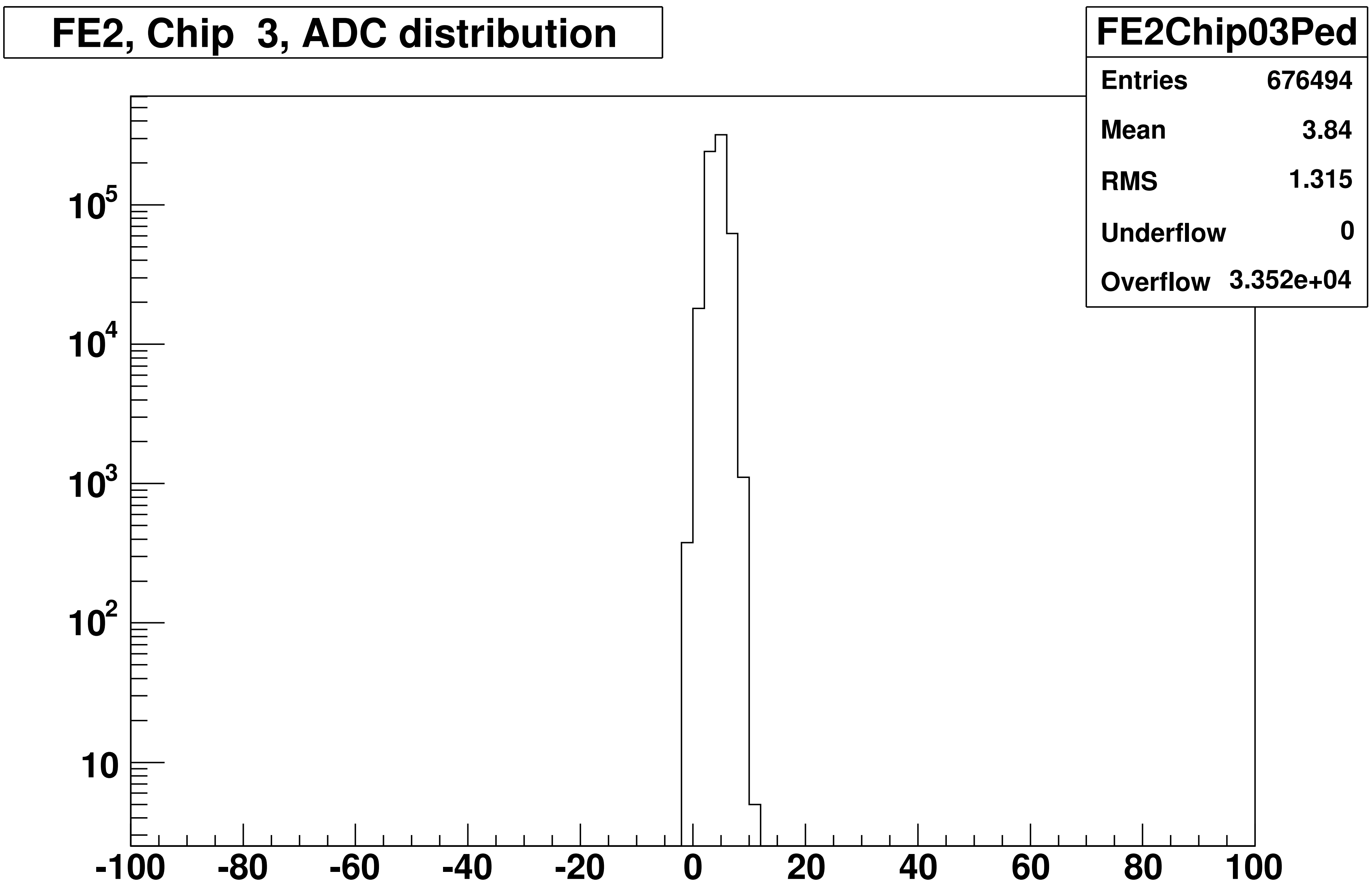
19300
19400


FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 676494
Mean 935.8
RMS 4084
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 676494
Mean 3.84
RMS 1.315
Underflow 0
Overflow 3.352e+04
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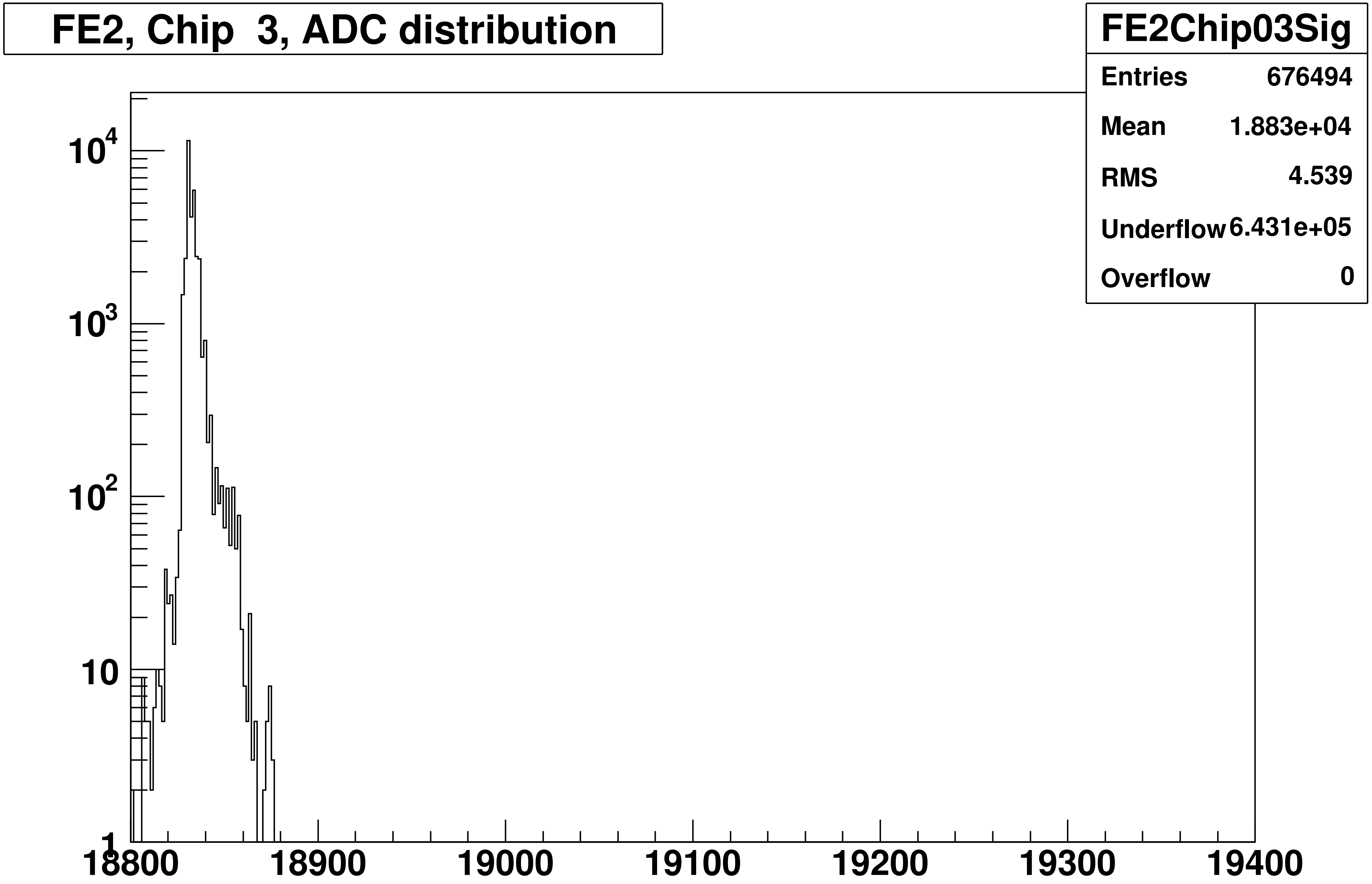
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 676494
Mean 1.883e+04
RMS 4.539
Underflow 6.431e+05
Overflow 0
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10^3
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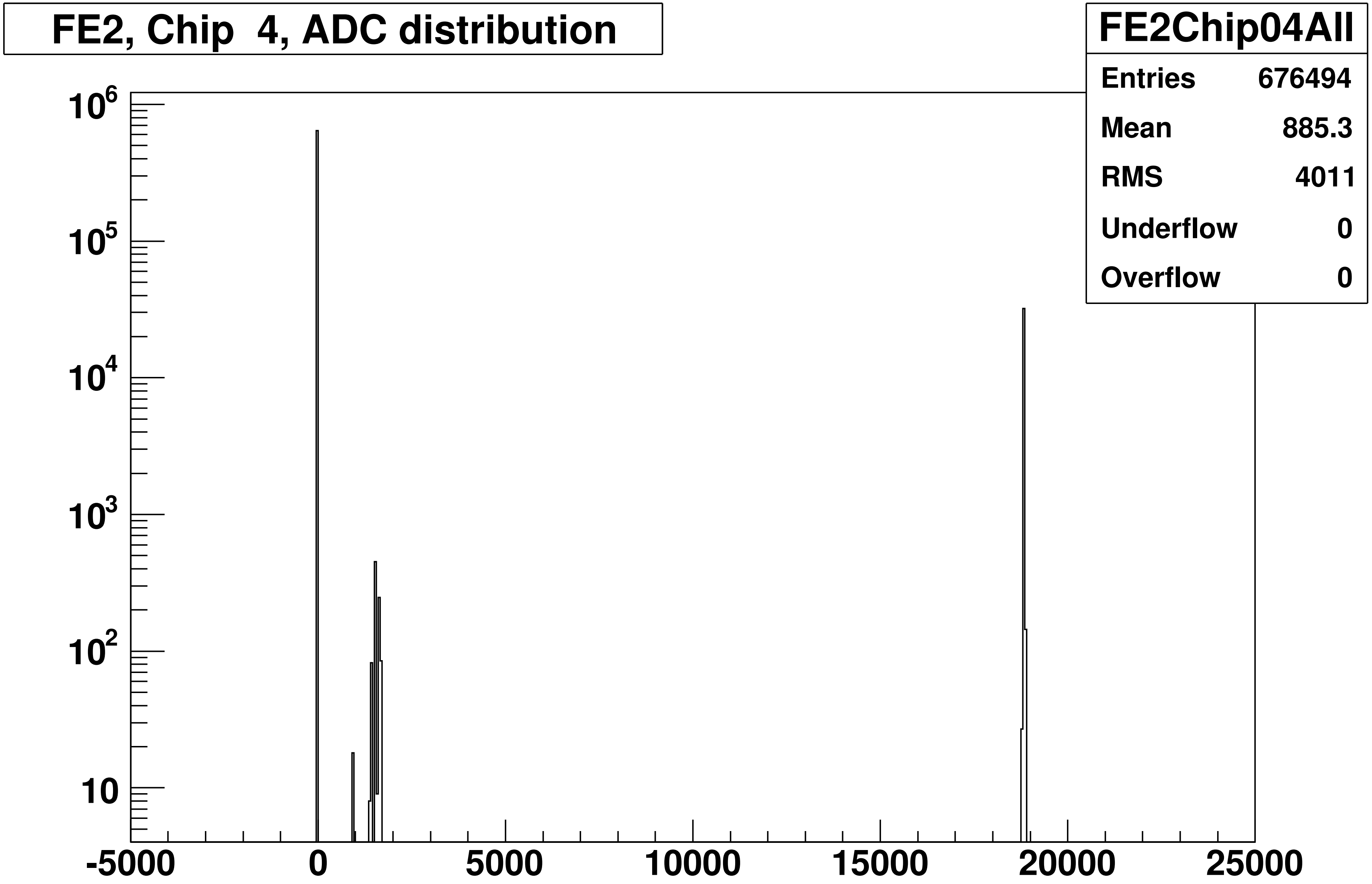
FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 676494
Mean 885.3
RMS 4011
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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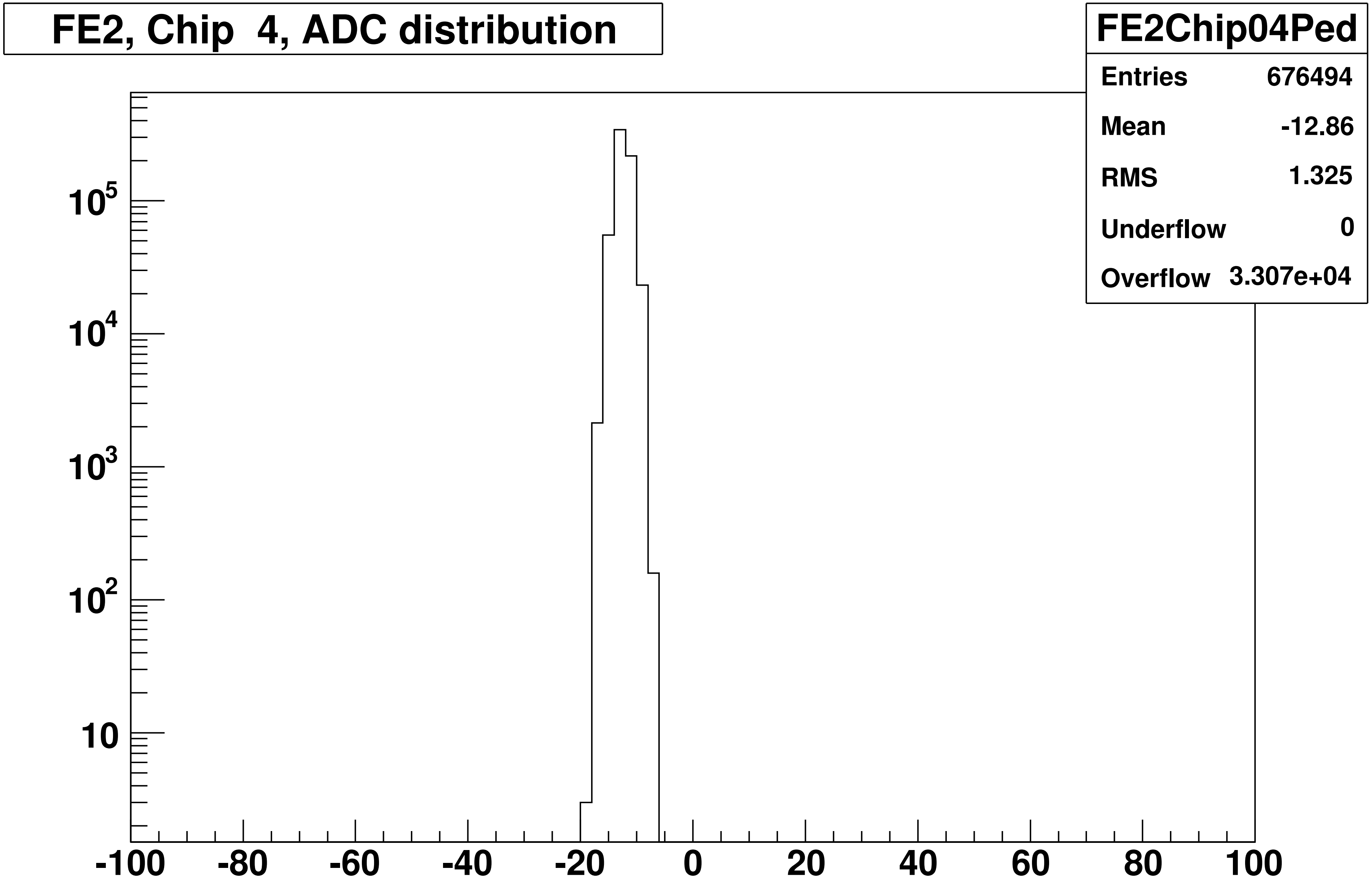
FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 676494
Mean -12.86
RMS 1.325
Underflow 0
Overflow 3.307e+04
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10^4
10^5
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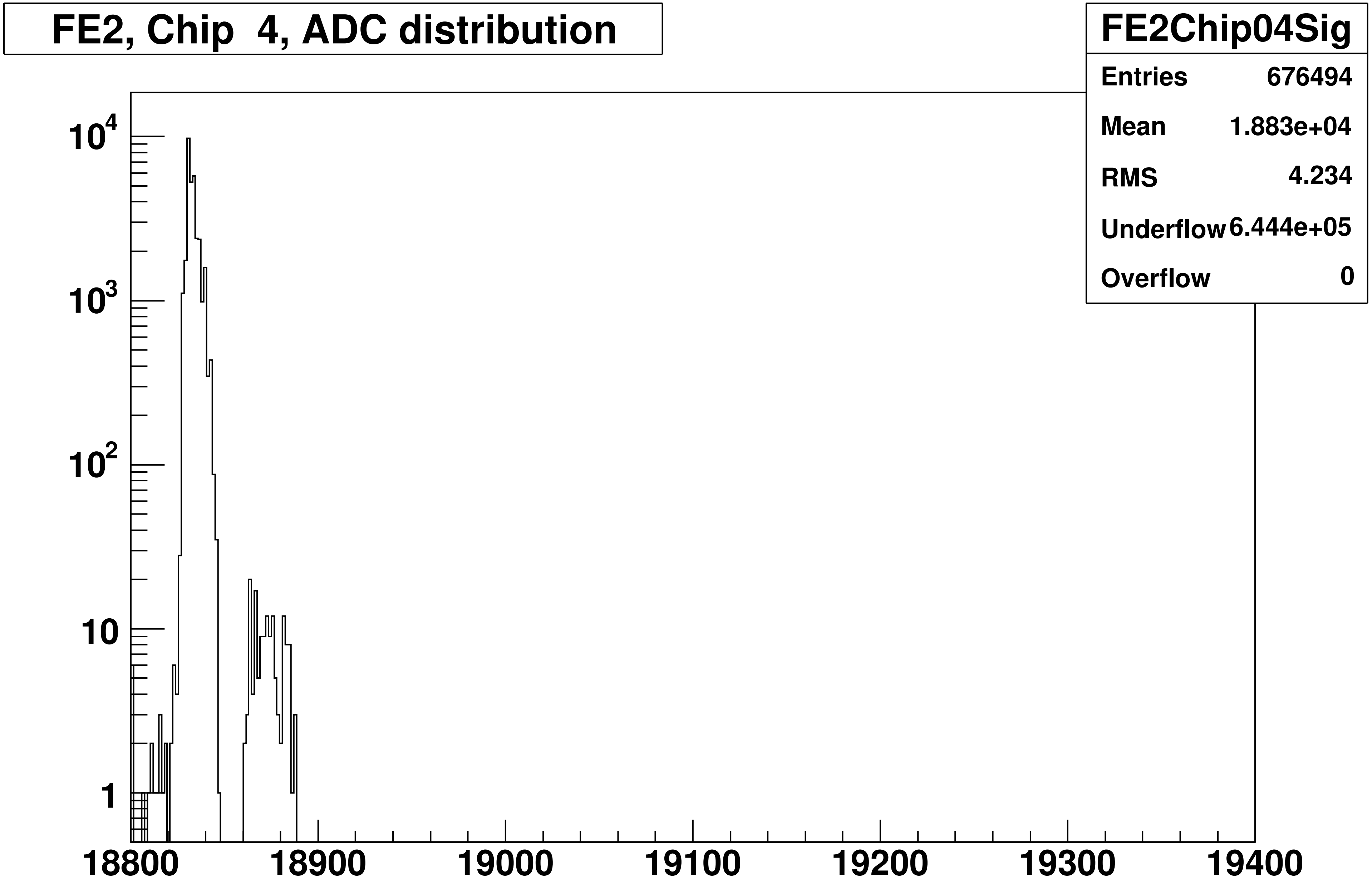
FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 676494
Mean 1.883e+04
RMS 4.234
Underflow 6.444e+05
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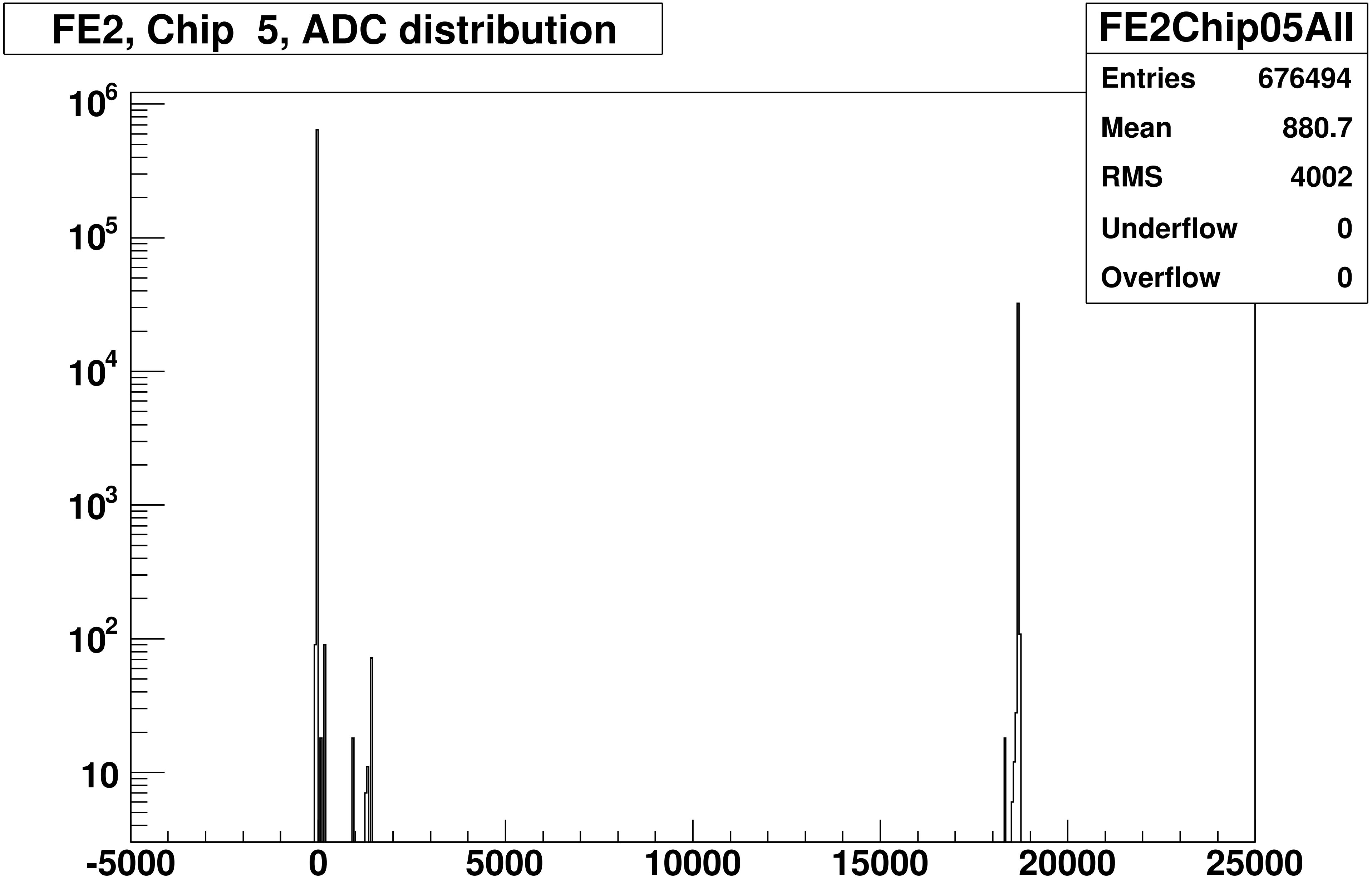
FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 676494
Mean 880.7
RMS 4002
Underflow 0
Overflow 0
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10^5
10^4
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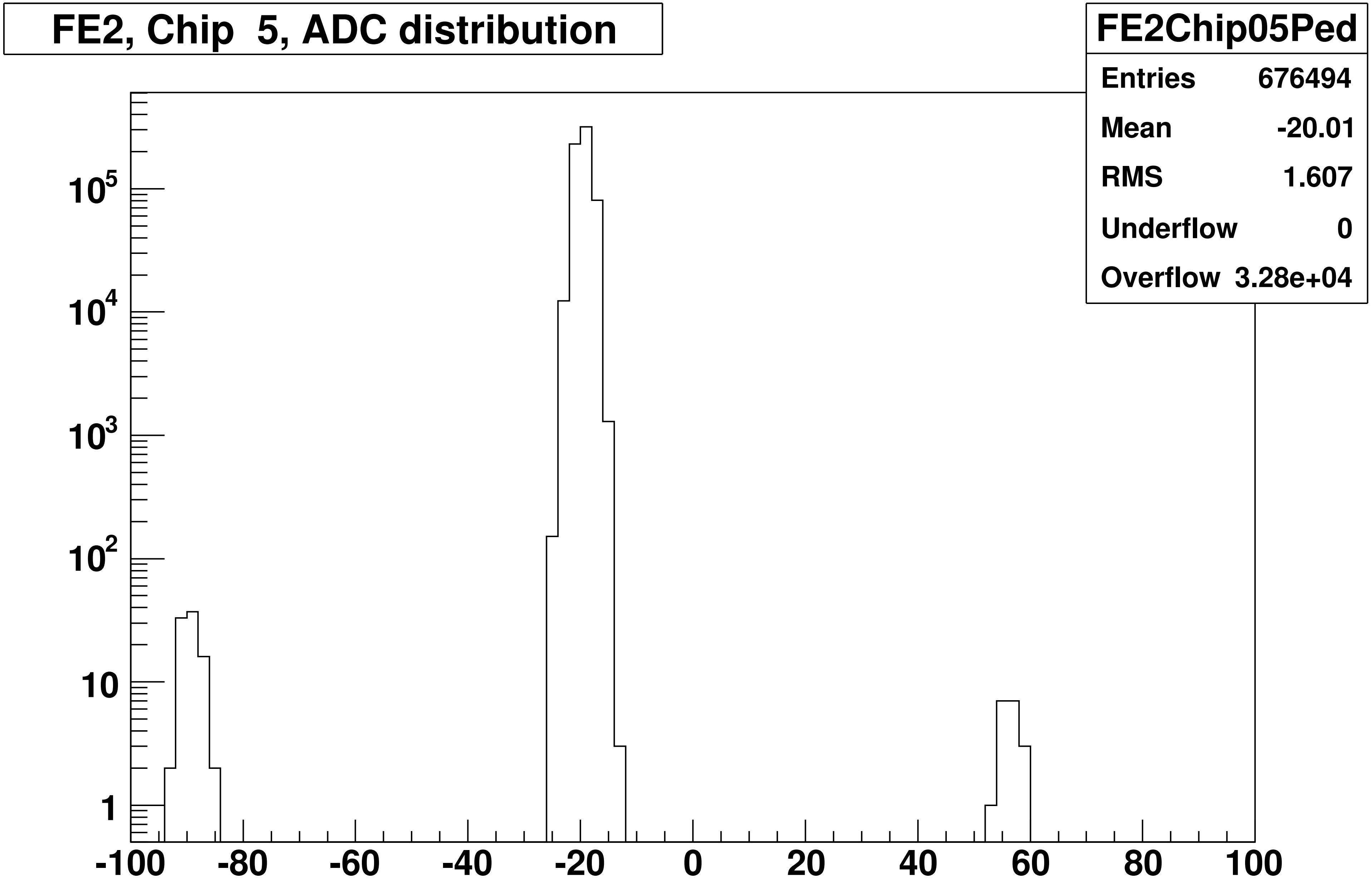
FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 676494
Mean -20.01
RMS 1.607
Underflow 0
Overflow 3.28e+04
10^5
10^4
10^3
10^2
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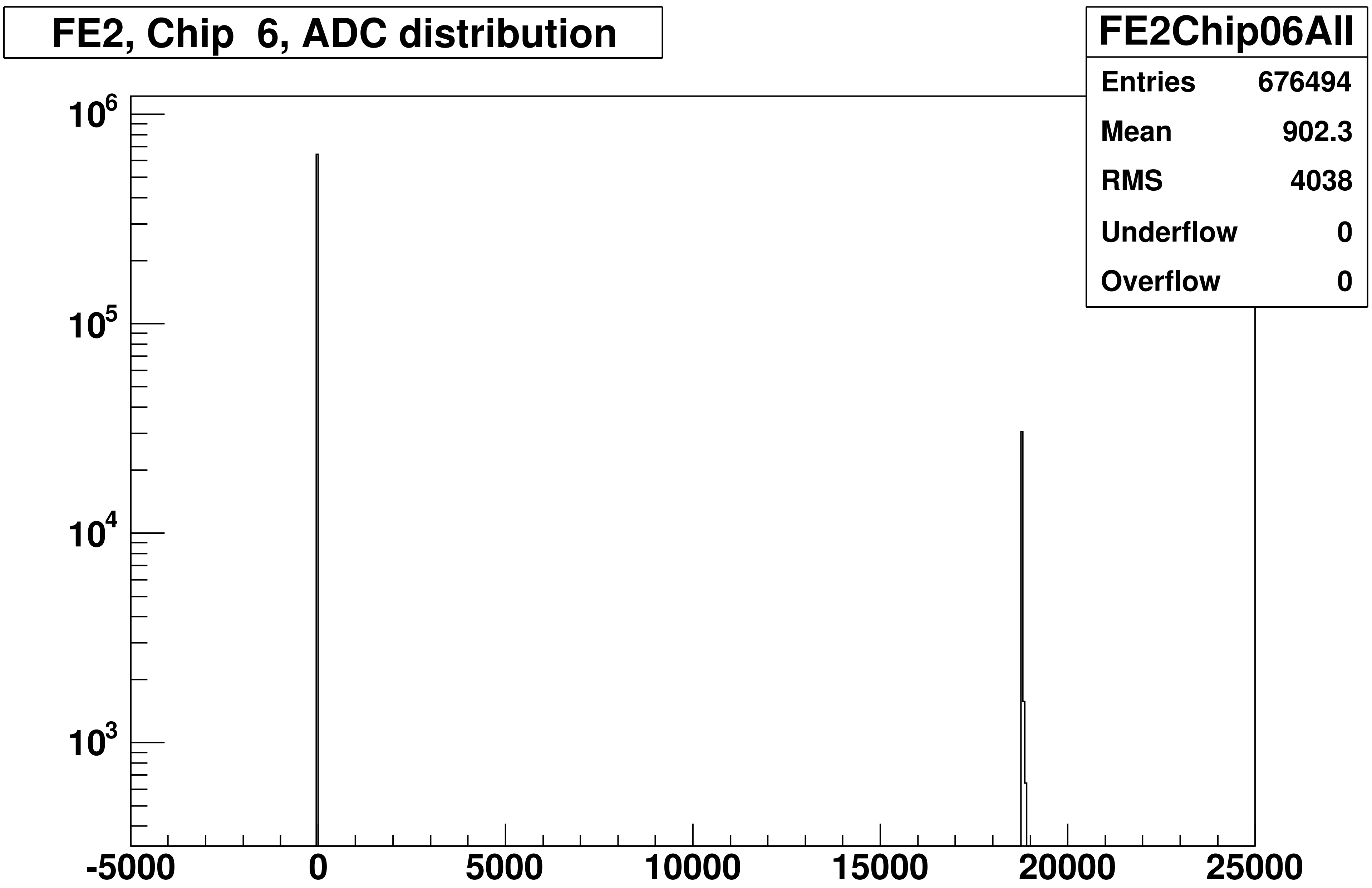
FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 676494
Mean 902.3
RMS 4038
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
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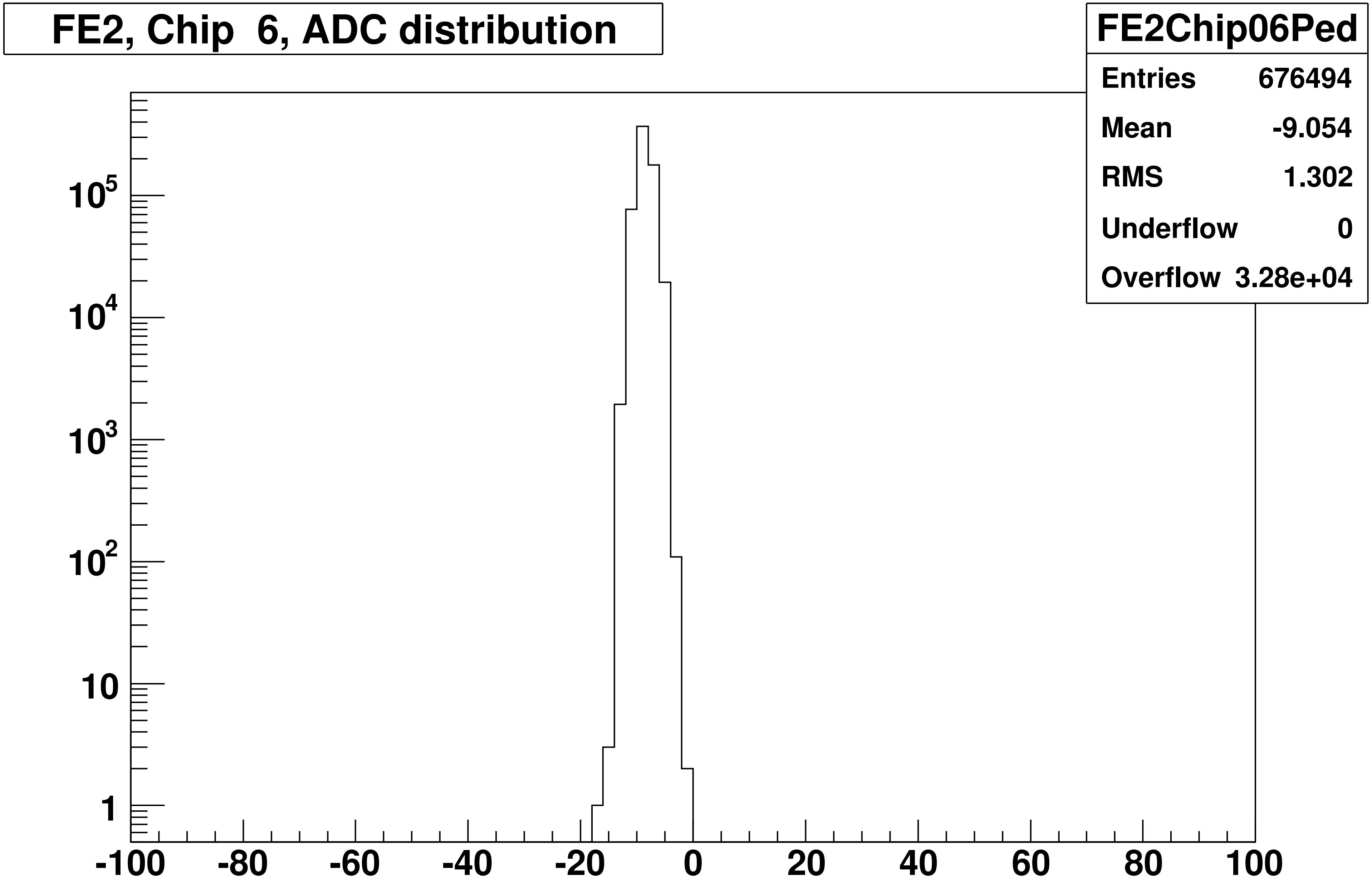
FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 676494
Mean -9.054
RMS 1.302
Underflow 0
Overflow 3.28e+04
10^5
10^4
10^3
10^2
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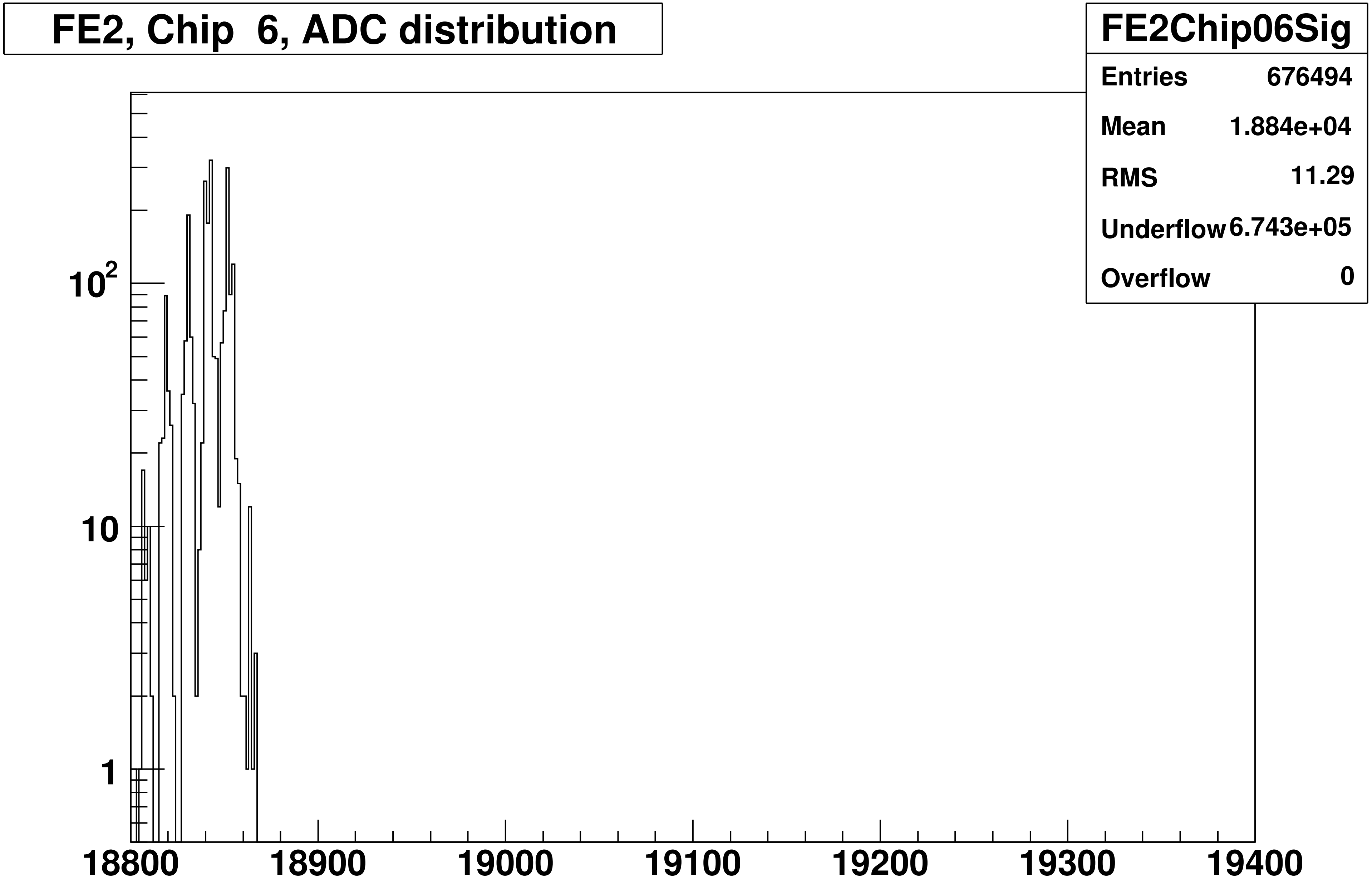
FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 676494
Mean 1.884e+04
RMS 11.29
Underflow 6.743e+05
Overflow 0
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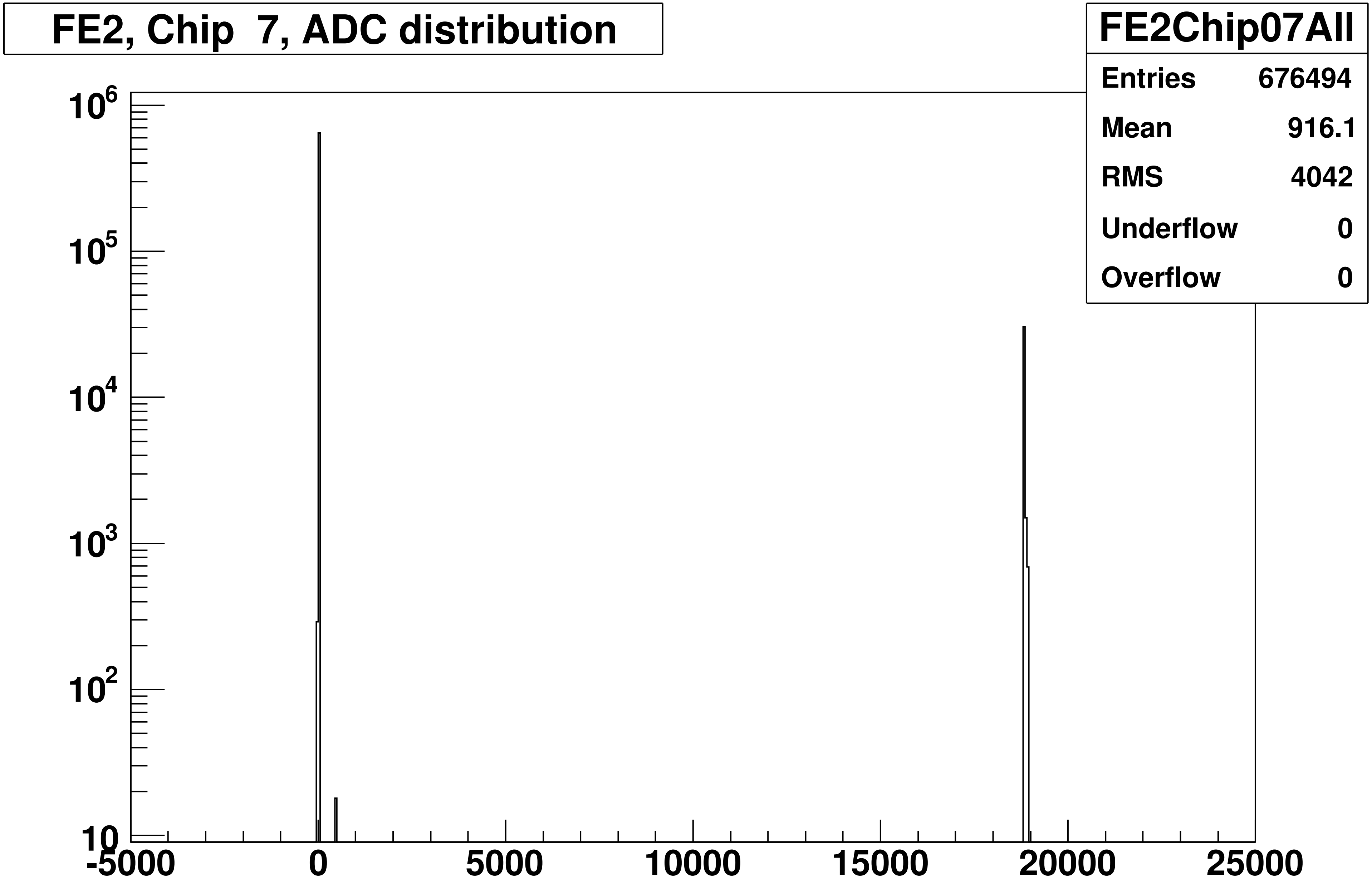

FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 676494
Mean 916.1
RMS 4042
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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10000
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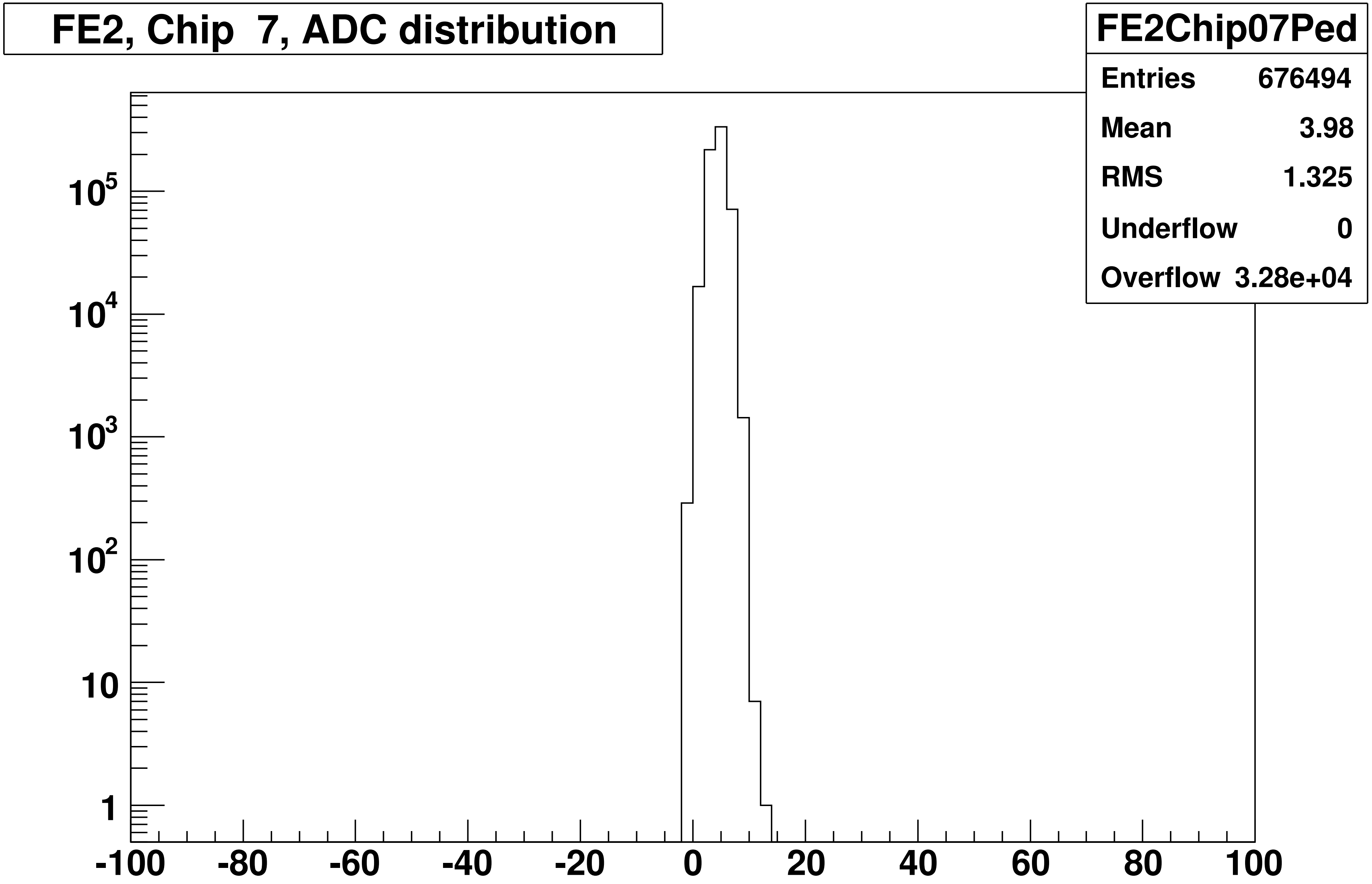
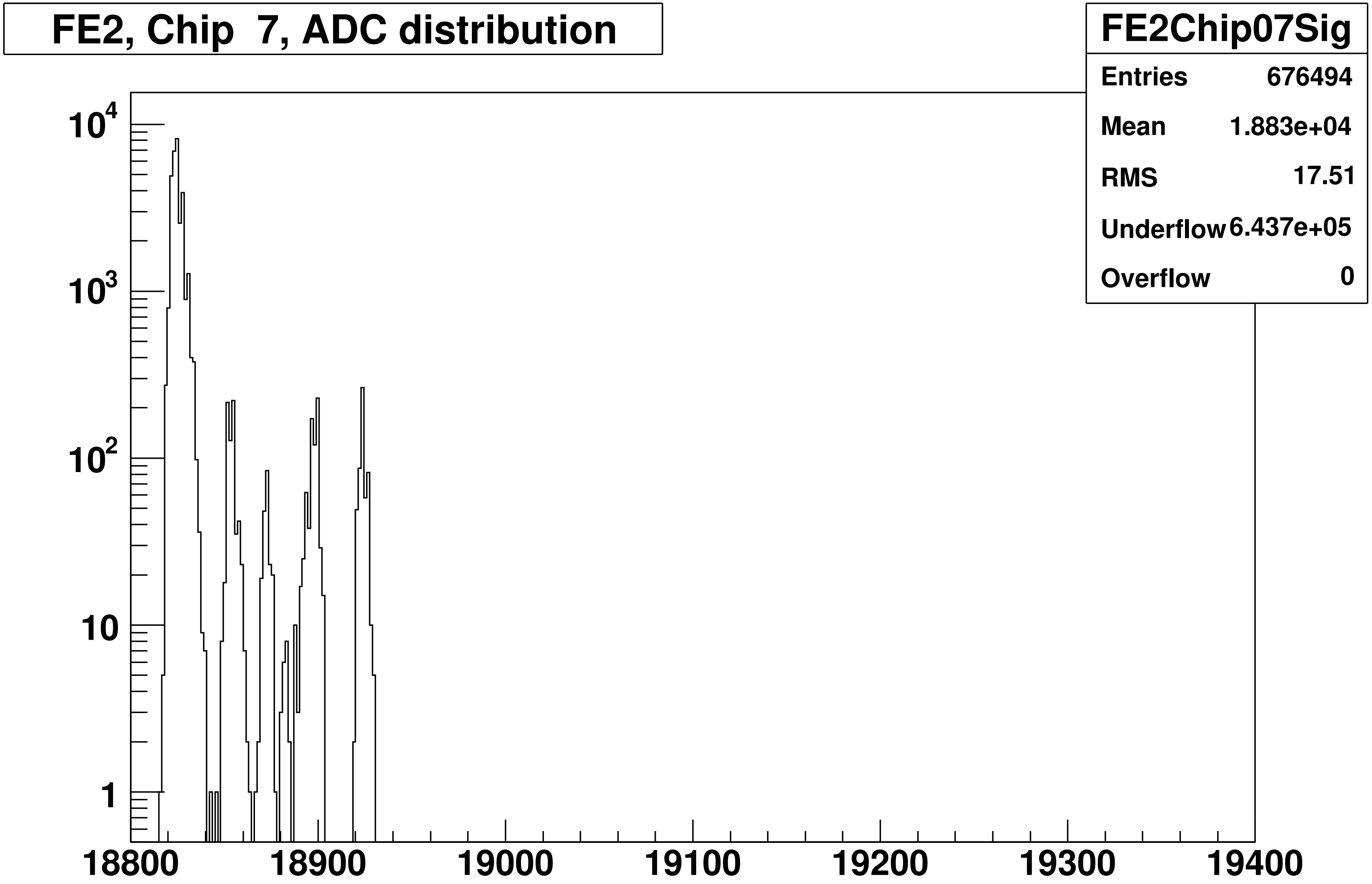
20000
25000


FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 676494
Mean 3.98
RMS 1.325
Underflow 0
Overflow 3.28e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 676494
Mean 1.883e+04
RMS 17.51
Underflow 6.437e+05
Overflow 0
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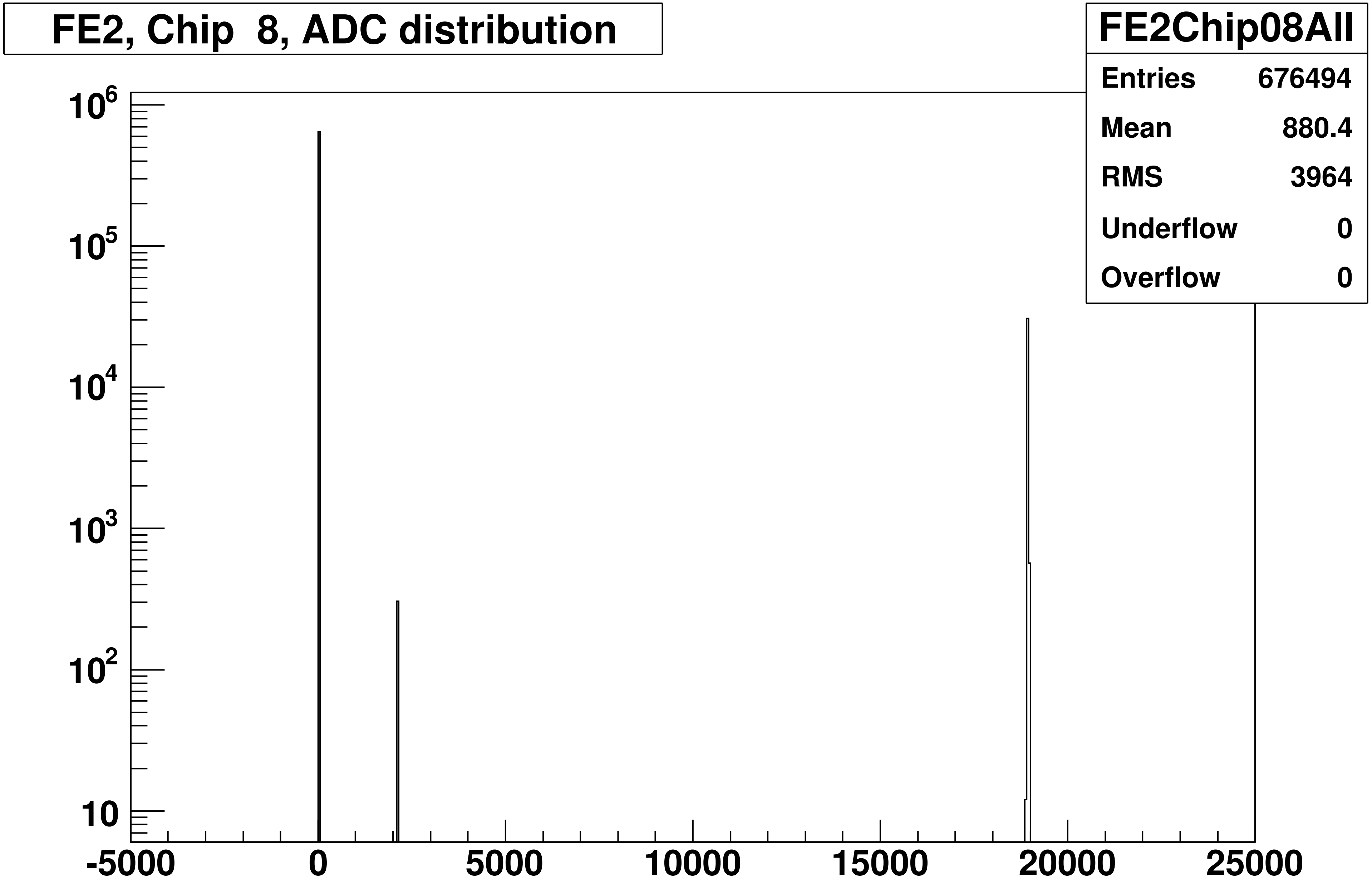
FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 676494
Mean 880.4
RMS 3964
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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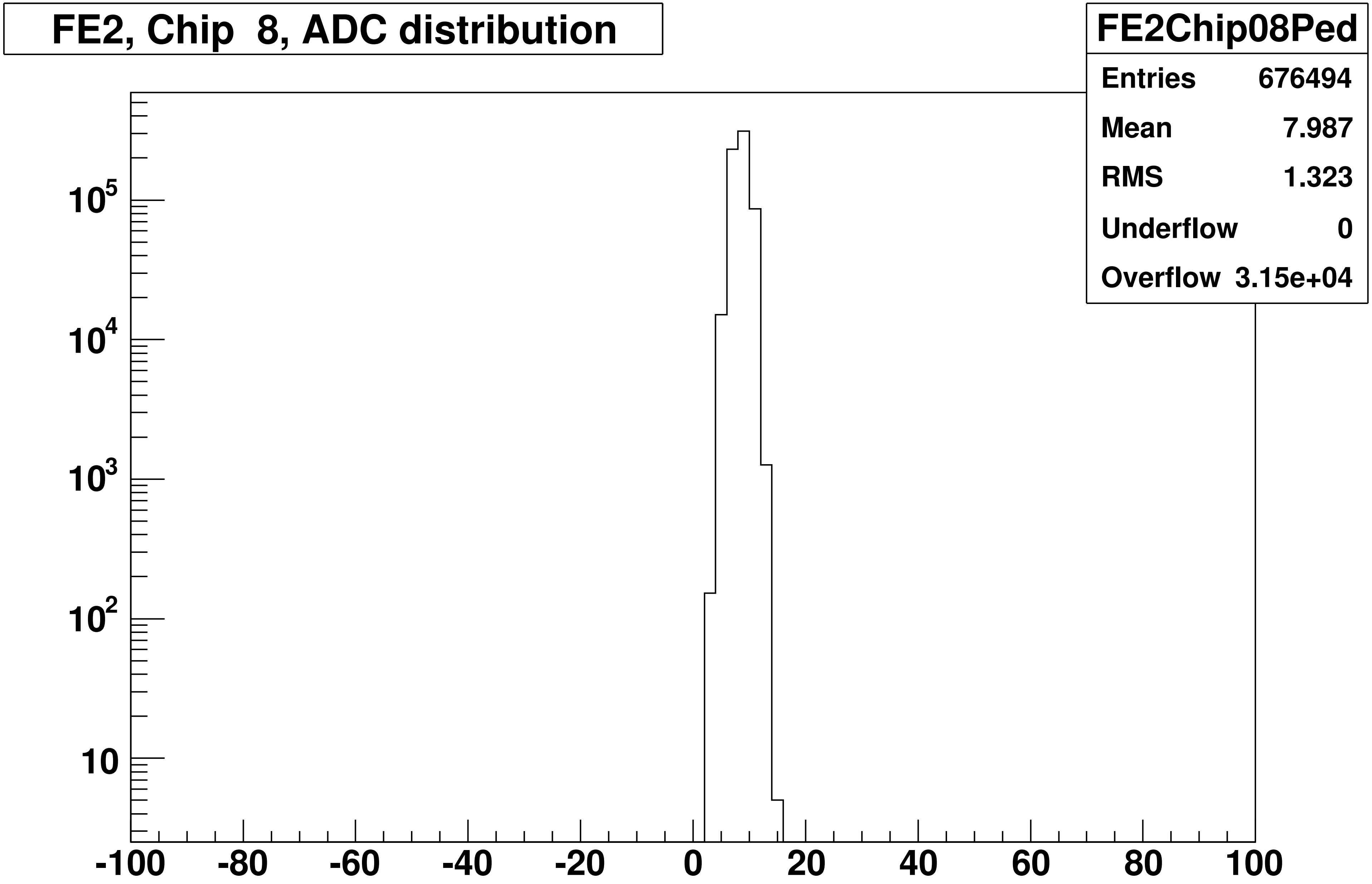
20000
25000


FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 676494
Mean 7.987
RMS 1.323
Underflow 0
Overflow 3.15e+04
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10^4
10^3
10^2
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-40
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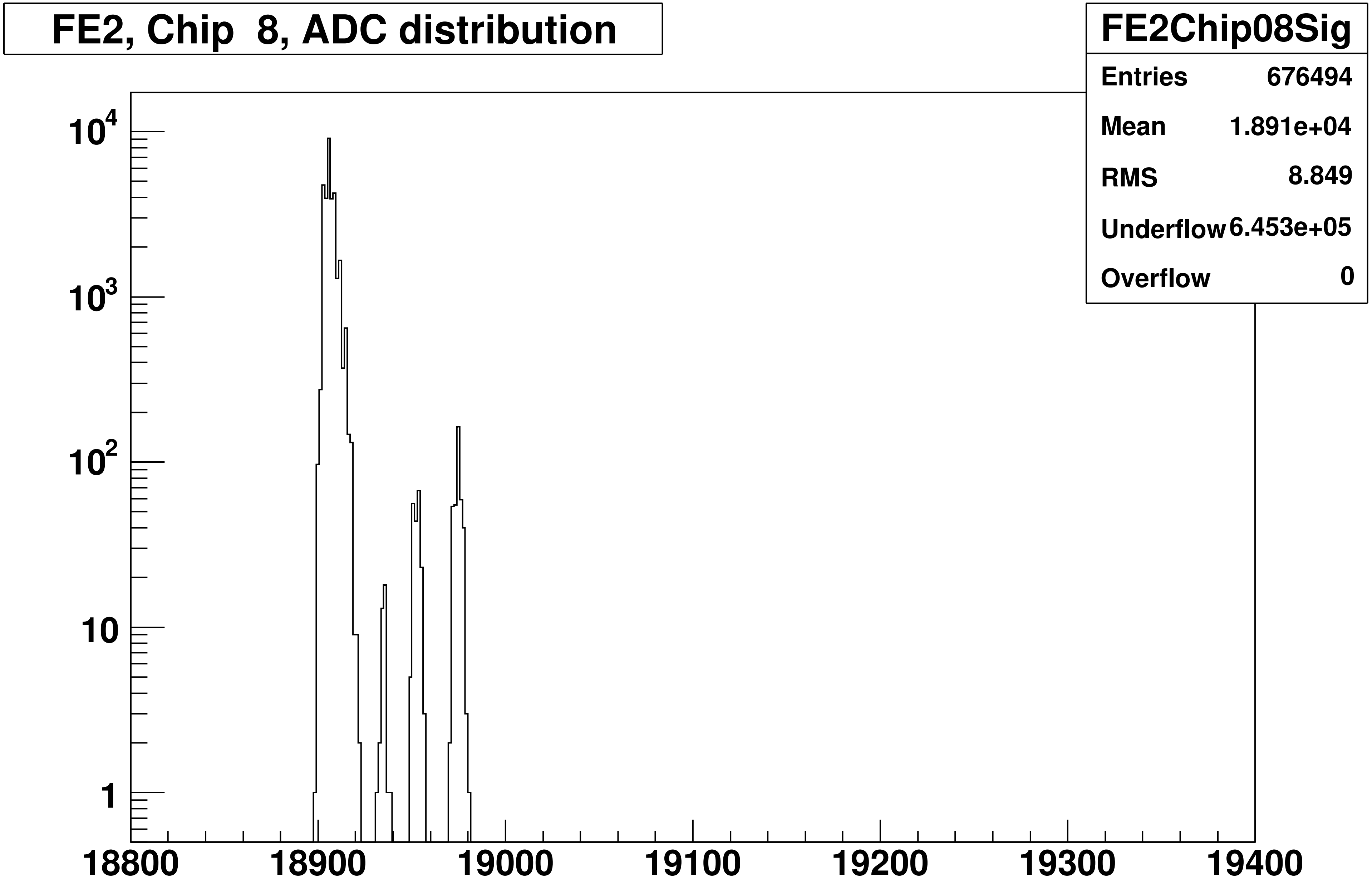
FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 676494
Mean 1.891e+04
RMS 8.849
Underflow 6.453e+05
Overflow 0
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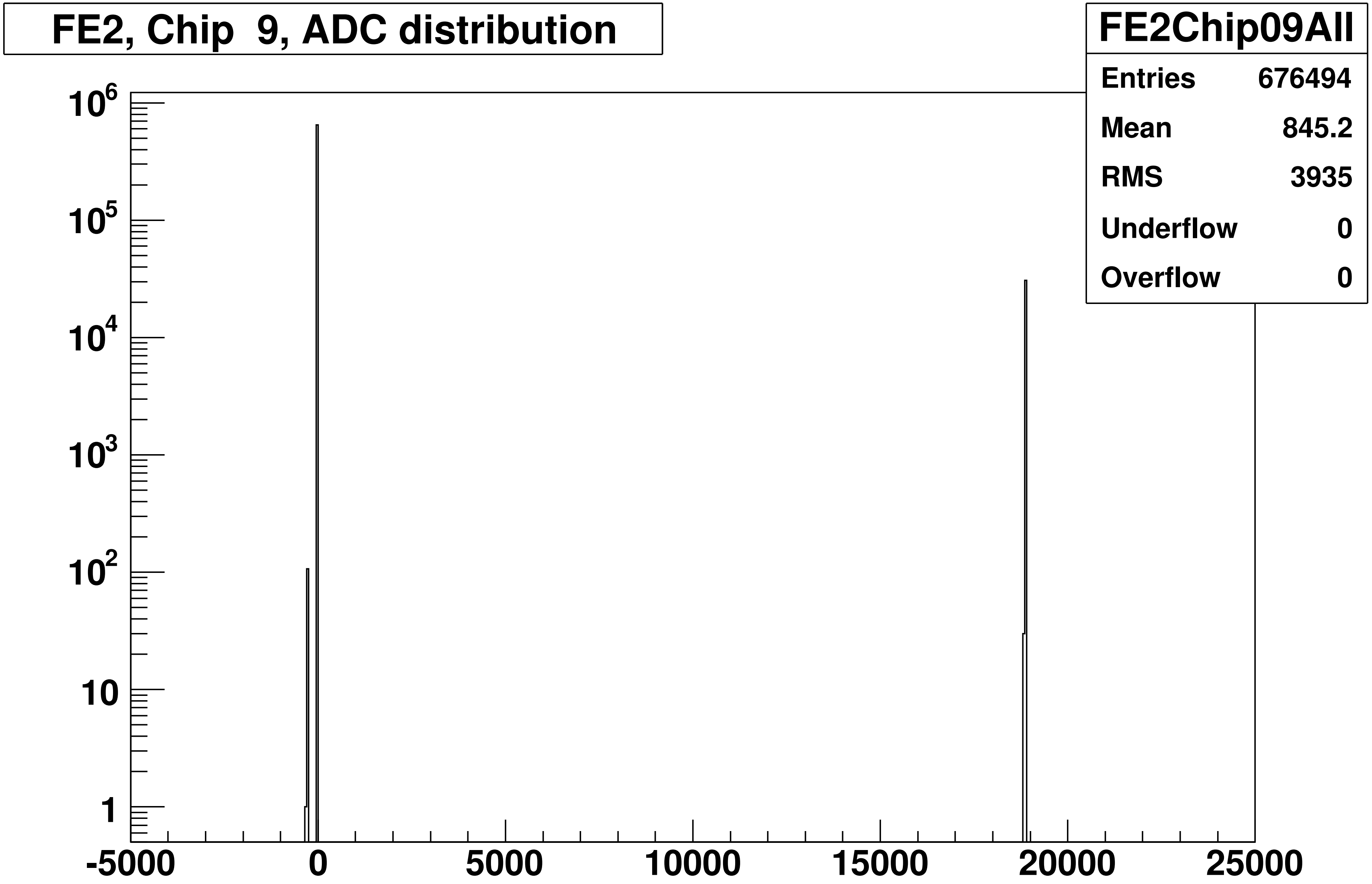

FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 676494
Mean 845.2
RMS 3935
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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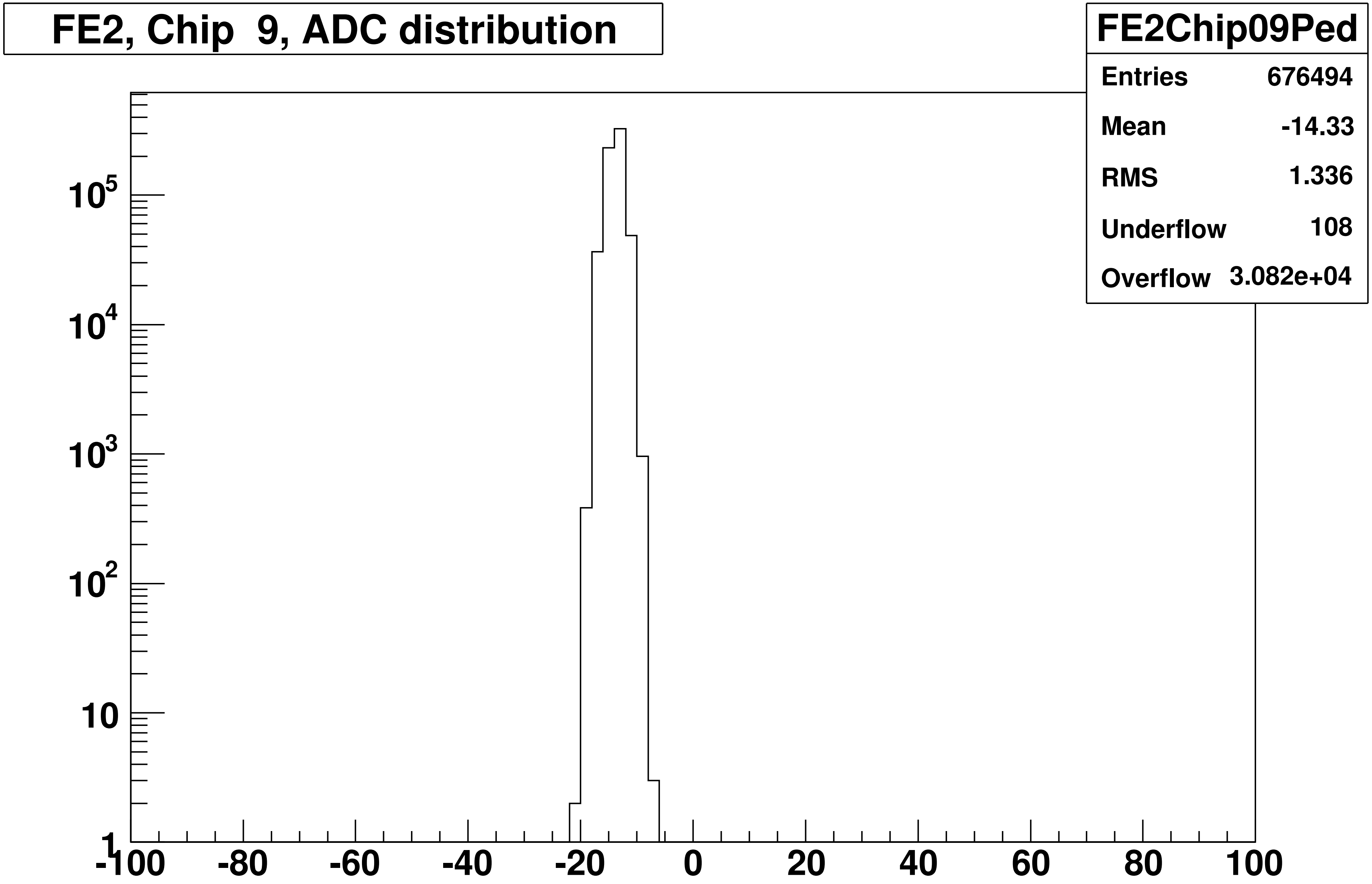
20000
25000


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 676494
Mean -14.33
RMS 1.336
Underflow 108
Overflow 3.082e+04
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10^3
10^2
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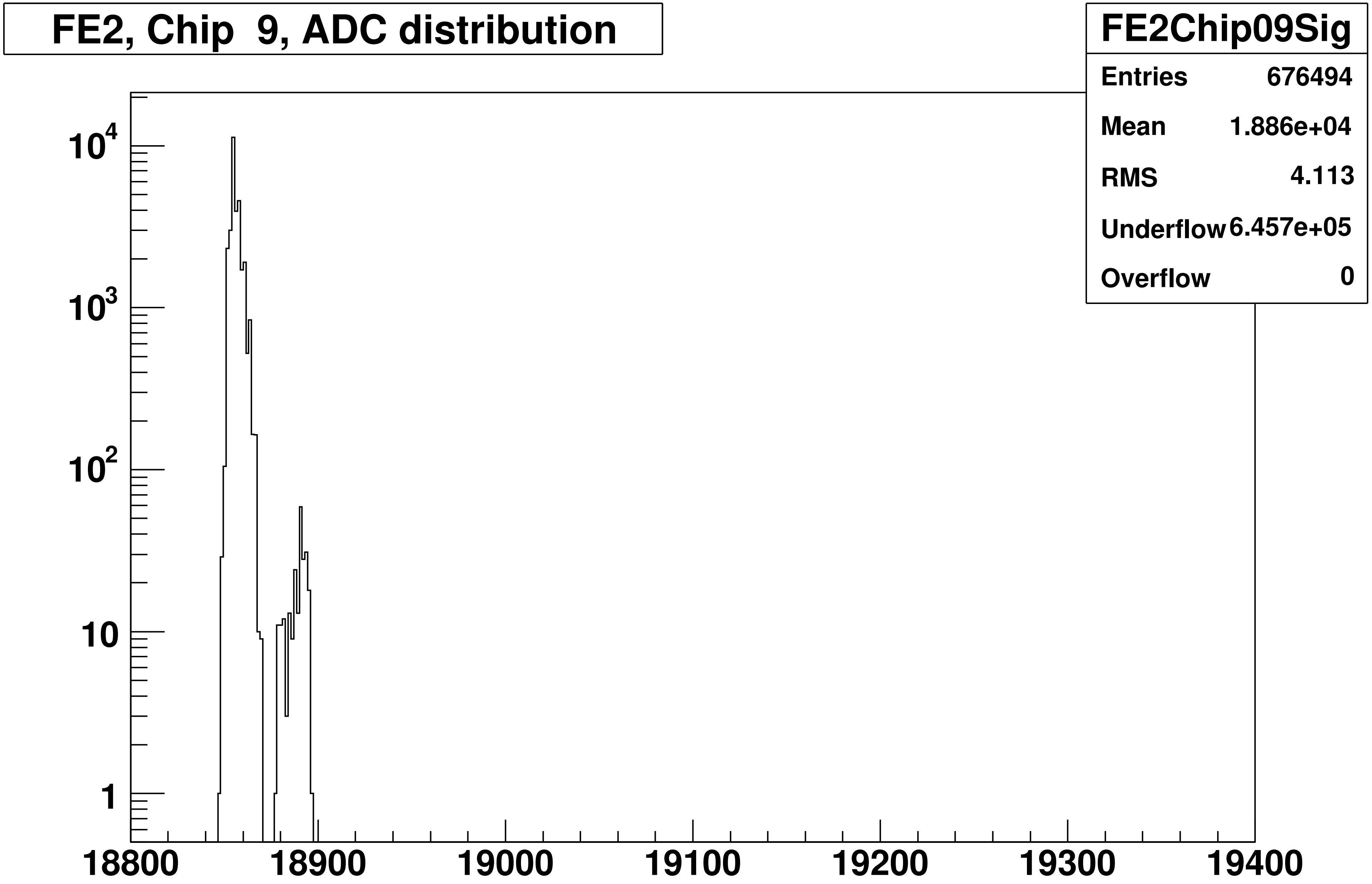
FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 676494
Mean 1.886e+04
RMS 4.113
Underflow 6.457e+05
Overflow 0
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10^3
10^2
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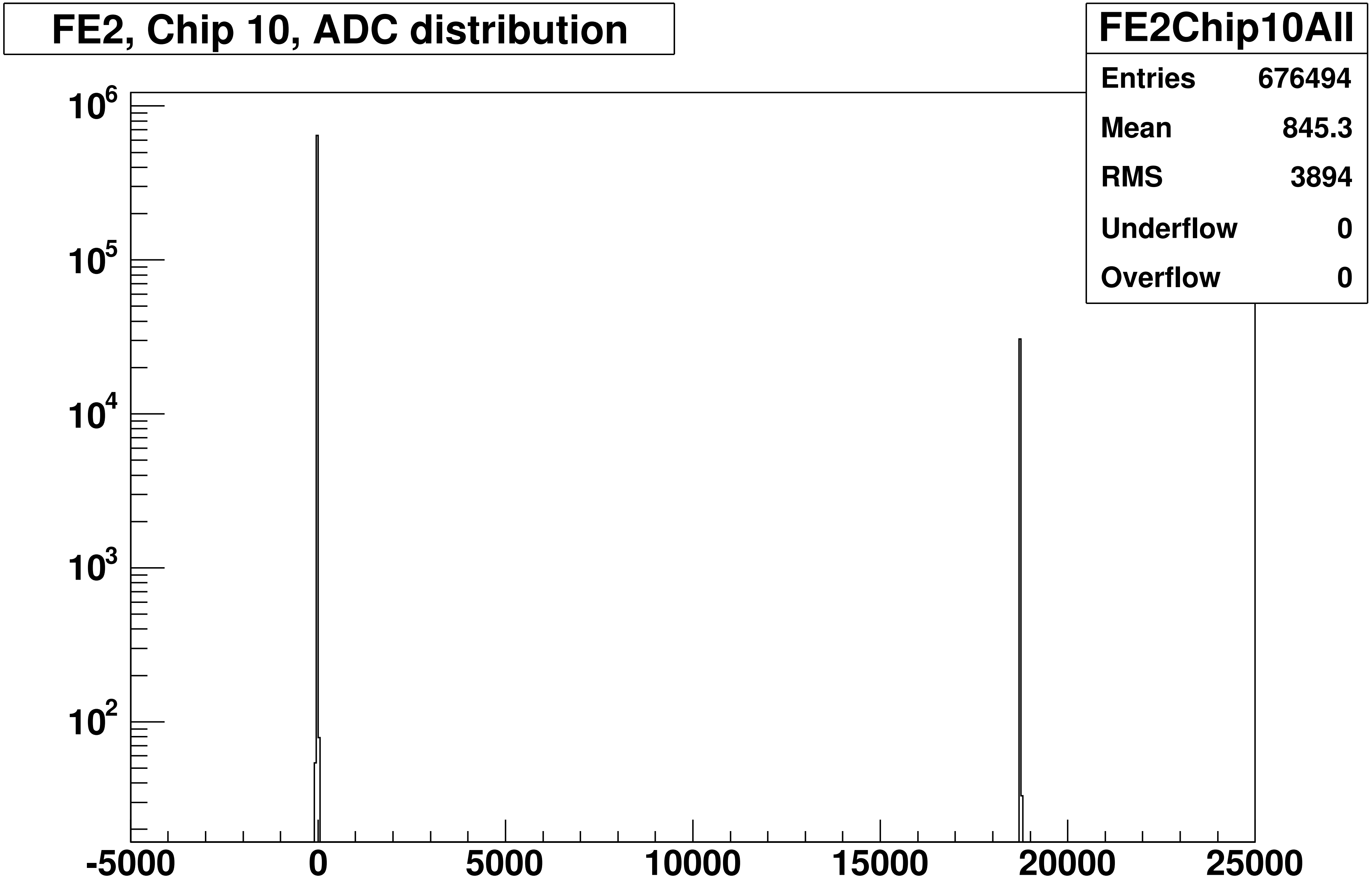
FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 676494
Mean 845.3
RMS 3894
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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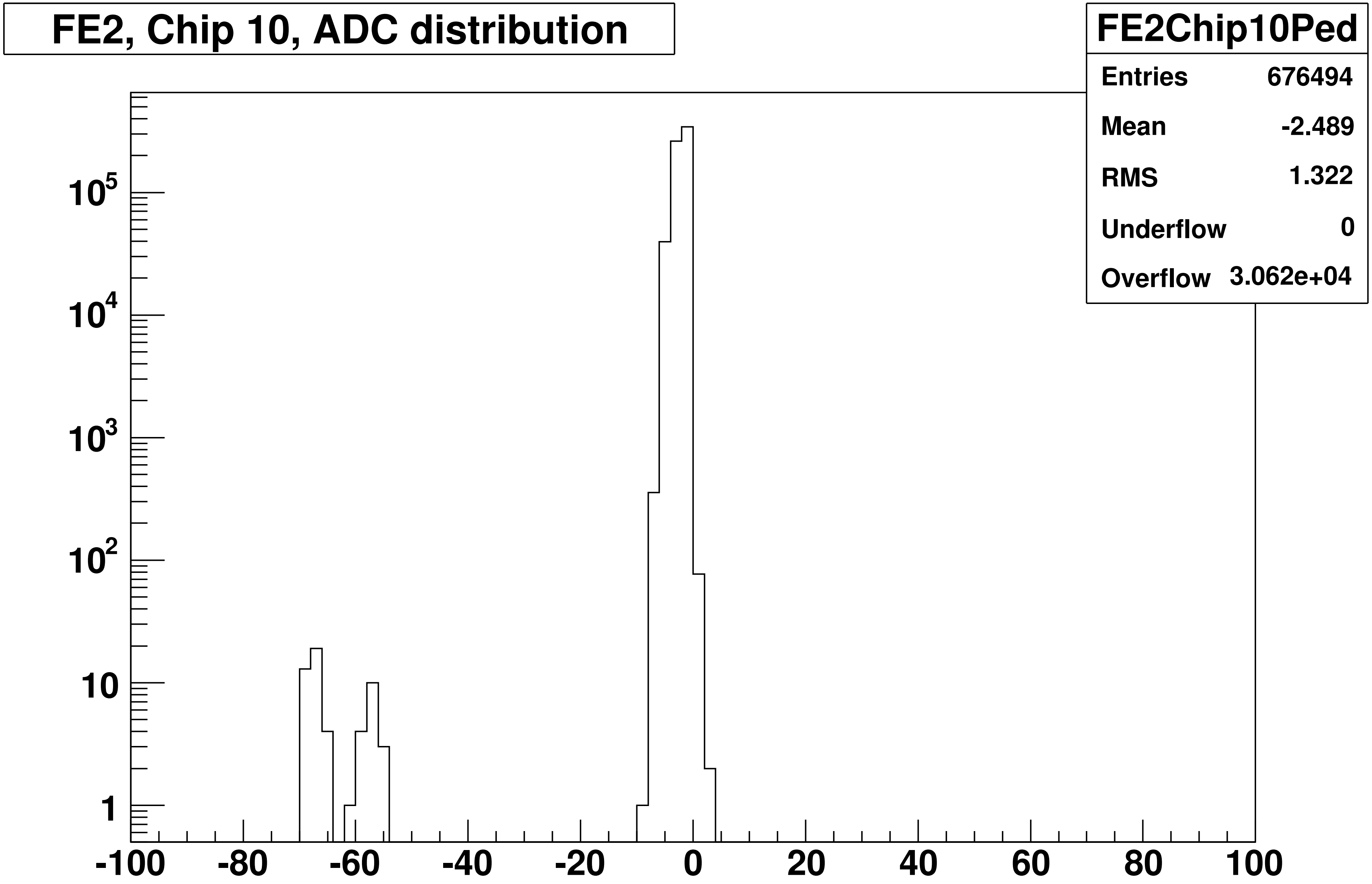

FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 676494
Mean -2.489
RMS 1.322
Underflow 0
Overflow 3.062e+04
10^5
10^4
10^3
10^2
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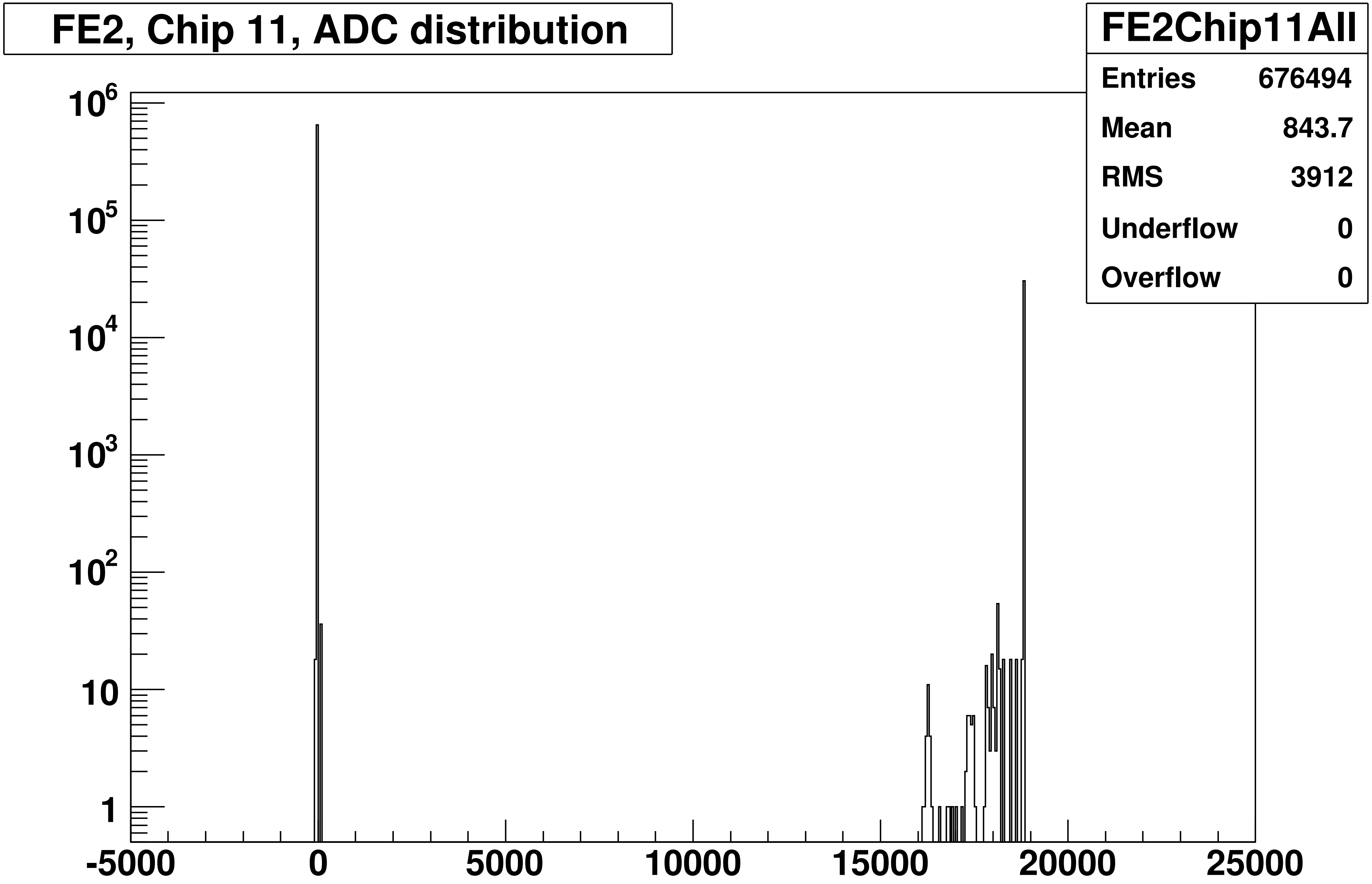

FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 676494
Mean 843.7
RMS 3912
Underflow 0
Overflow 0
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10^3
10^2
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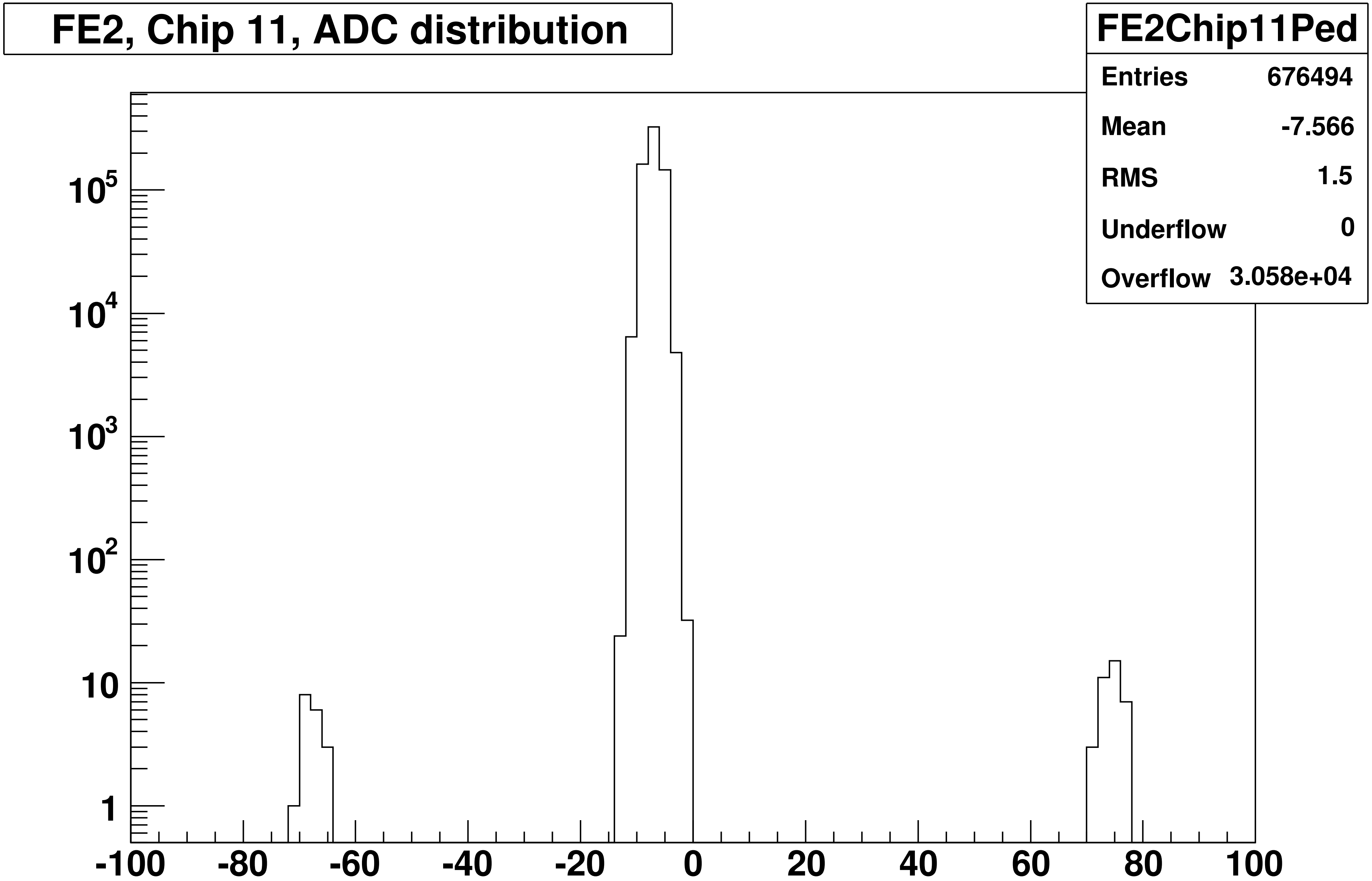
FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 676494
Mean -7.566
RMS 1.5
Underflow 0
Overflow 3.058e+04
10^5
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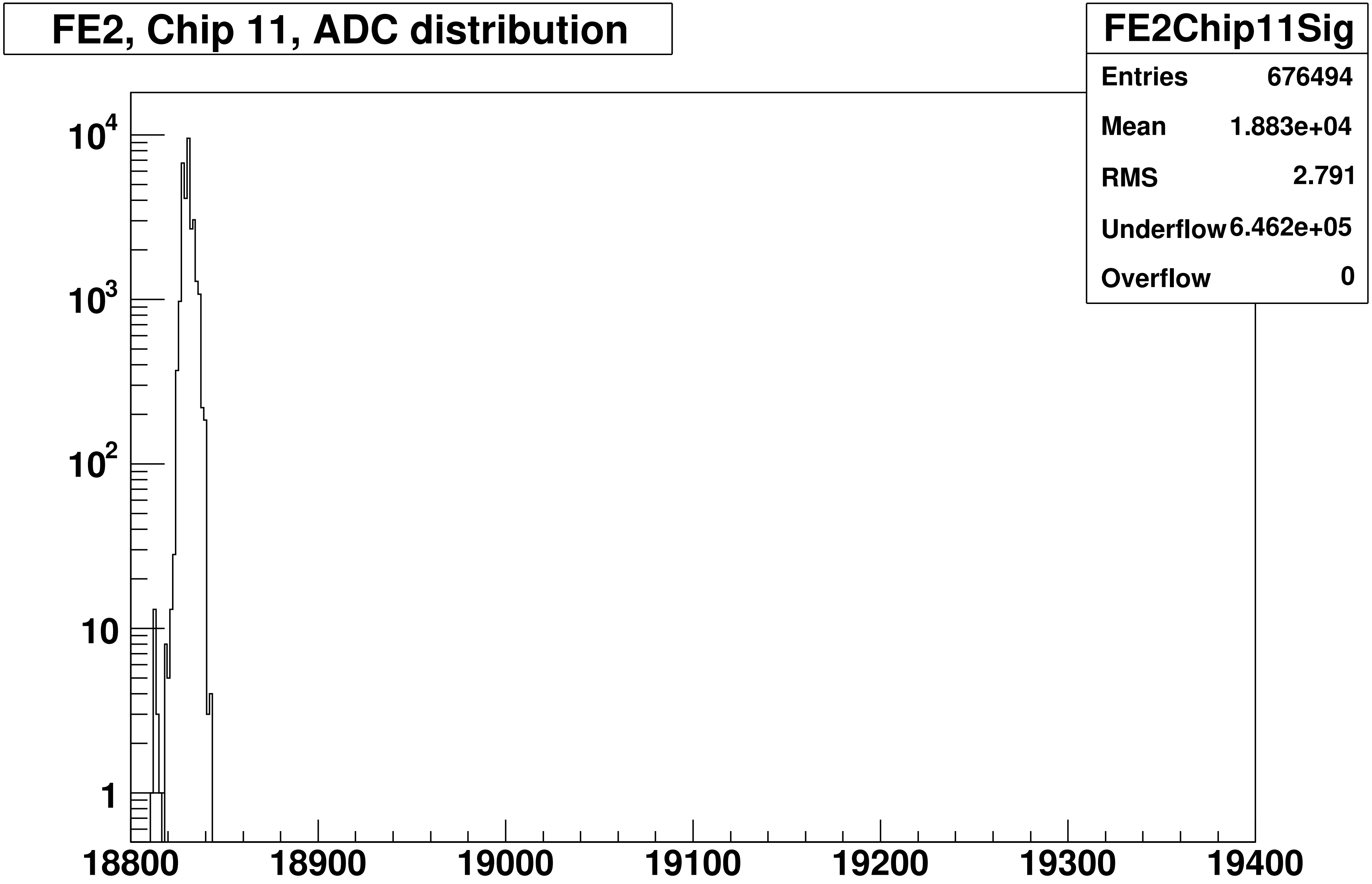

FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 676494
Mean 1.883e+04
RMS 2.791
Underflow 6.462e+05
Overflow 0
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10^3
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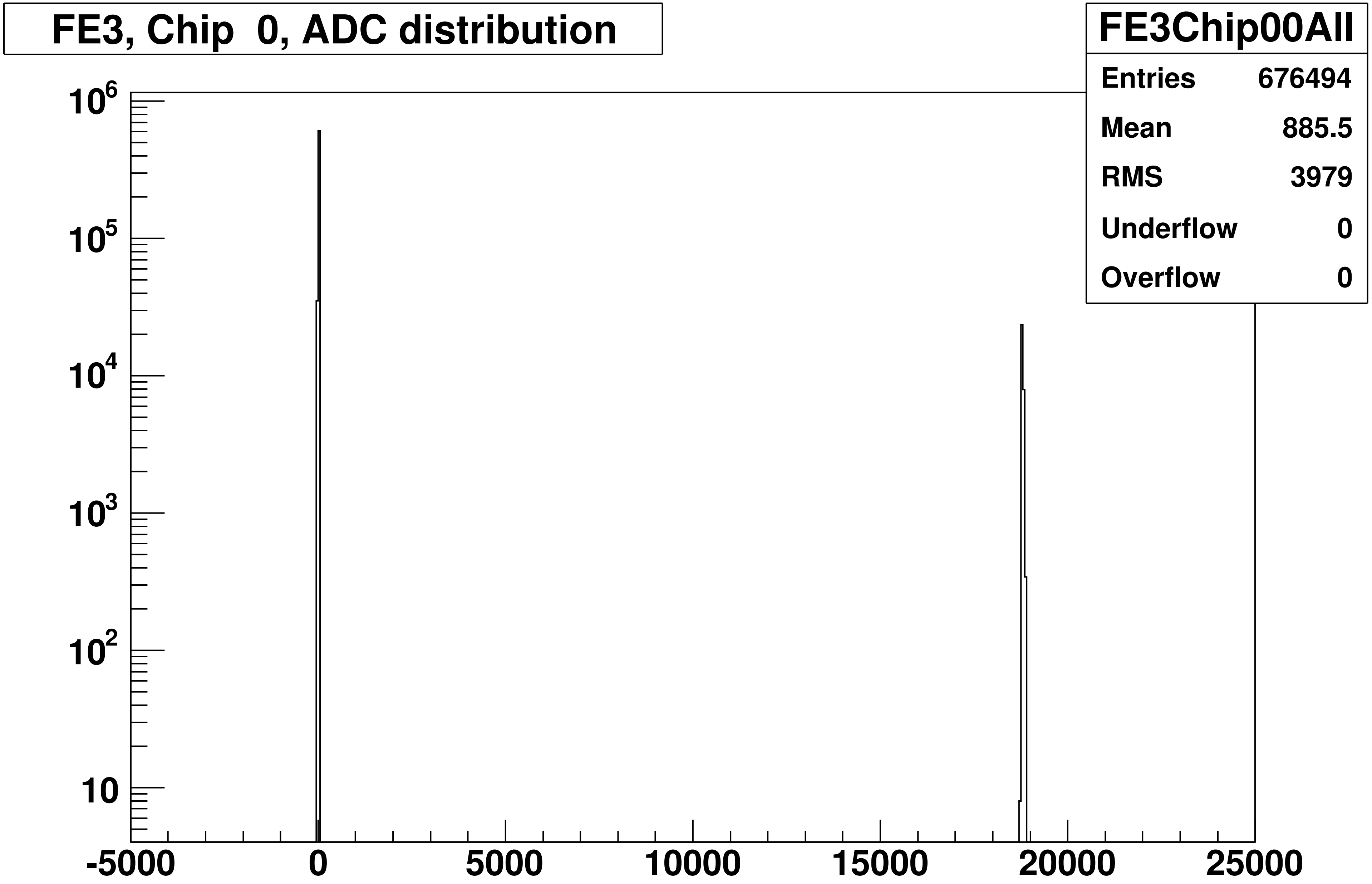

FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 676494
Mean 885.5
RMS 3979
Underflow 0
Overflow 0
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10^4
10^3
10^2
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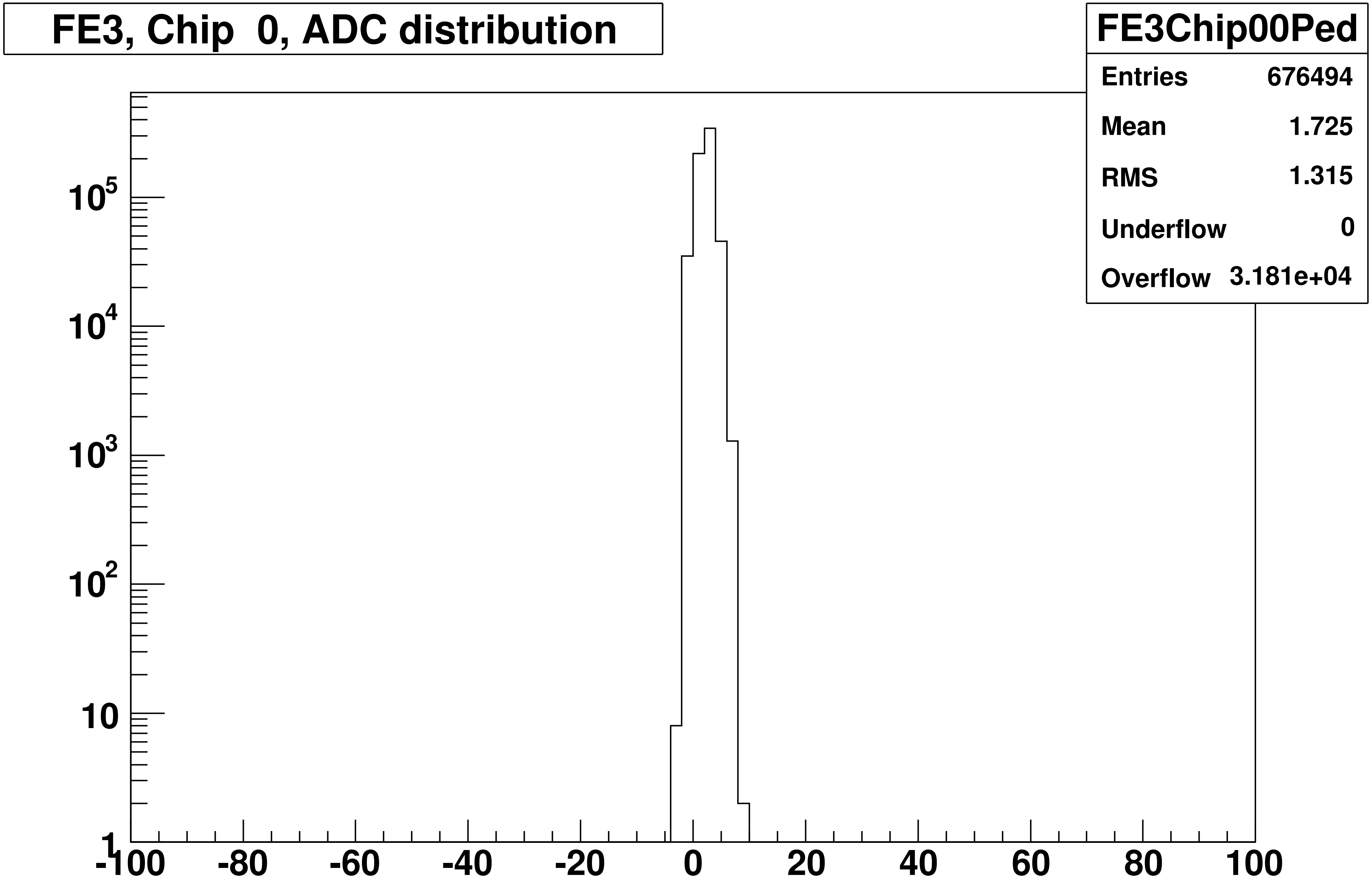

FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 676494
Mean 1.725
RMS 1.315
Underflow 0
Overflow 3.181e+04
10^5
10^4
10^3
10^2
10
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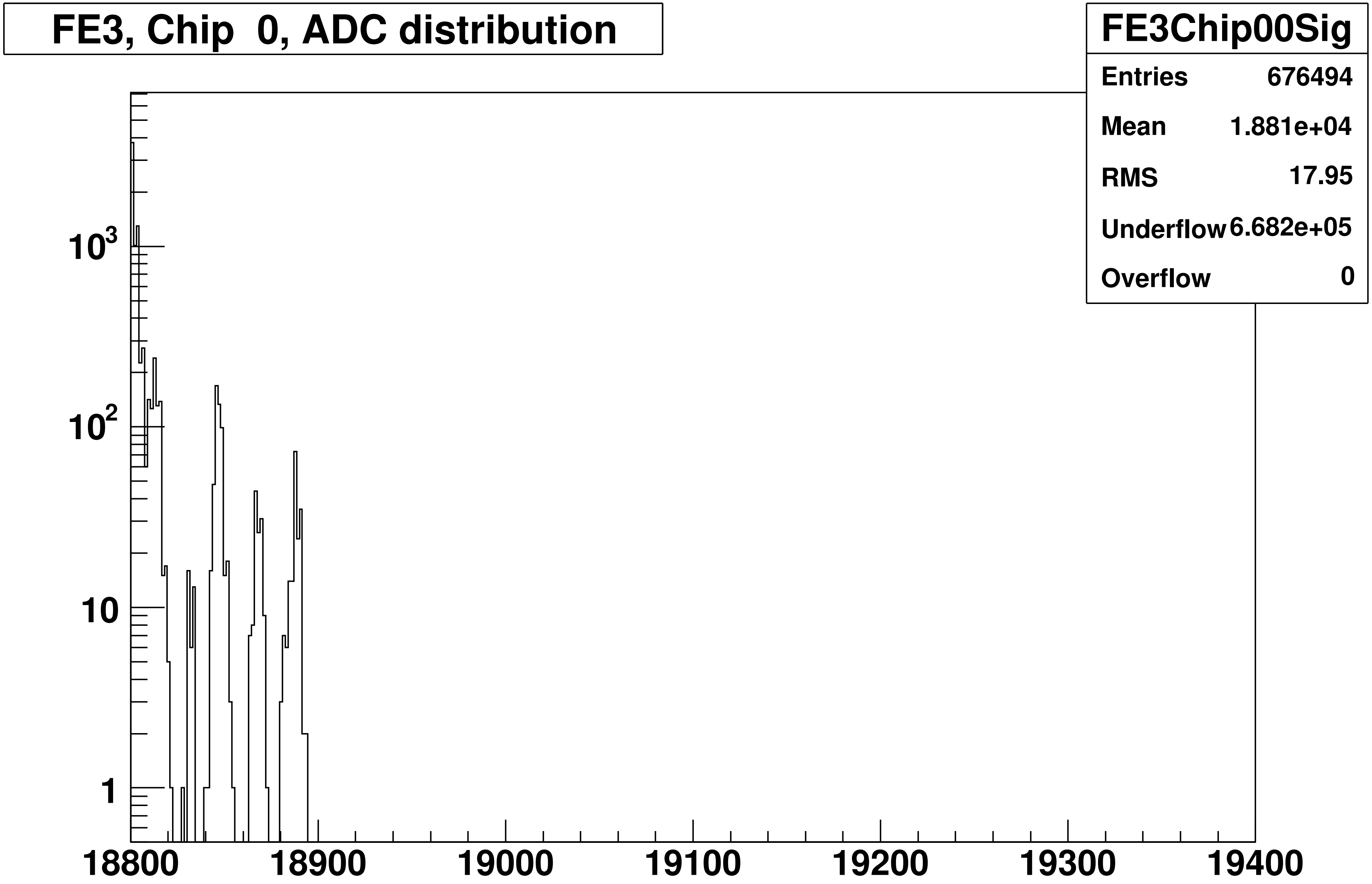
FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 676494
Mean 1.881e+04
RMS 17.95
Underflow 6.682e+05
Overflow 0
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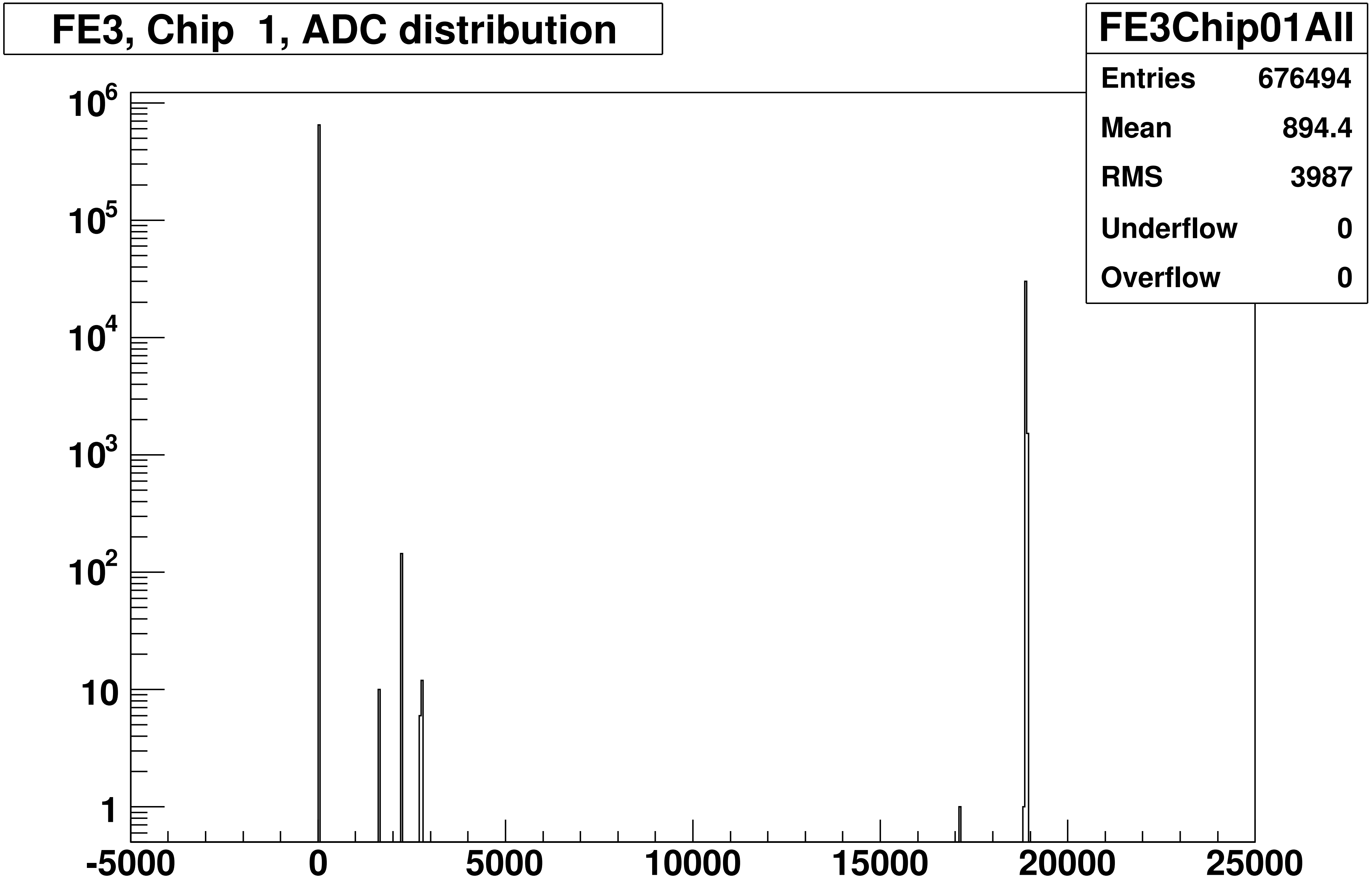

FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 676494
Mean 894.4
RMS 3987
Underflow 0
Overflow 0
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10^2
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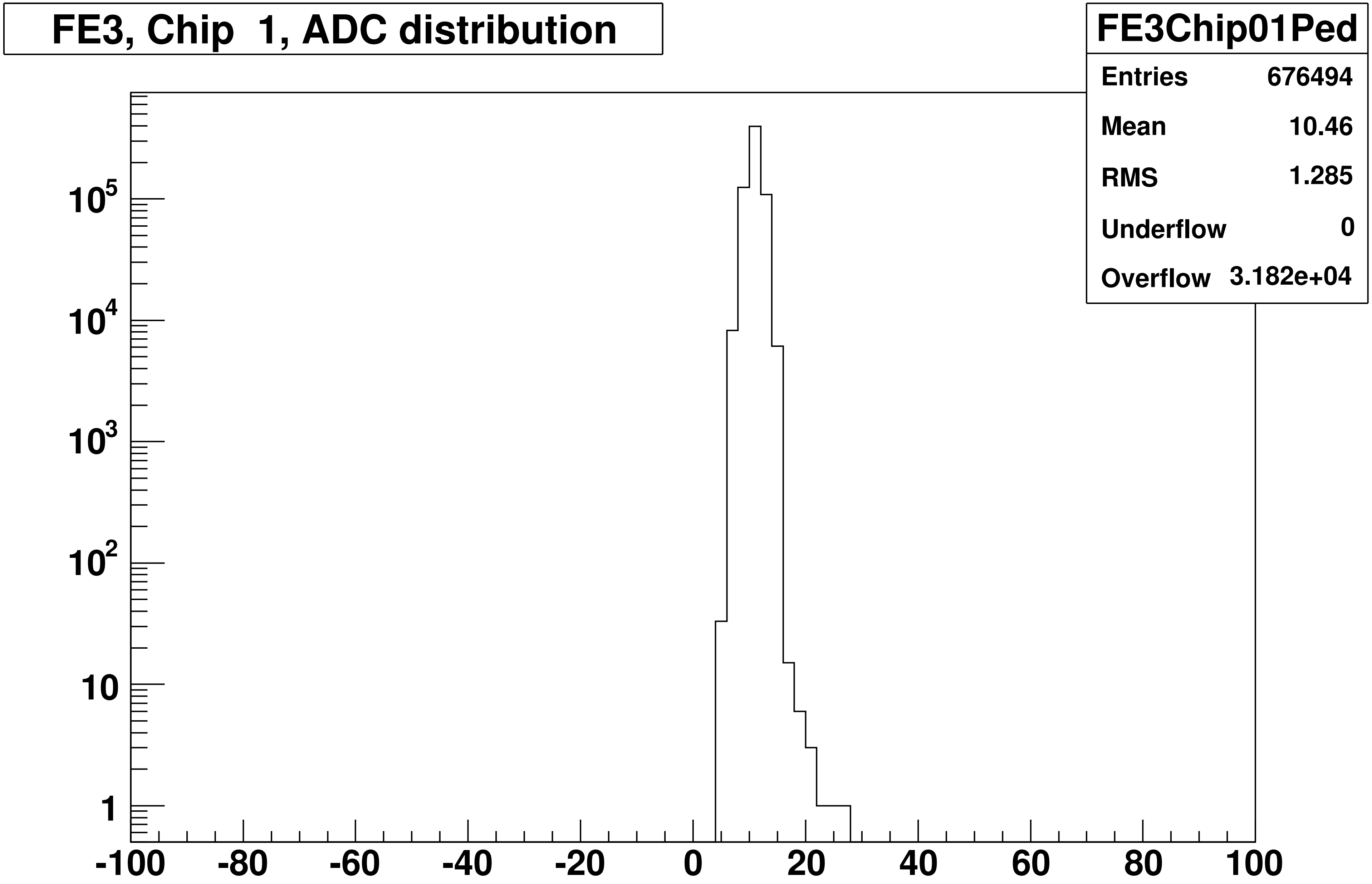
FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 676494
Mean 10.46
RMS 1.285
Underflow 0
Overflow 3.182e+04
10^5
10^4
10^3
10^2
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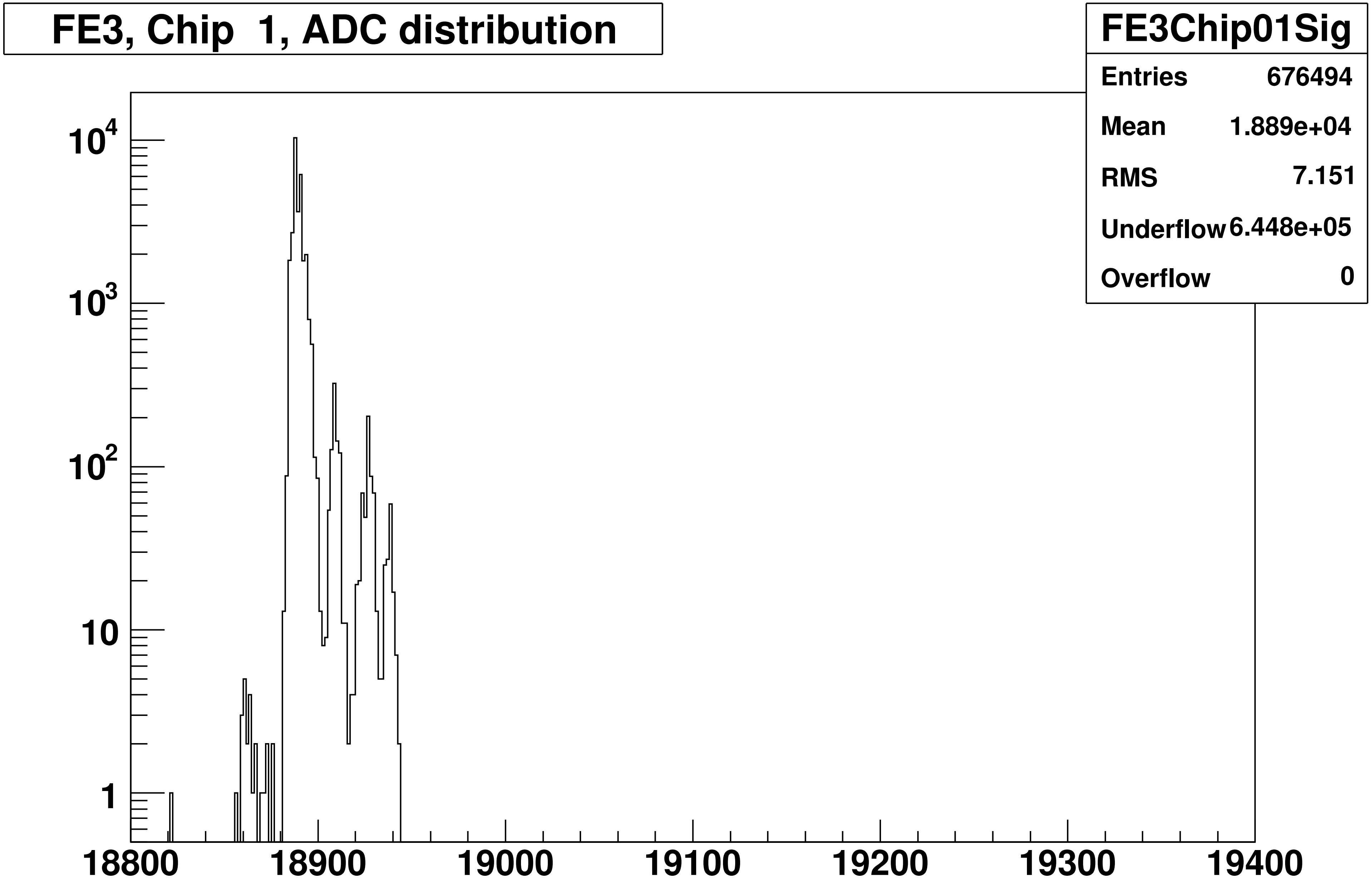
FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 676494
Mean 1.889e+04
RMS 7.151
Underflow 6.448e+05
Overflow 0
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10^3
10^2
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1
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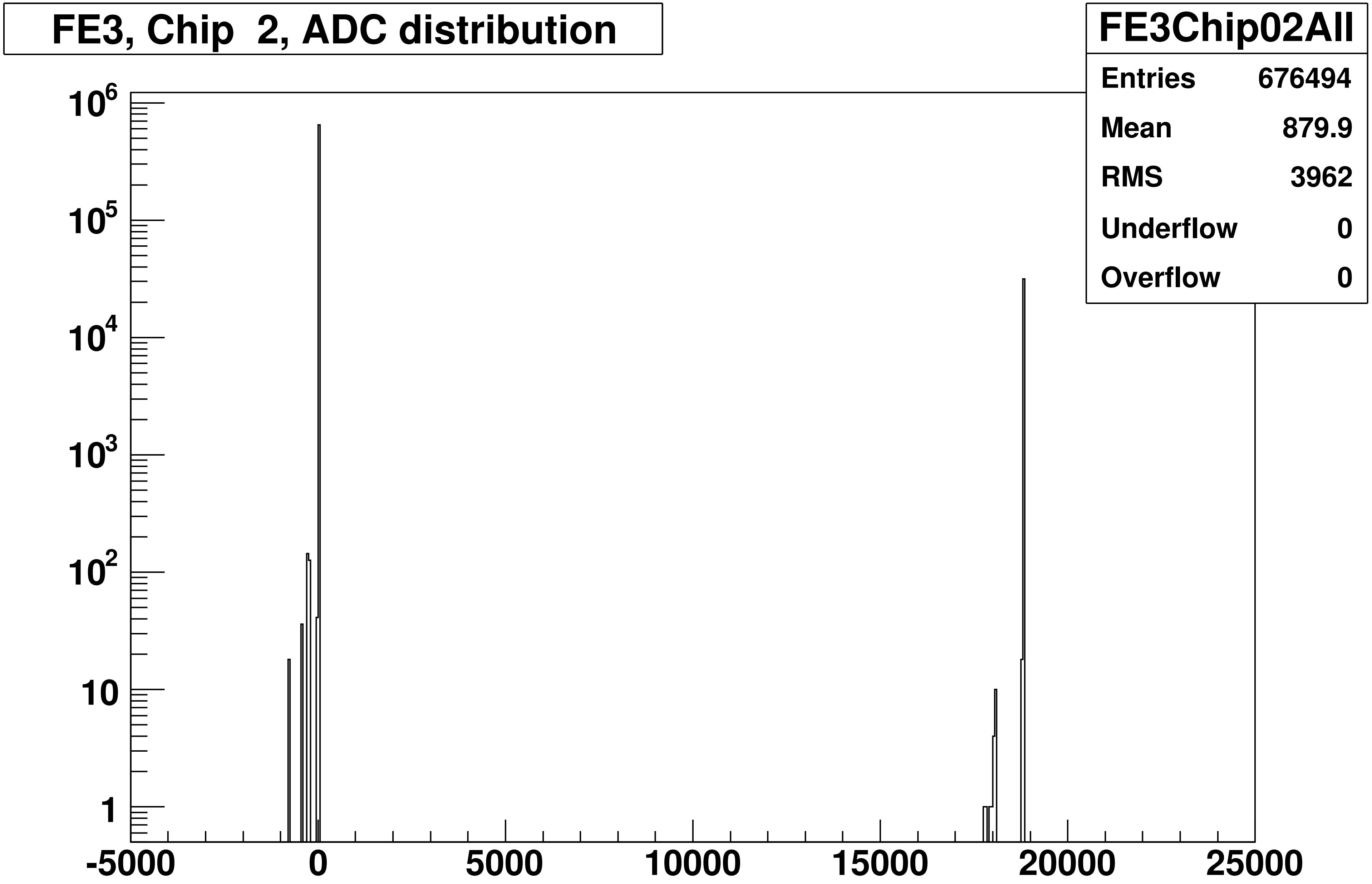
FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 676494
Mean 879.9
RMS 3962
Underflow 0
Overflow 0
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10^4
10^3
10^2
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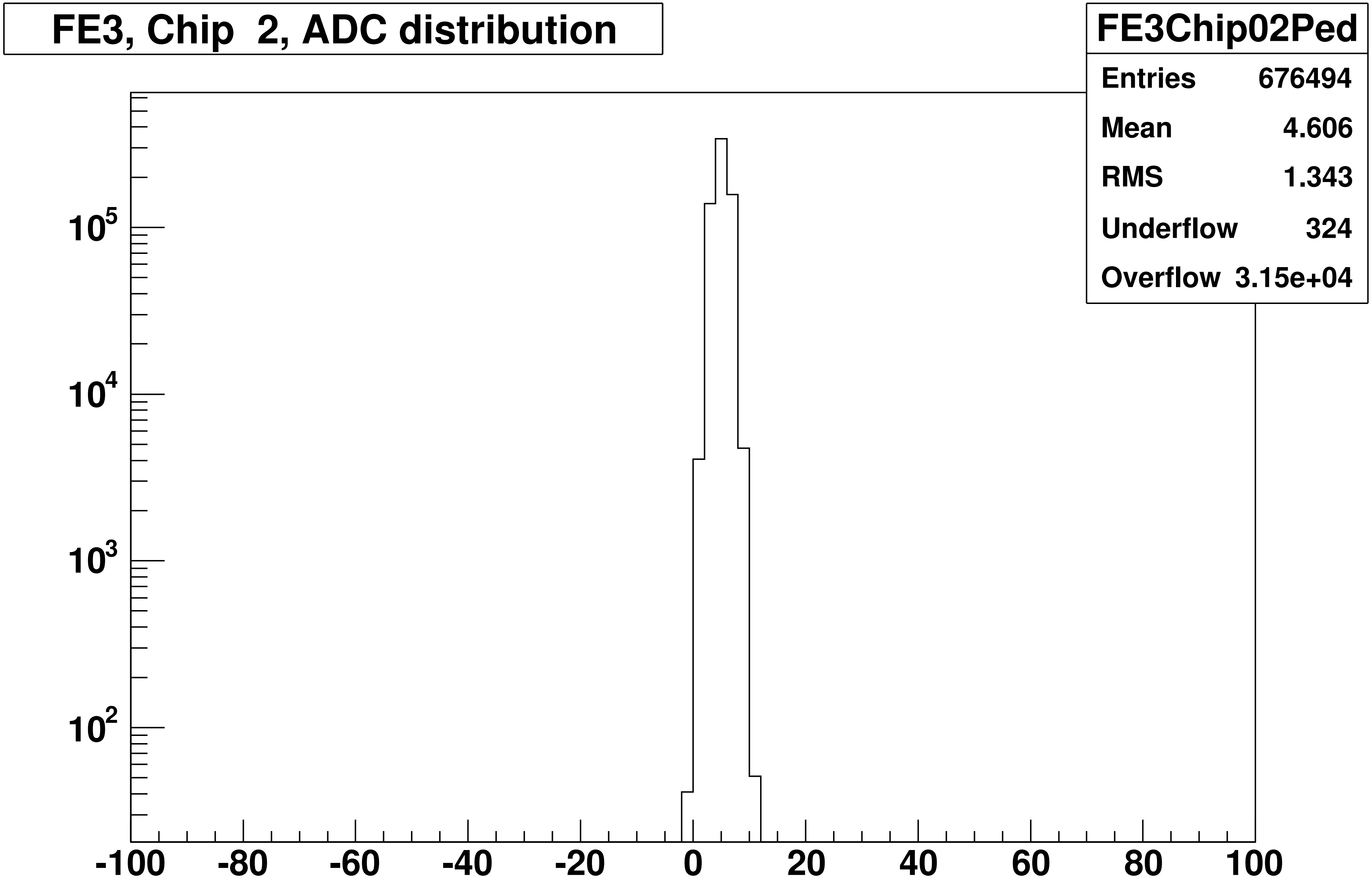
FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 676494
Mean 4.606
RMS 1.343
Underflow 324
Overflow 3.15e+04
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10^3
10^2
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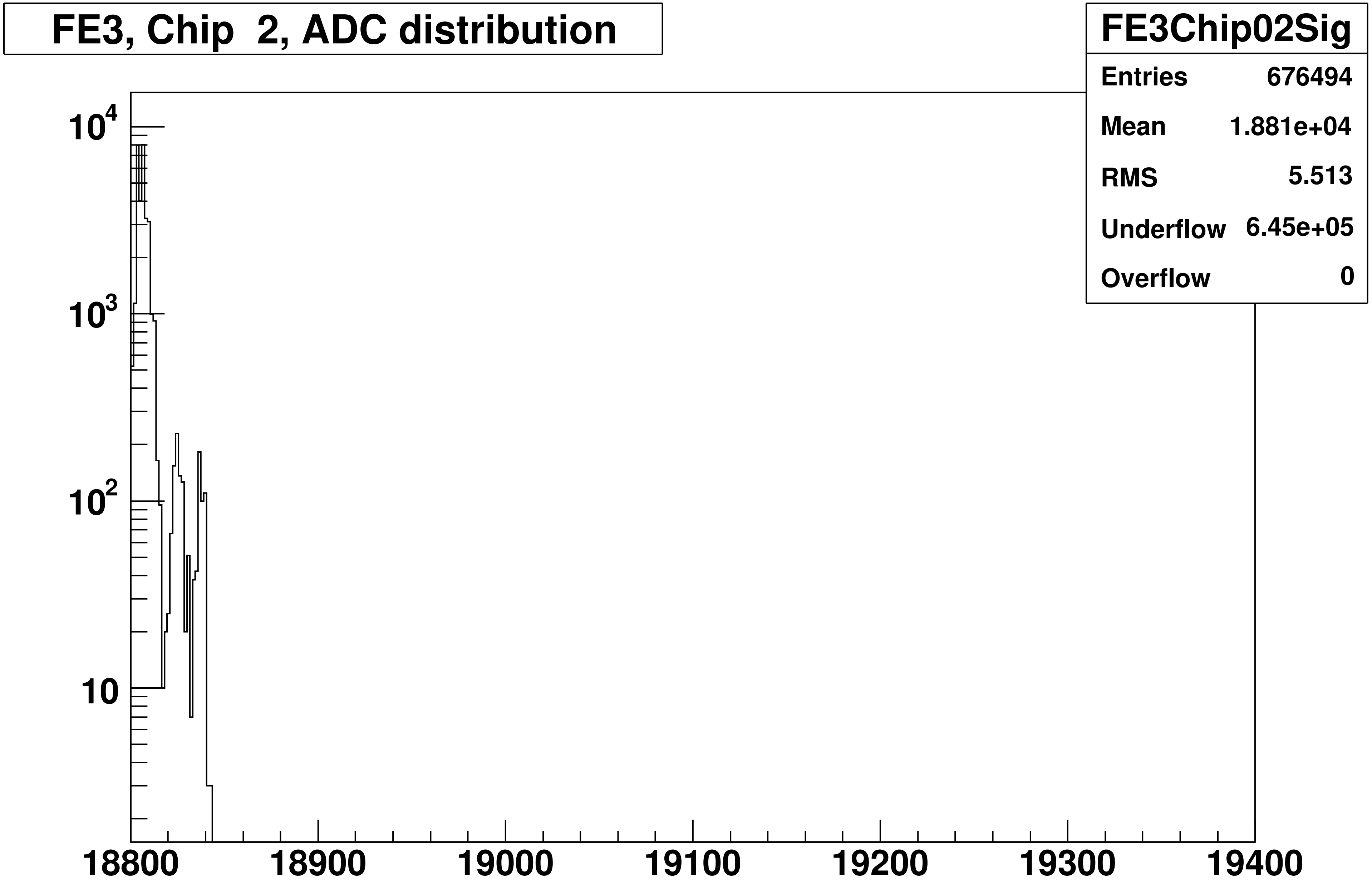
FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 676494
Mean 1.881e+04
RMS 5.513
Underflow 6.45e+05
Overflow 0
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10^3
10^2
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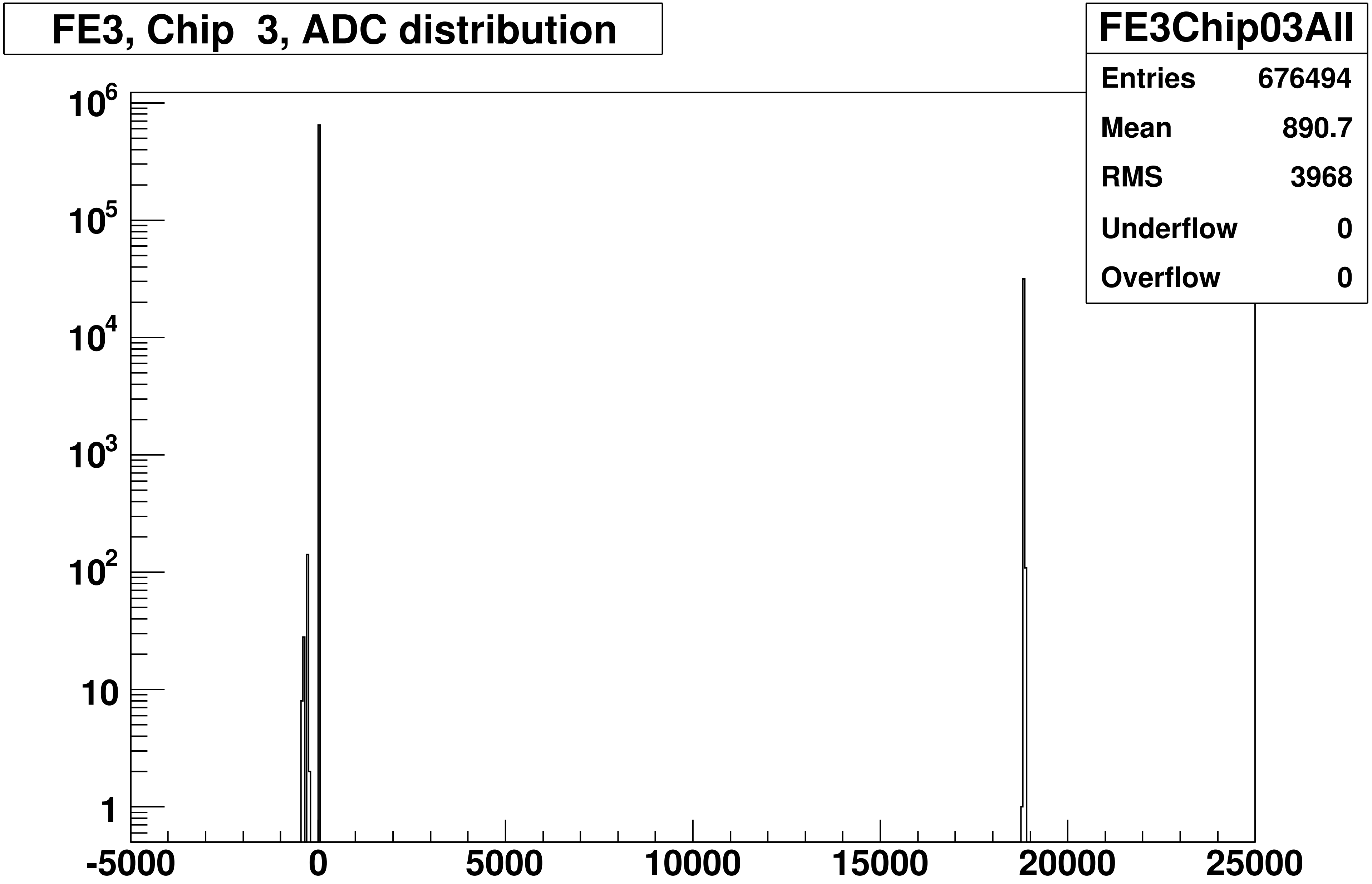

FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 676494
Mean 890.7
RMS 3968
Underflow 0
Overflow 0
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10^2
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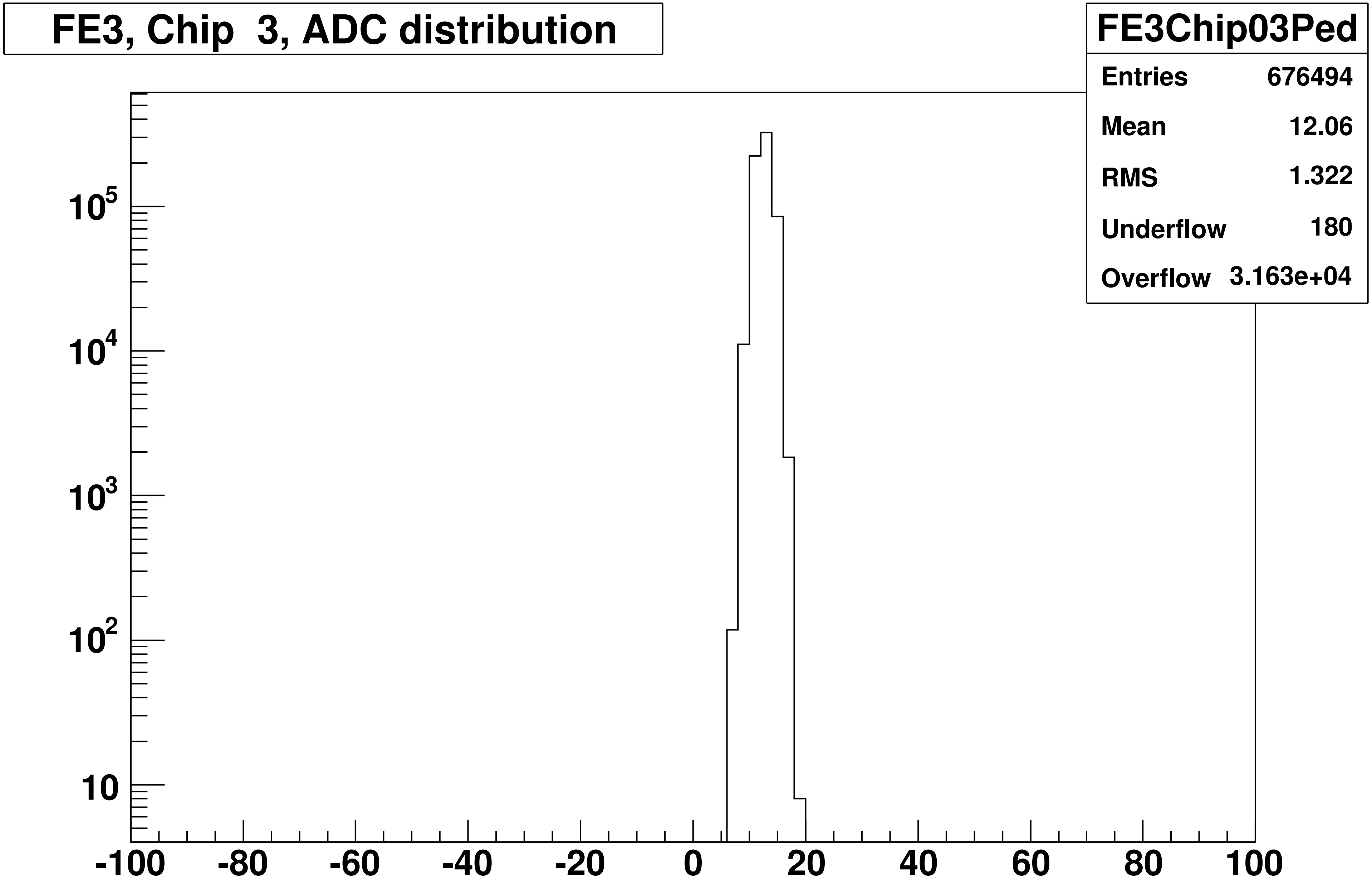
FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 676494
Mean 12.06
RMS 1.322
Underflow 180
Overflow 3.163e+04
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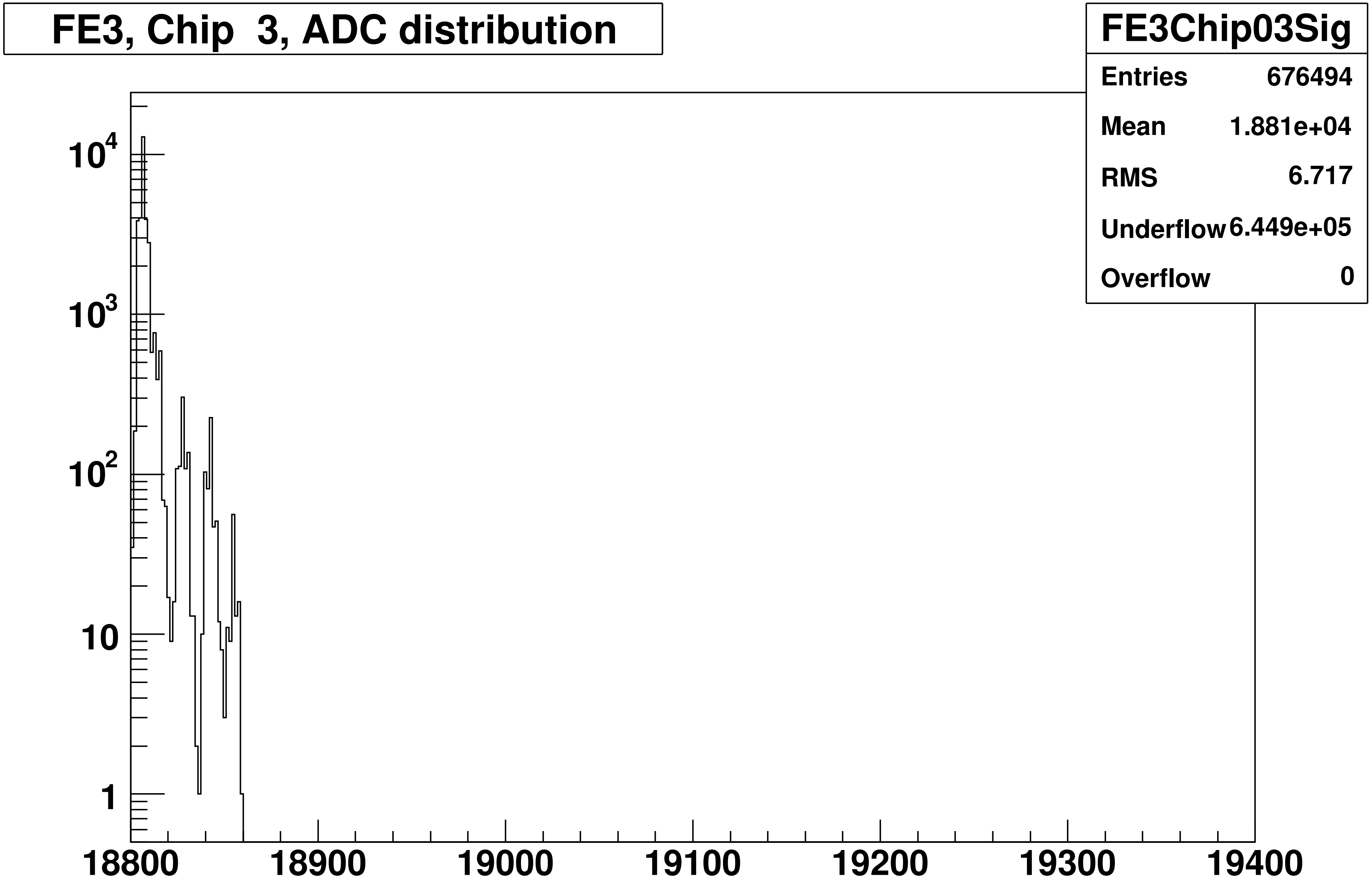
FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 676494
Mean 1.881e+04
RMS 6.717
Underflow 6.449e+05
Overflow 0
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10^3
10^2
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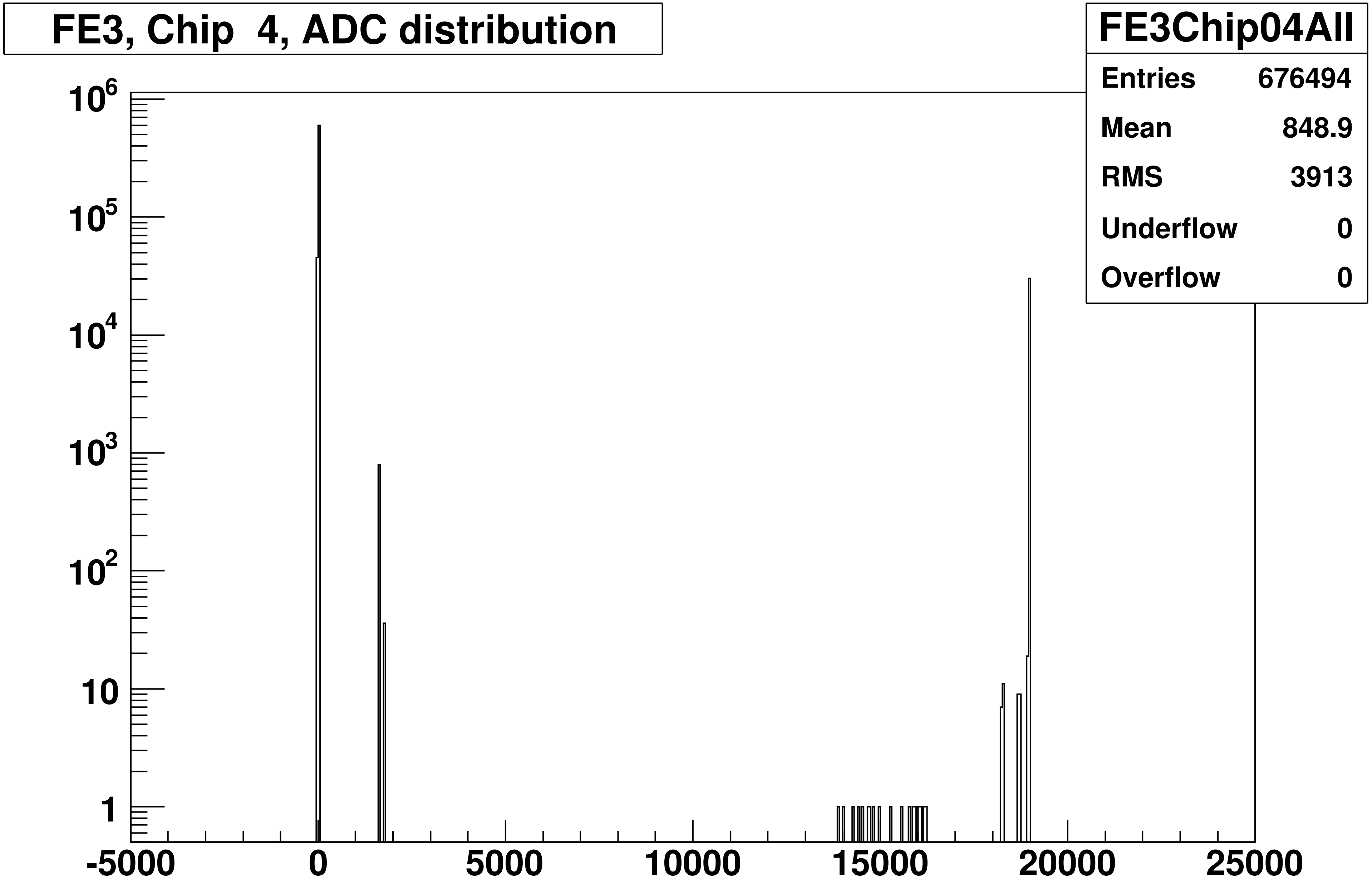
FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 676494
Mean 848.9
RMS 3913
Underflow 0
Overflow 0
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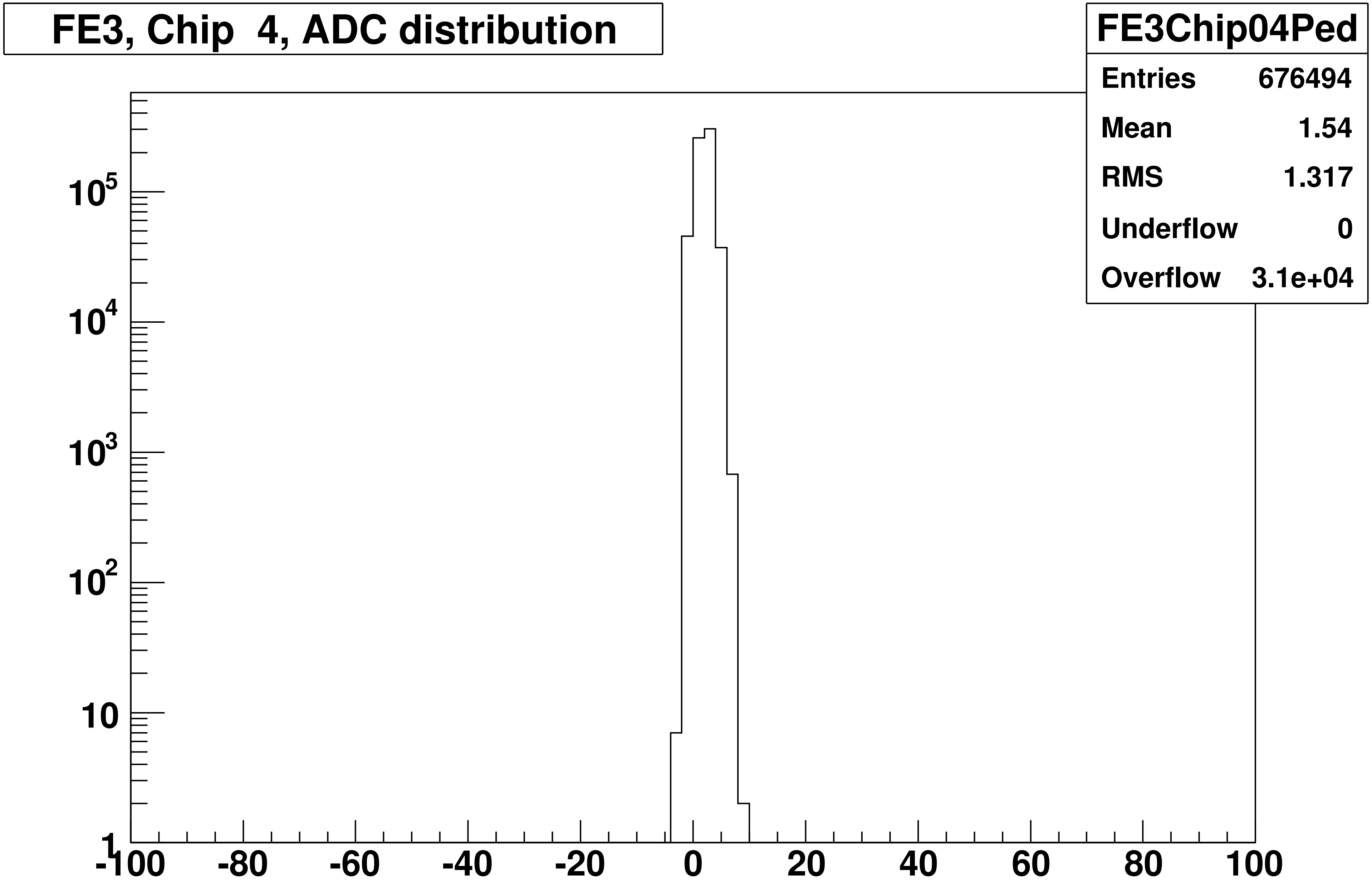

FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 676494
Mean 1.54
RMS 1.317
Underflow 0
Overflow 3.1e+04
10^5
10^4
10^3
10^2
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80
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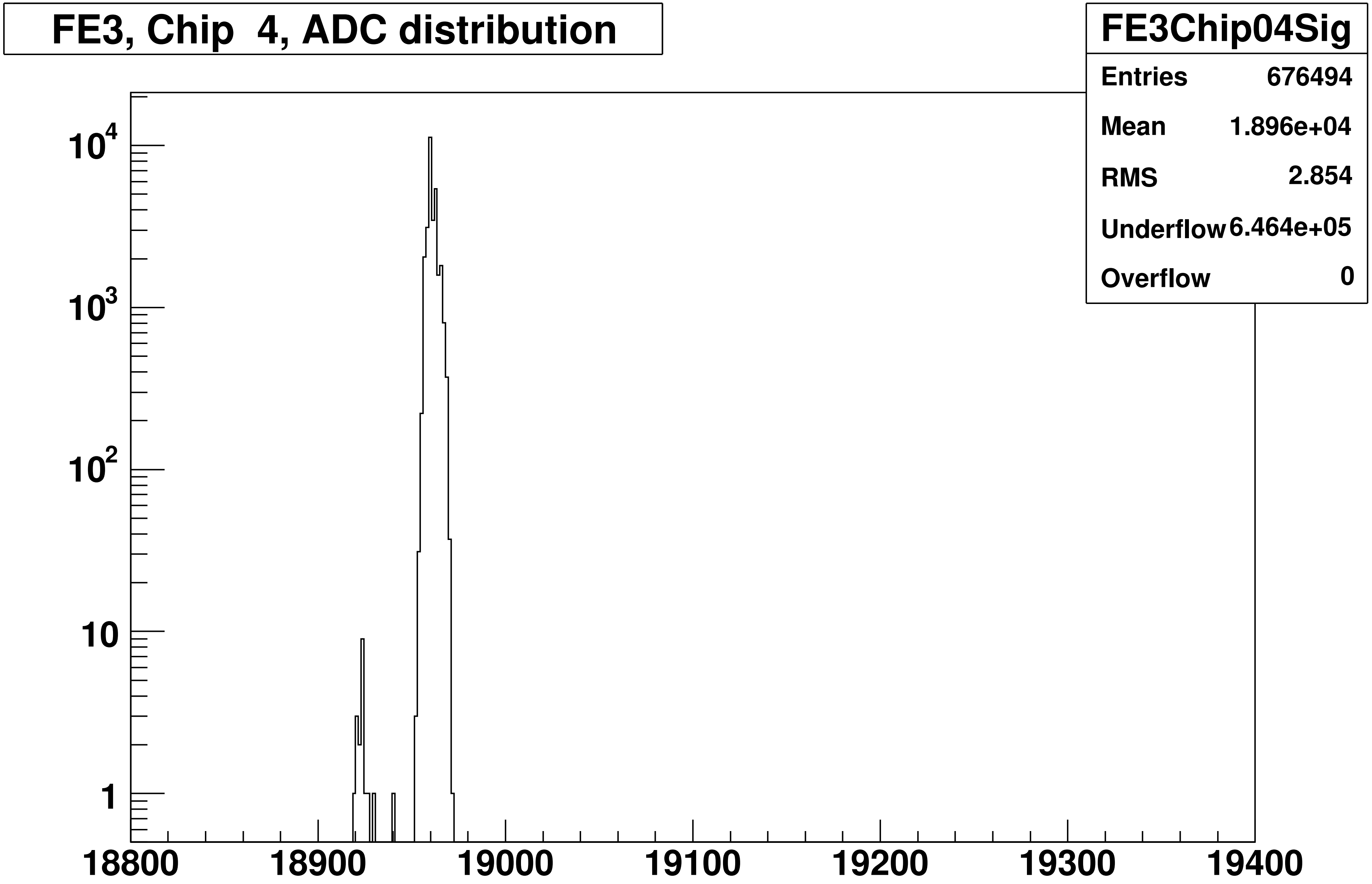
FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 676494
Mean 1.896e+04
RMS 2.854
Underflow 6.464e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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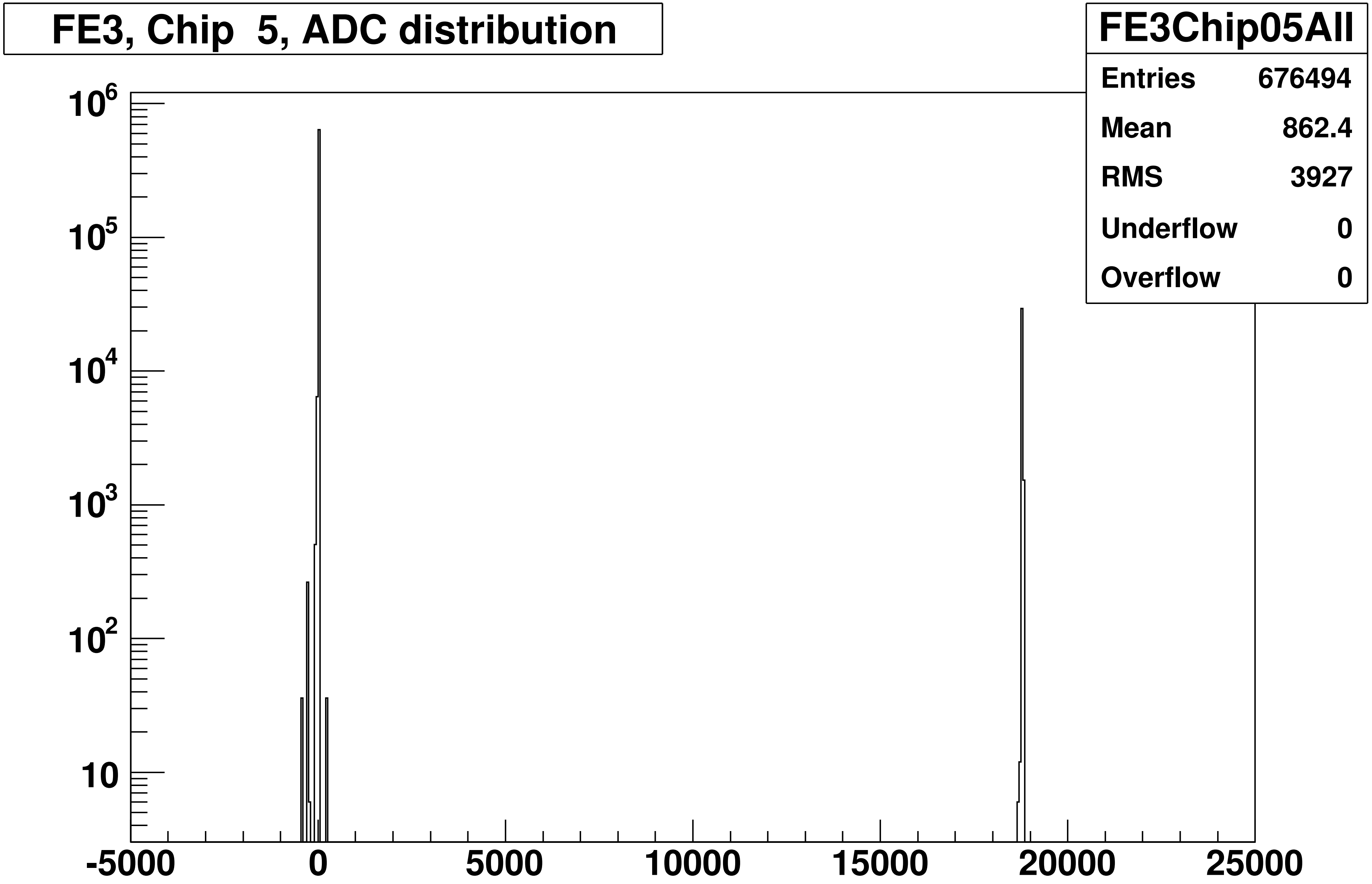
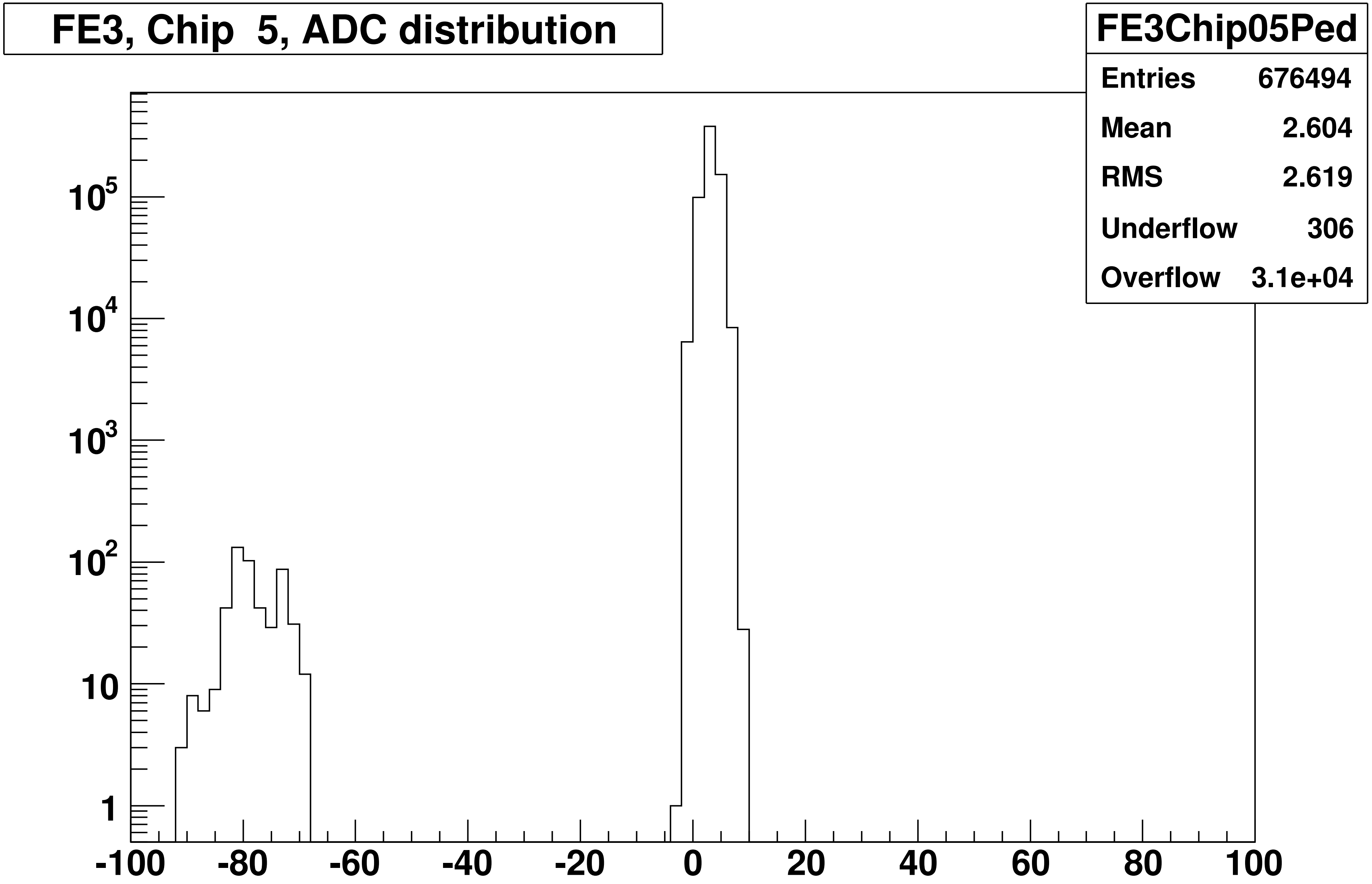
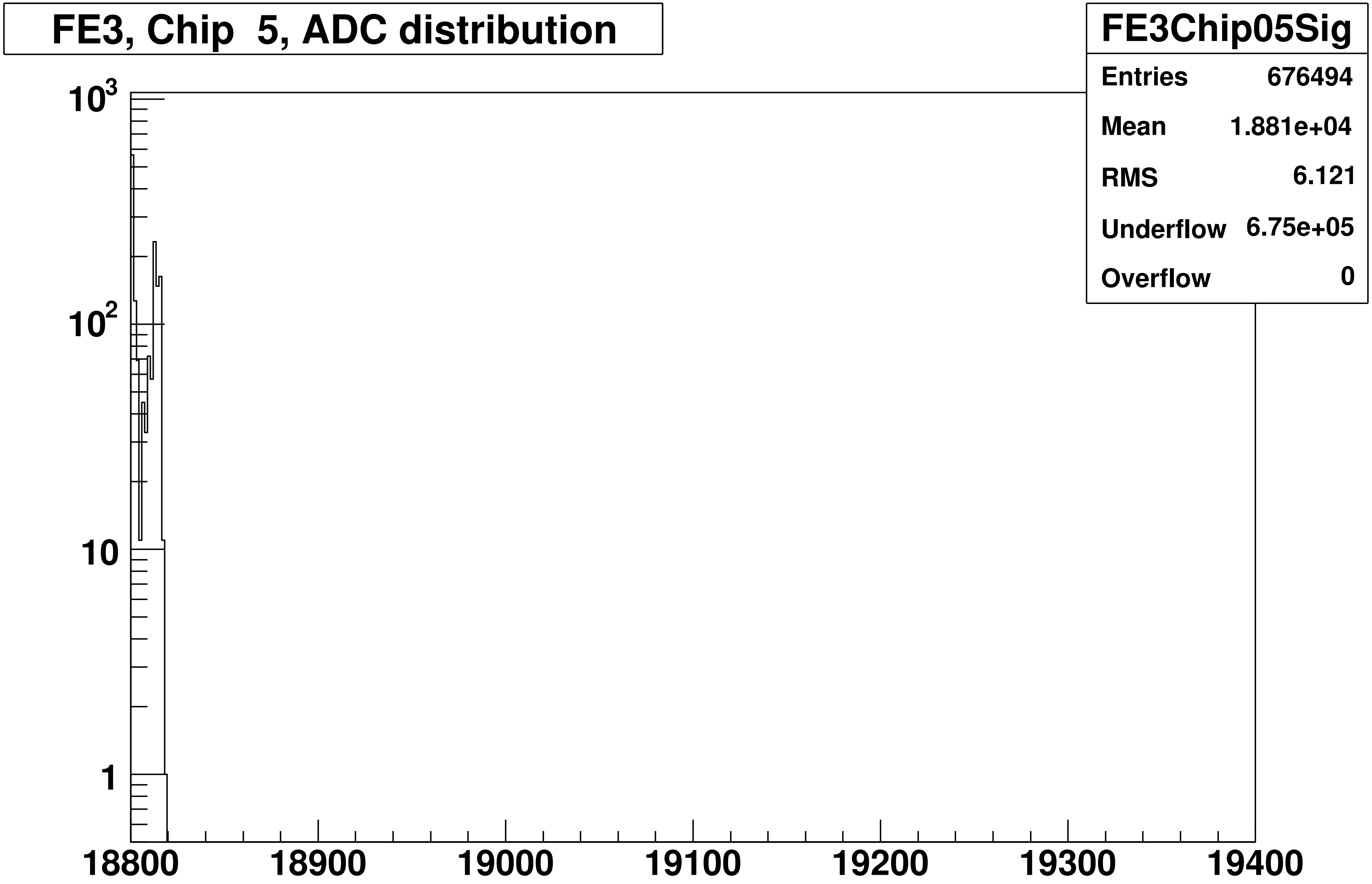
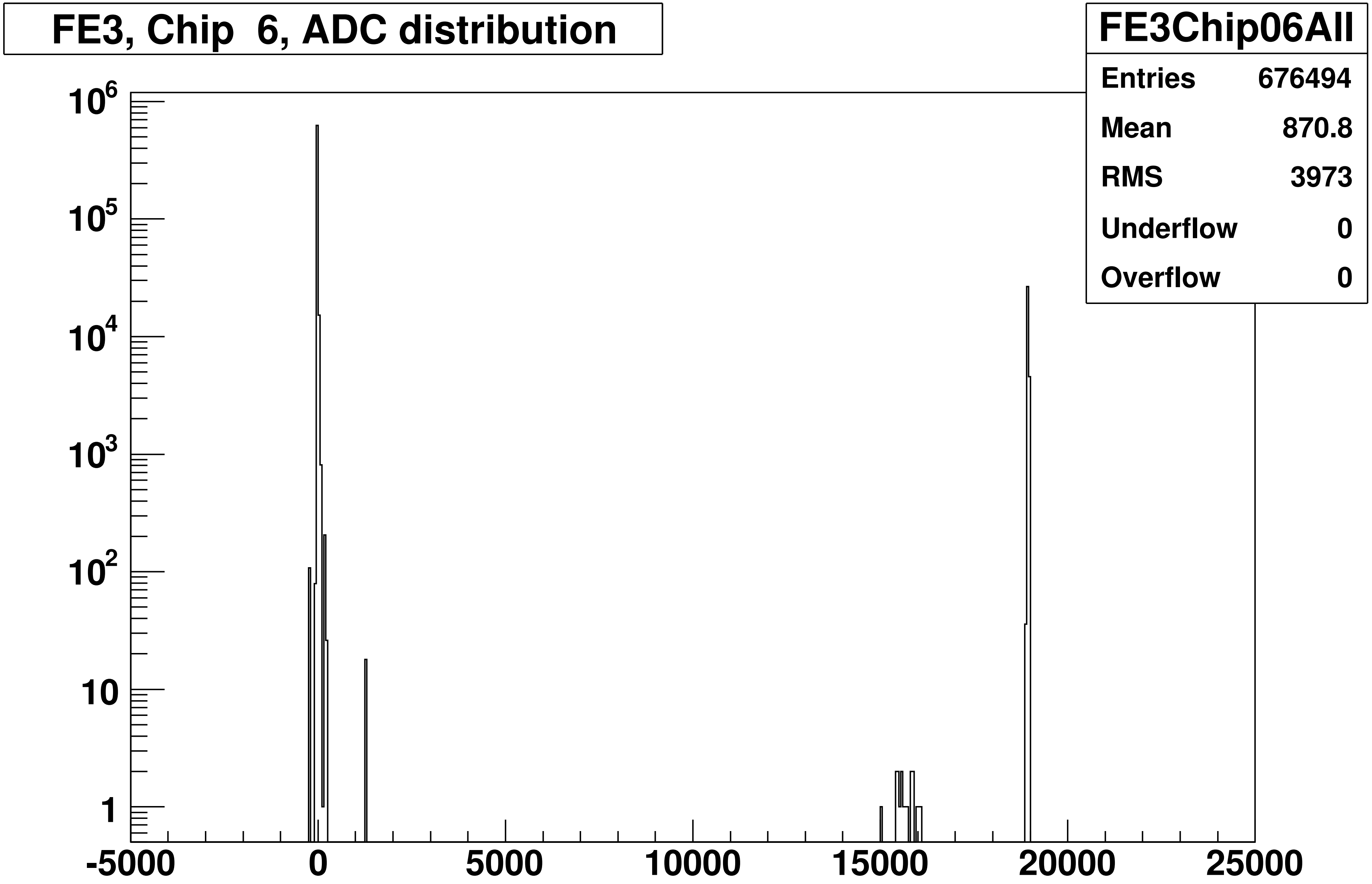
19300
19400


FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 676494
Mean 862.4
RMS 3927
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 676494
Mean 2.604
RMS 2.619
Underflow 306
Overflow 3.1e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 676494
Mean 1.881e+04
RMS 6.121
Underflow 6.75e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 676494
Mean 870.8
RMS 3973
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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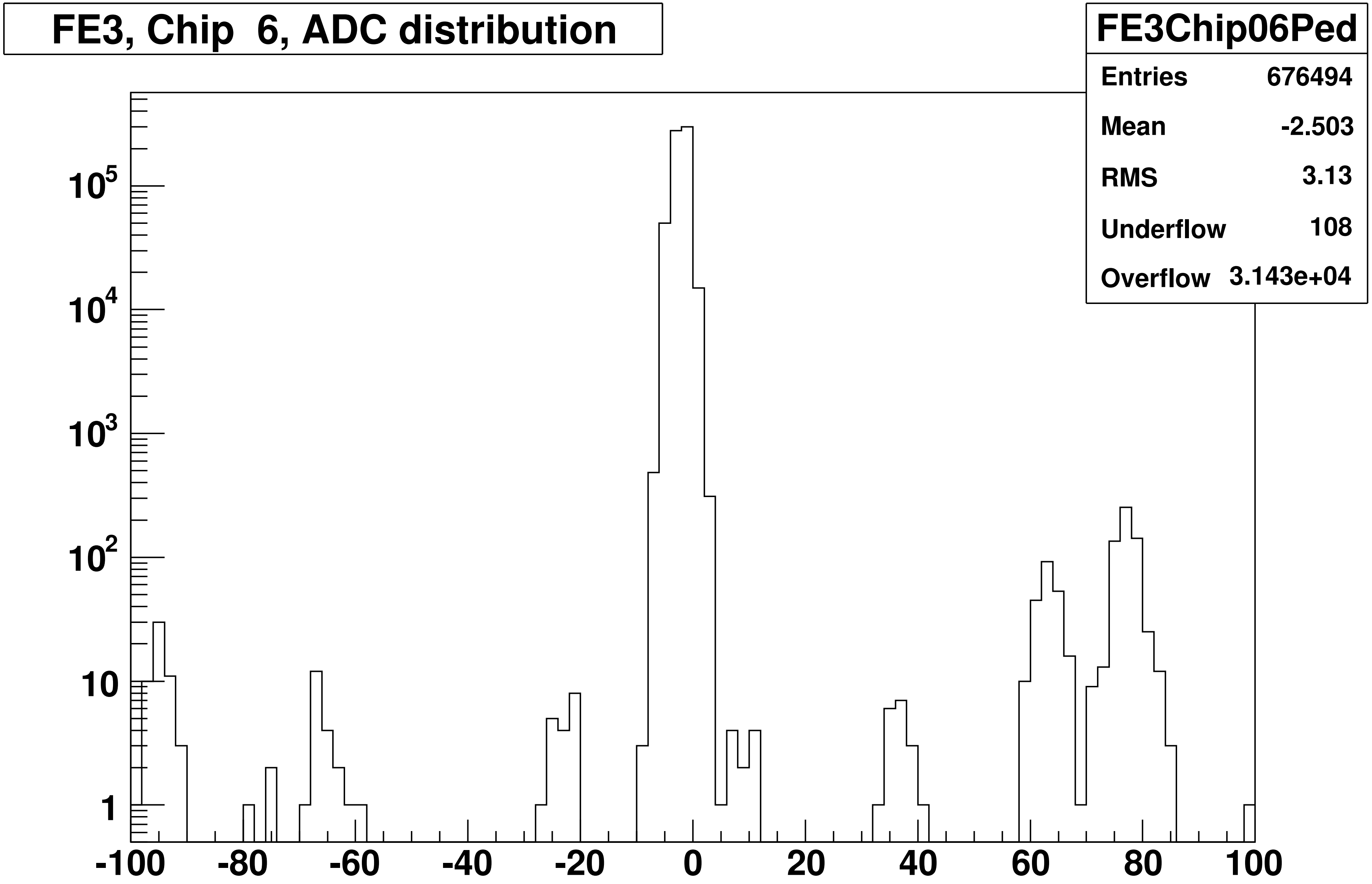
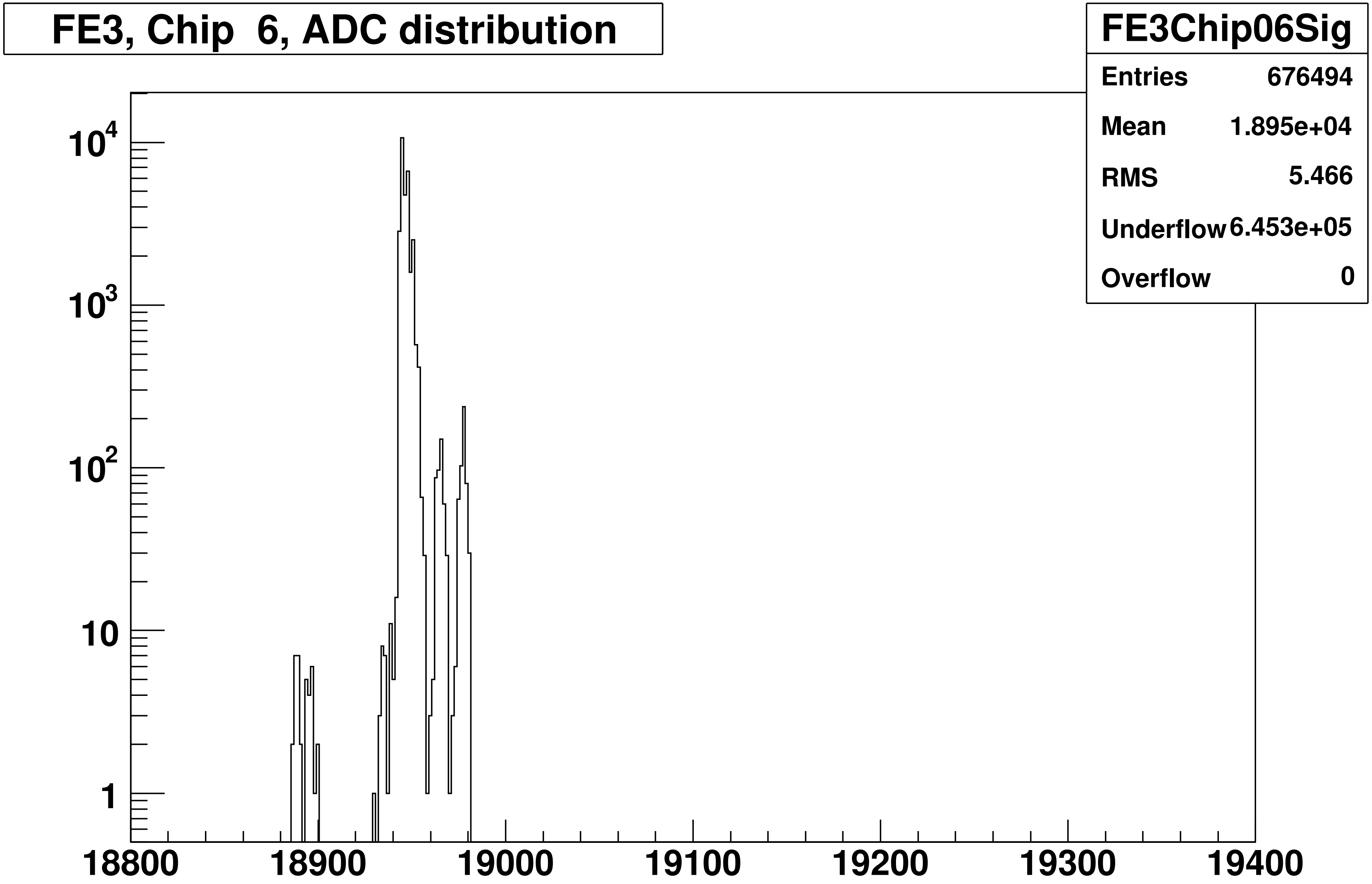
10000
15000
20000
25000


FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 676494
Mean -2.503
RMS 3.13
Underflow 108
Overflow 3.143e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 676494
Mean 1.895e+04
RMS 5.466
Underflow 6.453e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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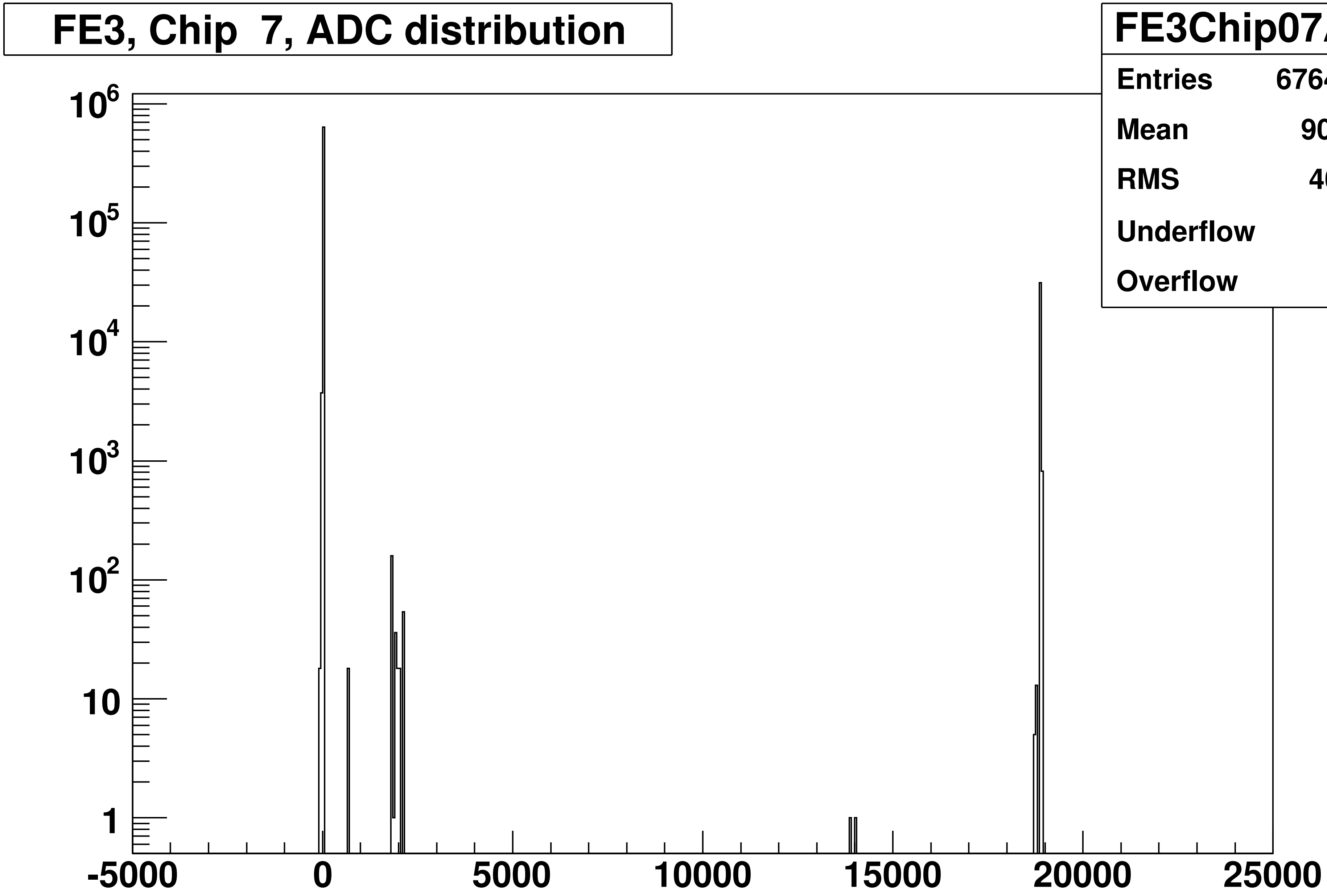
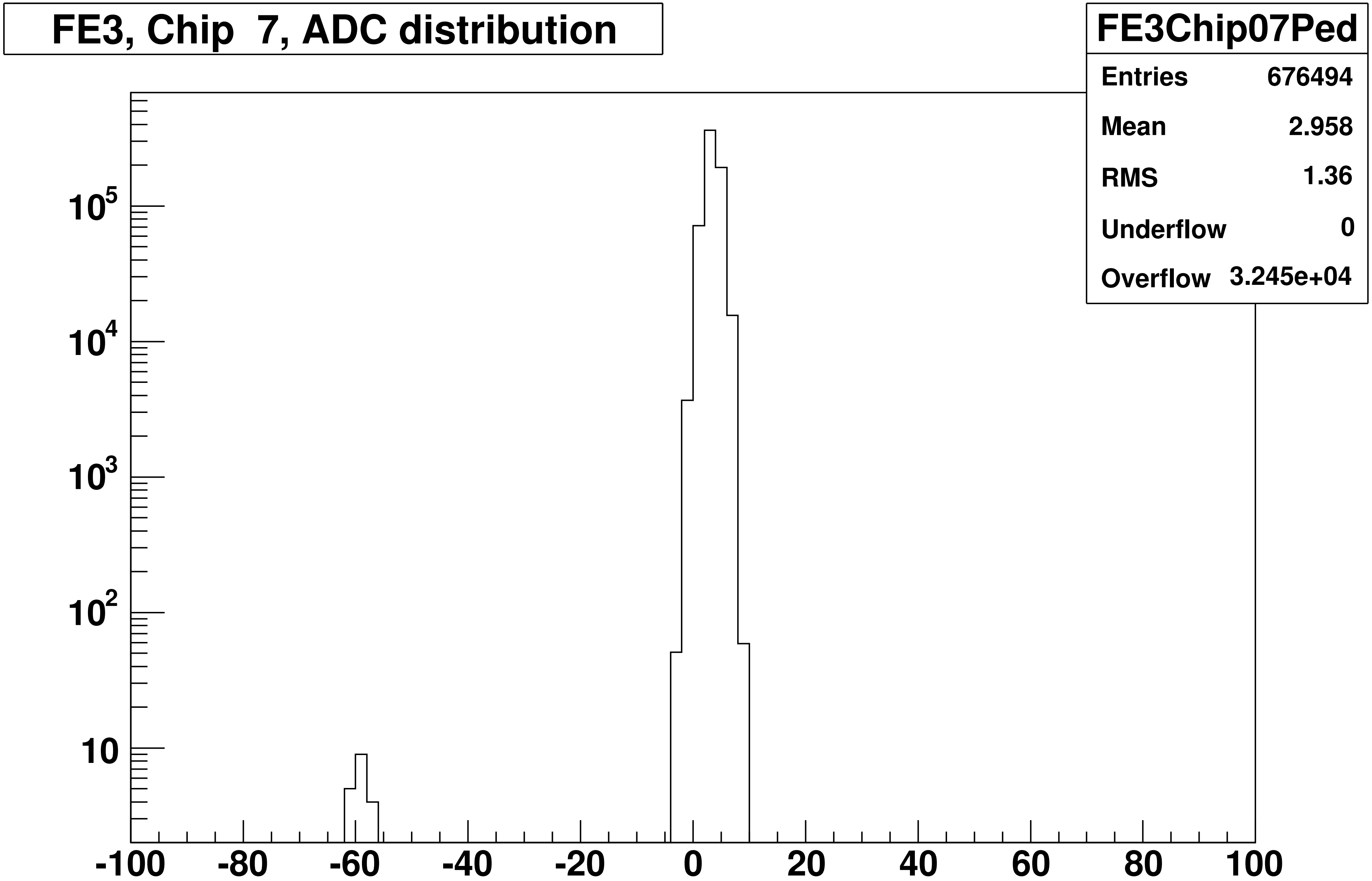
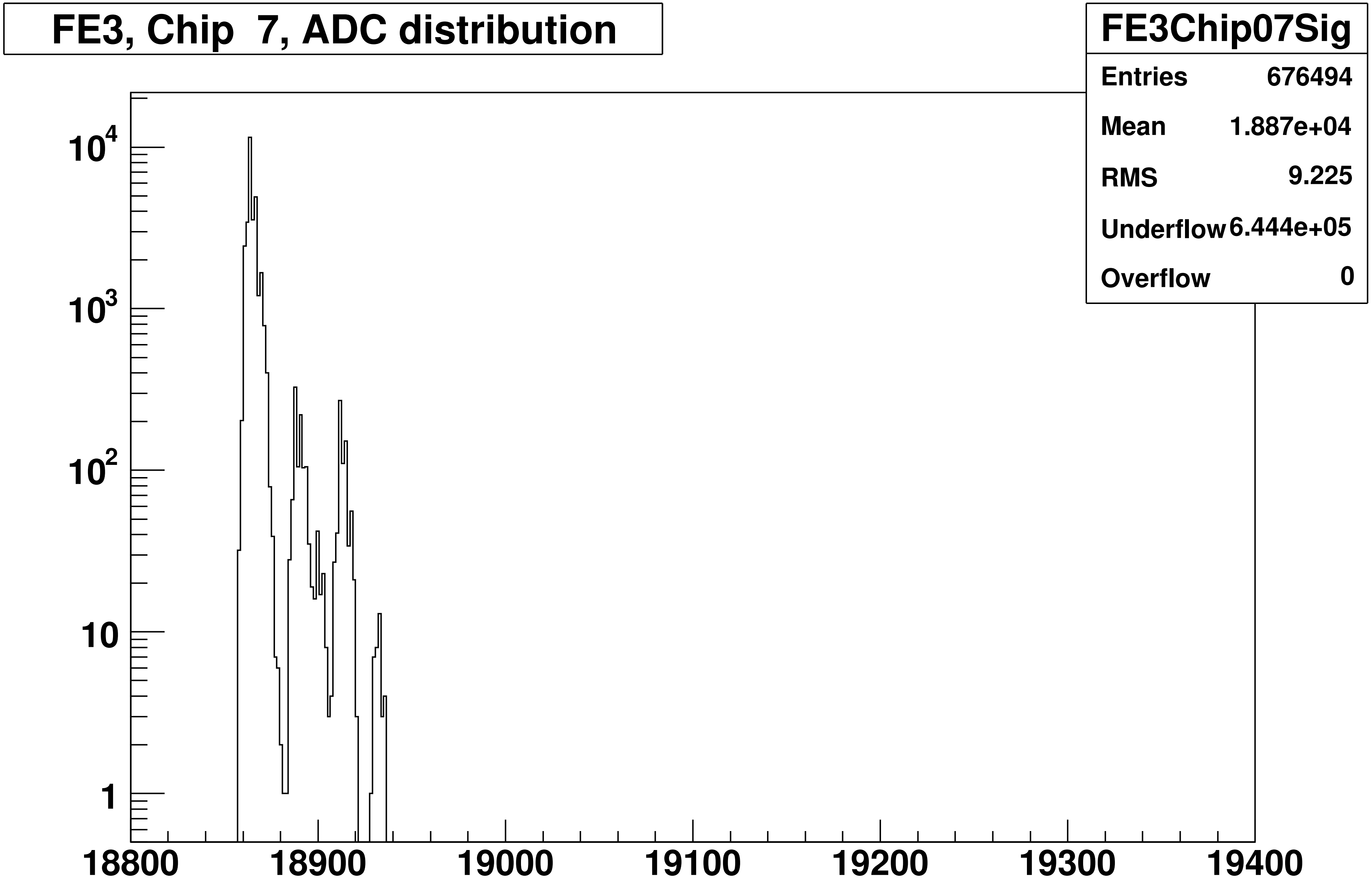
19100
19200
19300
19400

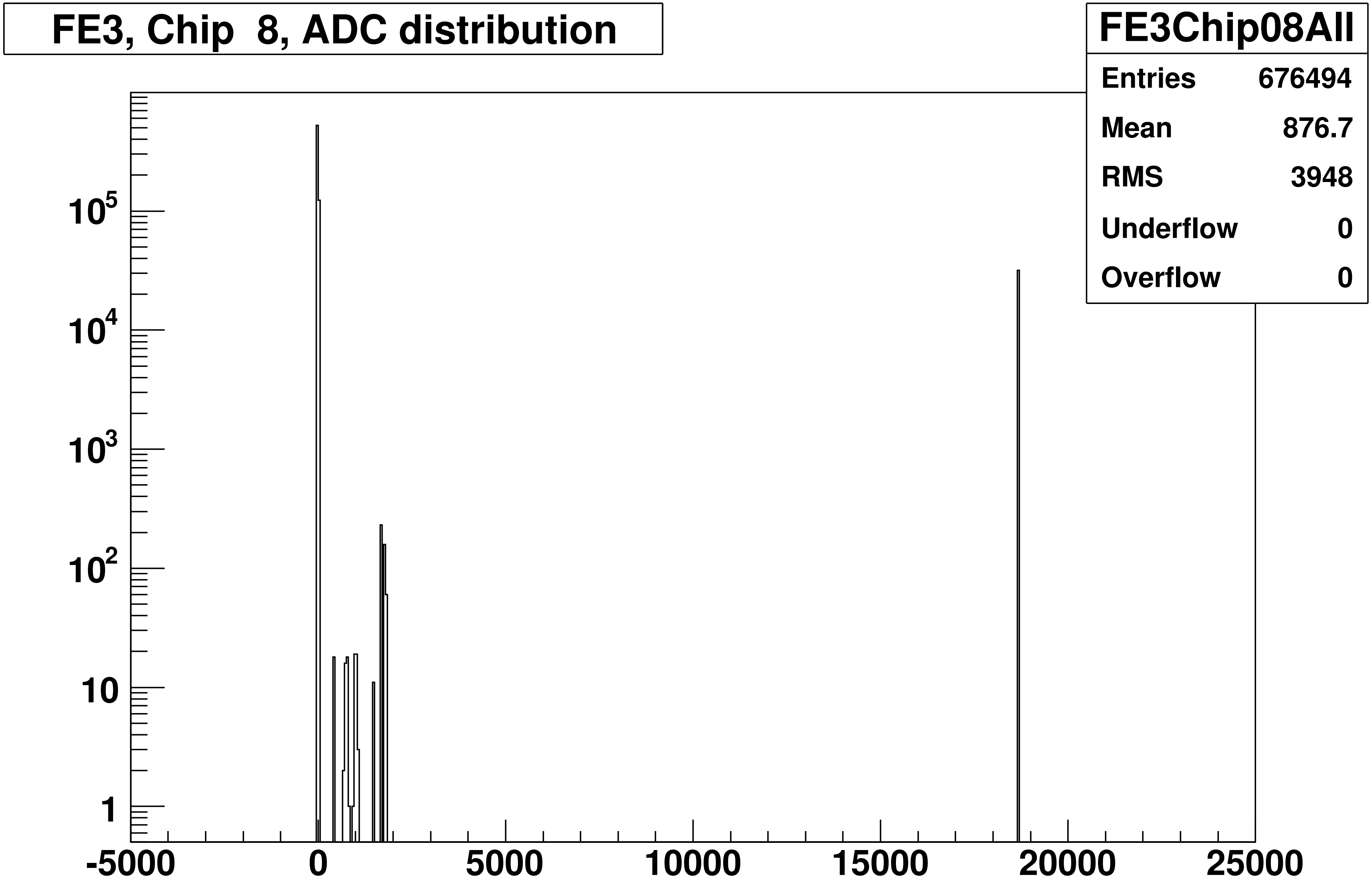


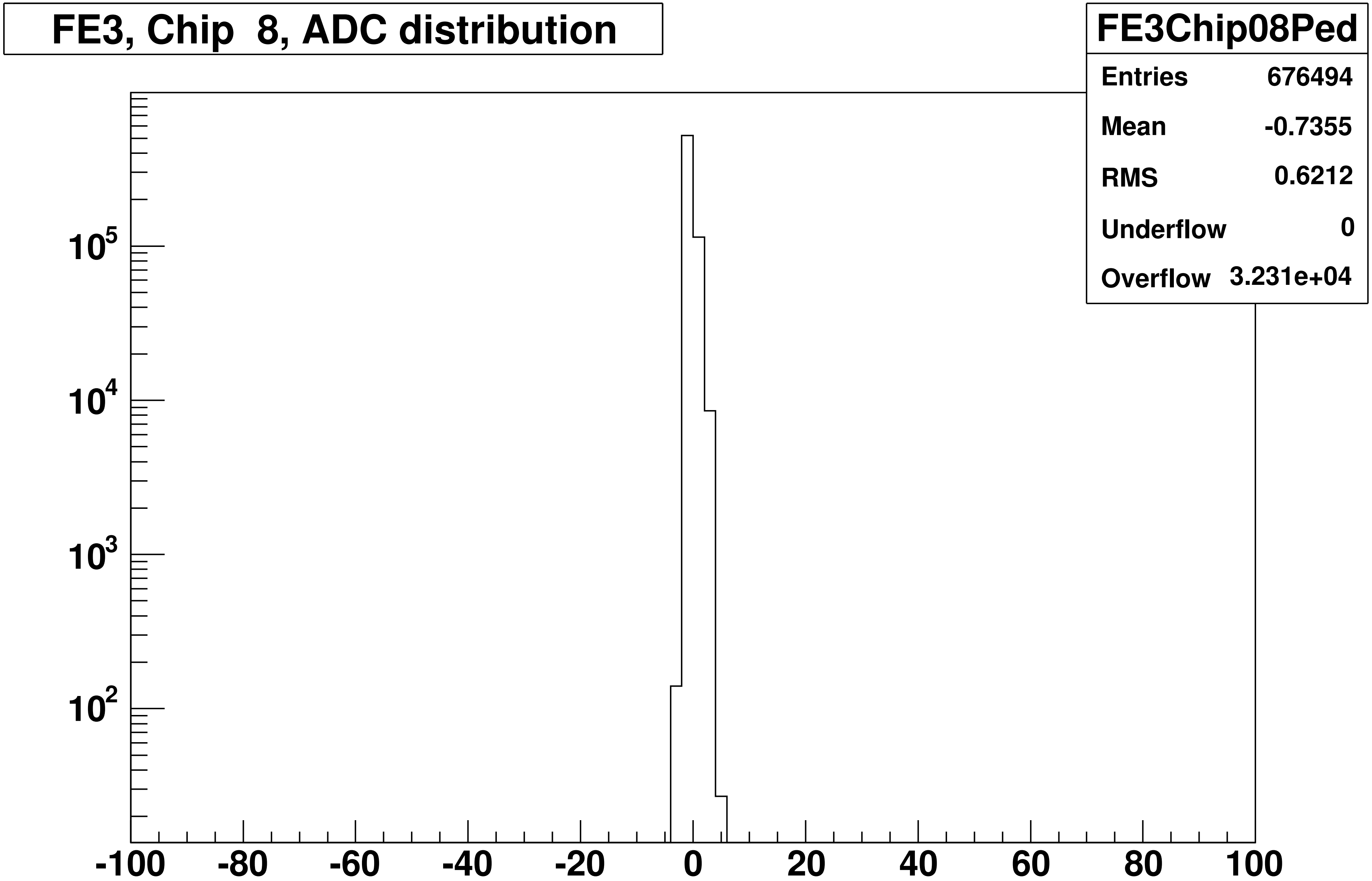
FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 676494
Mean 900.3
RMS 4013
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

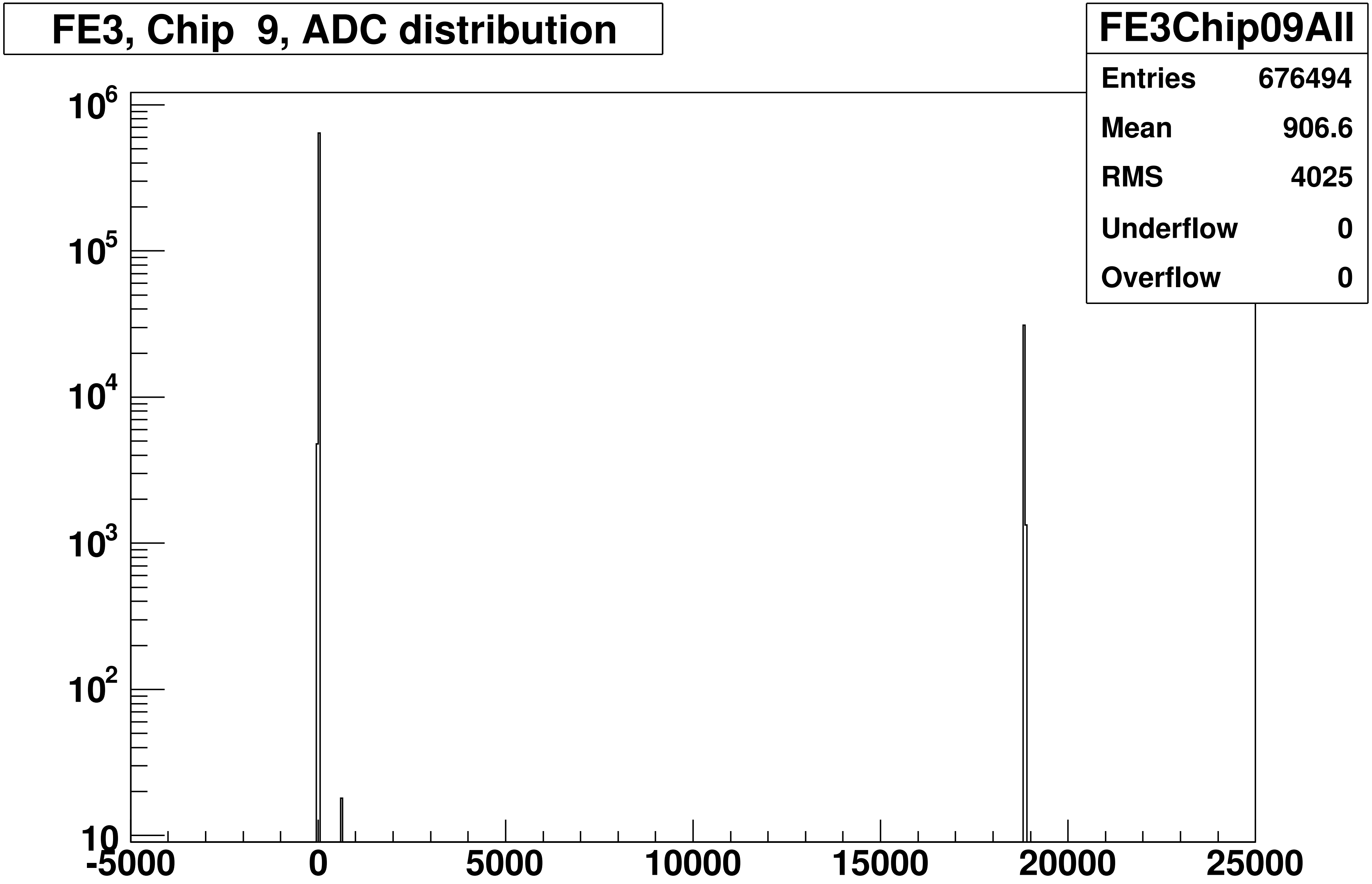


FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 676494
Mean 2.958
RMS 1.36
Underflow 0
Overflow 3.245e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

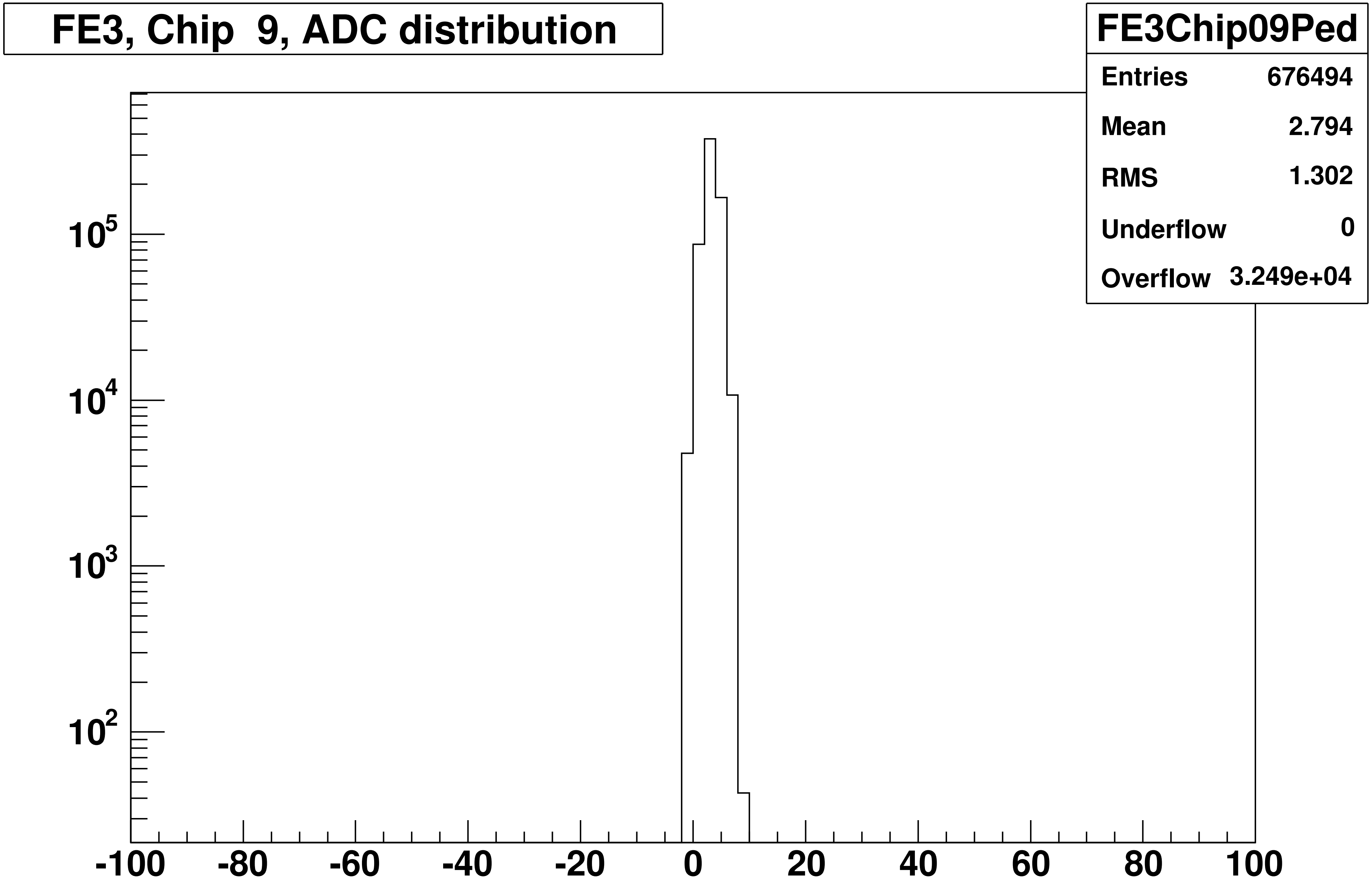

FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 676494
Mean 1.887e+04
RMS 9.225
Underflow 6.444e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

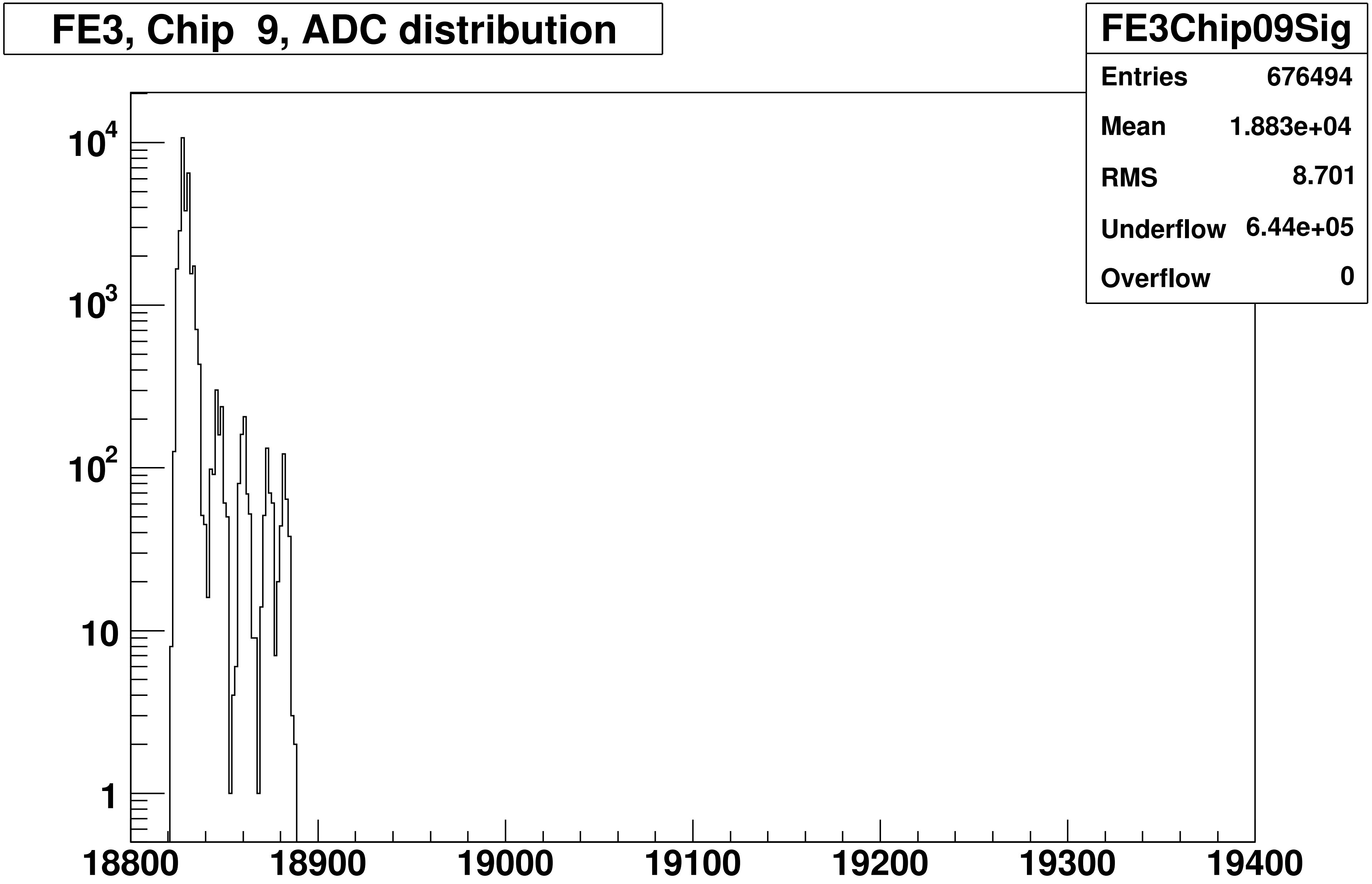

FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 676494
Mean 876.7
RMS 3948
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 676494
Mean -0.7355
RMS 0.6212
Underflow 0
Overflow 3.231e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 676494
Mean 906.6
RMS 4025
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 676494
Mean 2.794
RMS 1.302
Underflow 0
Overflow 3.249e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 676494
Mean 1.883e+04
RMS 8.701
Underflow 6.44e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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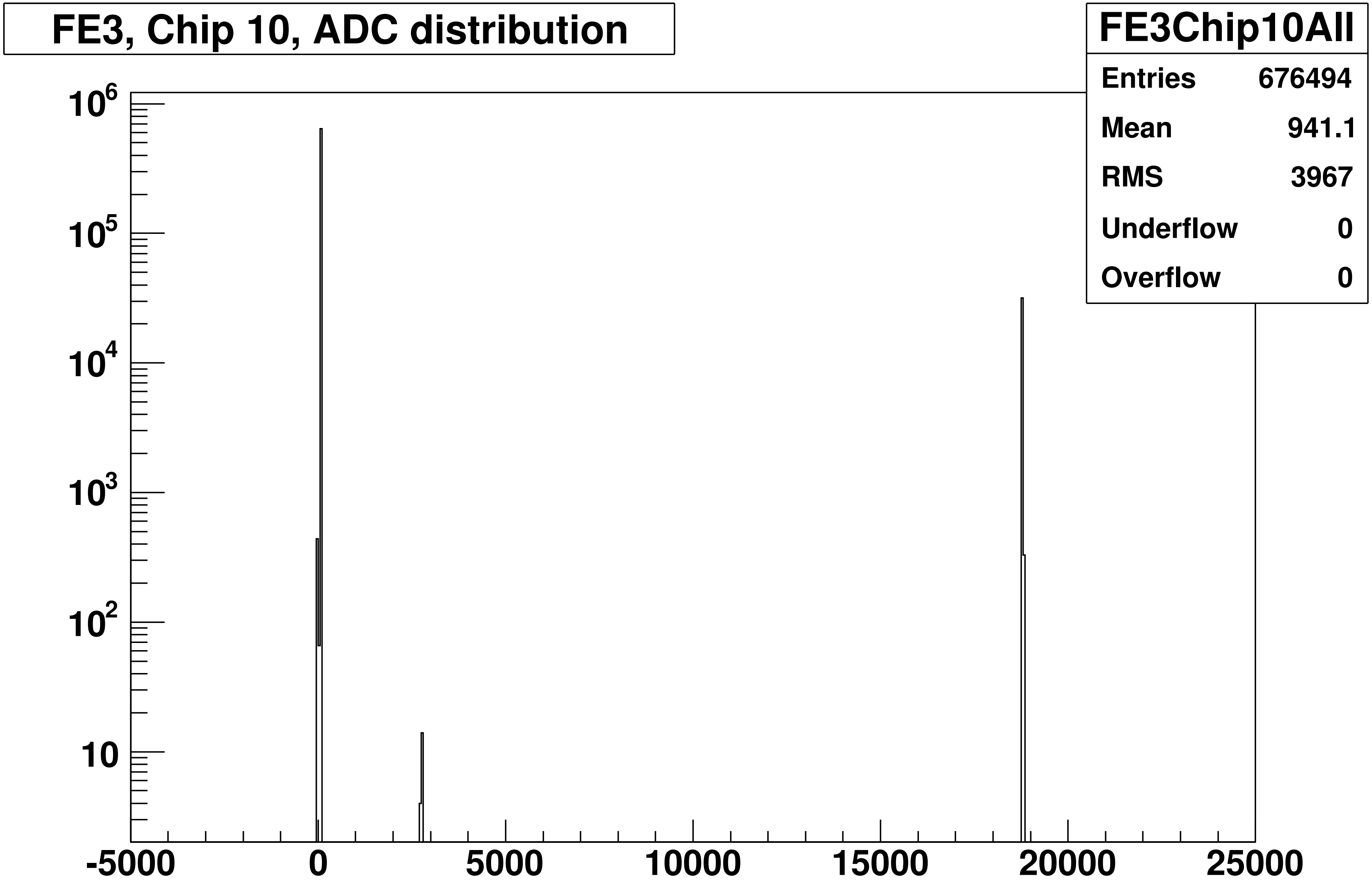
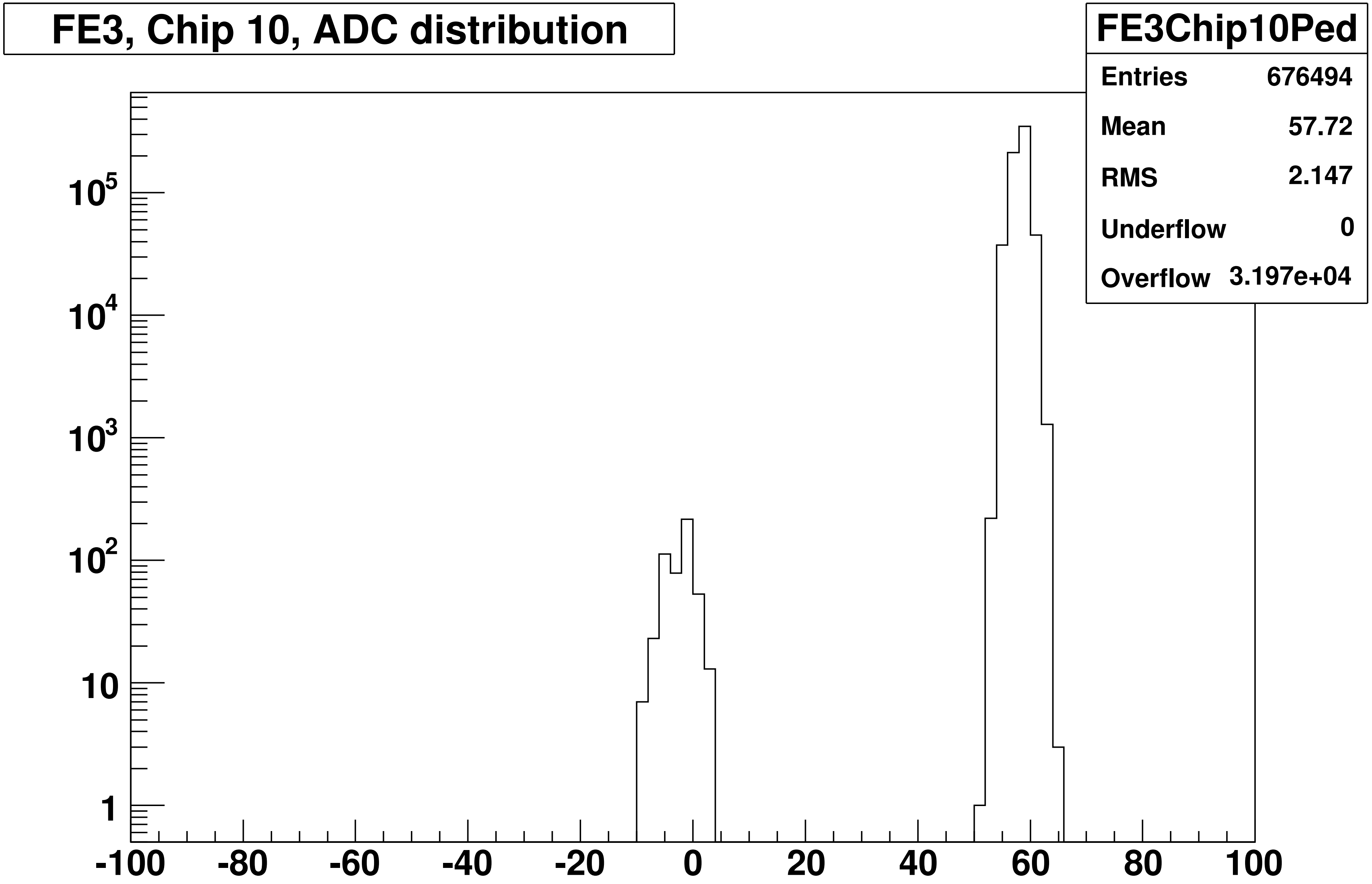
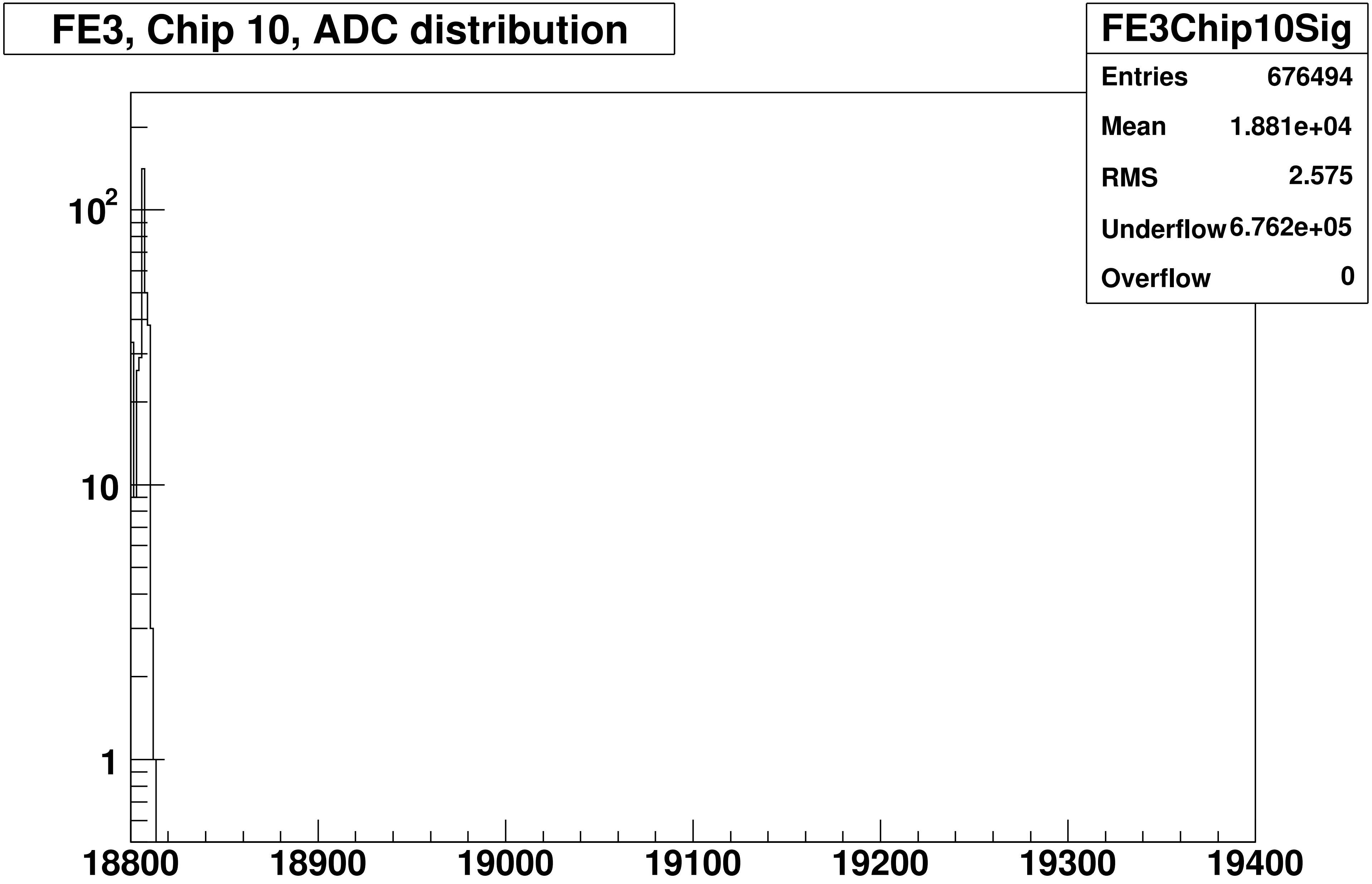
19100
19200
19300
19400

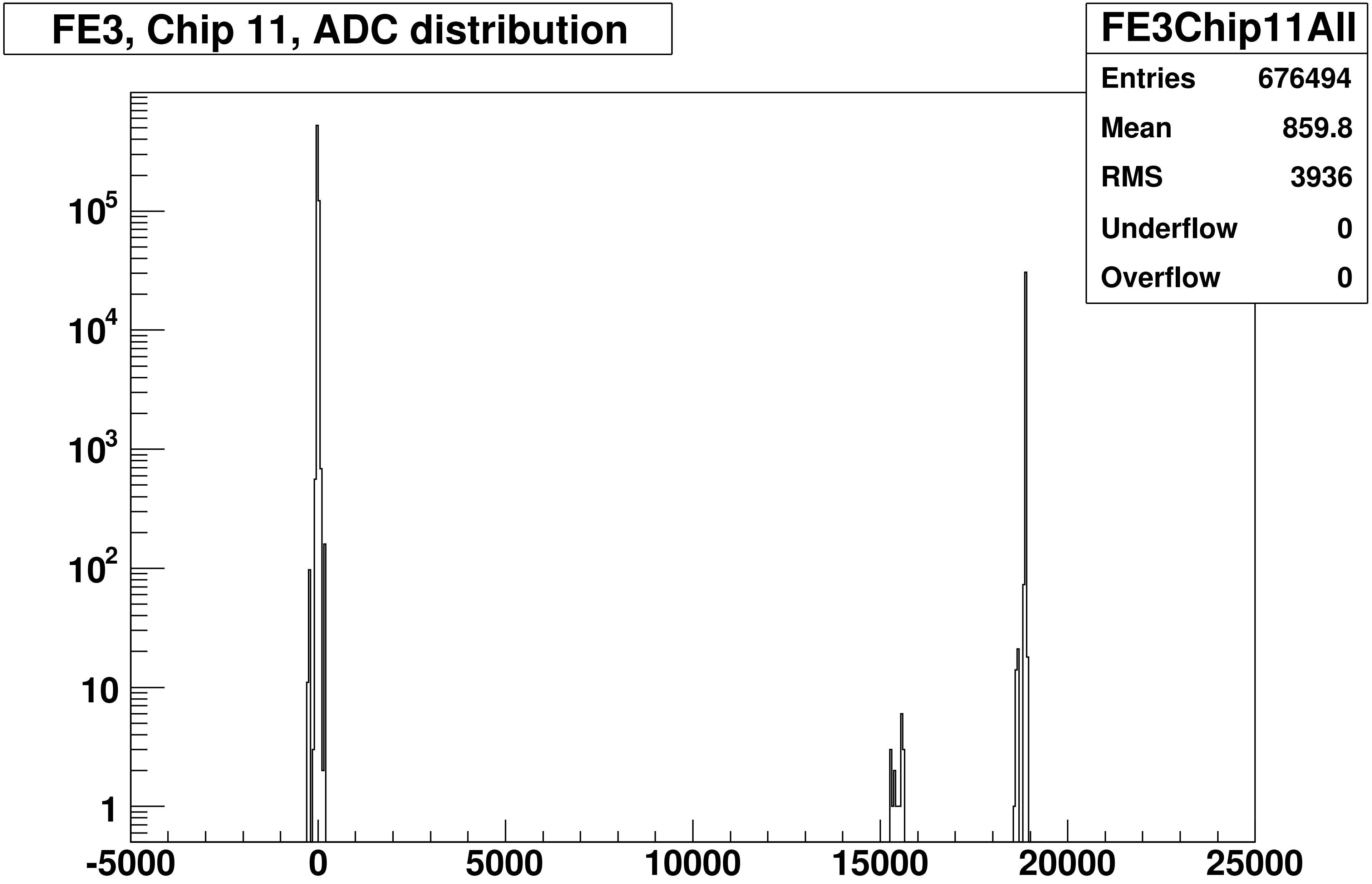

FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 676494
Mean 941.1
RMS 3967
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

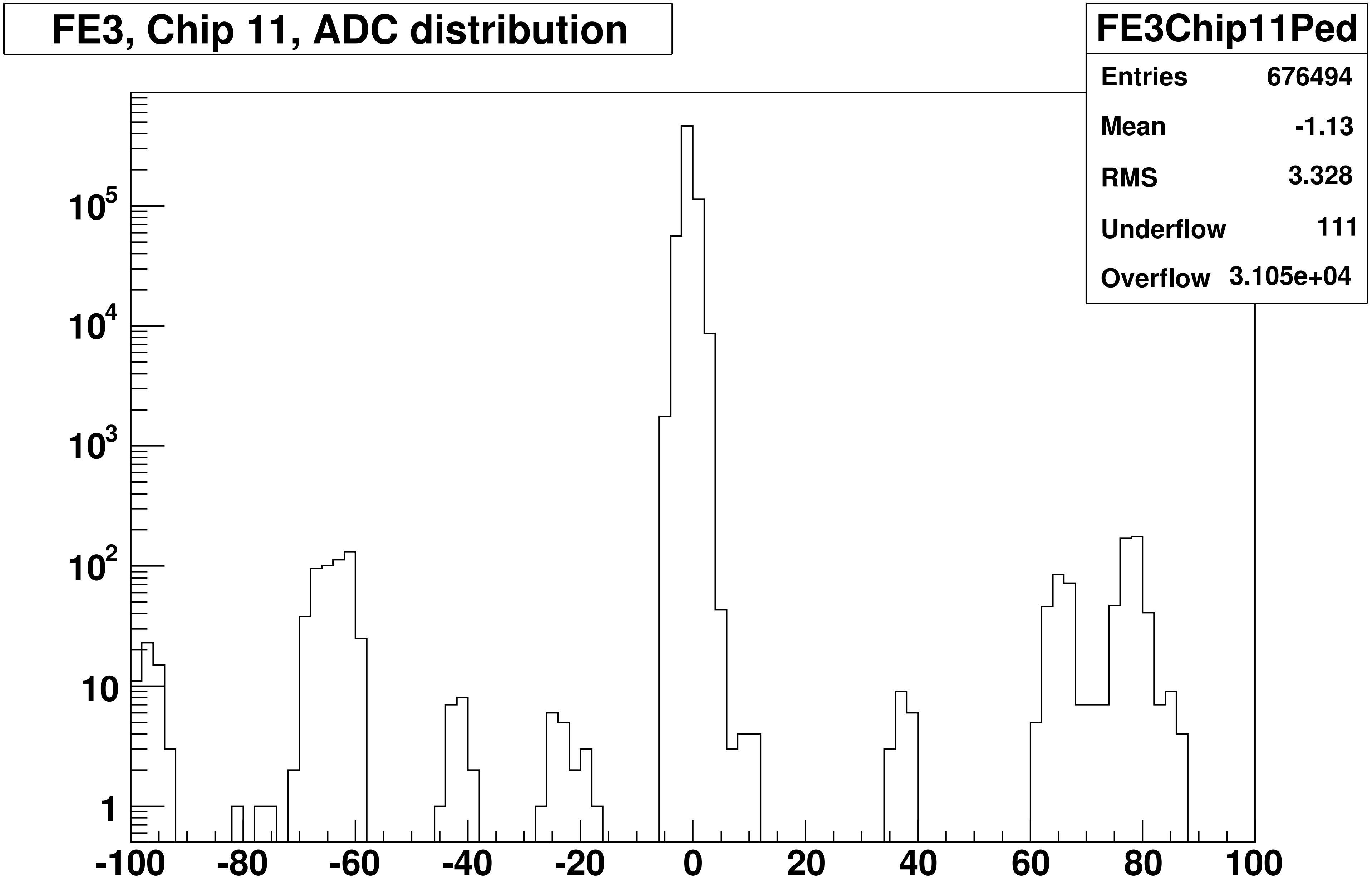


FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 676494
Mean 57.72
RMS 2.147
Underflow 0
Overflow 3.197e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

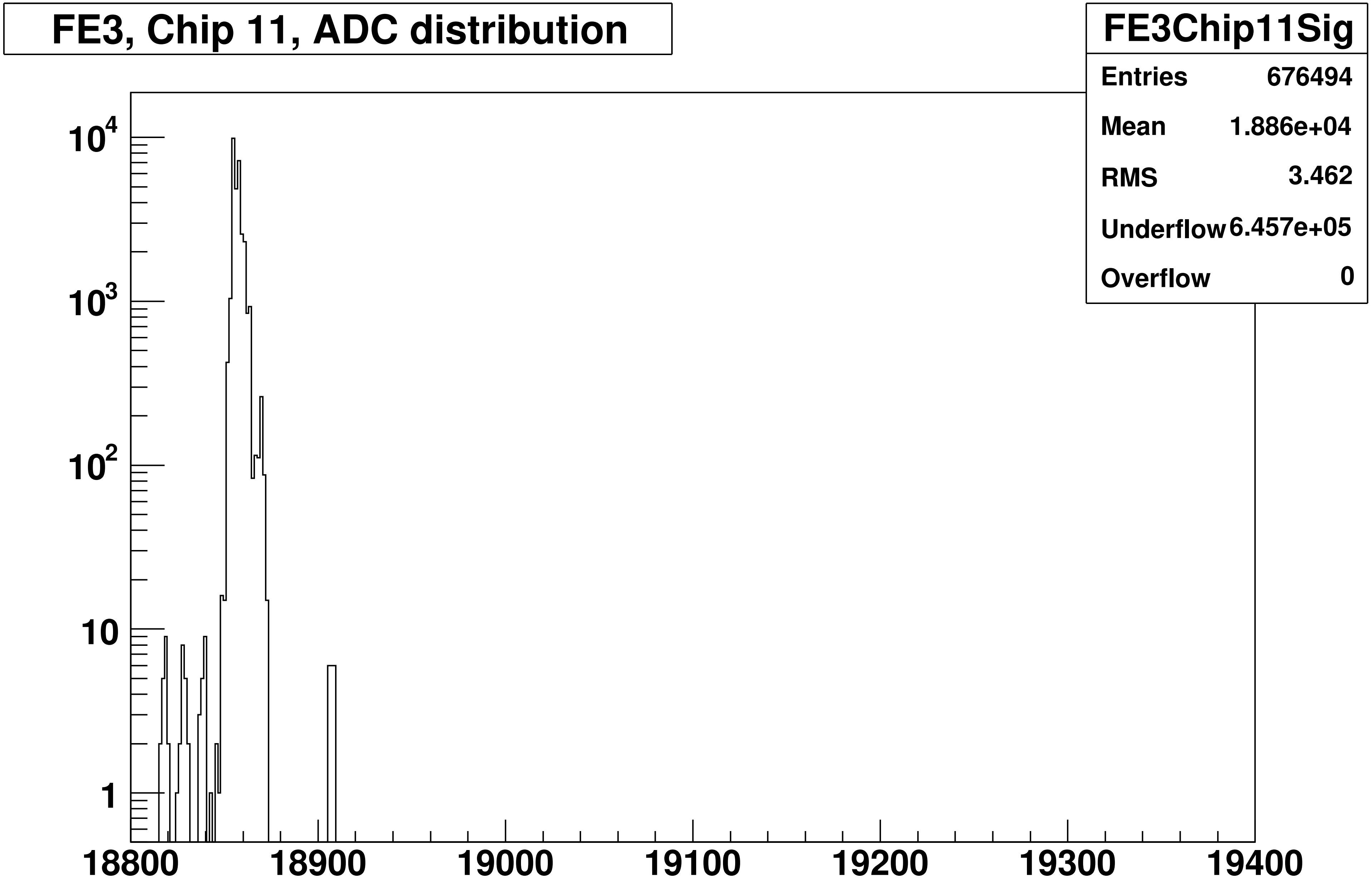


FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 676494
Mean 1.881e+04
RMS 2.575
Underflow 6.762e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 676494
Mean 859.8
RMS 3936
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 676494
Mean -1.13
RMS 3.328
Underflow 111
Overflow 3.105e+04


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 676494
Mean 1.886e+04
RMS 3.462
Underflow 6.457e+05
Overflow 0
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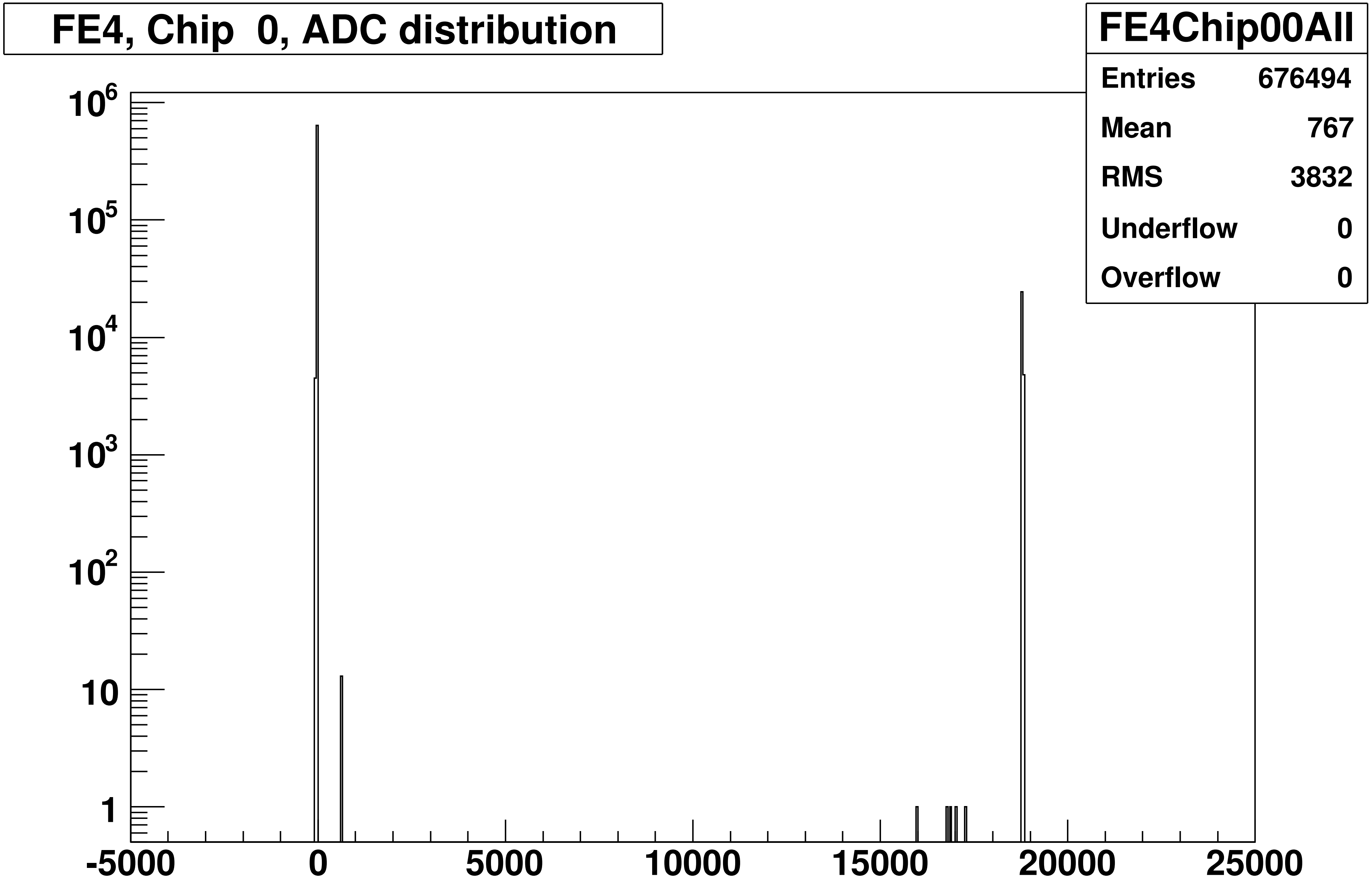
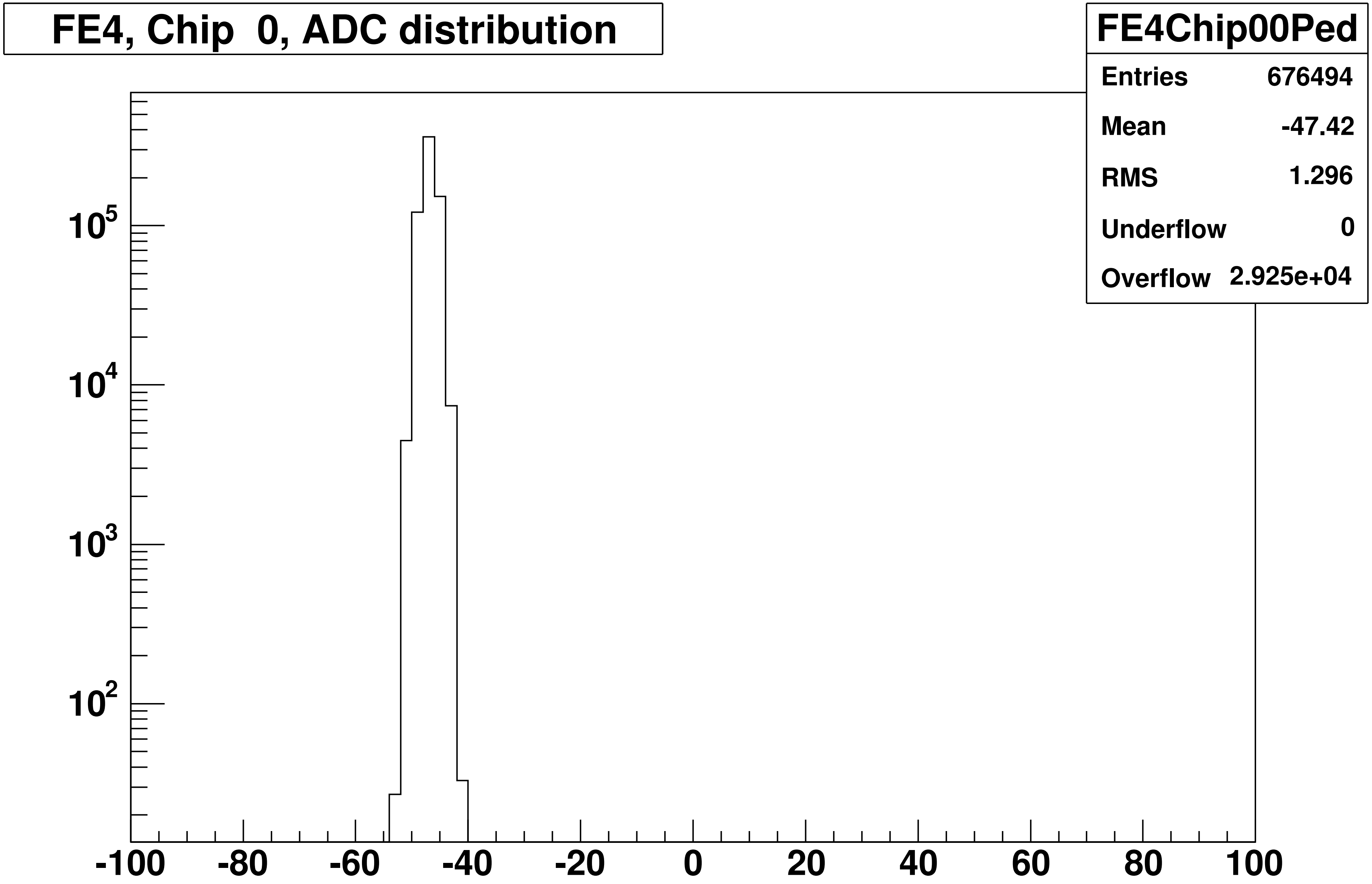
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 676494
Mean 767
RMS 3832
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 676494
Mean -47.42
RMS 1.296
Underflow 0
Overflow 2.925e+04
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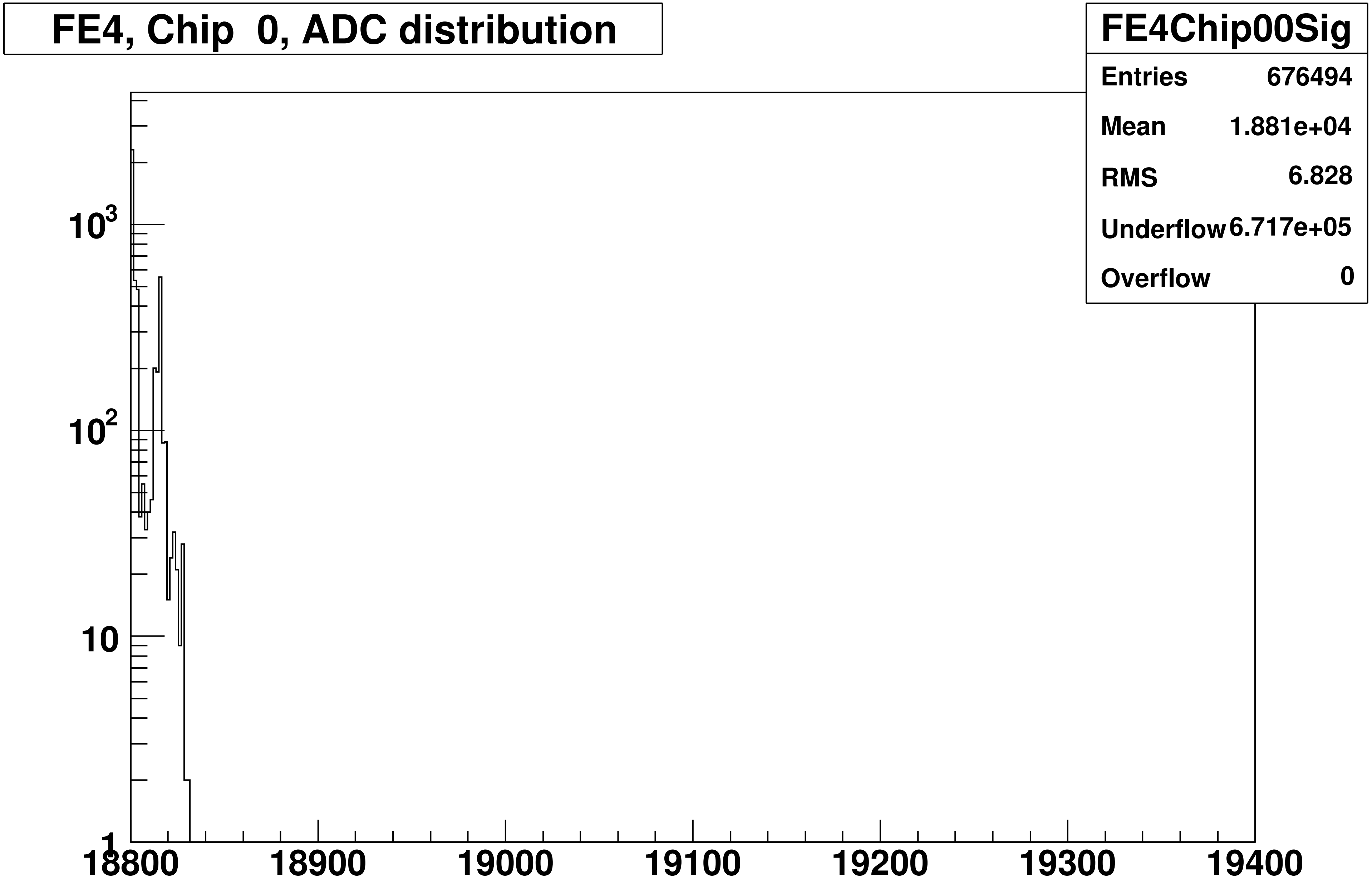
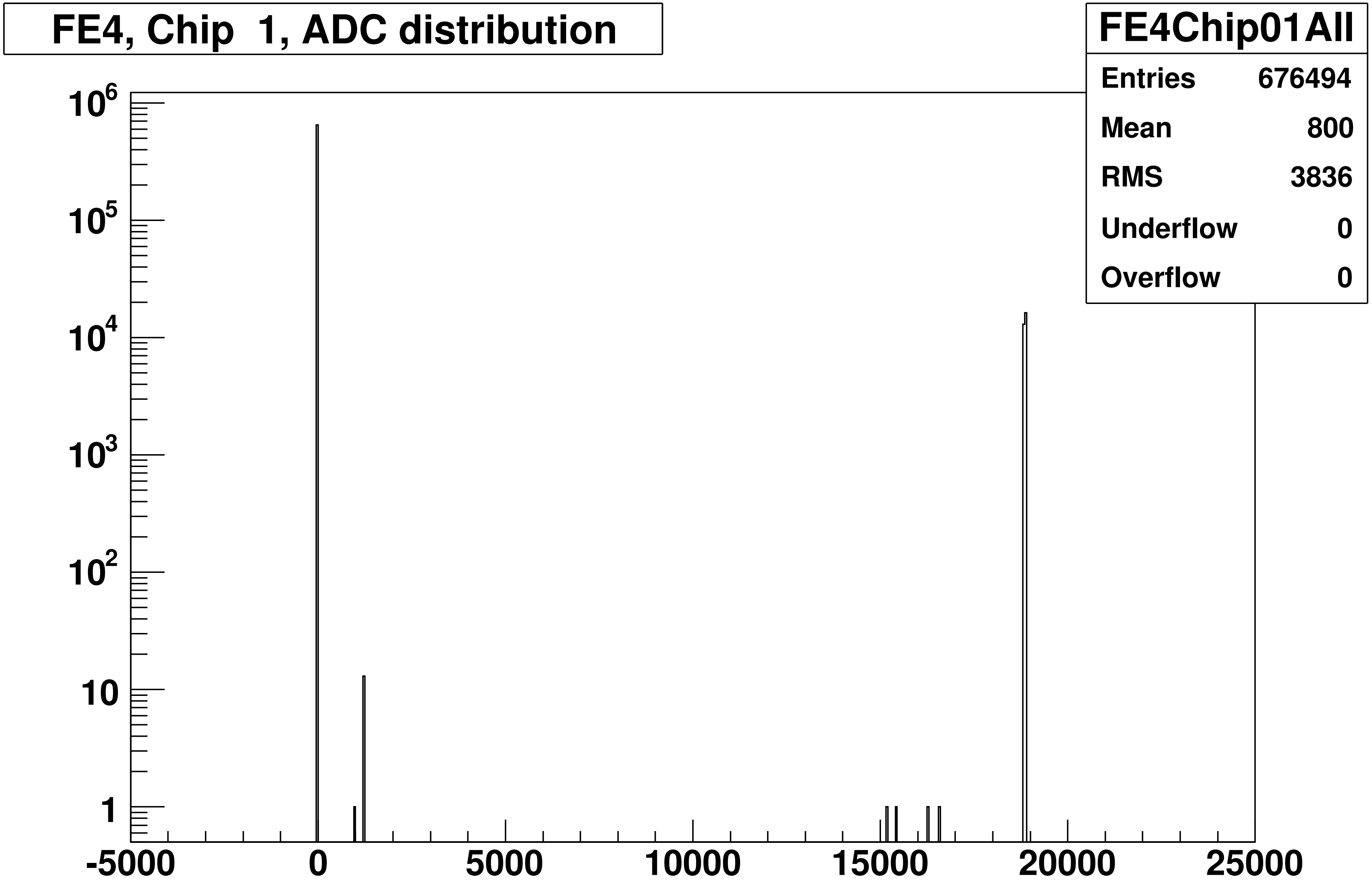
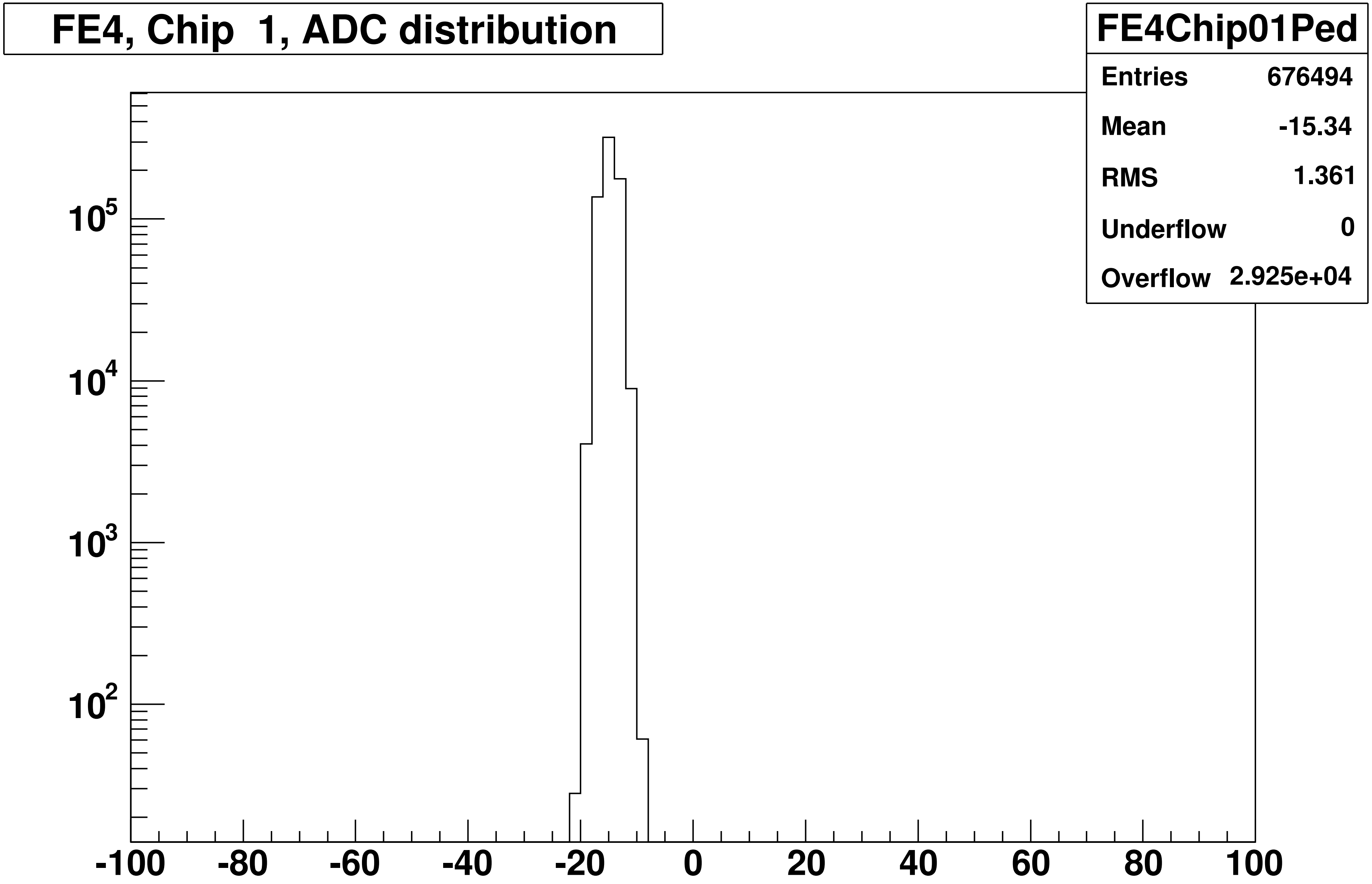
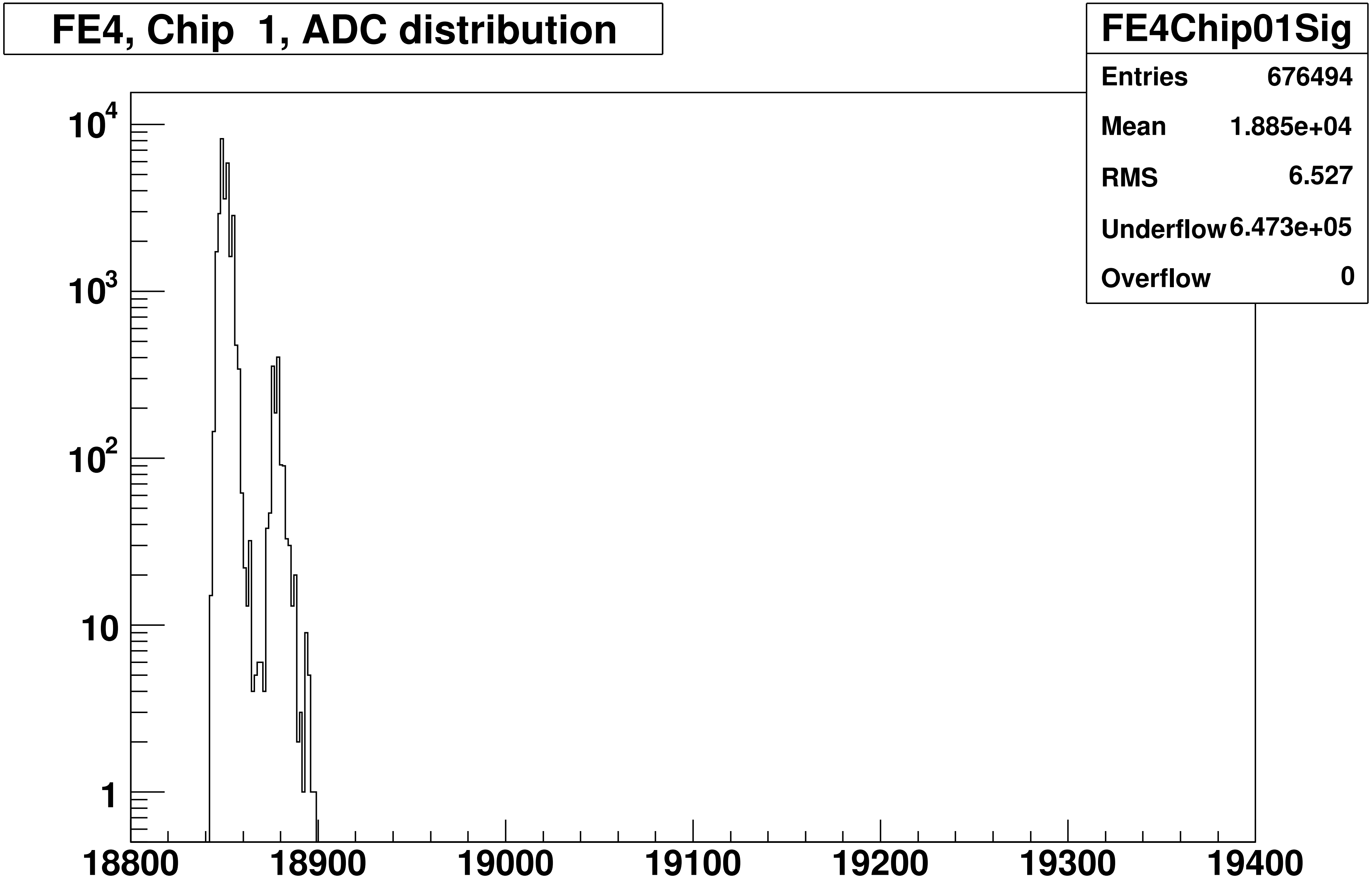
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

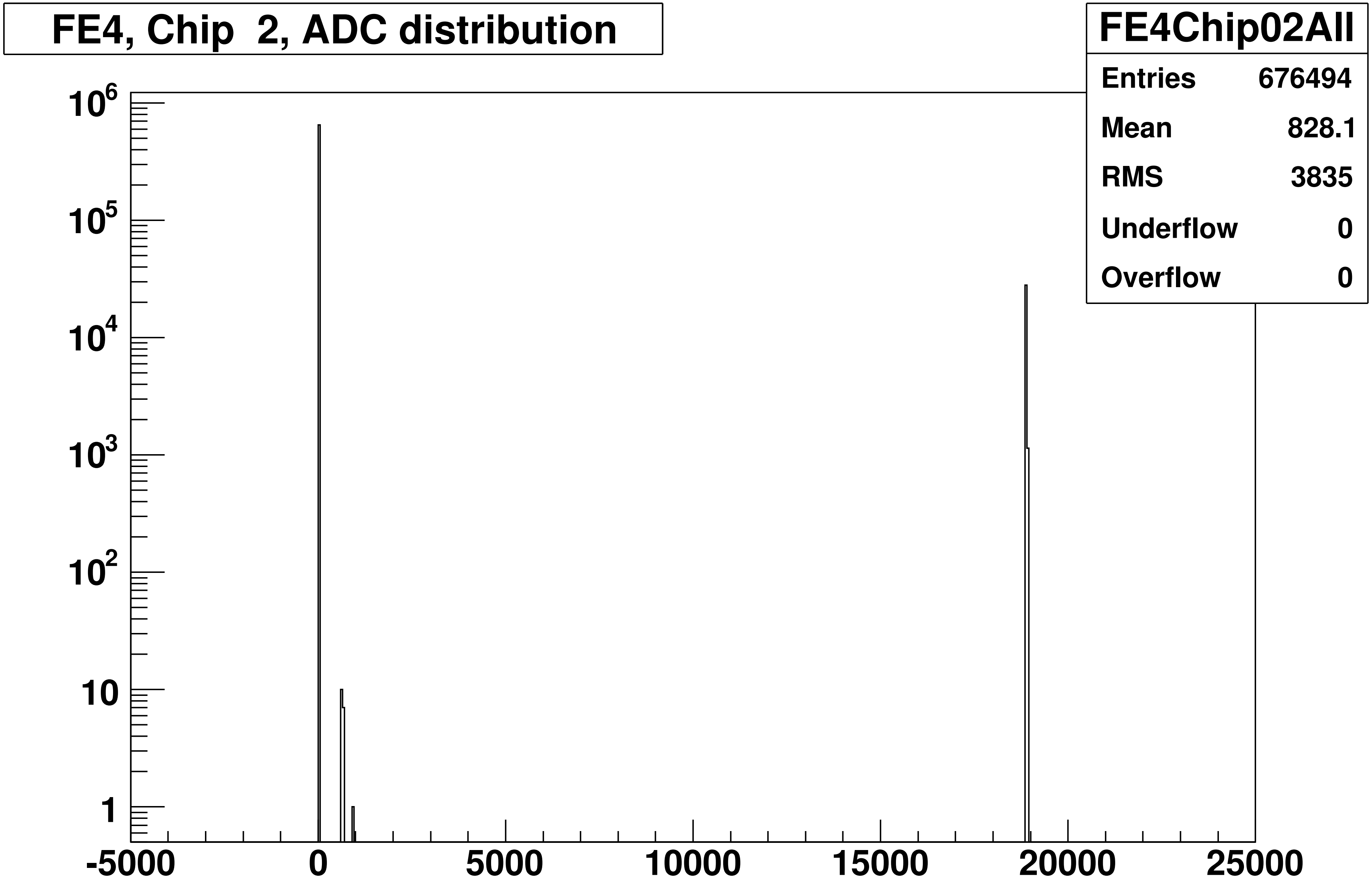

FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 676494
Mean 1.881e+04
RMS 6.828
Underflow 6.717e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 676494
Mean 800
RMS 3836
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 676494
Mean -15.34
RMS 1.361
Underflow 0
Overflow 2.925e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 676494
Mean 1.885e+04
RMS 6.527
Underflow 6.473e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 676494
Mean 828.1
RMS 3835
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
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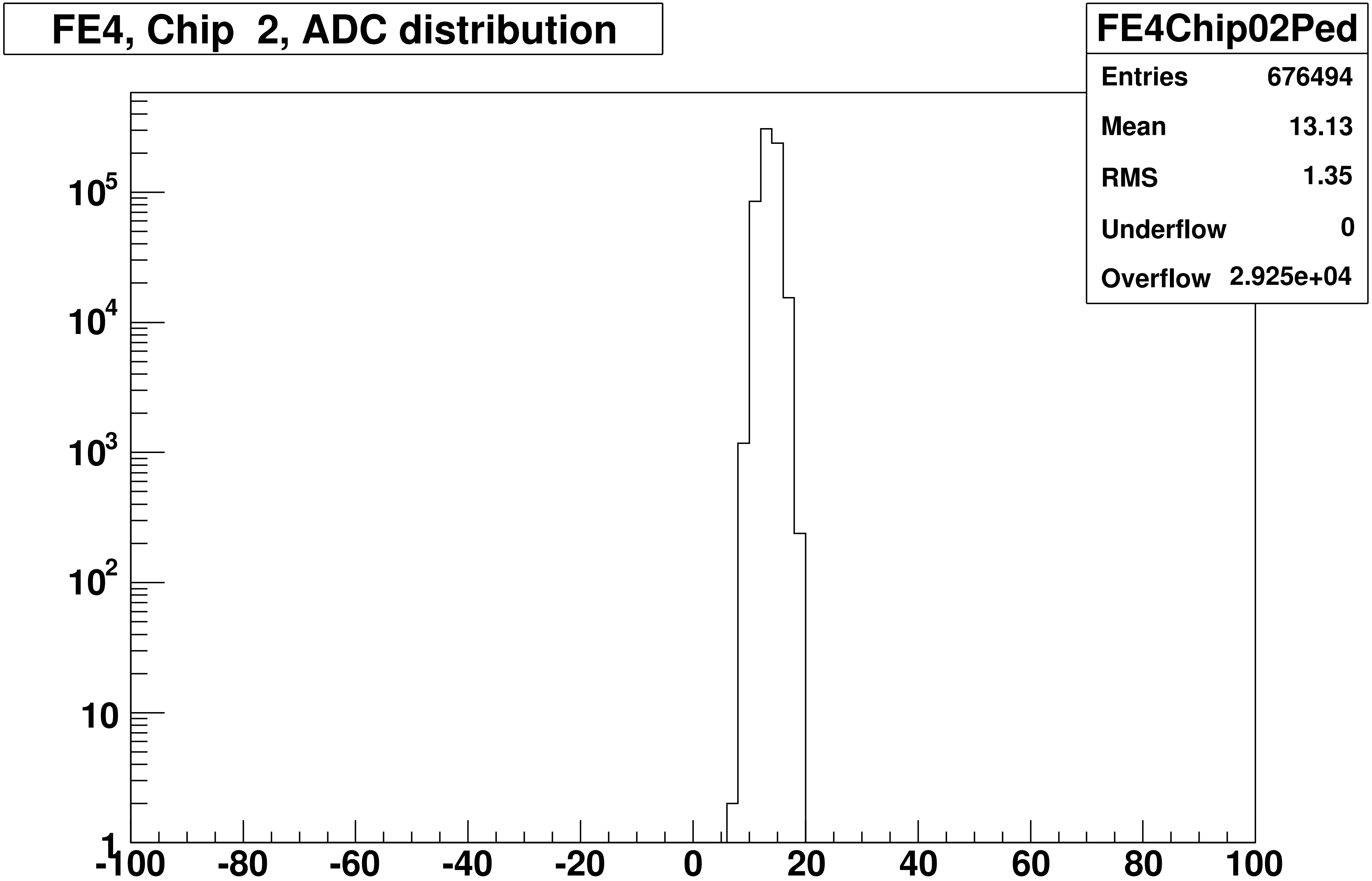
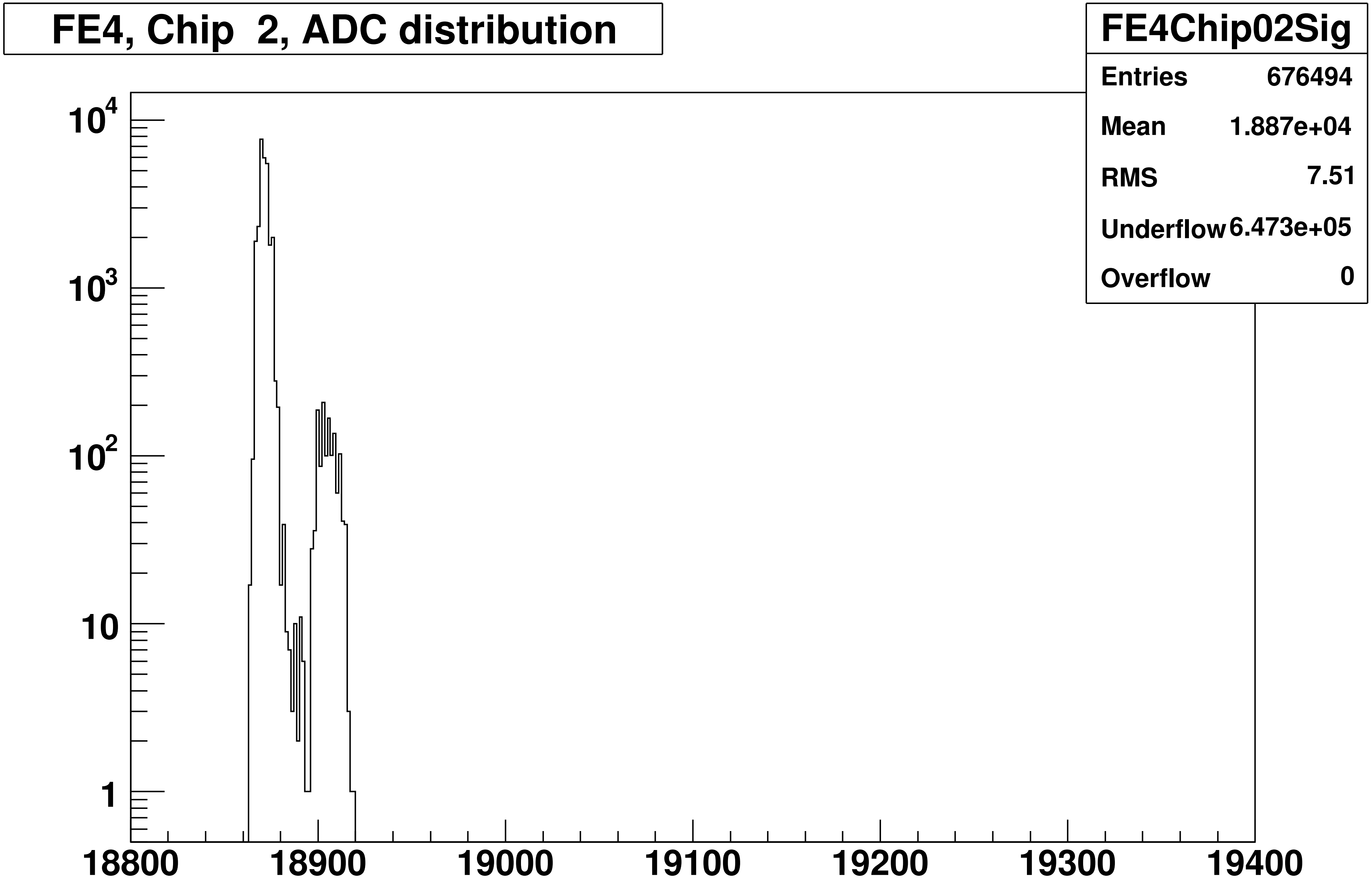
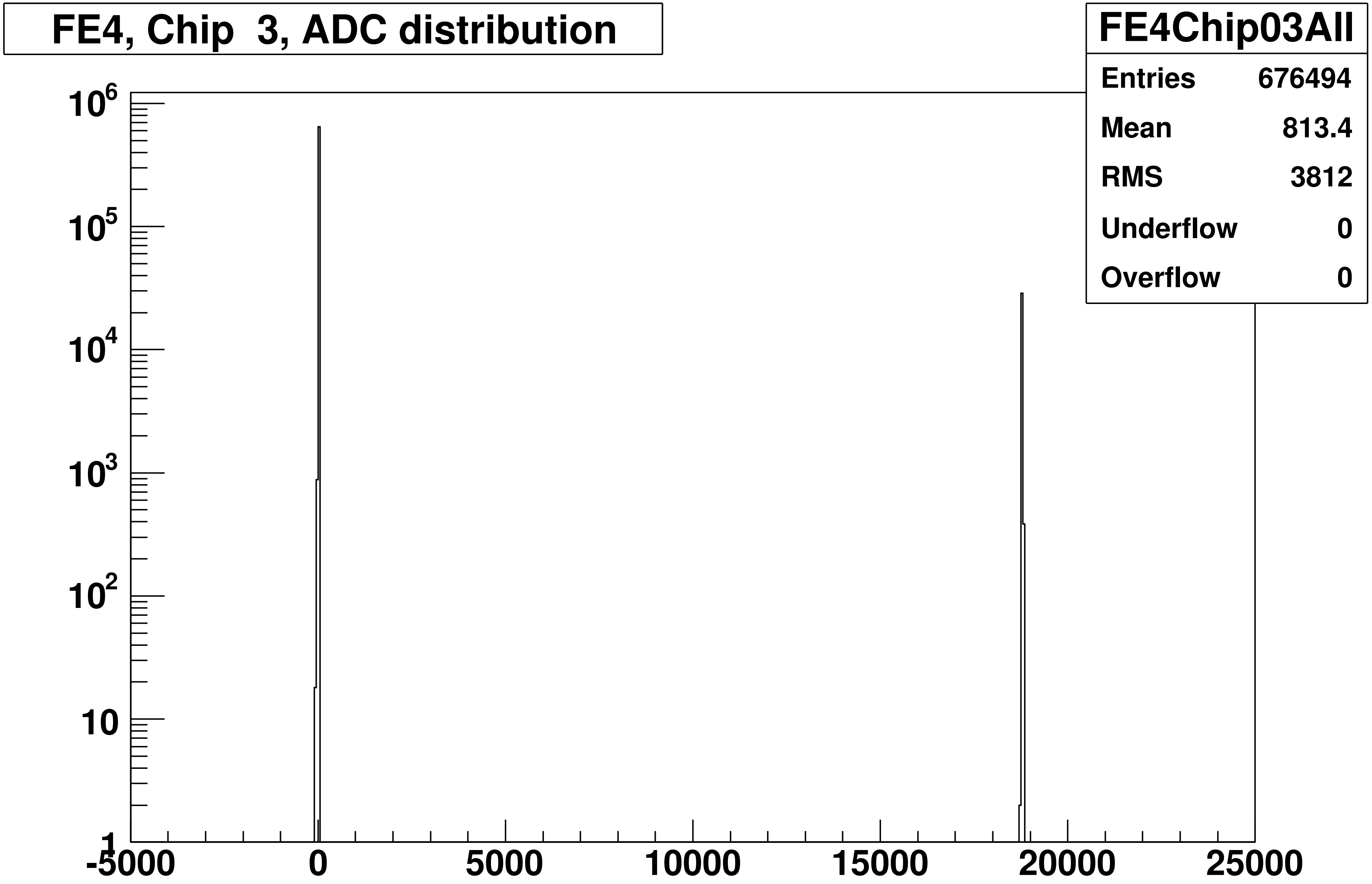
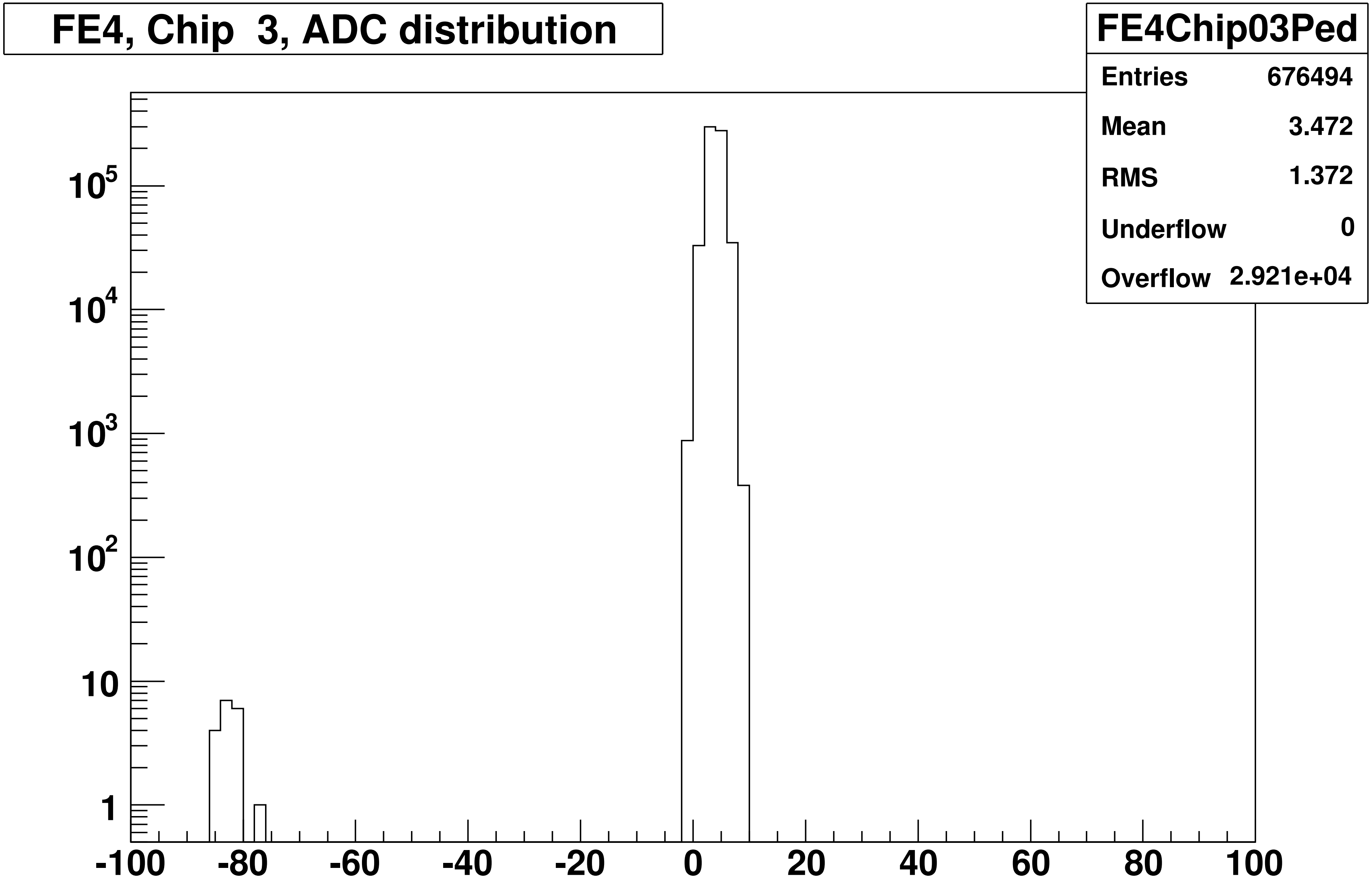
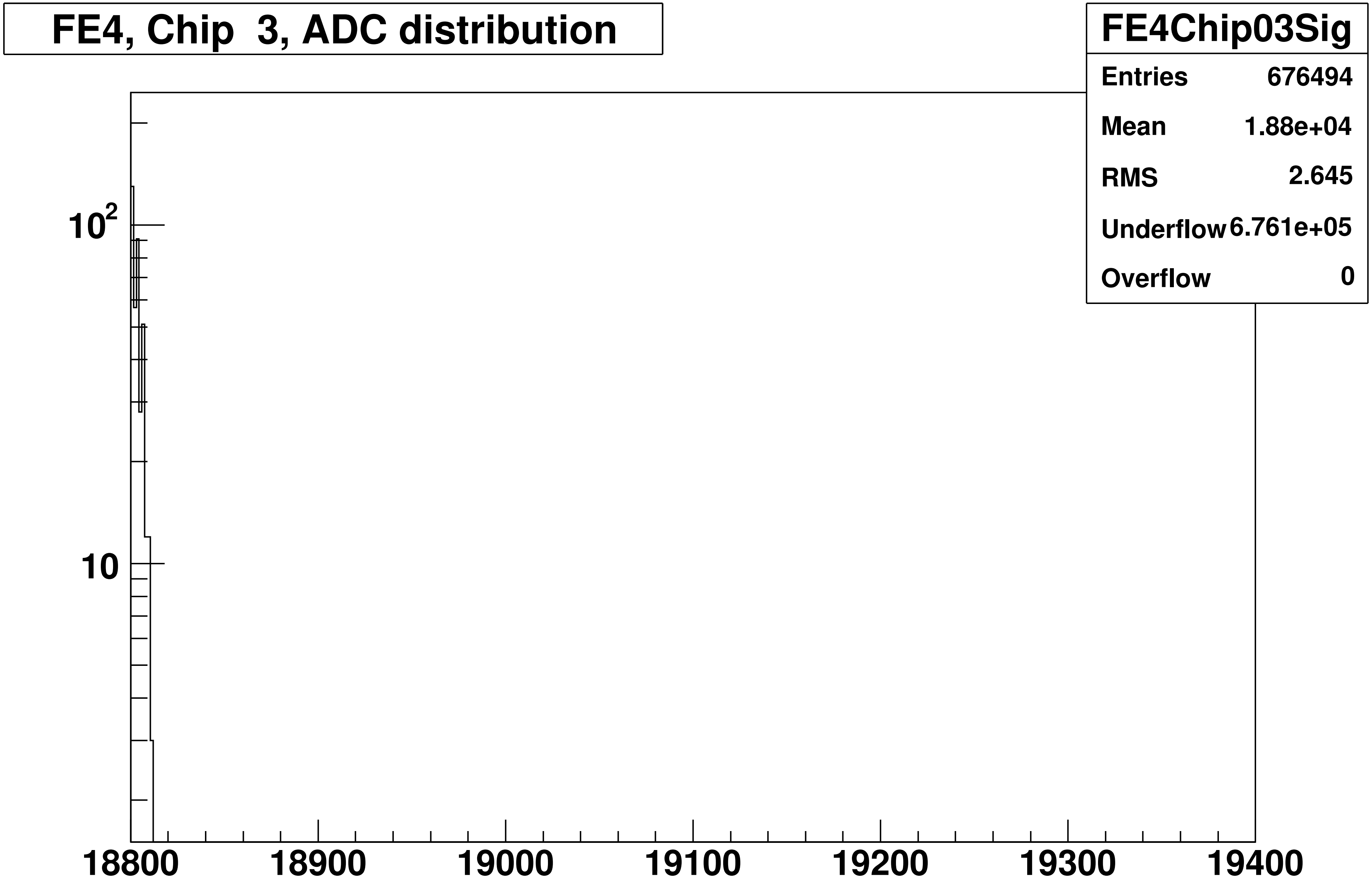
-5000
0
5000
10000
15000
20000
25000

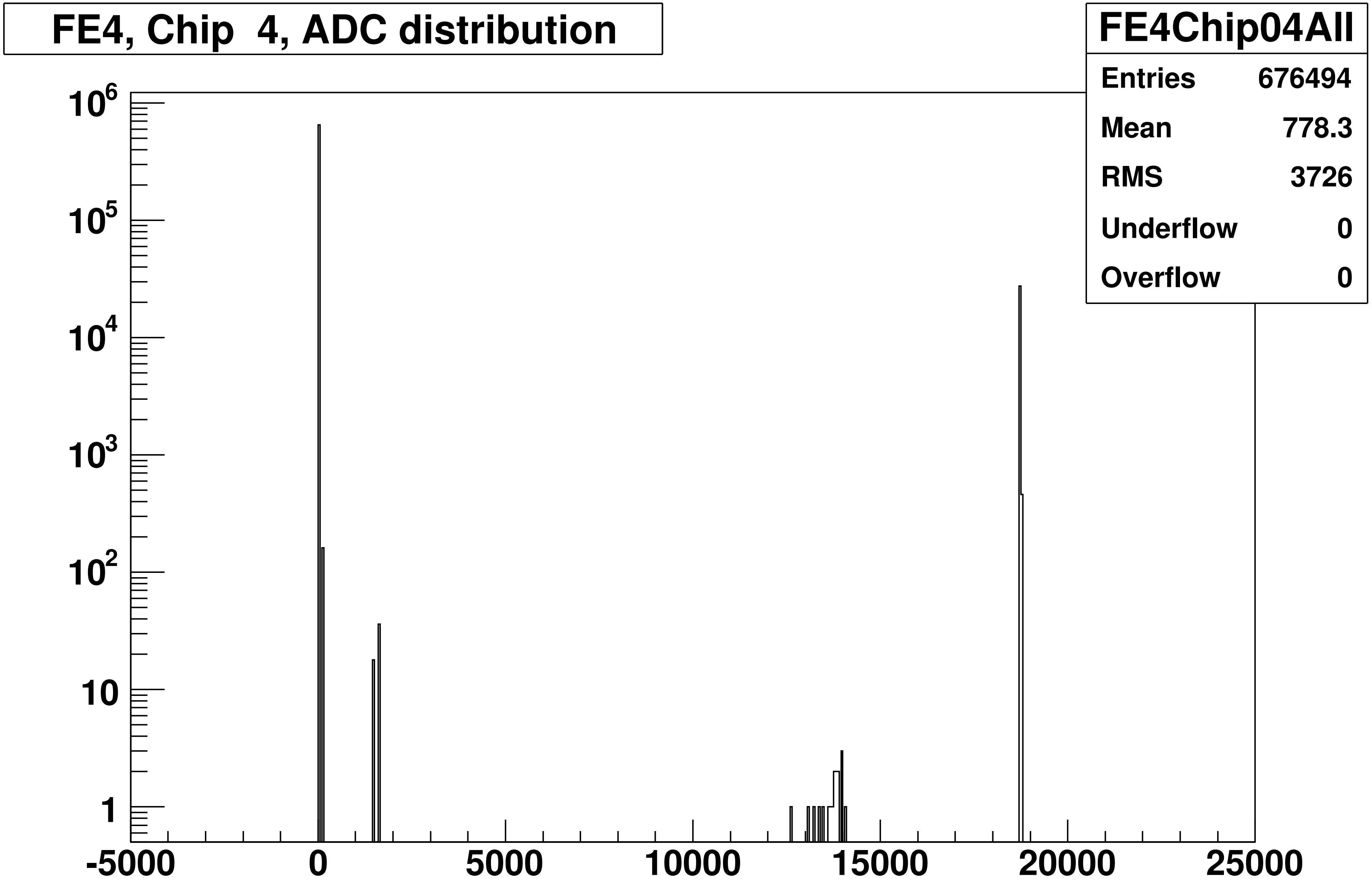

FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 676494
Mean 13.13
RMS 1.35
Underflow 0
Overflow 2.925e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 676494
Mean 1.887e+04
RMS 7.51
Underflow 6.473e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 676494
Mean 813.4
RMS 3812
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 676494
Mean 3.472
RMS 1.372
Underflow 0
Overflow 2.921e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 676494
Mean 1.88e+04
RMS 2.645
Underflow 6.761e+05
Overflow 0
10^2
10
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 676494
Mean 778.3
RMS 3726
Underflow 0
Overflow 0
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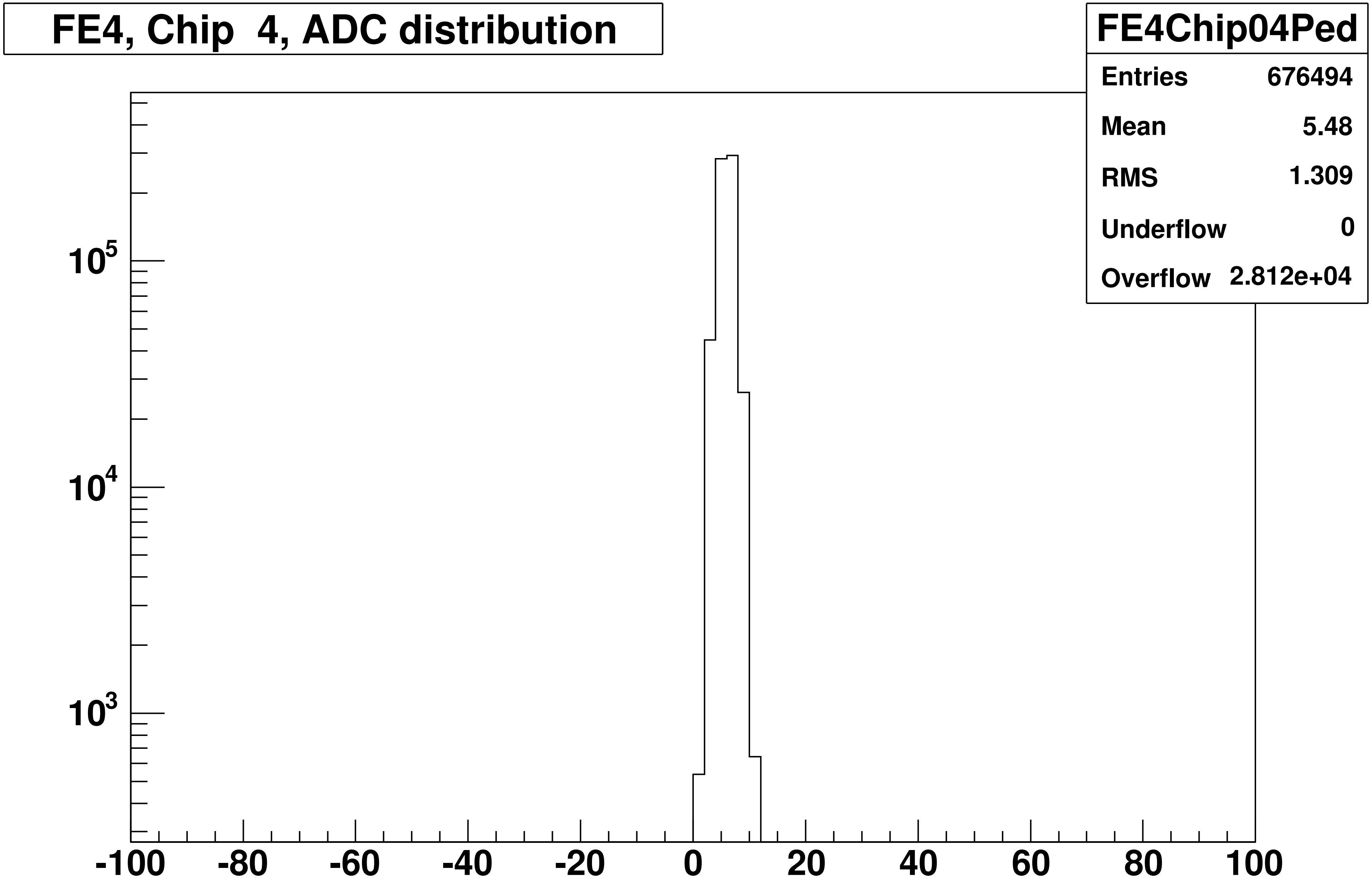
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 676494
Mean 5.48
RMS 1.309
Underflow 0
Overflow 2.812e+04
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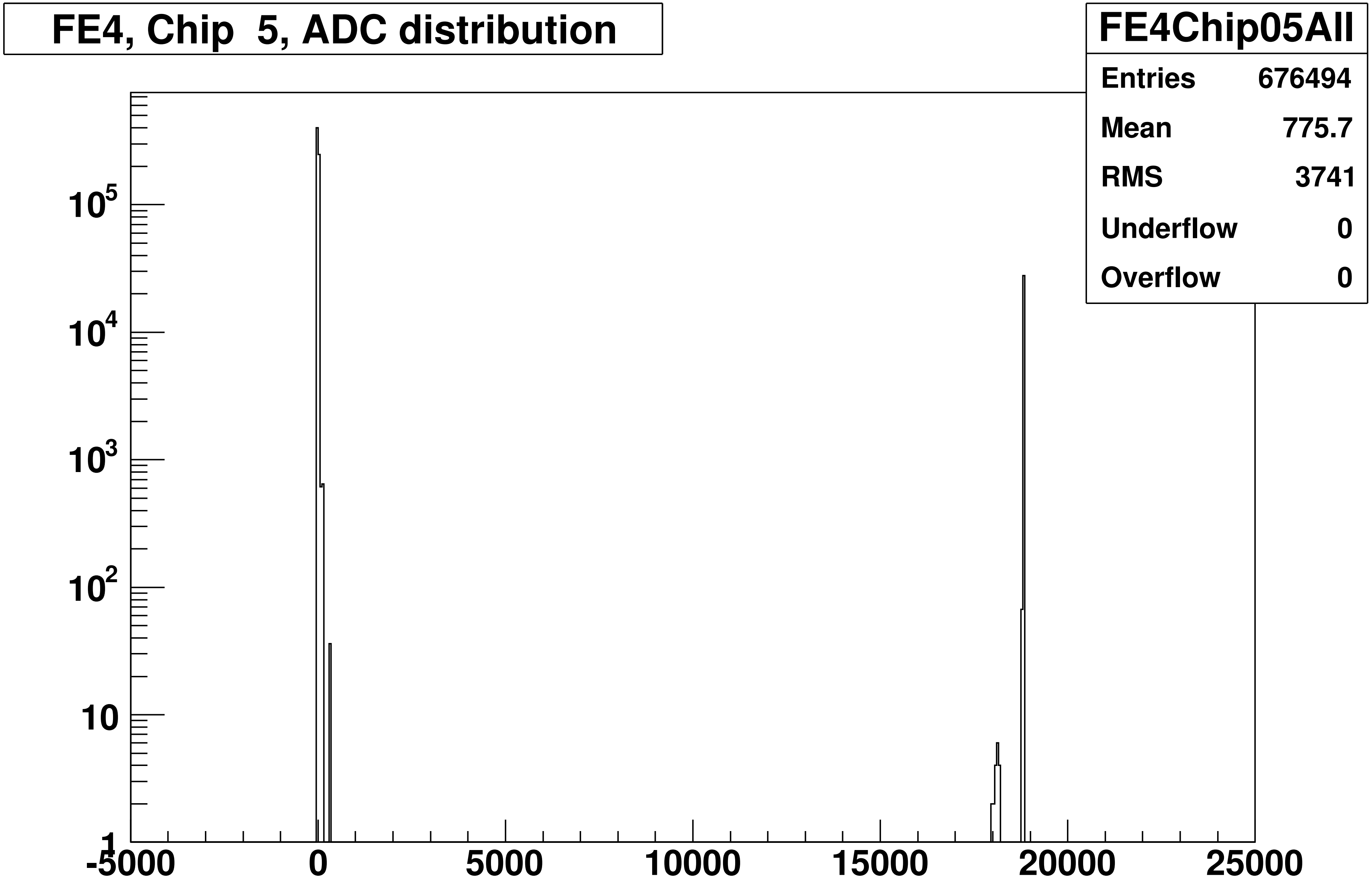
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 676494
Mean 775.7
RMS 3741
Underflow 0
Overflow 0
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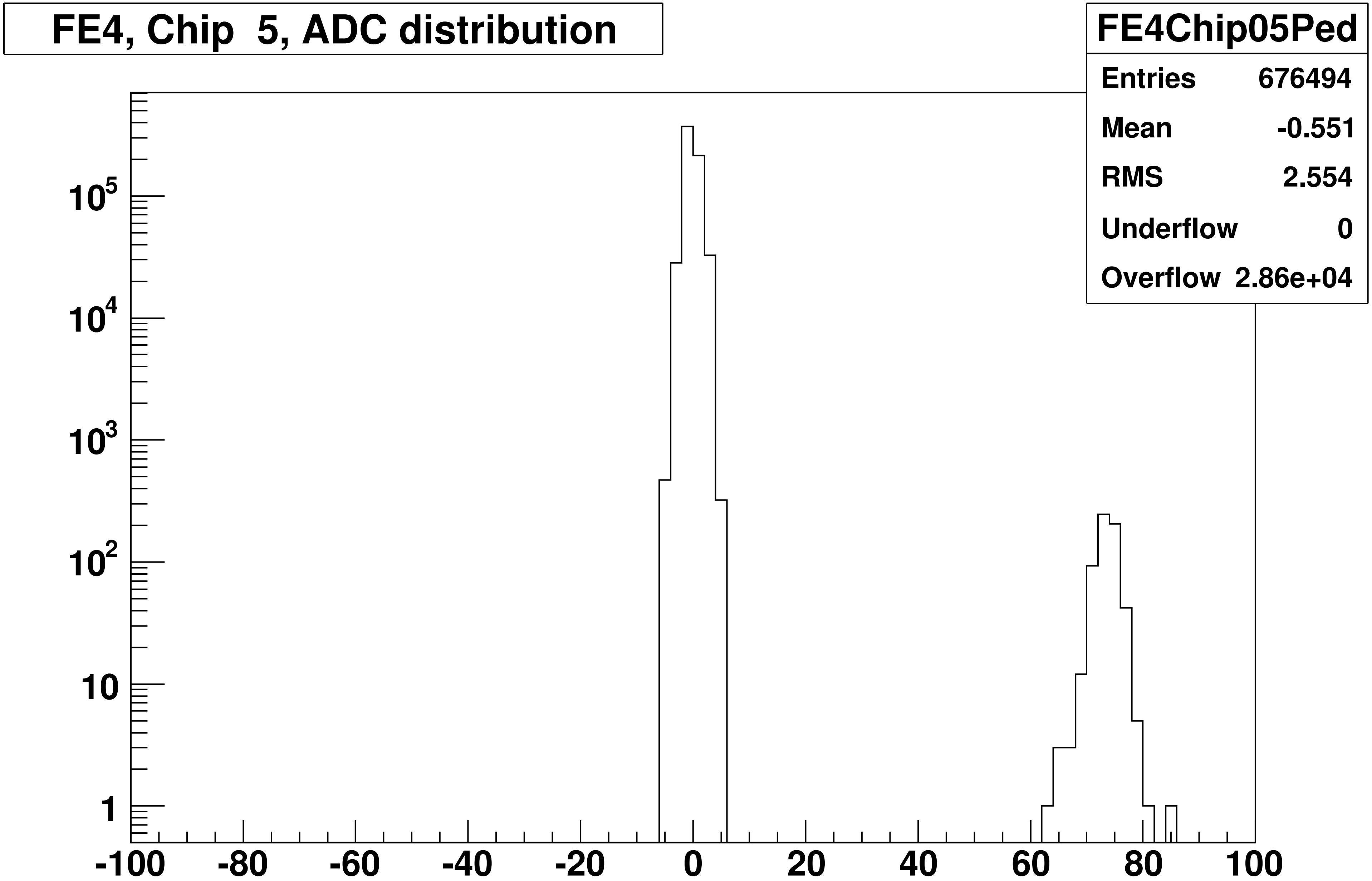
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 676494
Mean -0.551
RMS 2.554
Underflow 0
Overflow 2.86e+04
10^5
10^4
10^3
10^2
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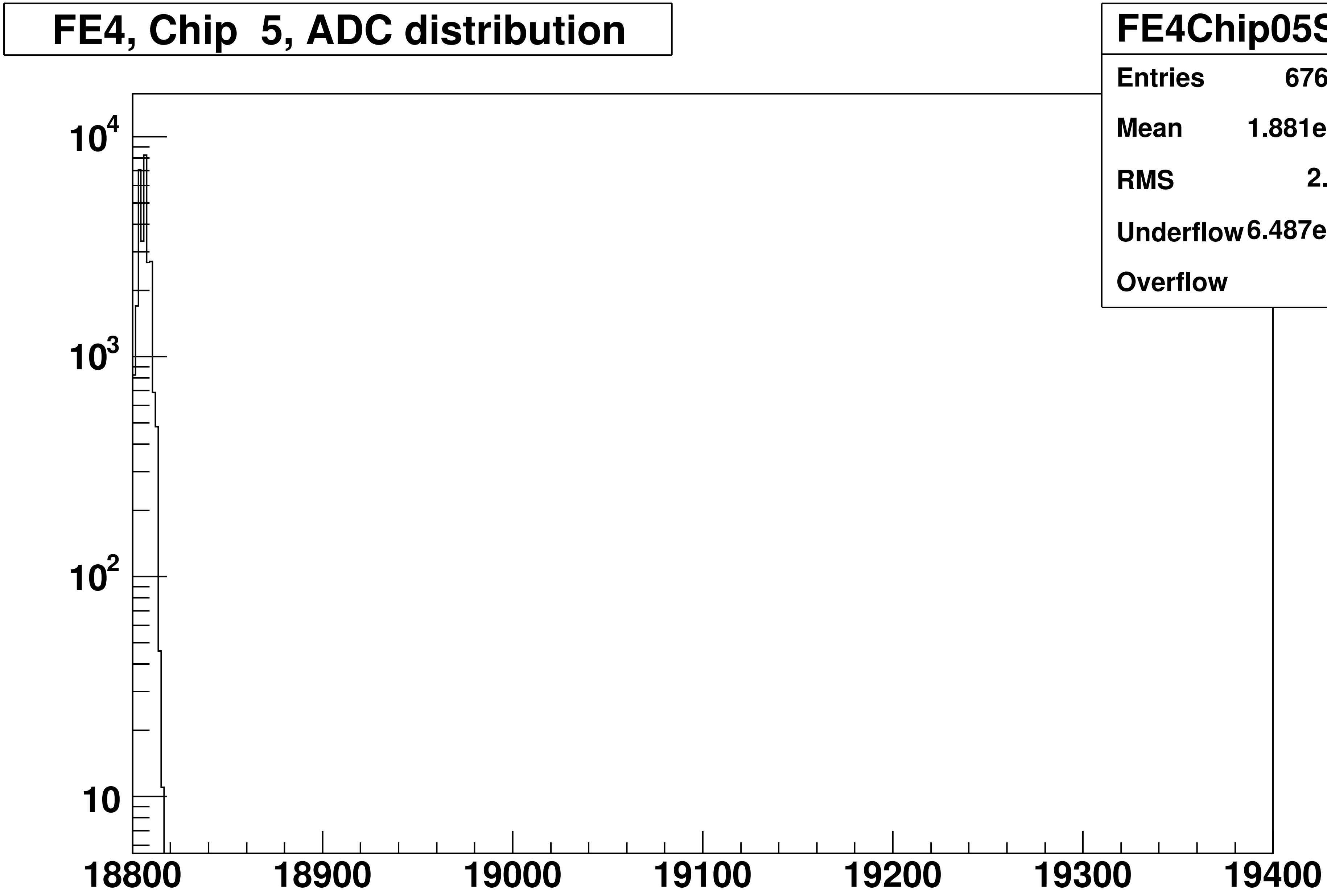
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 676494
Mean 1.881e+04
RMS 2.573
Underflow 6.487e+05
Overflow 0
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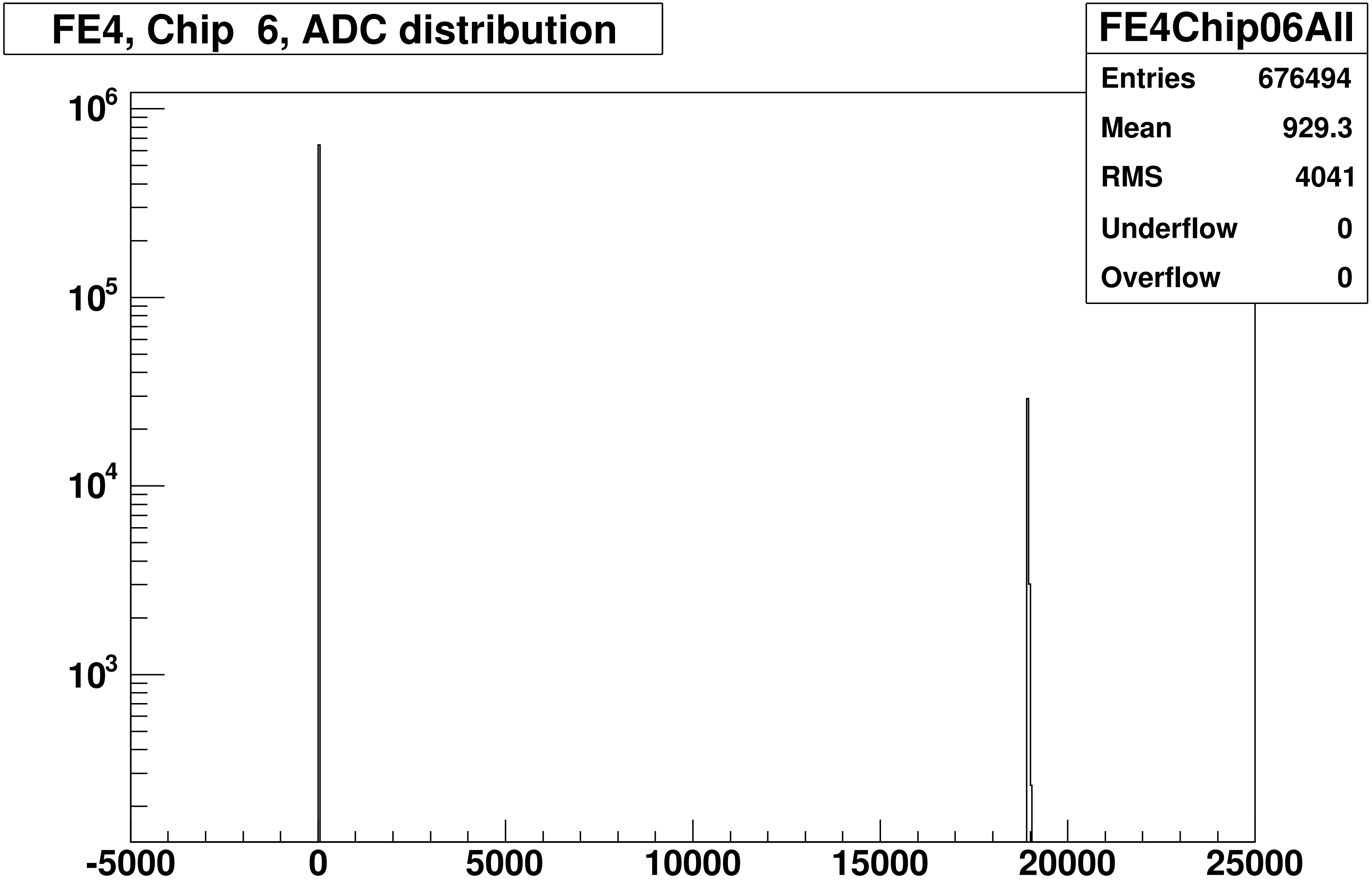
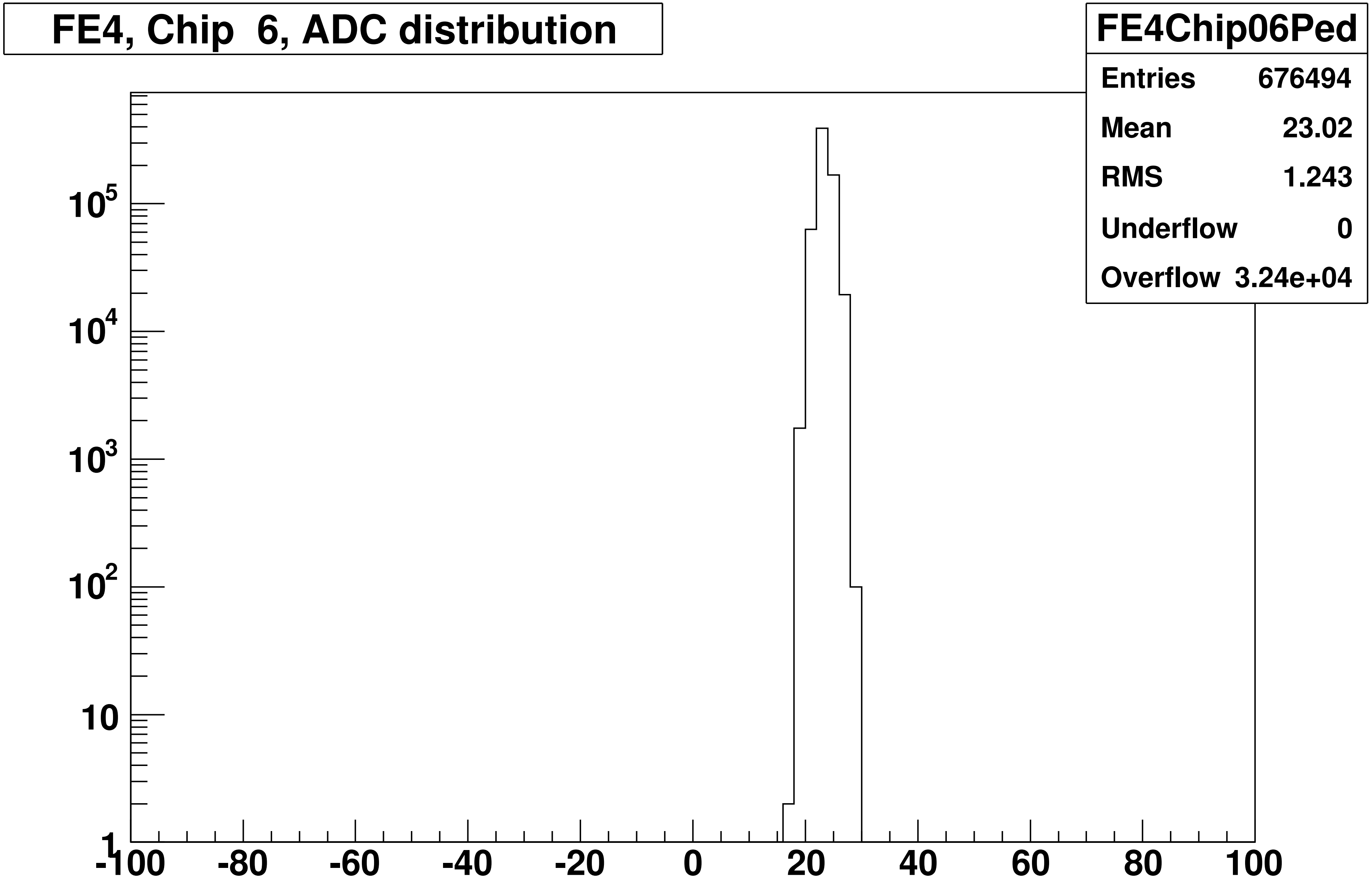
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 676494
Mean 929.3
RMS 4041
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 676494
Mean 23.02
RMS 1.243
Underflow 0
Overflow 3.24e+04
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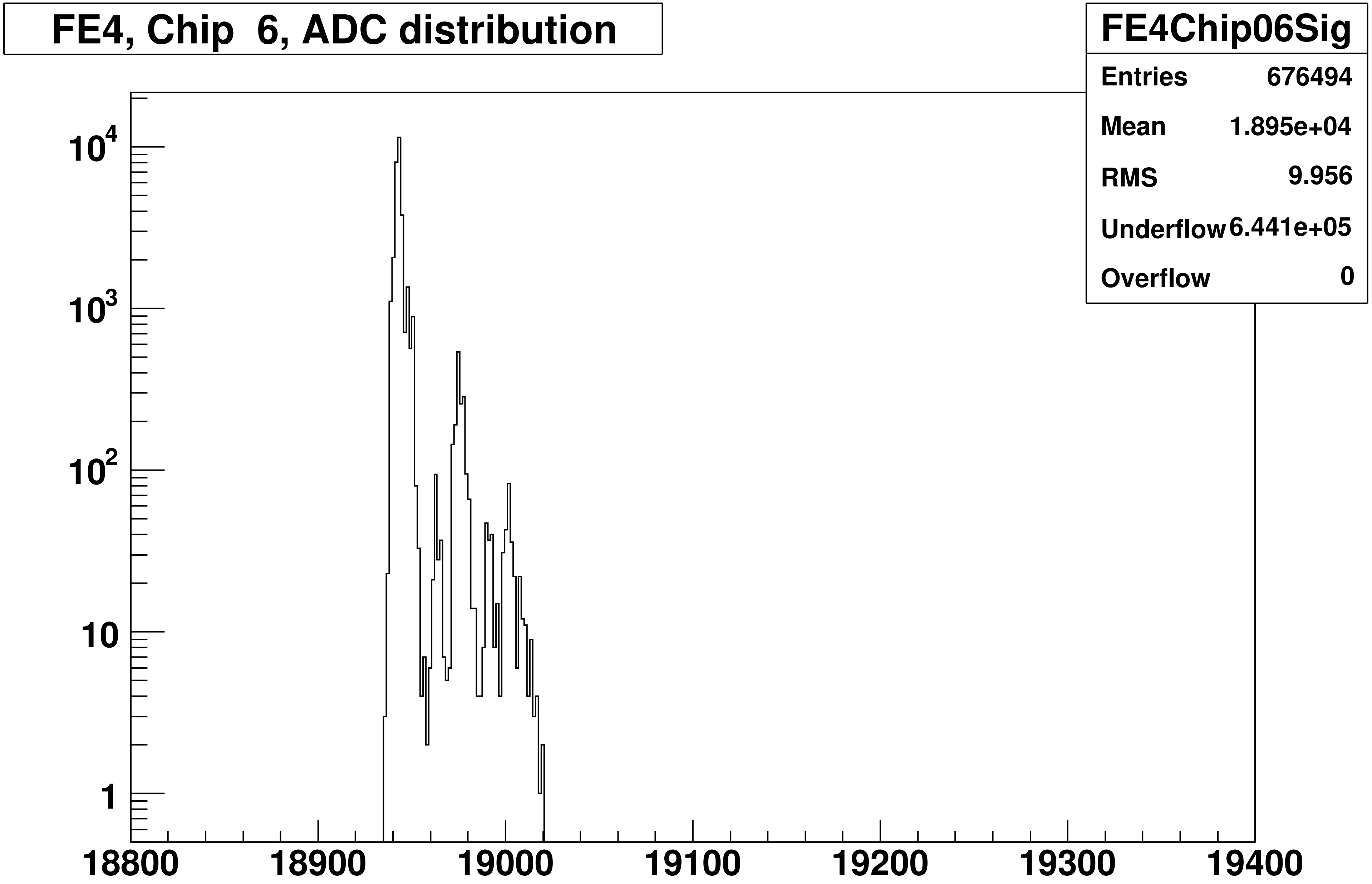
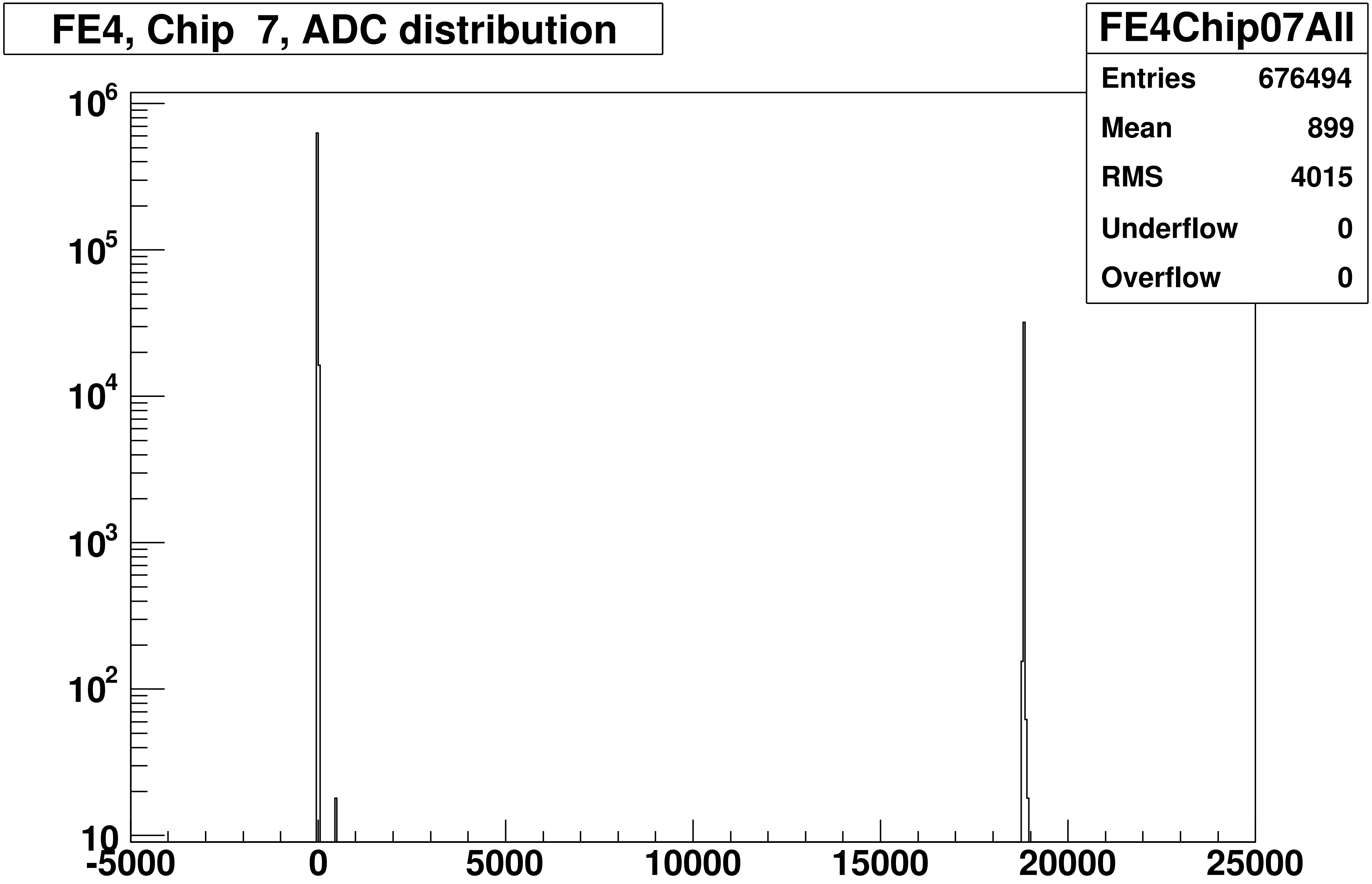
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 676494
Mean 1.895e+04
RMS 9.956
Underflow 6.441e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 676494
Mean 899
RMS 4015
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
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5000
10000
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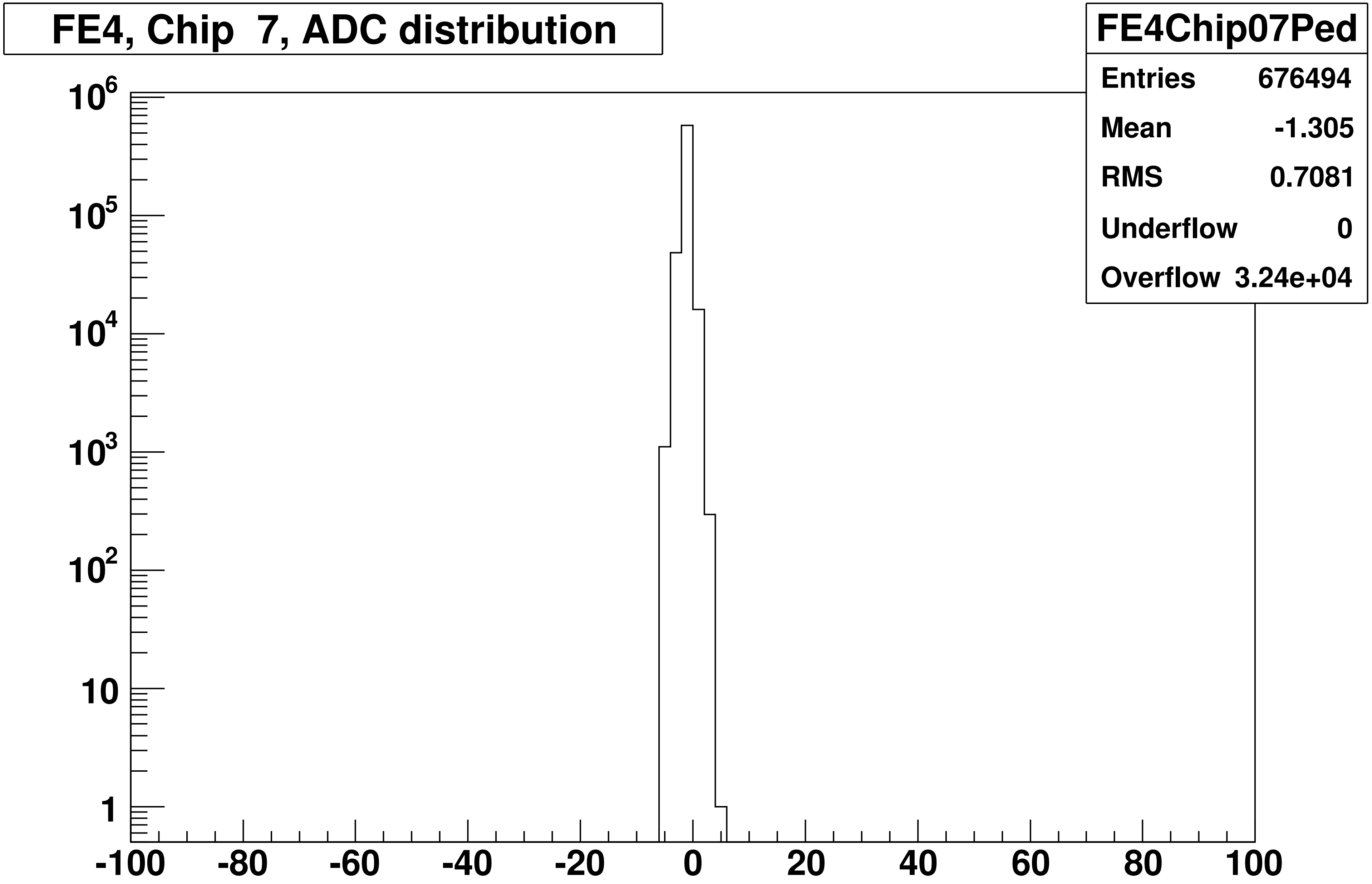
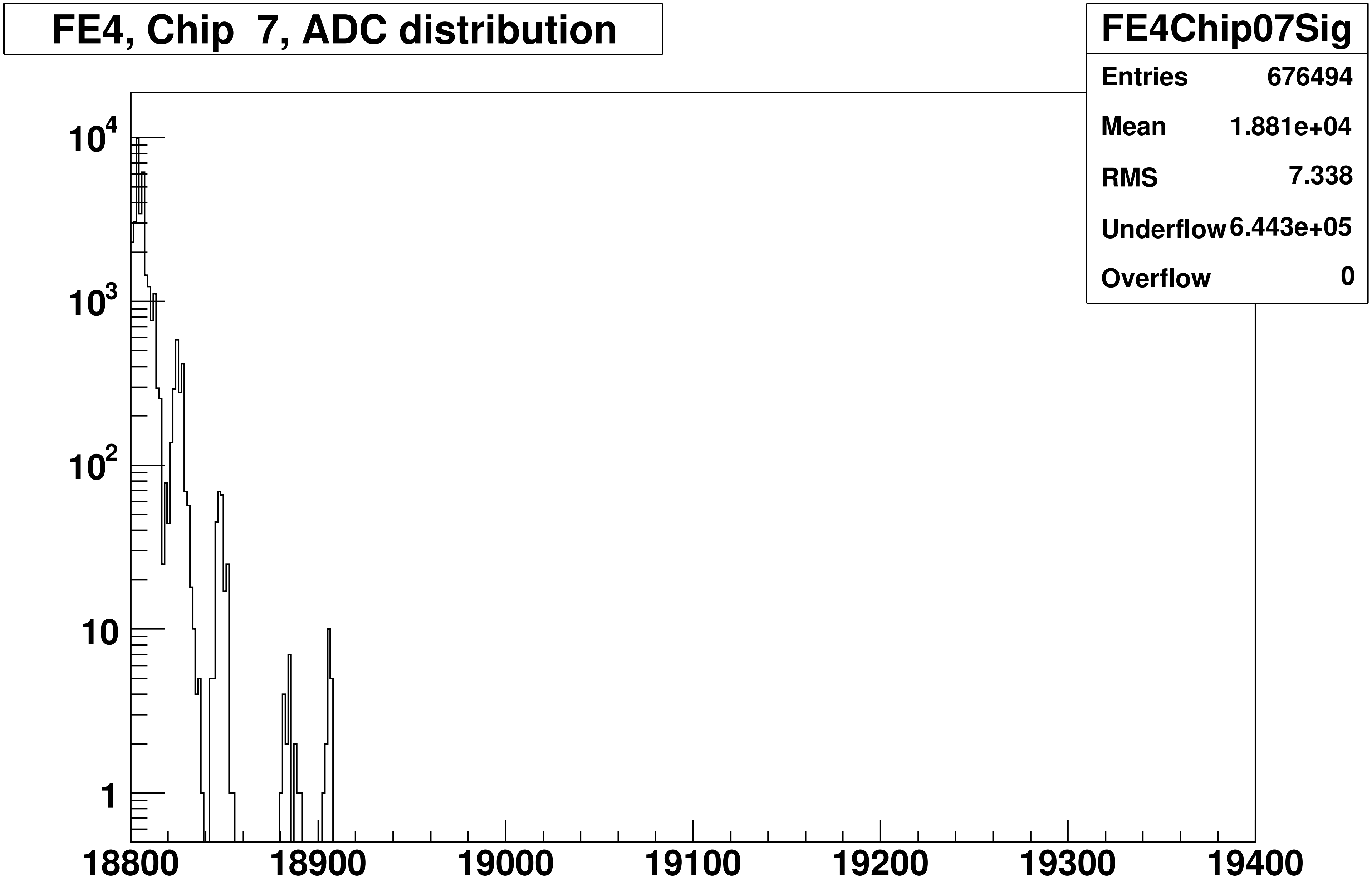
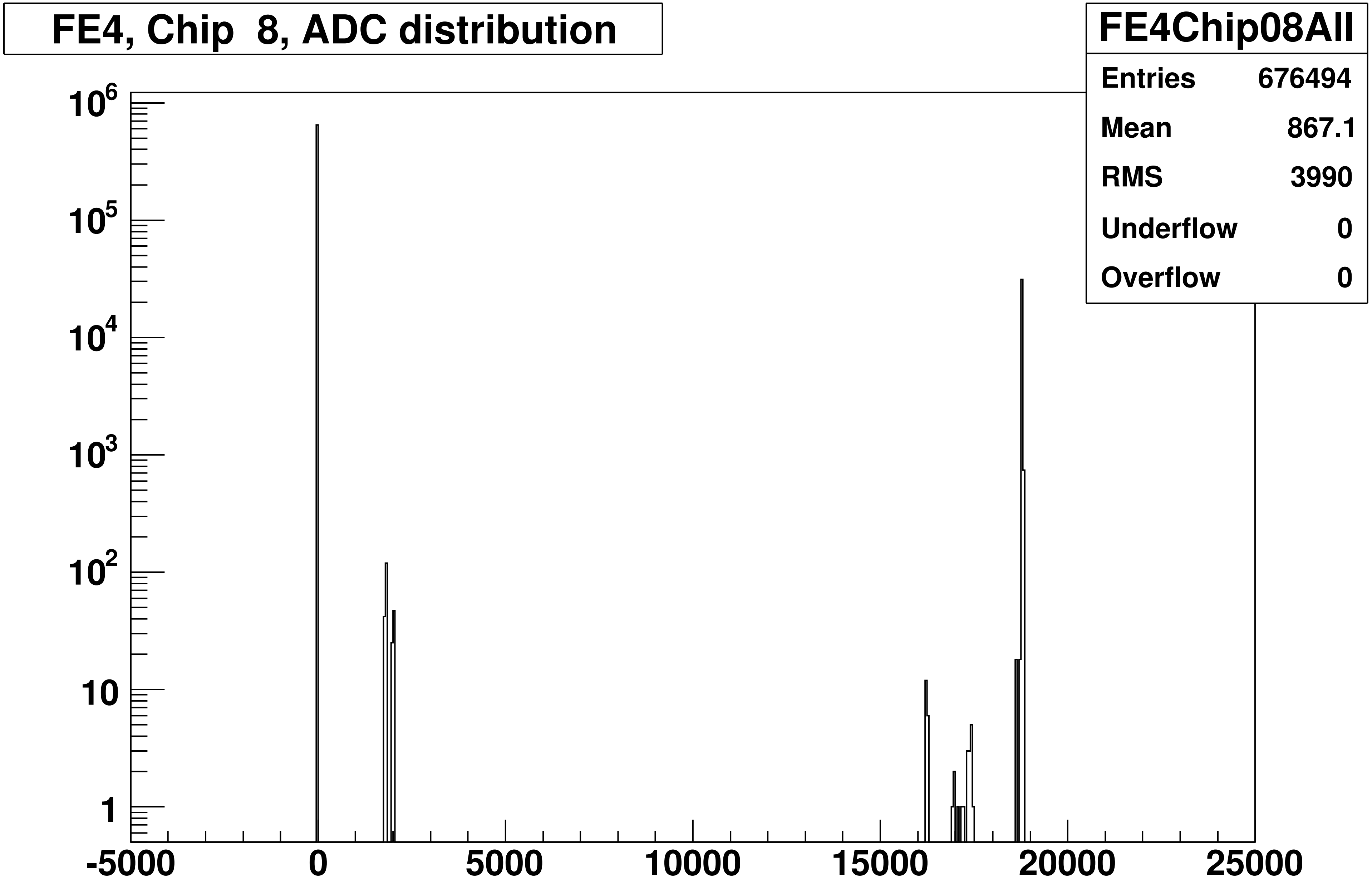
20000
25000


FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 676494
Mean -1.305
RMS 0.7081
Underflow 0
Overflow 3.24e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 676494
Mean 1.881e+04
RMS 7.338
Underflow 6.443e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 676494
Mean 867.1
RMS 3990
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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1
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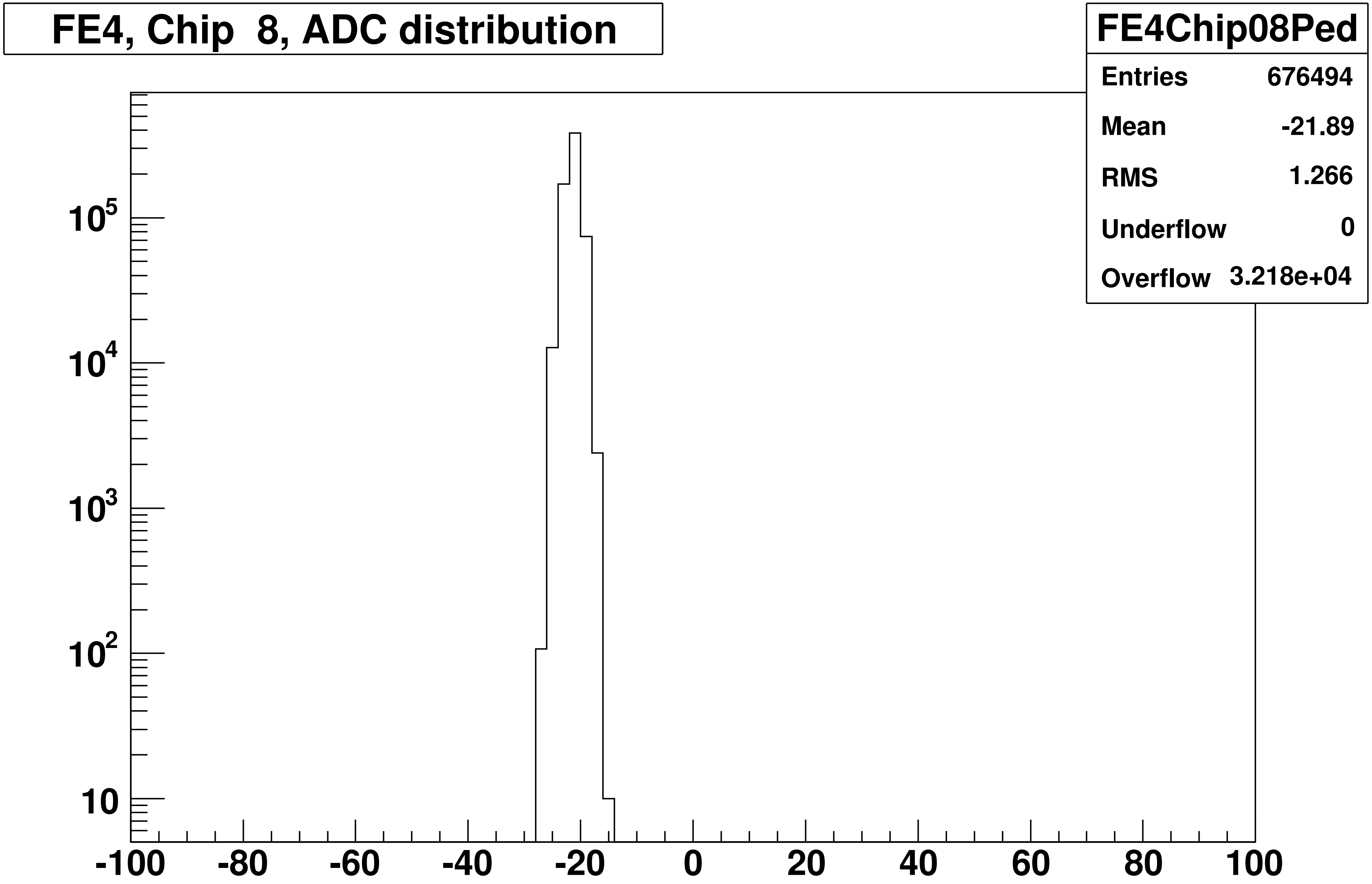
20000
25000


FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 676494
Mean -21.89
RMS 1.266
Underflow 0
Overflow 3.218e+04
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10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
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20
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80
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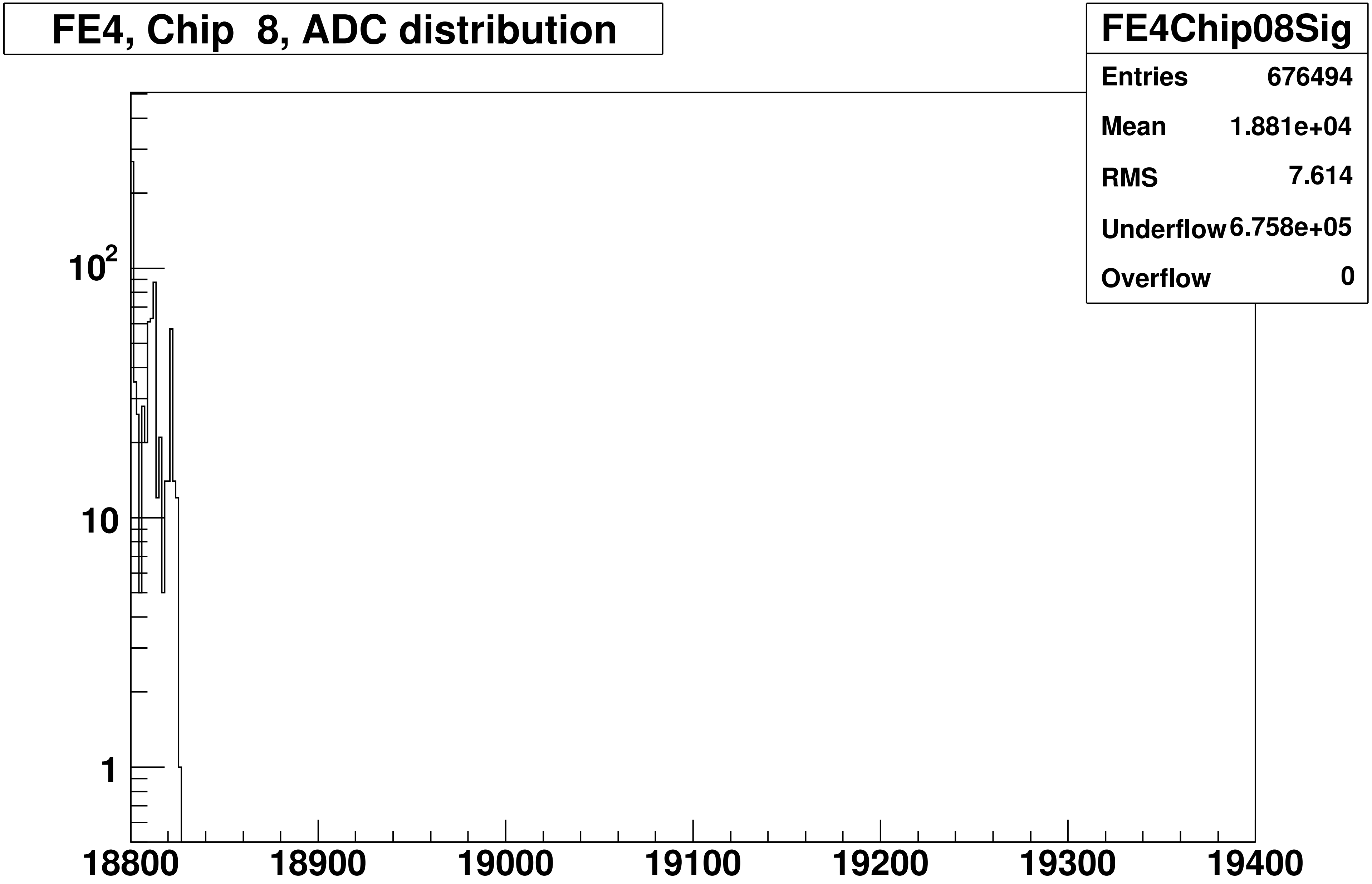
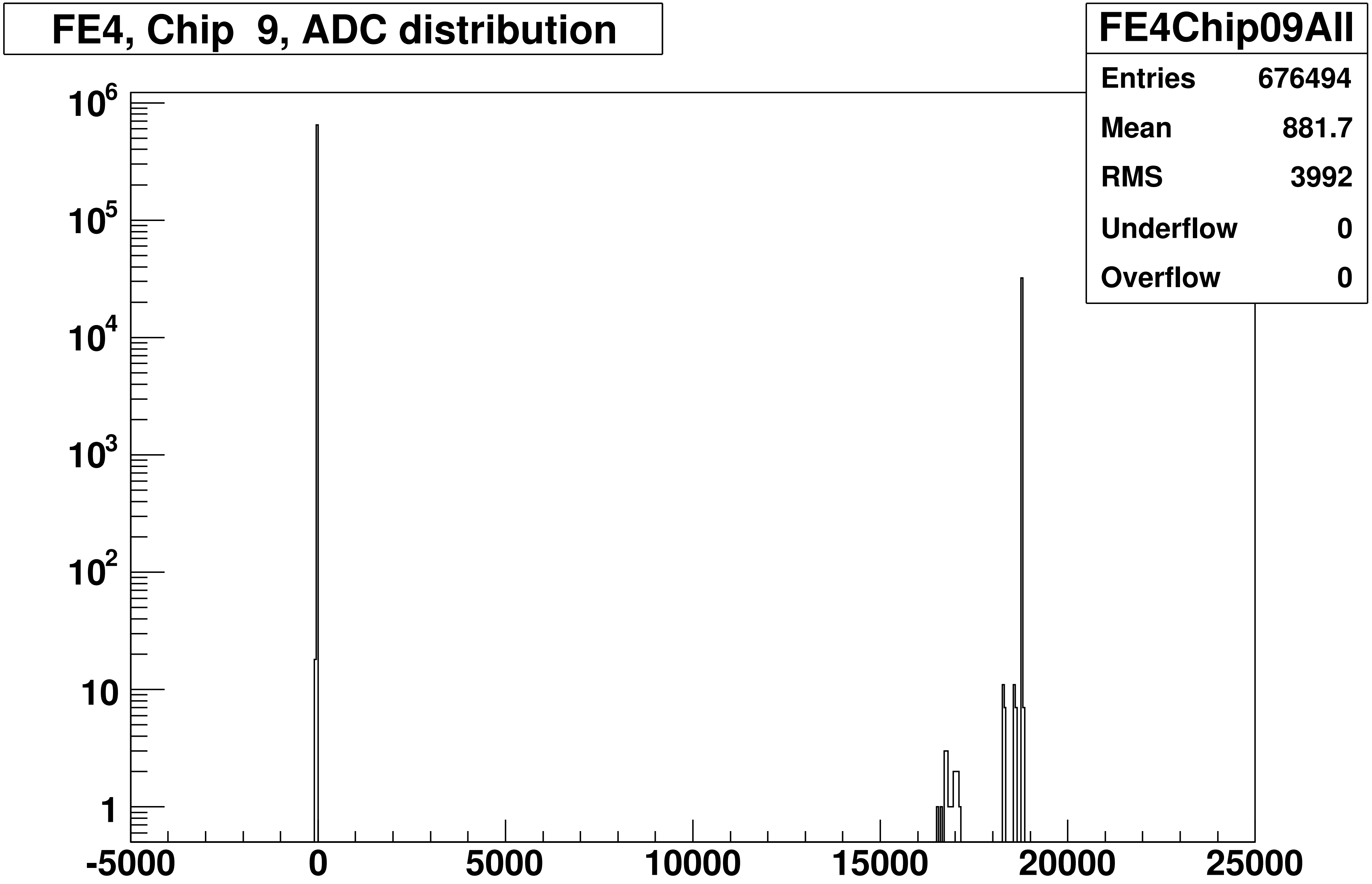

FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 676494
Mean 1.881e+04
RMS 7.614
Underflow 6.758e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 676494
Mean 881.7
RMS 3992
Underflow 0
Overflow 0
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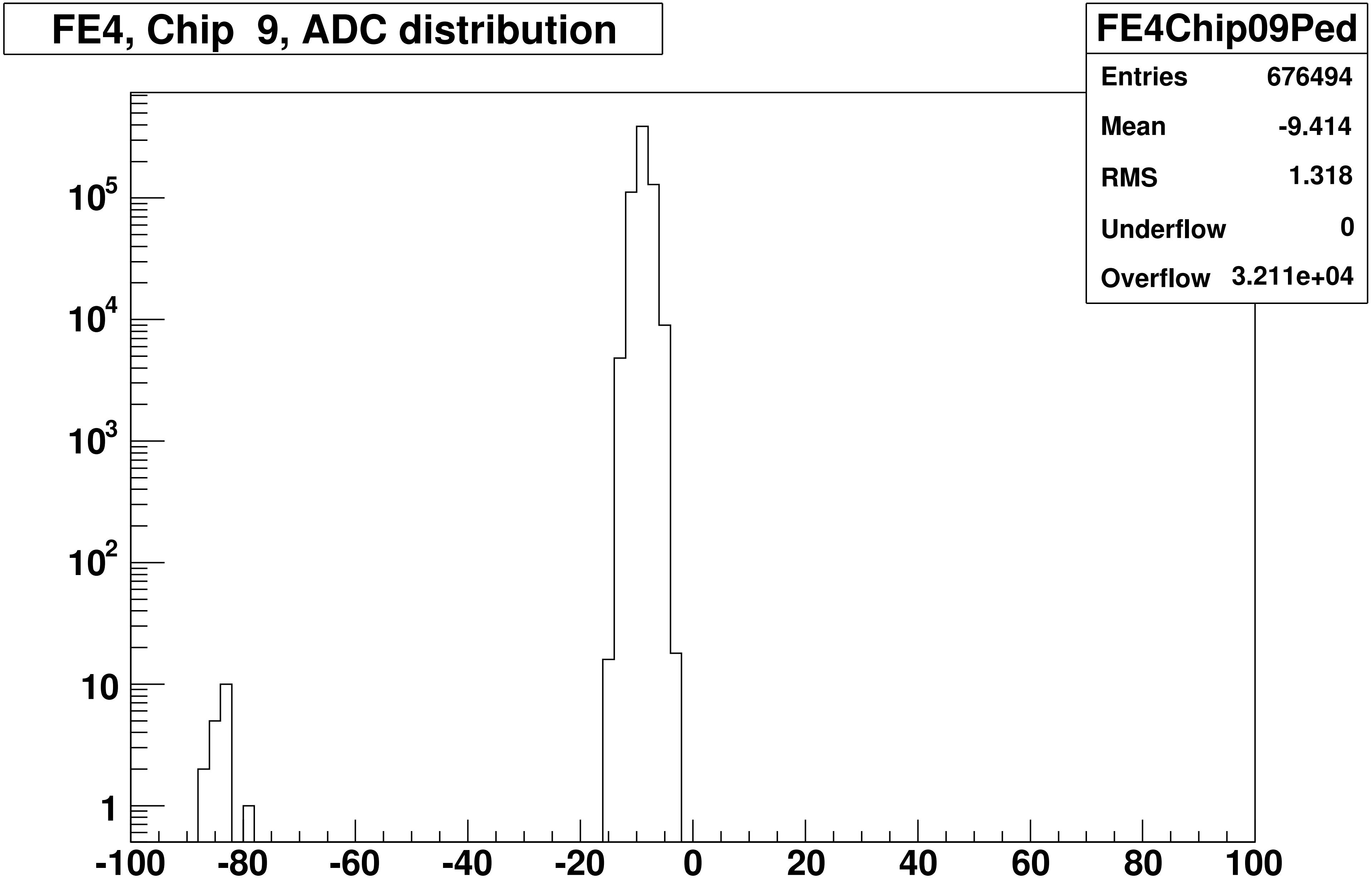
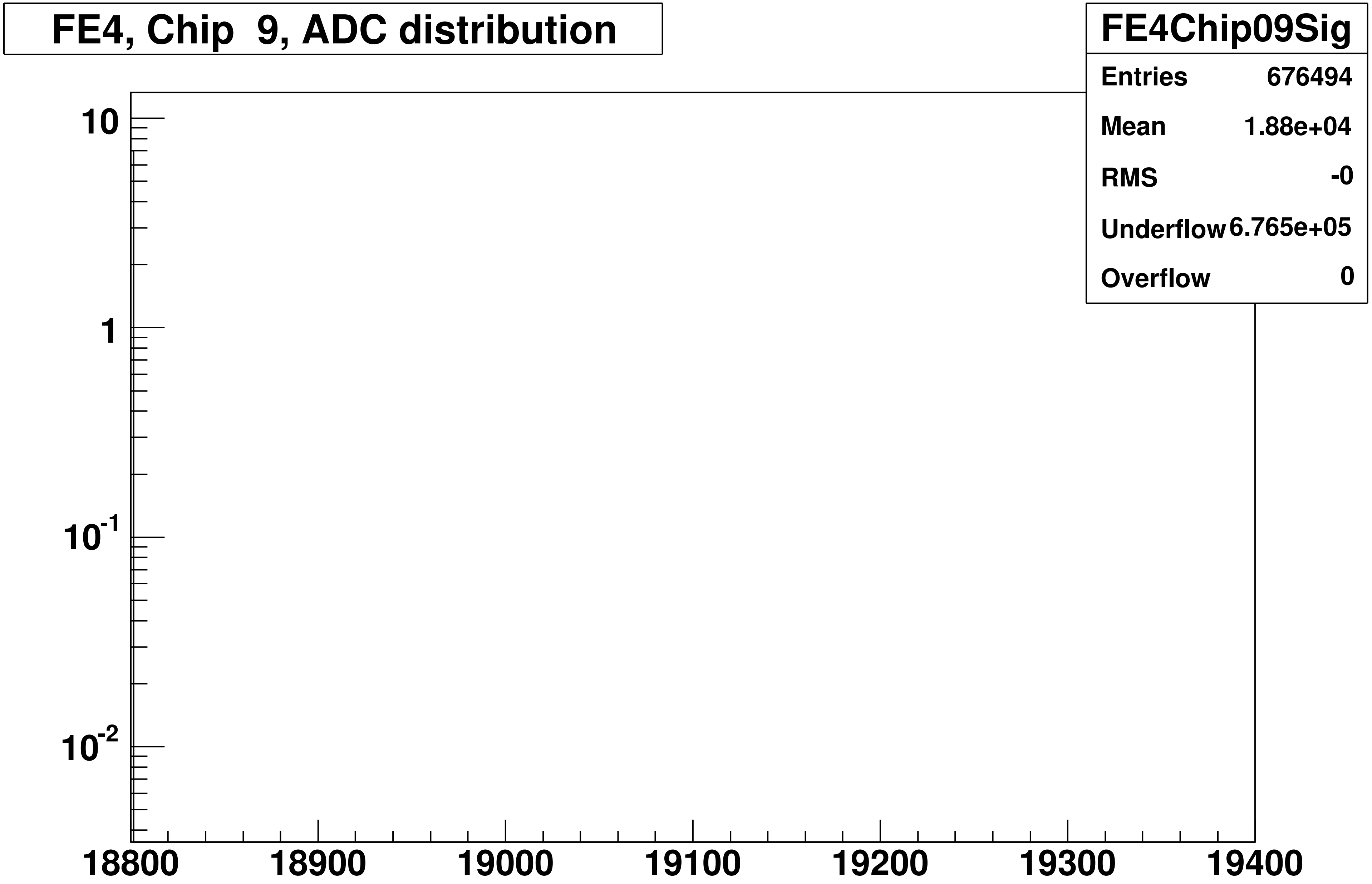
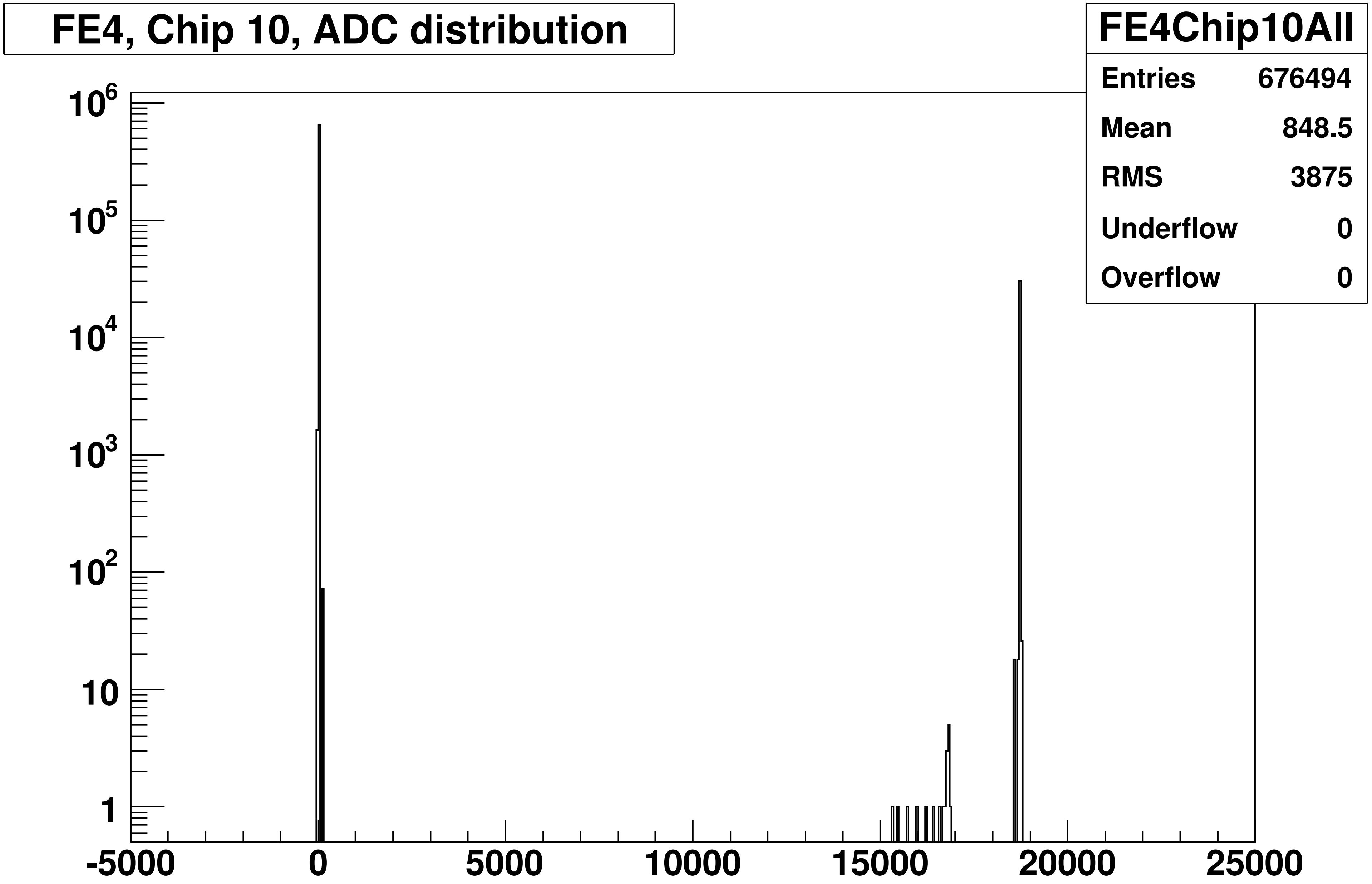
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 676494
Mean -9.414
RMS 1.318
Underflow 0
Overflow 3.211e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 676494
Mean 1.88e+04
RMS -0
Underflow 6.765e+05
Overflow 0
10
1
10^-1
10^-2
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 676494
Mean 848.5
RMS 3875
Underflow 0
Overflow 0
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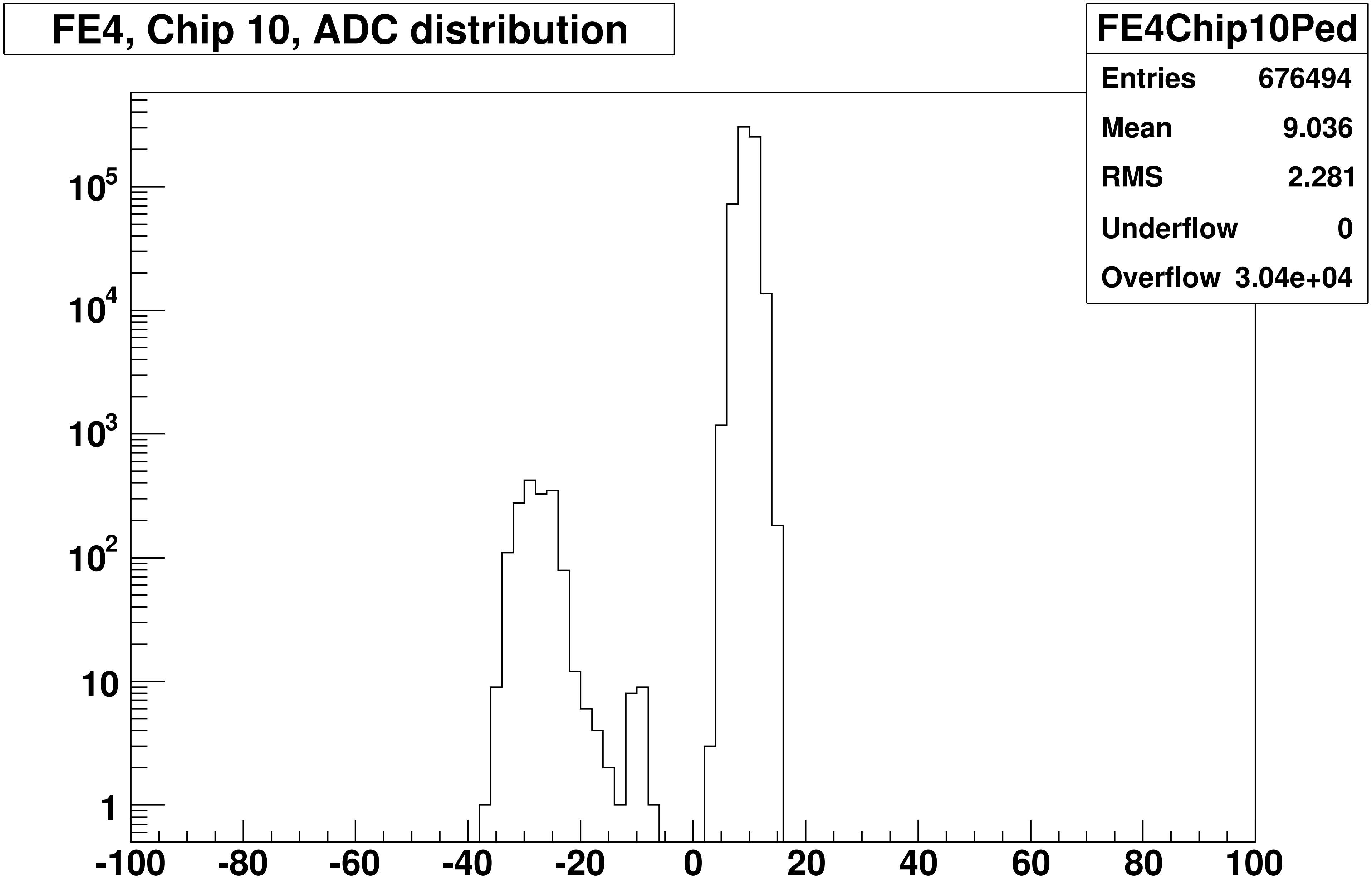
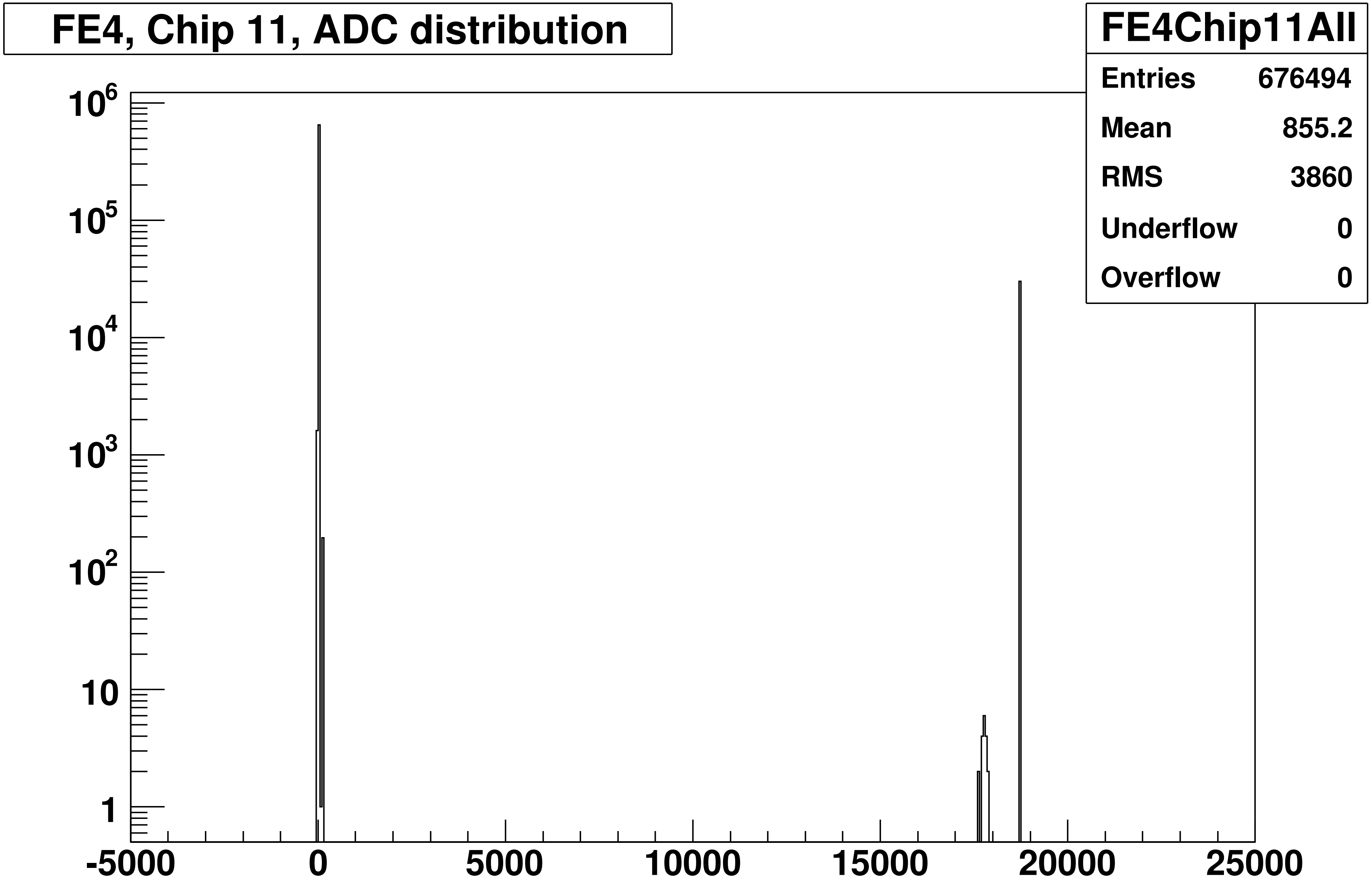
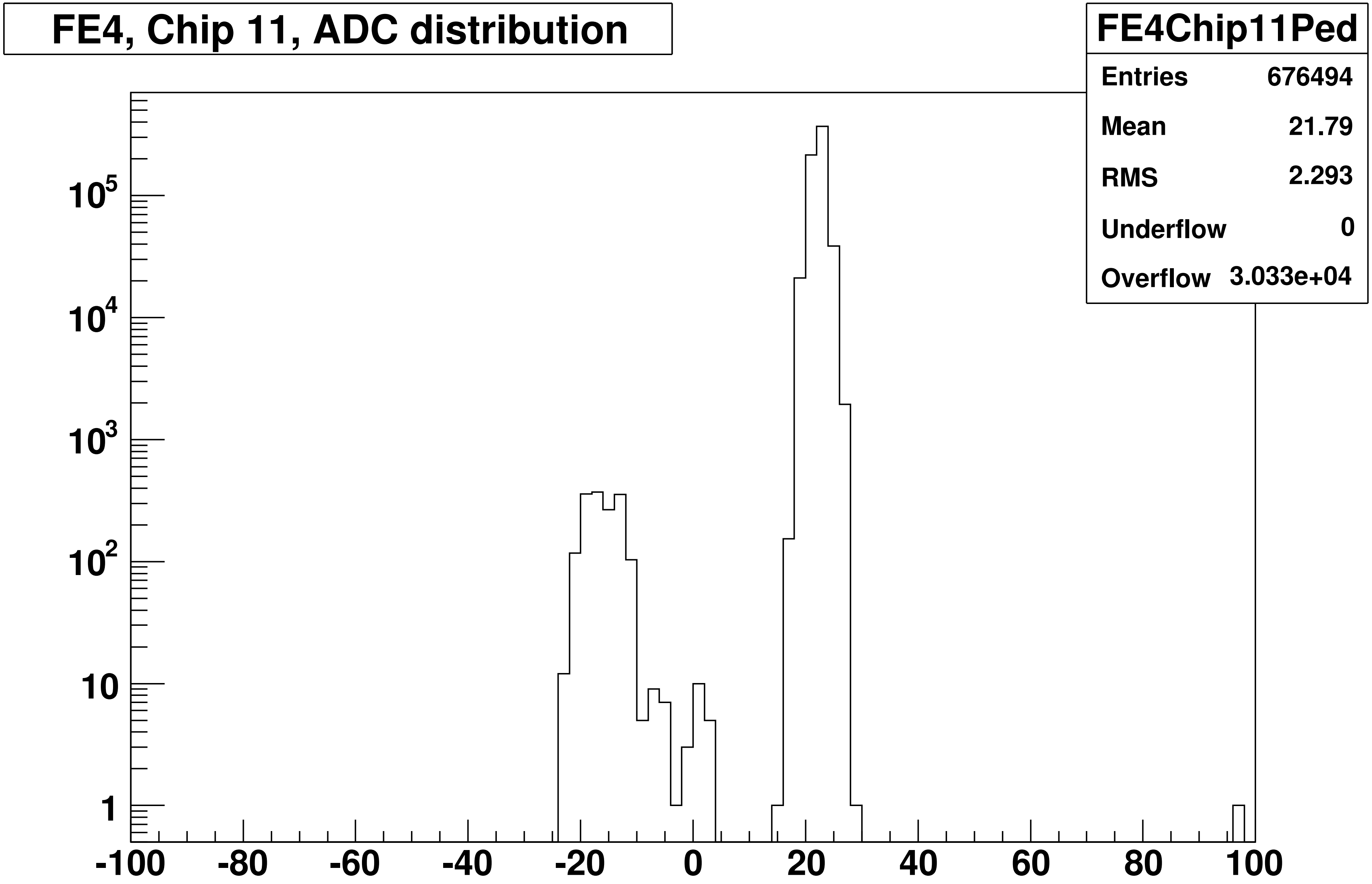
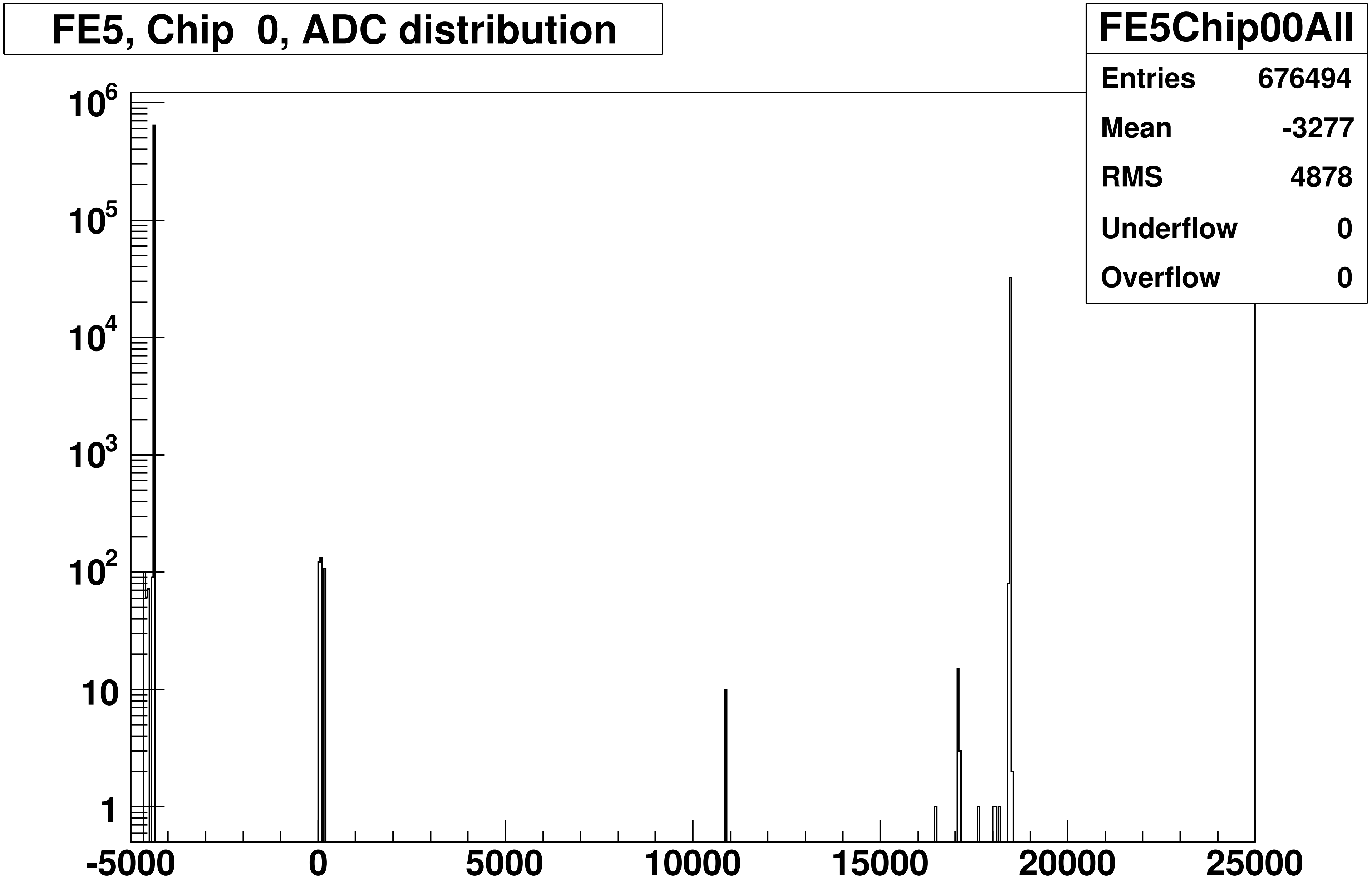
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

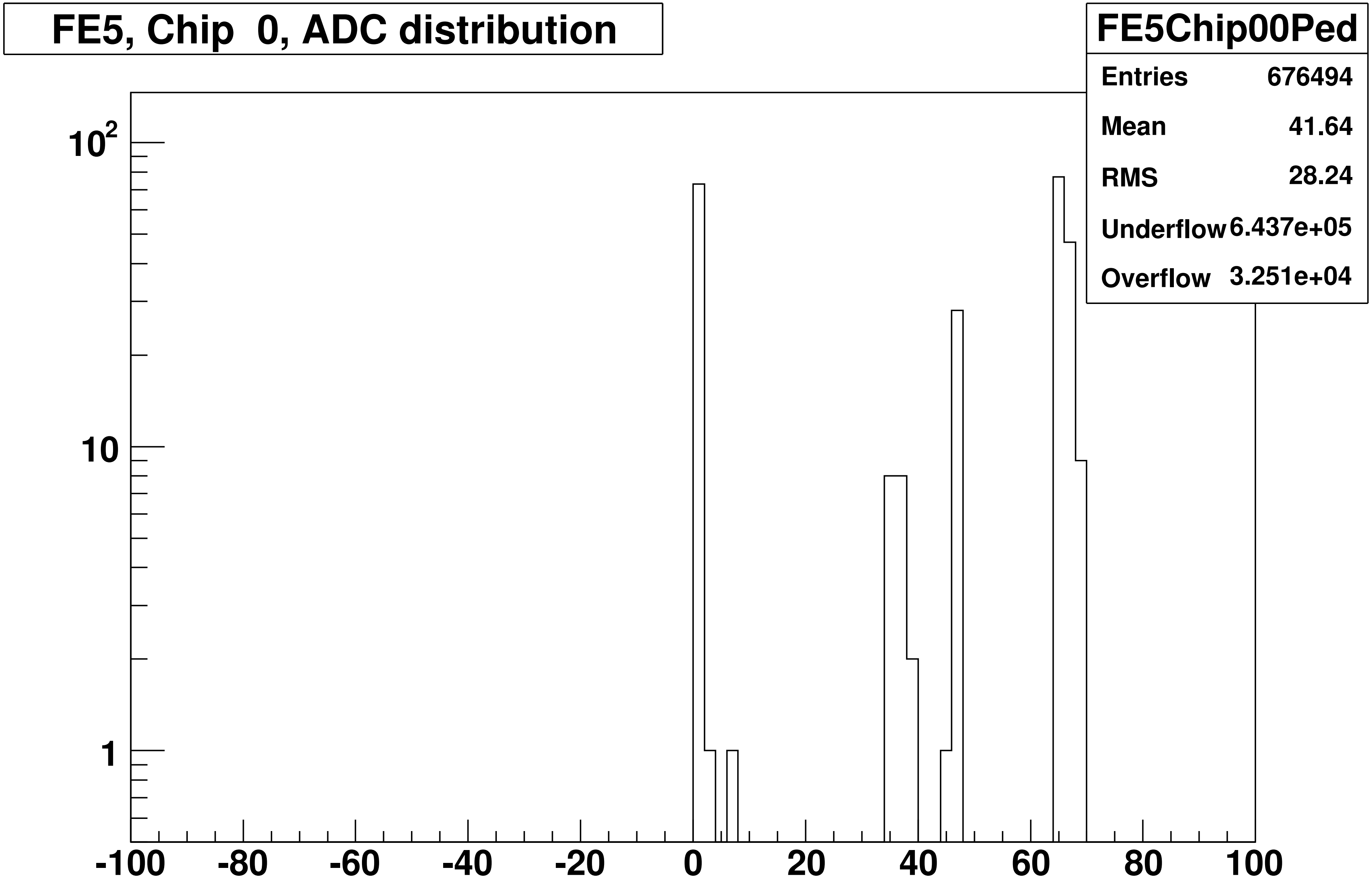


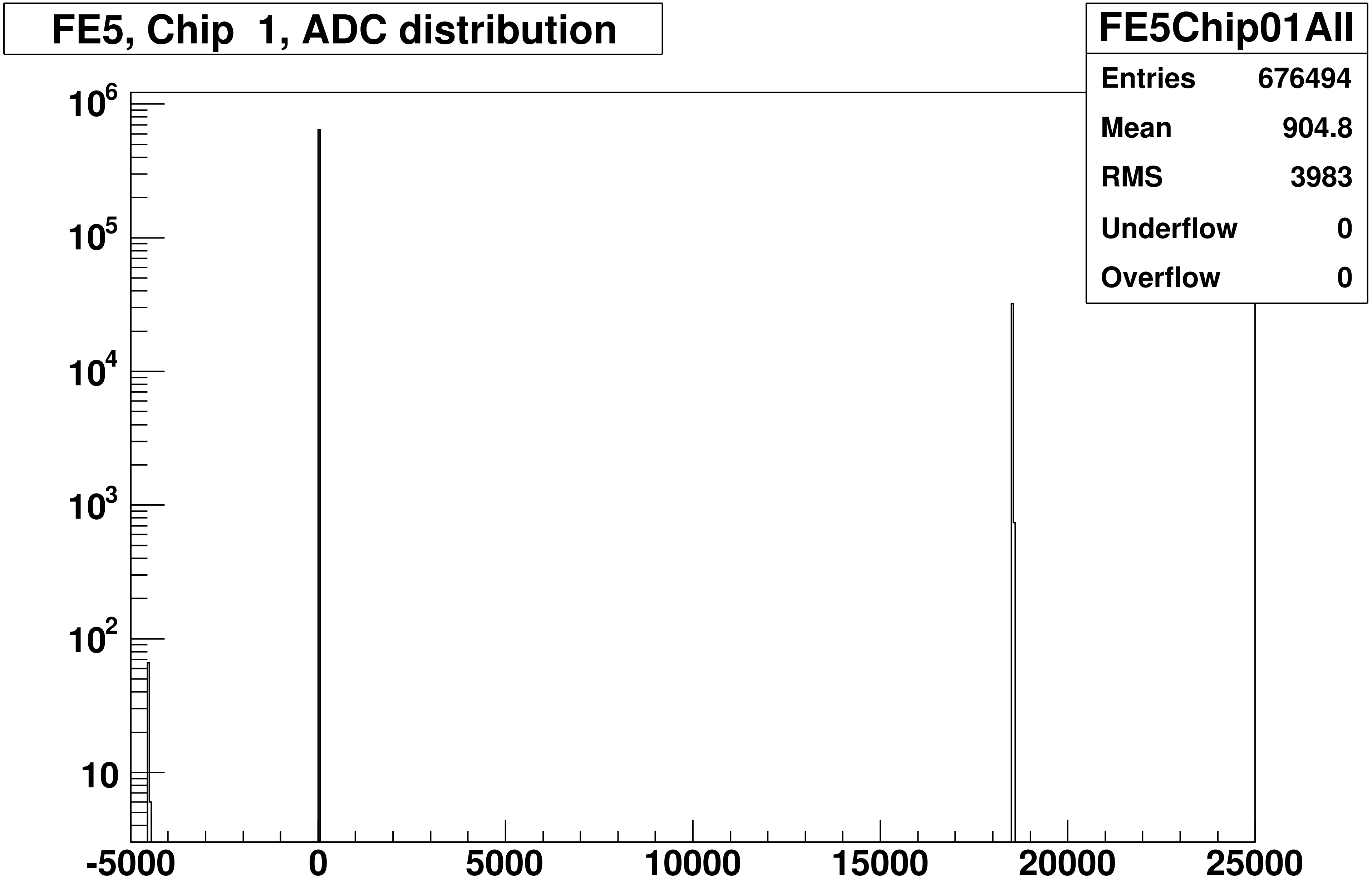
FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 676494
Mean 9.036
RMS 2.281
Underflow 0
Overflow 3.04e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

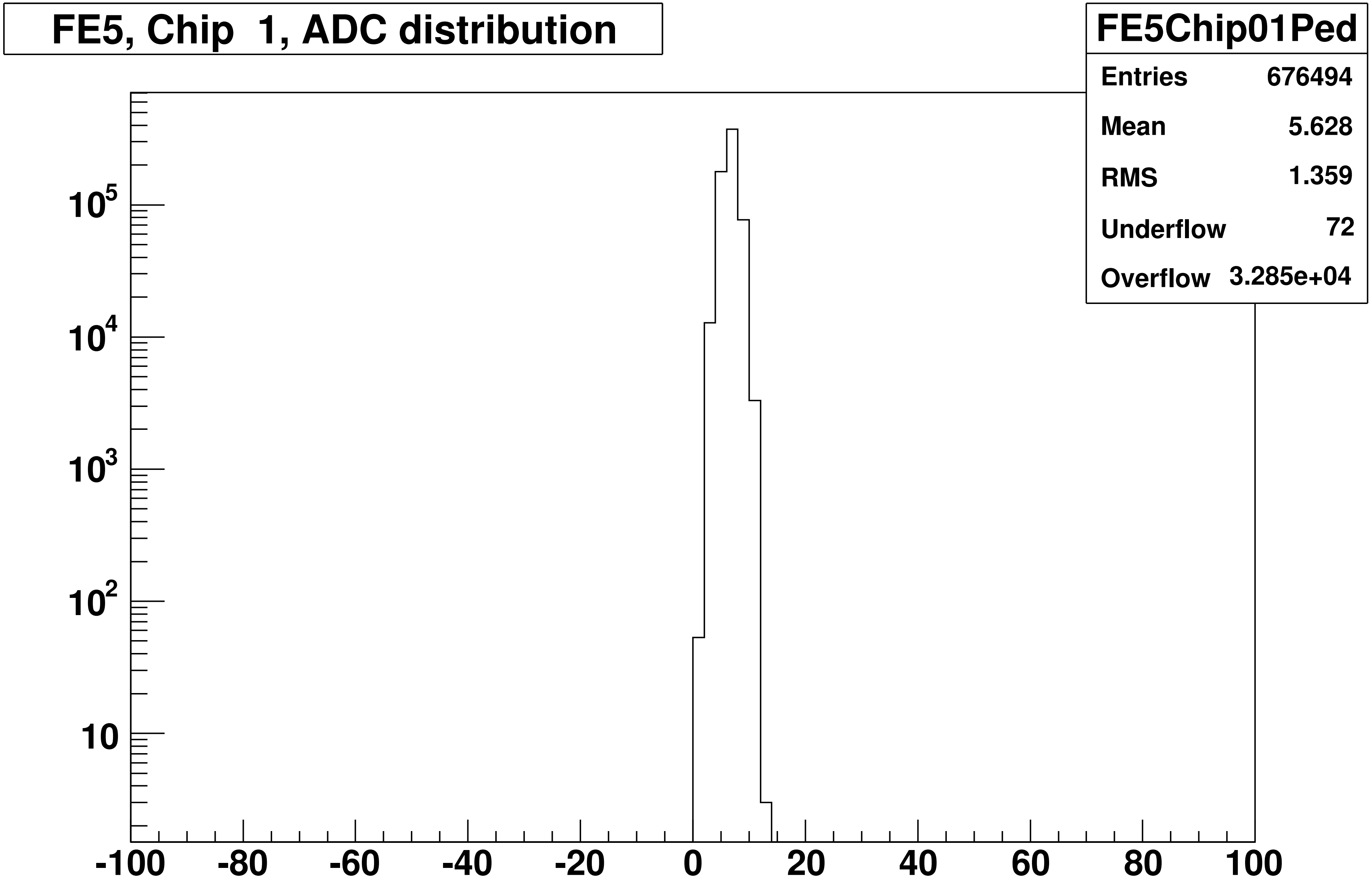

FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 676494
Mean 855.2
RMS 3860
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

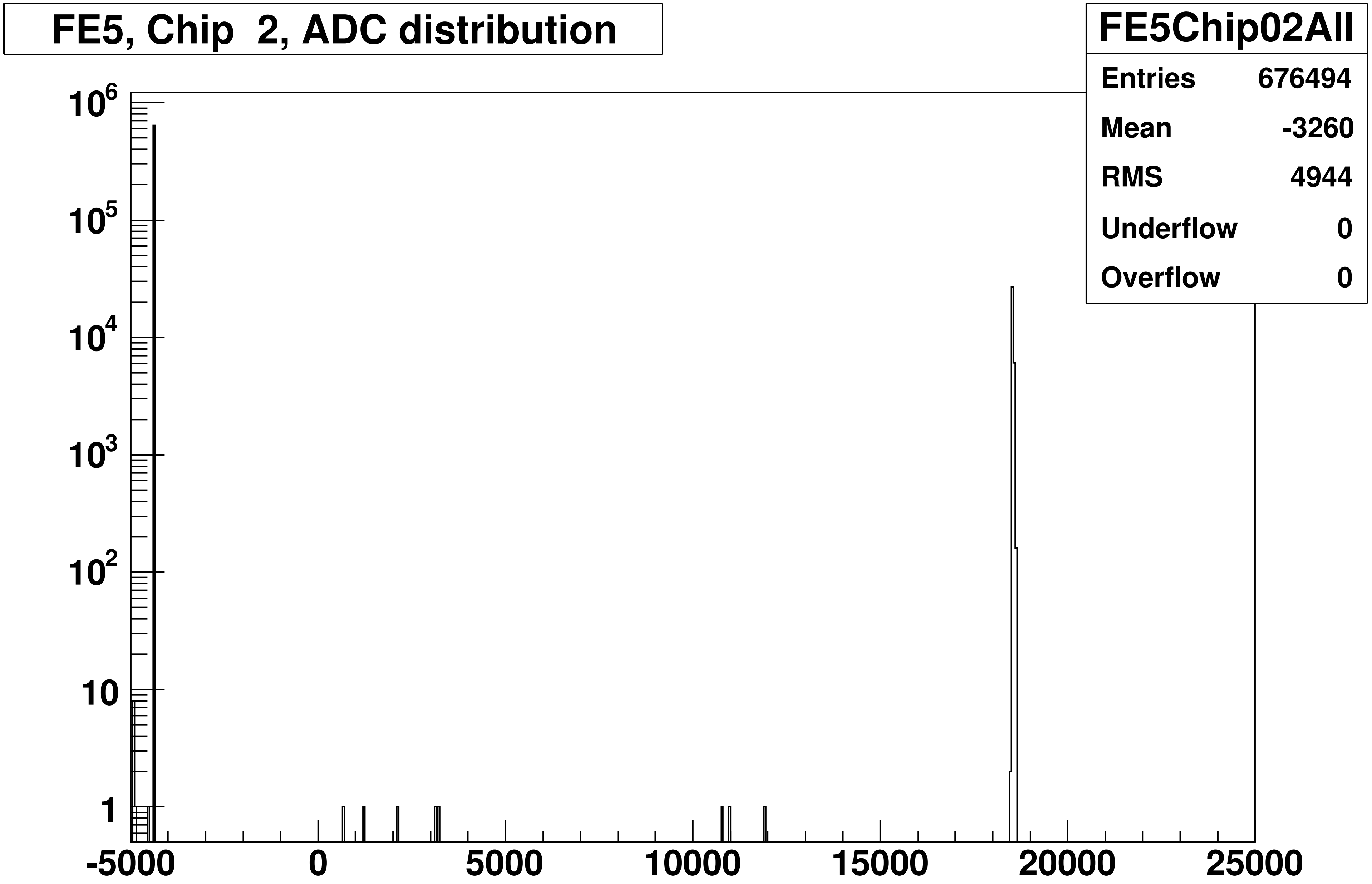

FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 676494
Mean 21.79
RMS 2.293
Underflow 0
Overflow 3.033e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

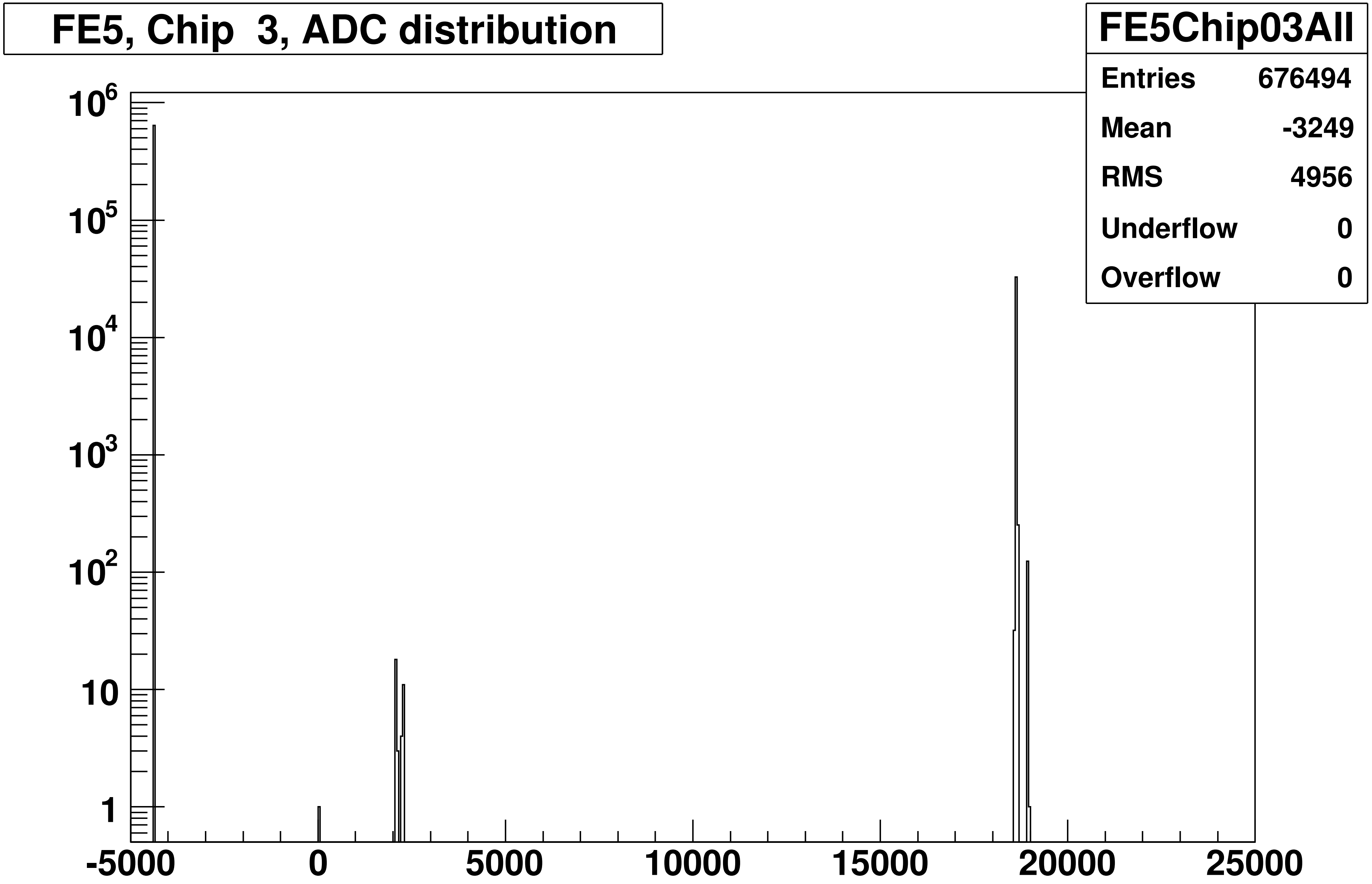

FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 676494
Mean -3277
RMS 4878
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 676494
Mean 41.64
RMS 28.24
Underflow 6.437e+05
Overflow 3.251e+04
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

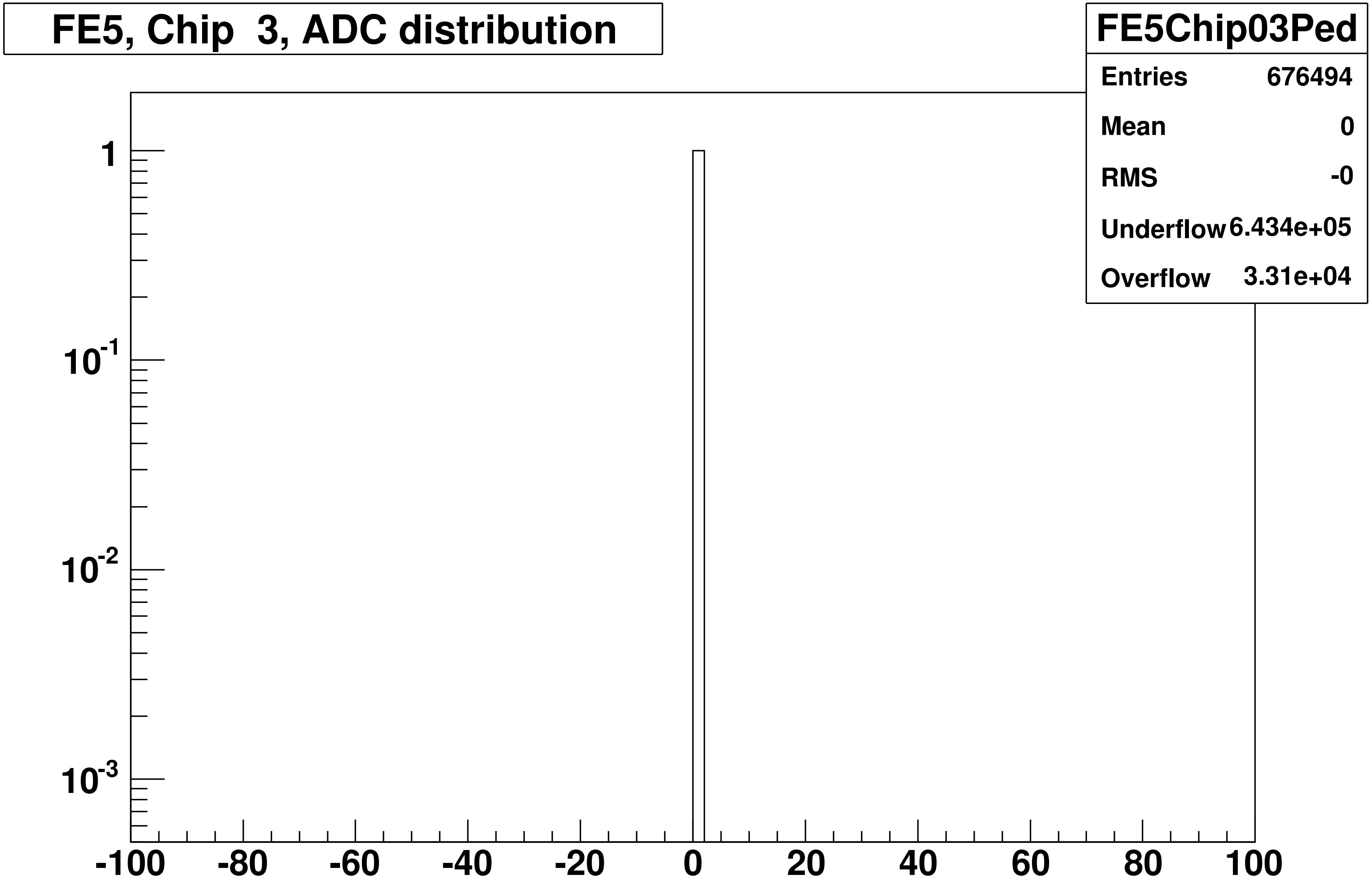

FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 676494
Mean 904.8
RMS 3983
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 676494
Mean 5.628
RMS 1.359
Underflow 72
Overflow 3.285e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

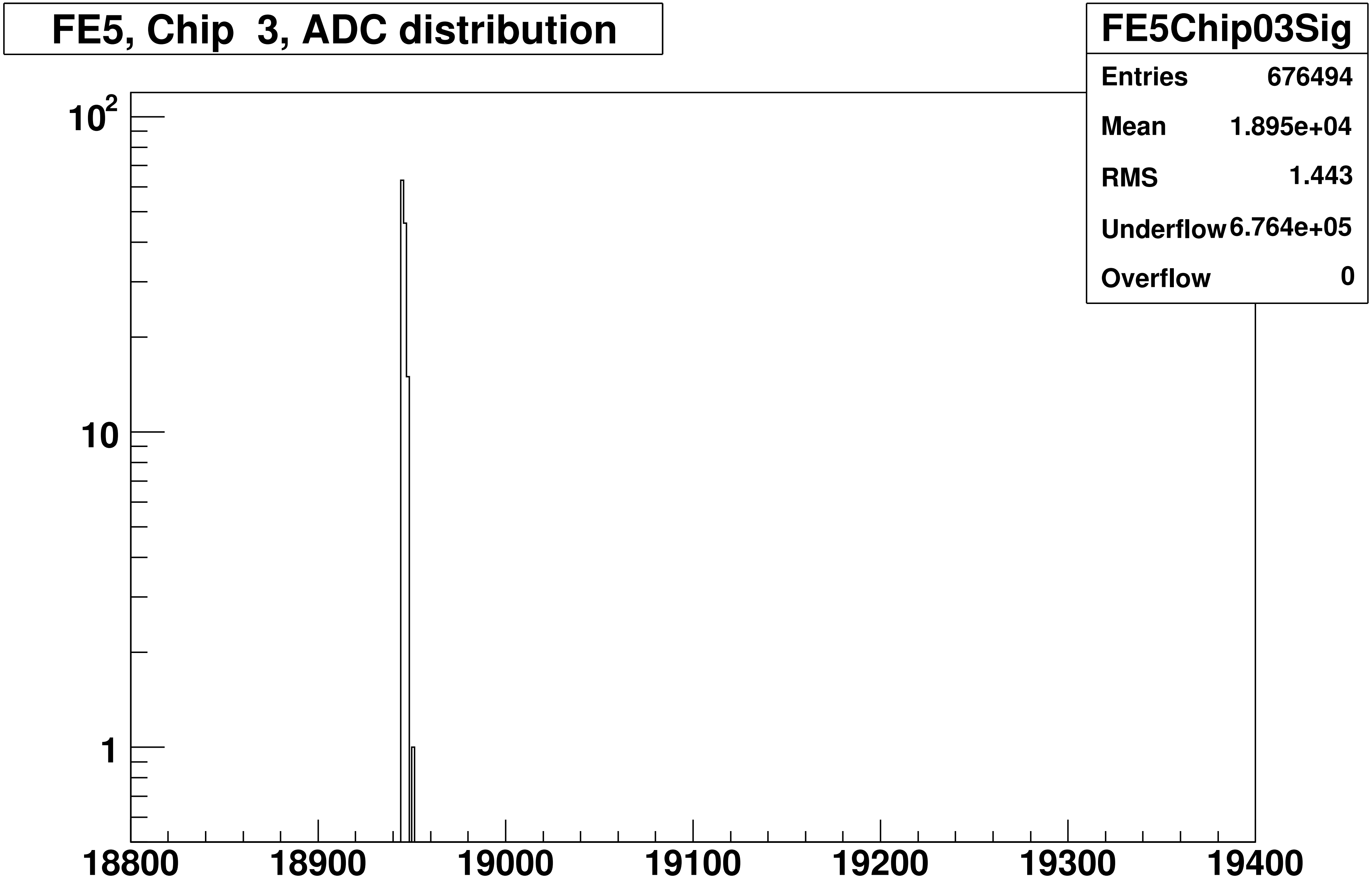

FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 676494
Mean -3260
RMS 4944
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

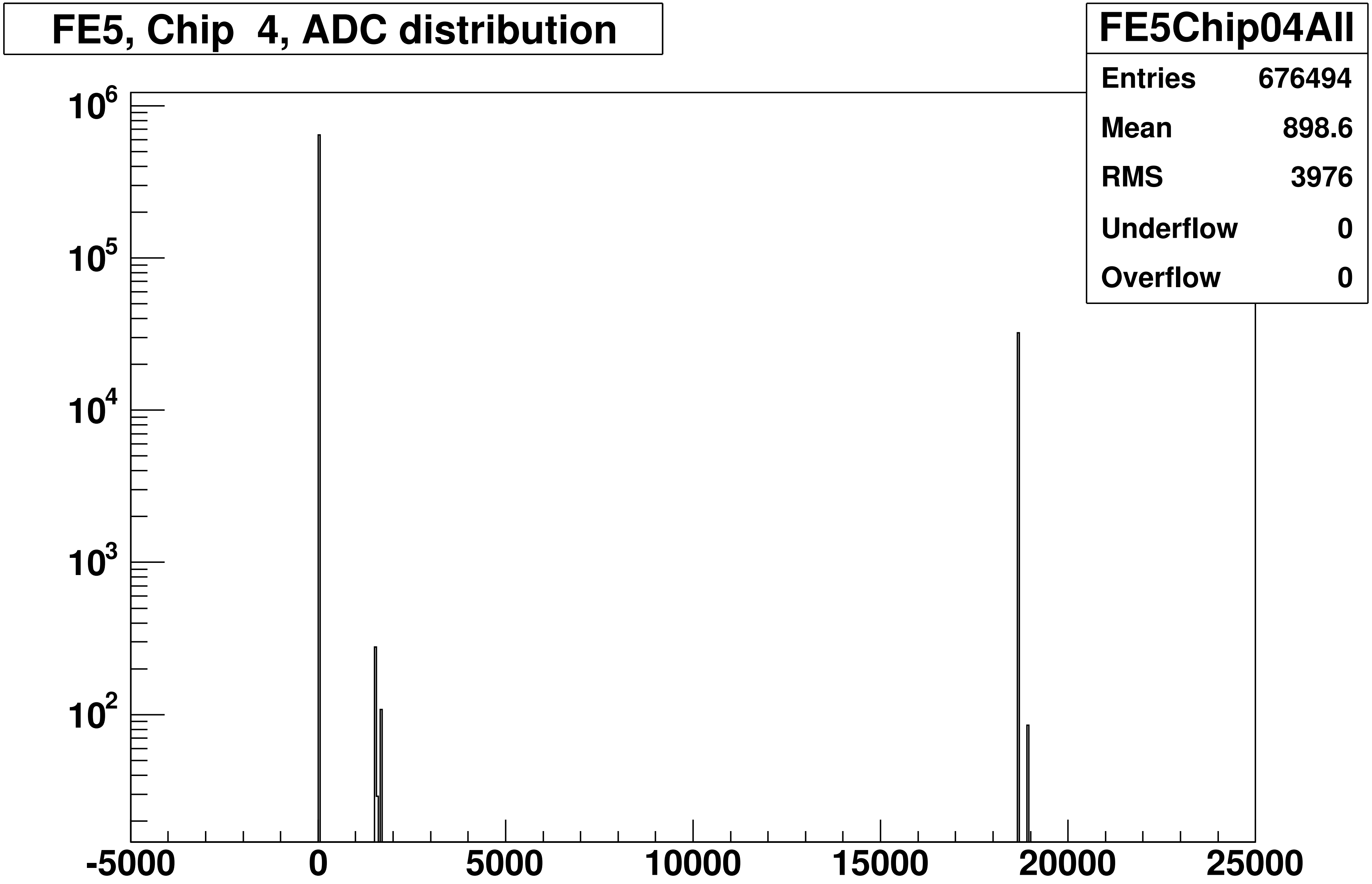


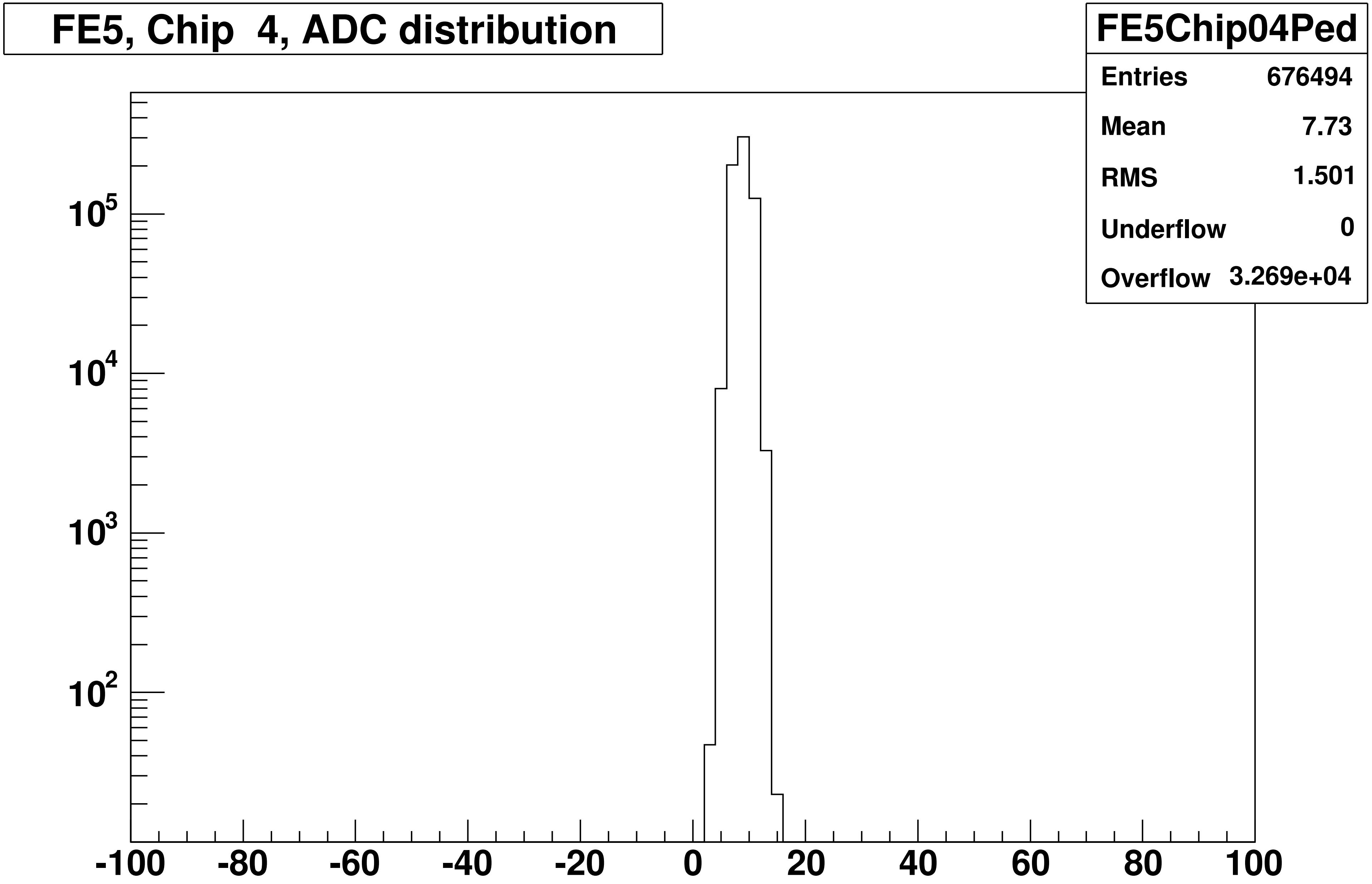
FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 676494
Mean -3249
RMS 4956
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

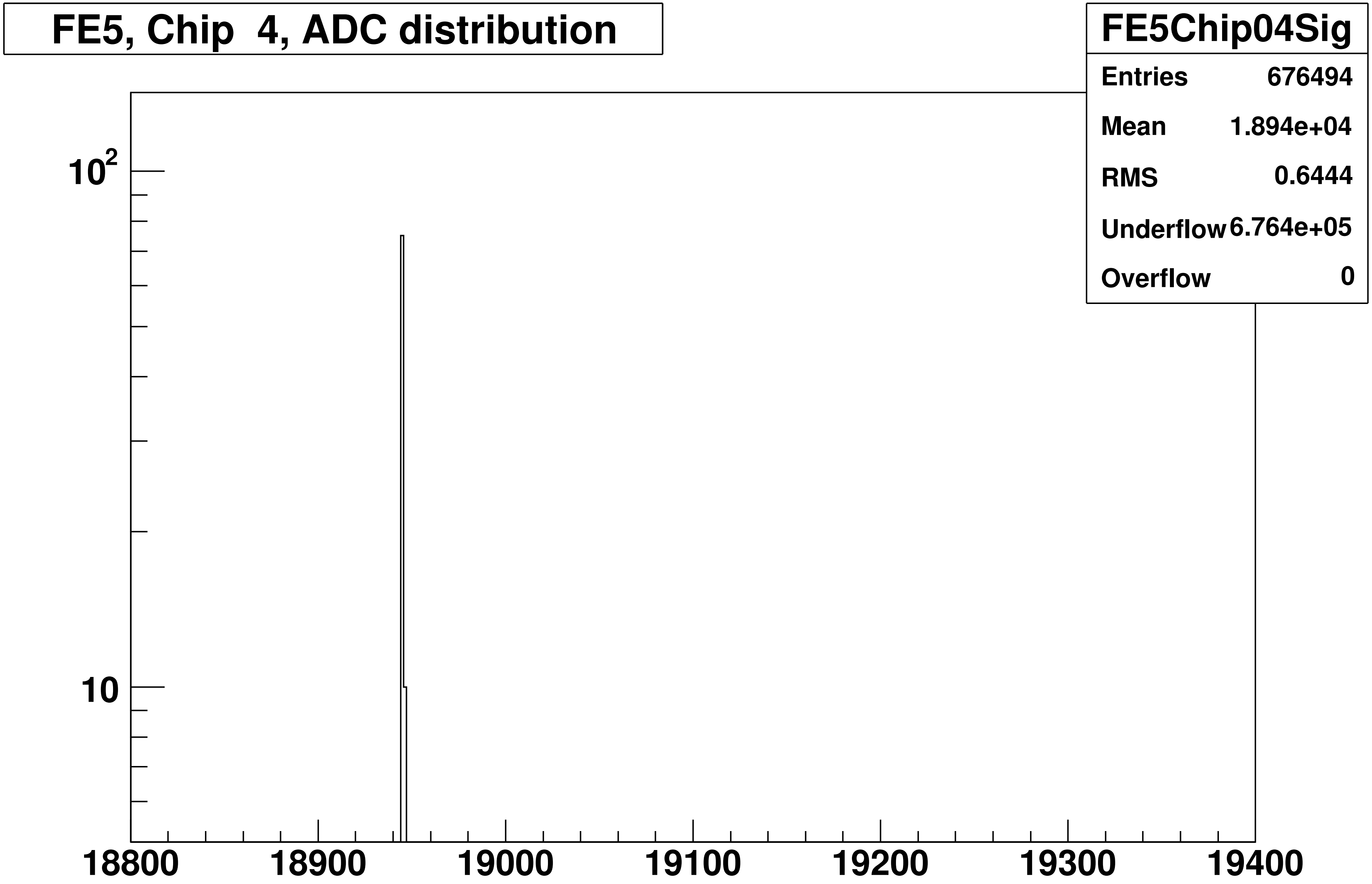


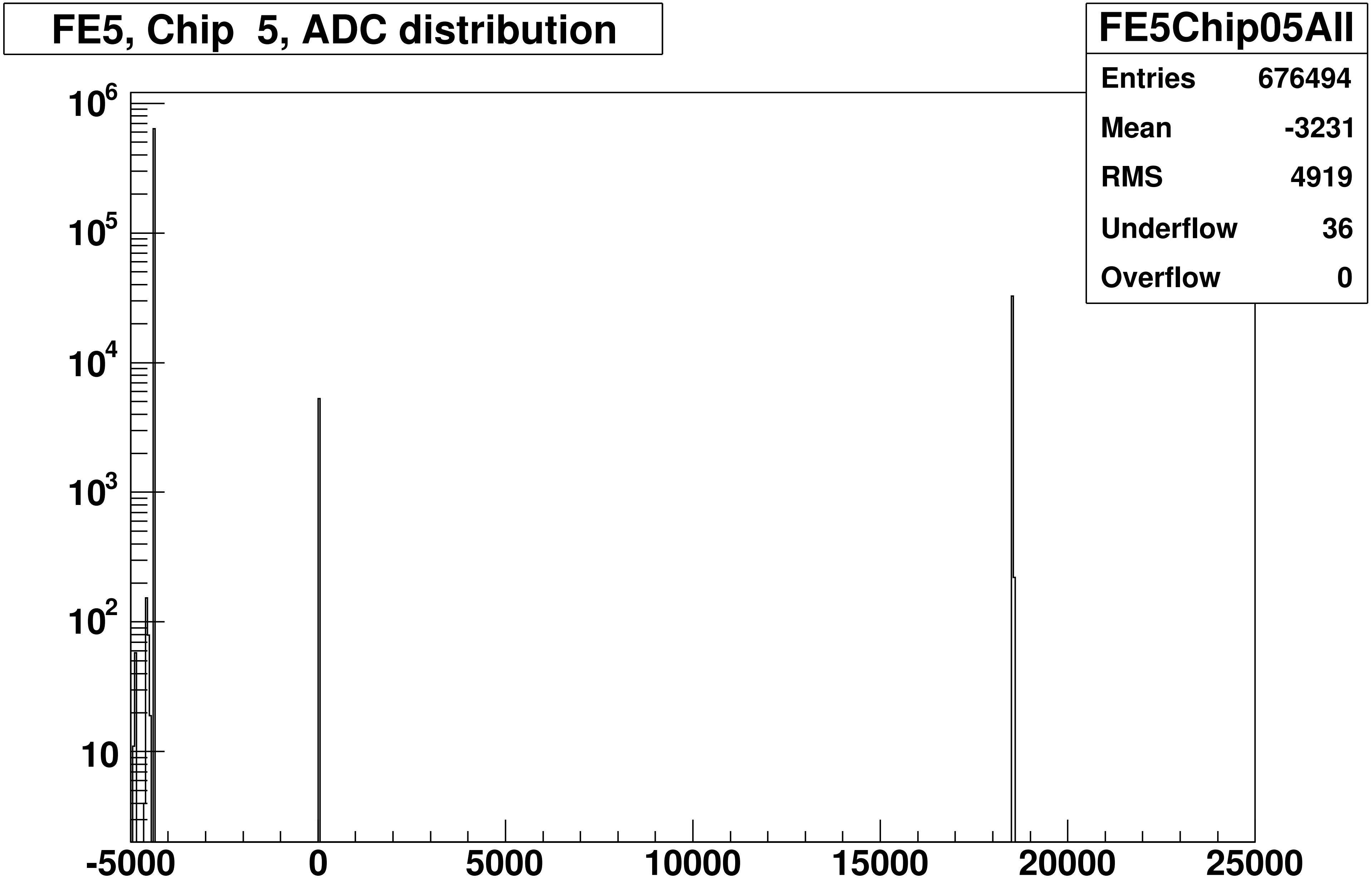
FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 676494
Mean 0
RMS -0
Underflow 6.434e+05
Overflow 3.31e+04
1
10^-1
10^-2
10^-3
-100
-80
-60
-40
-20
0
20
40
60
80
100

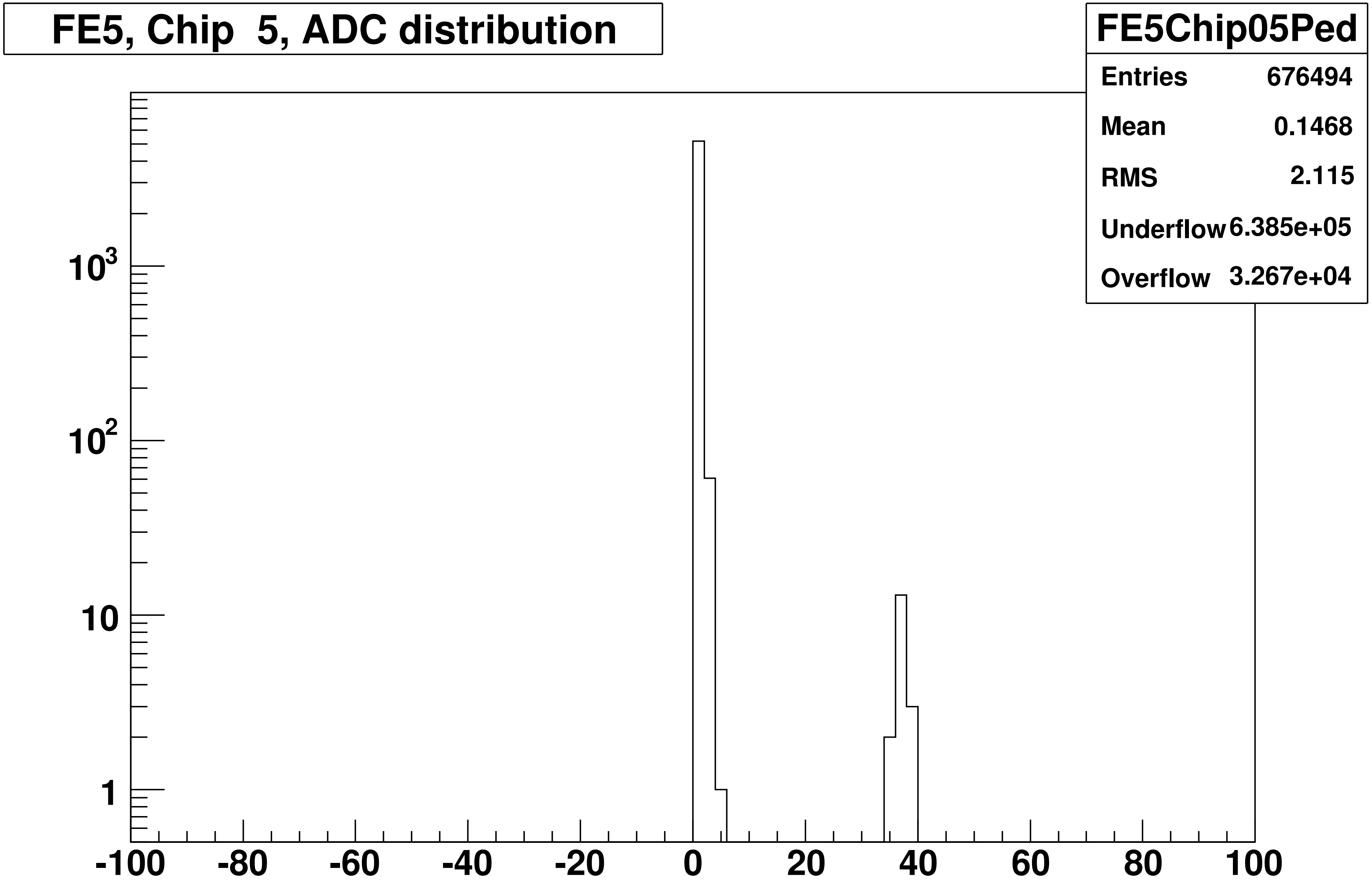

FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 676494
Mean 1.895e+04
RMS 1.443
Underflow 6.764e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 676494
Mean 898.6
RMS 3976
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

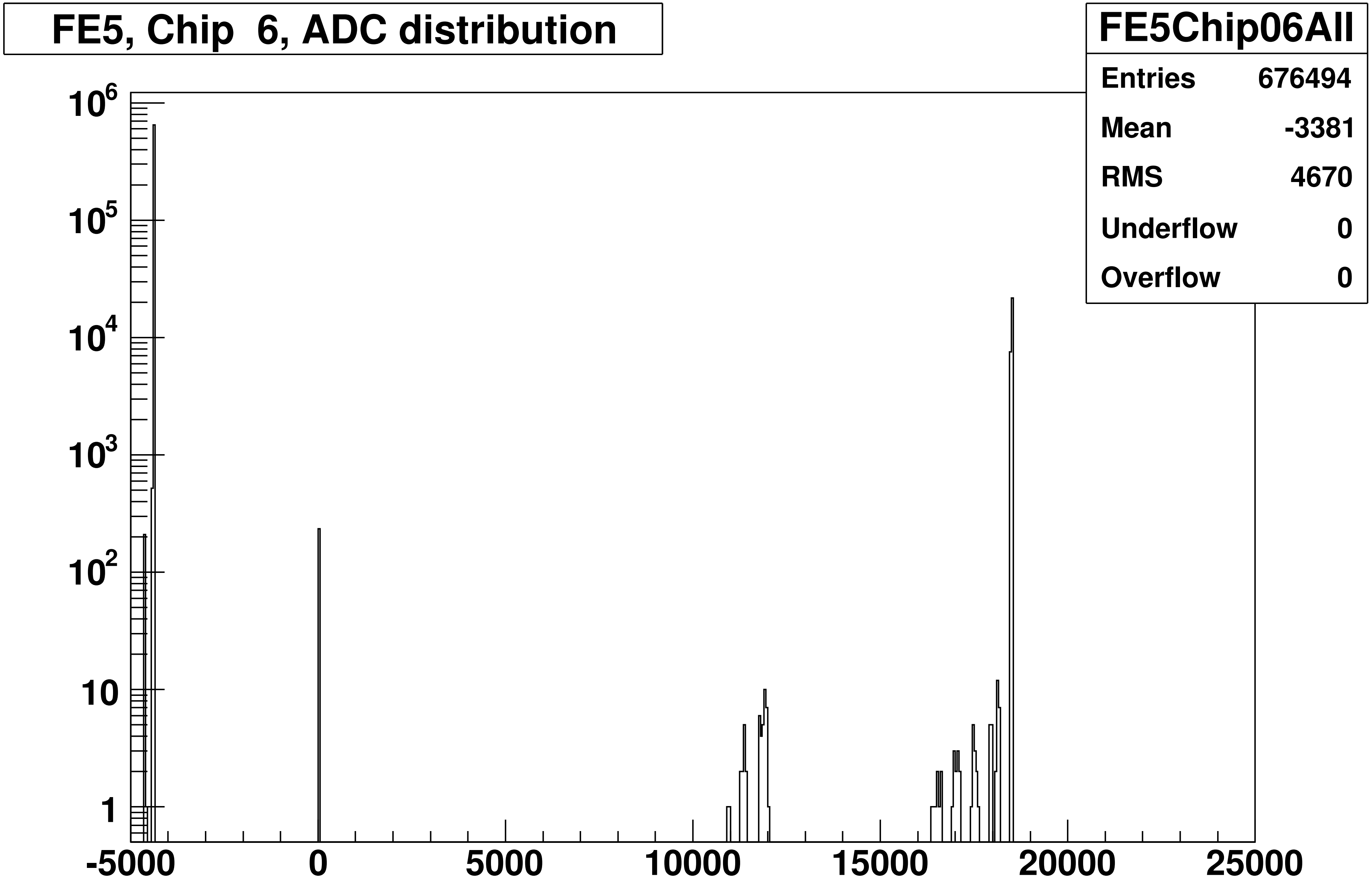

FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 676494
Mean 7.73
RMS 1.501
Underflow 0
Overflow 3.269e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

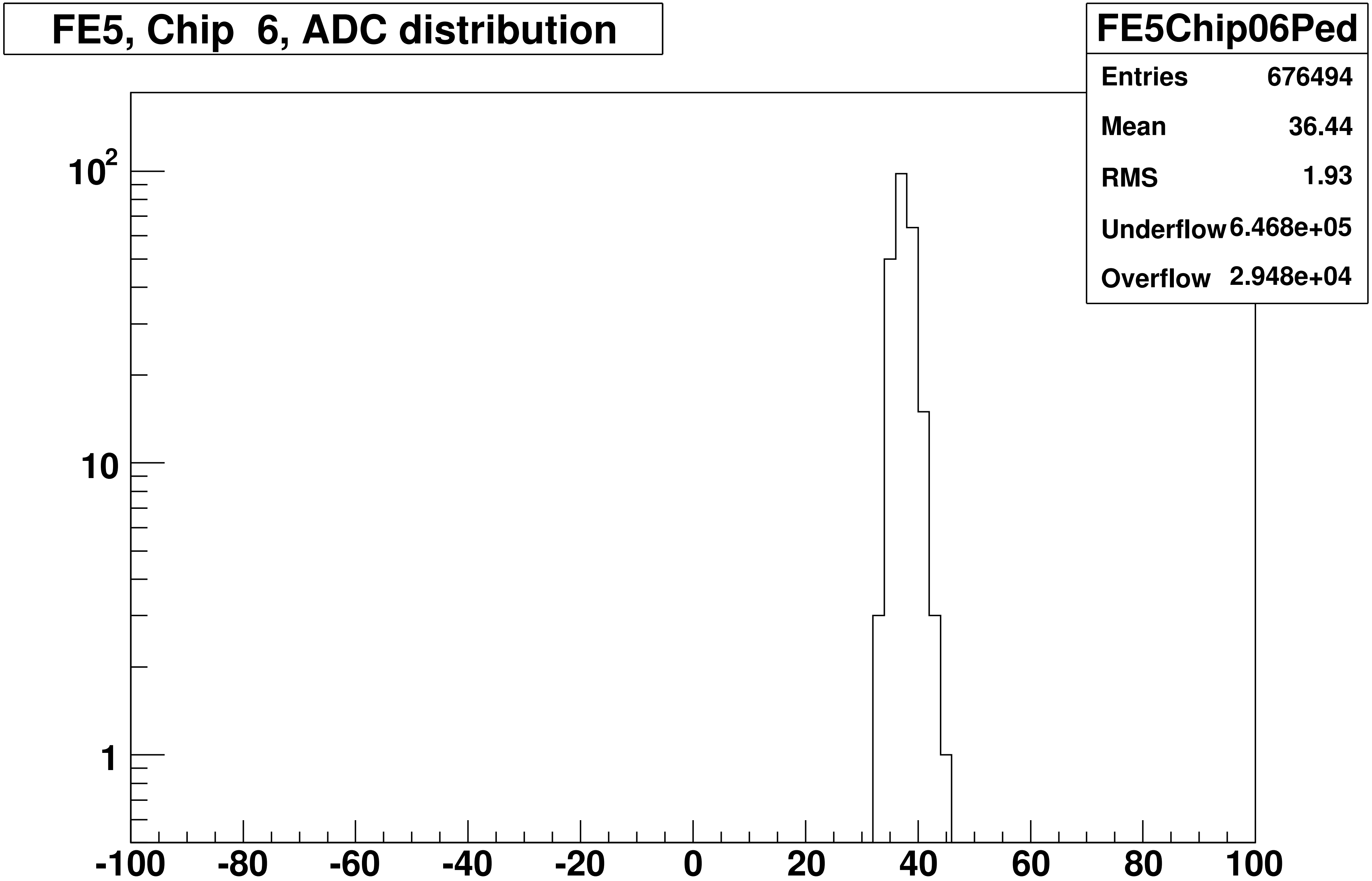

FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 676494
Mean 1.894e+04
RMS 0.6444
Underflow 6.764e+05
Overflow 0
10^2
10
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 676494
Mean -3231
RMS 4919
Underflow 36
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

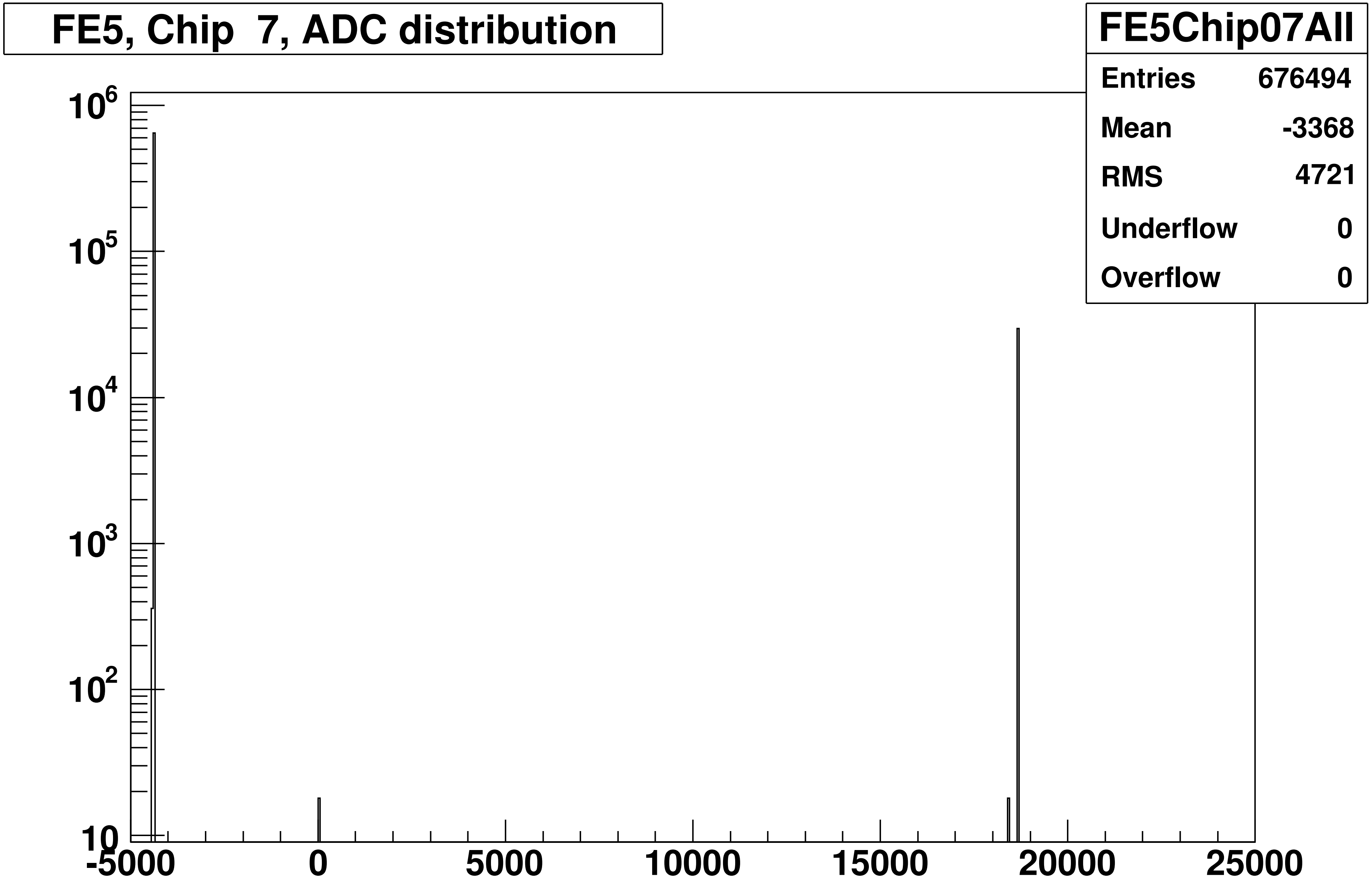


FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 676494
Mean 0.1468
RMS 2.115
Underflow 6.385e+05
Overflow 3.267e+04
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

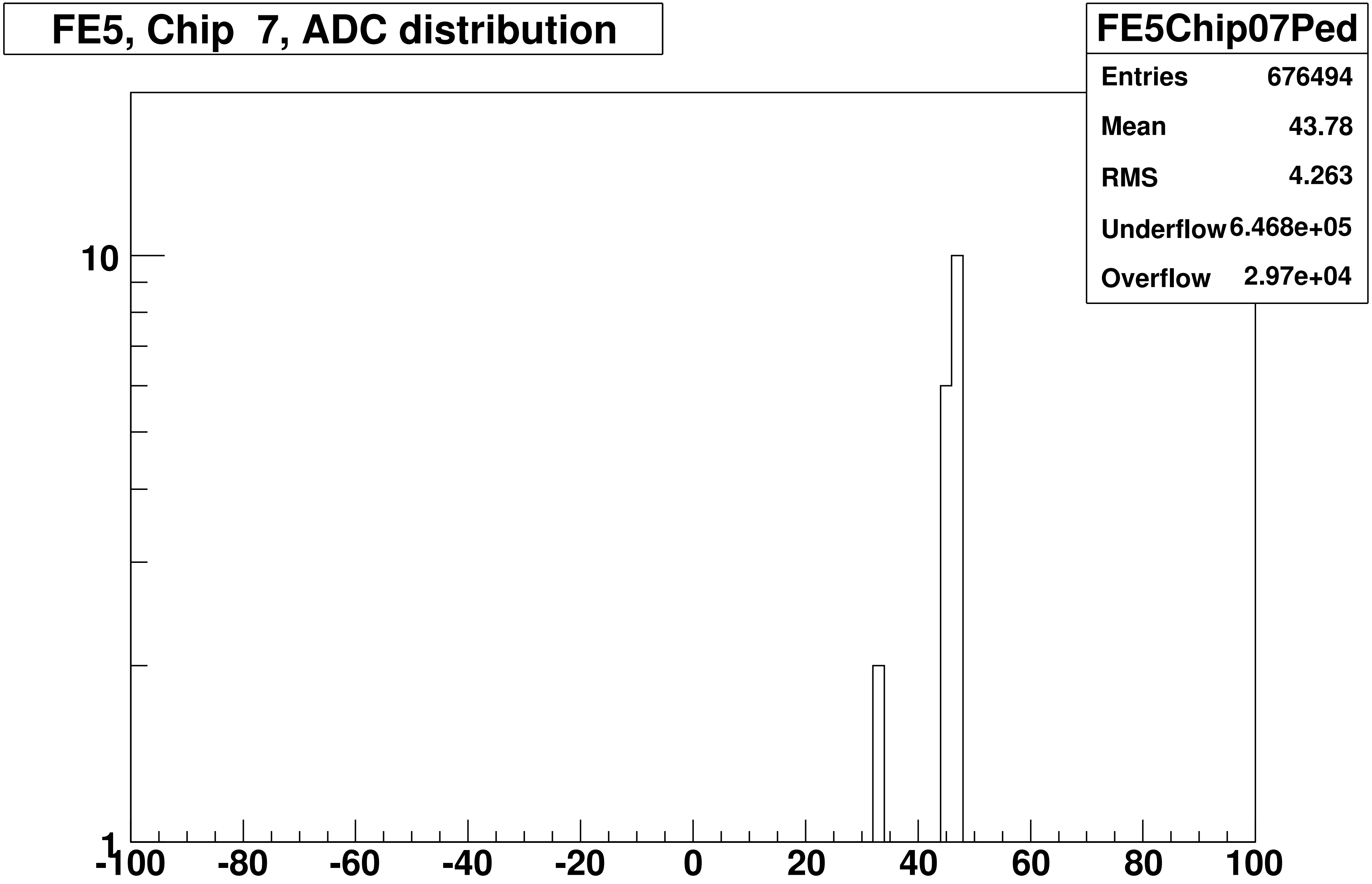

FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 676494
Mean -3381
RMS 4670
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

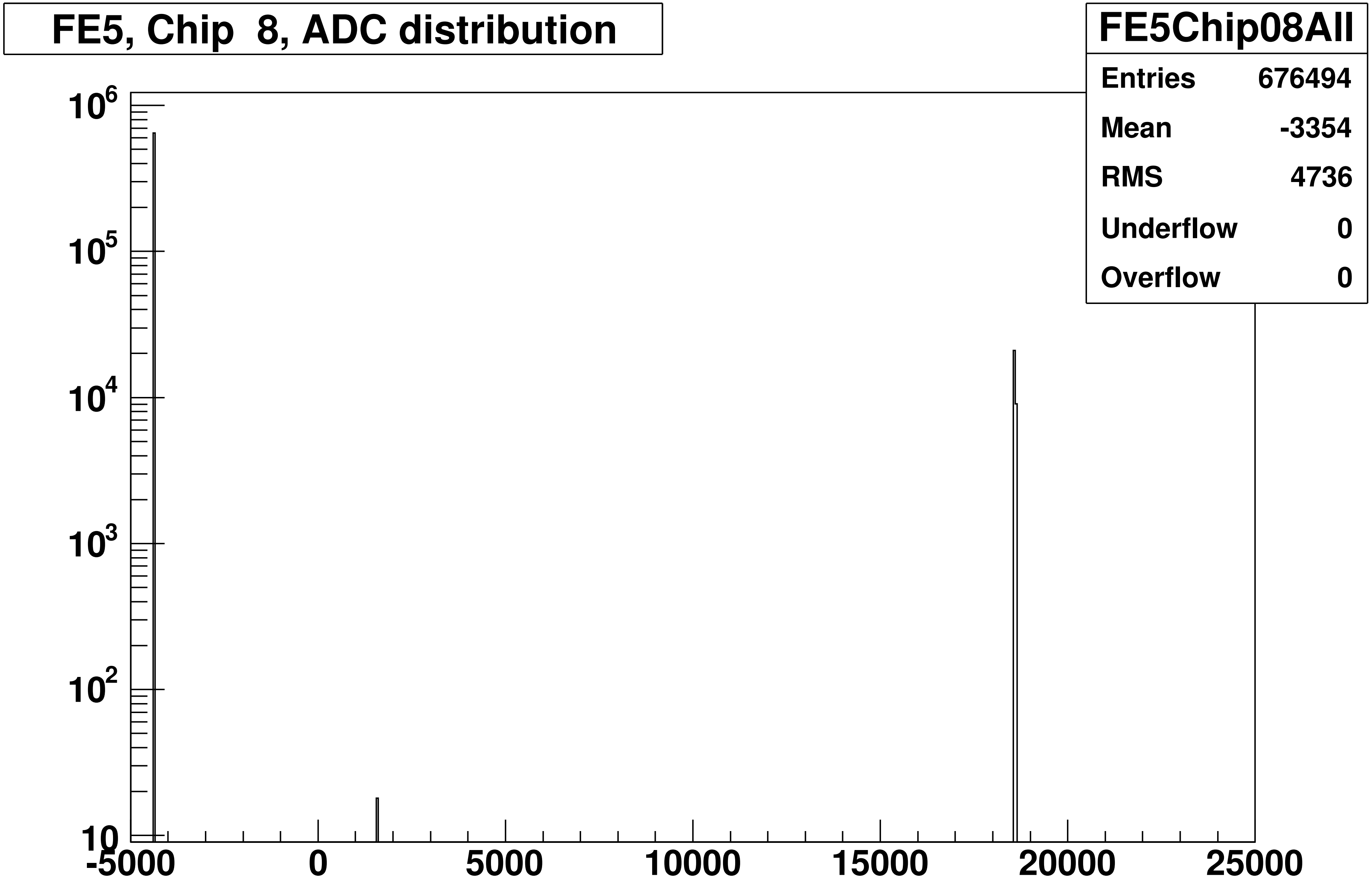


FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 676494
Mean 36.44
RMS 1.93
Underflow 6.468e+05
Overflow 2.948e+04
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 676494
Mean -3368
RMS 4721
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 676494
Mean 43.78
RMS 4.263
Underflow 6.468e+05
Overflow 2.97e+04
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 676494
Mean -3354
RMS 4736
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
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10000
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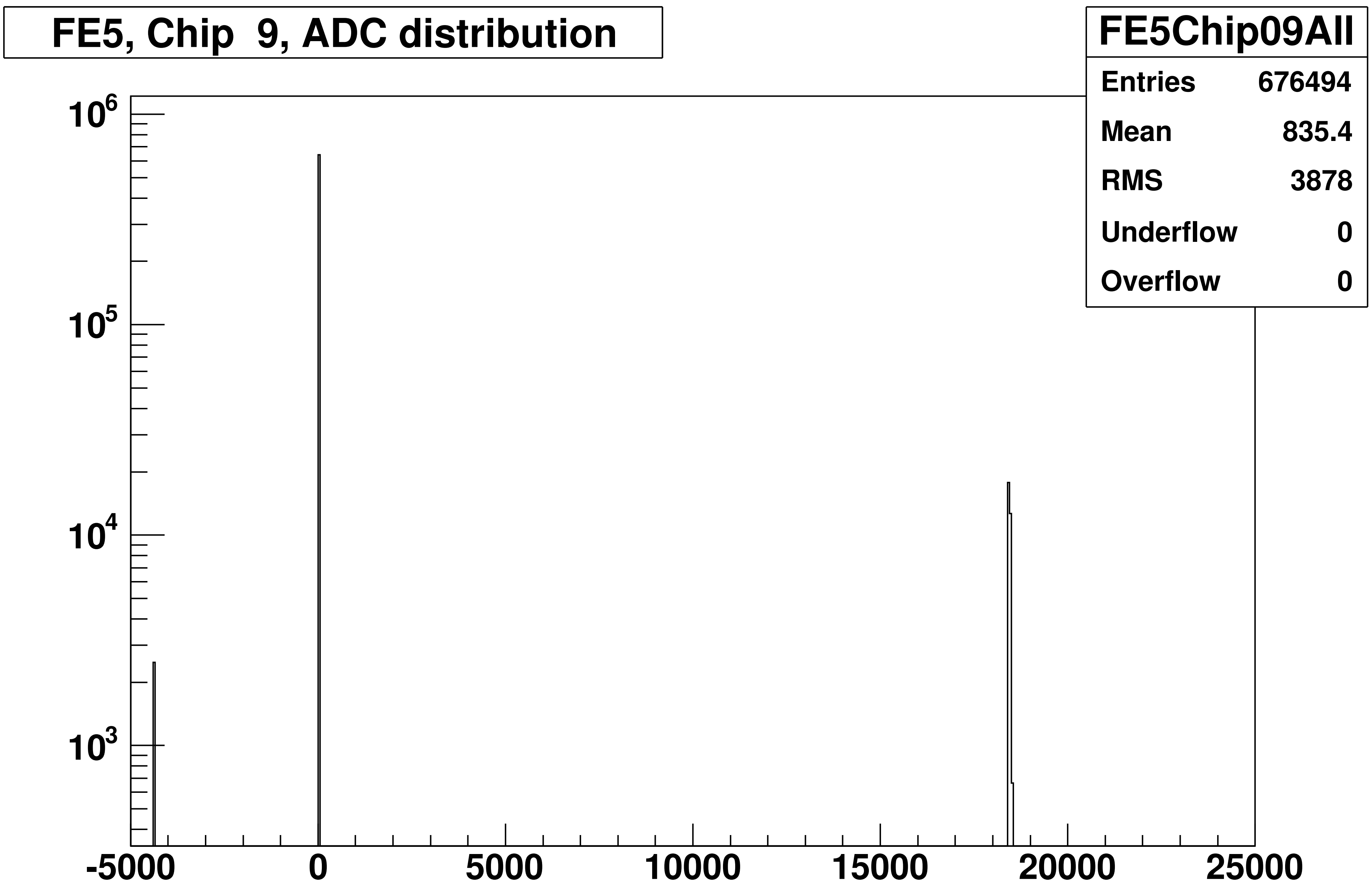
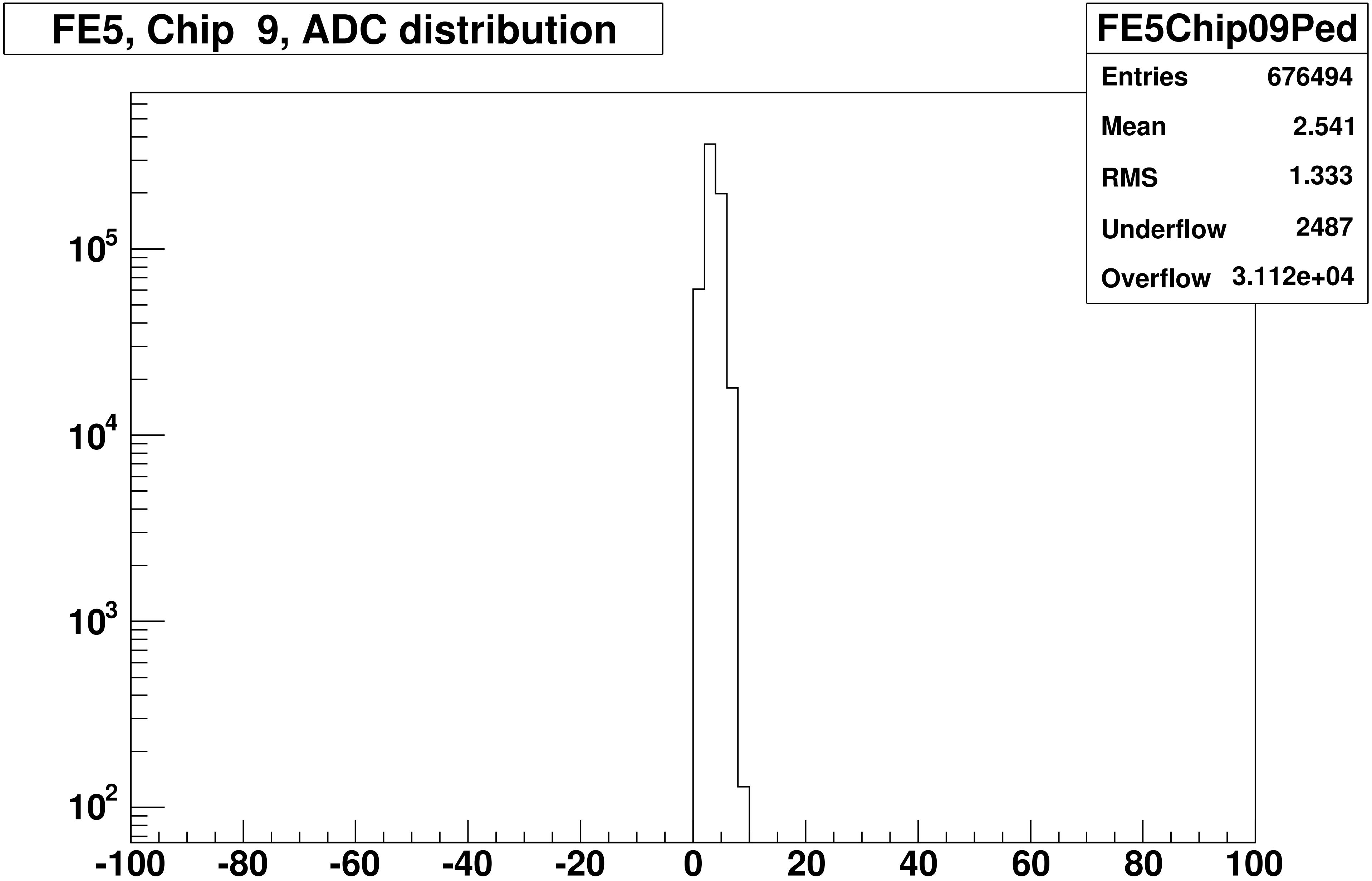
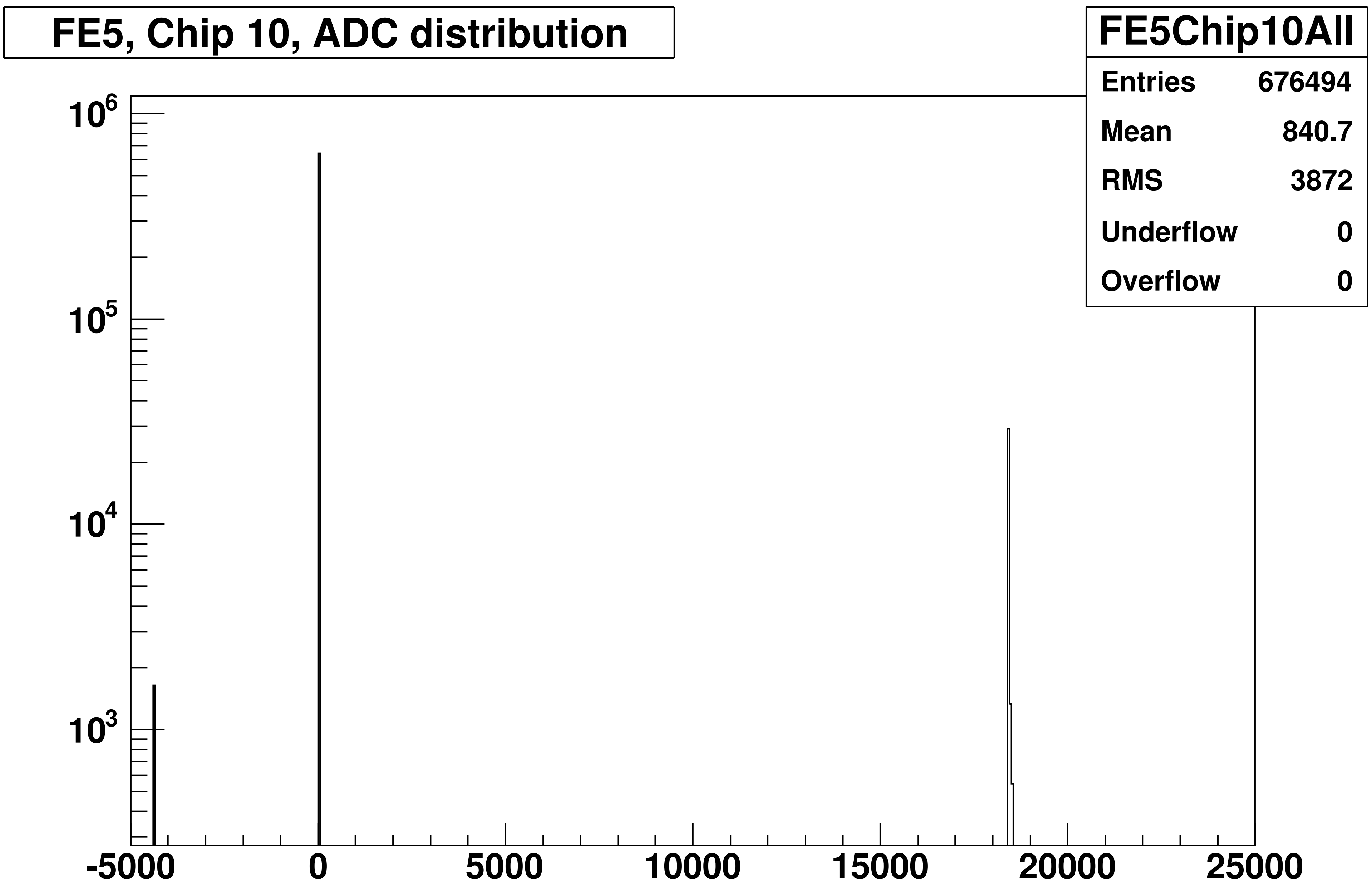
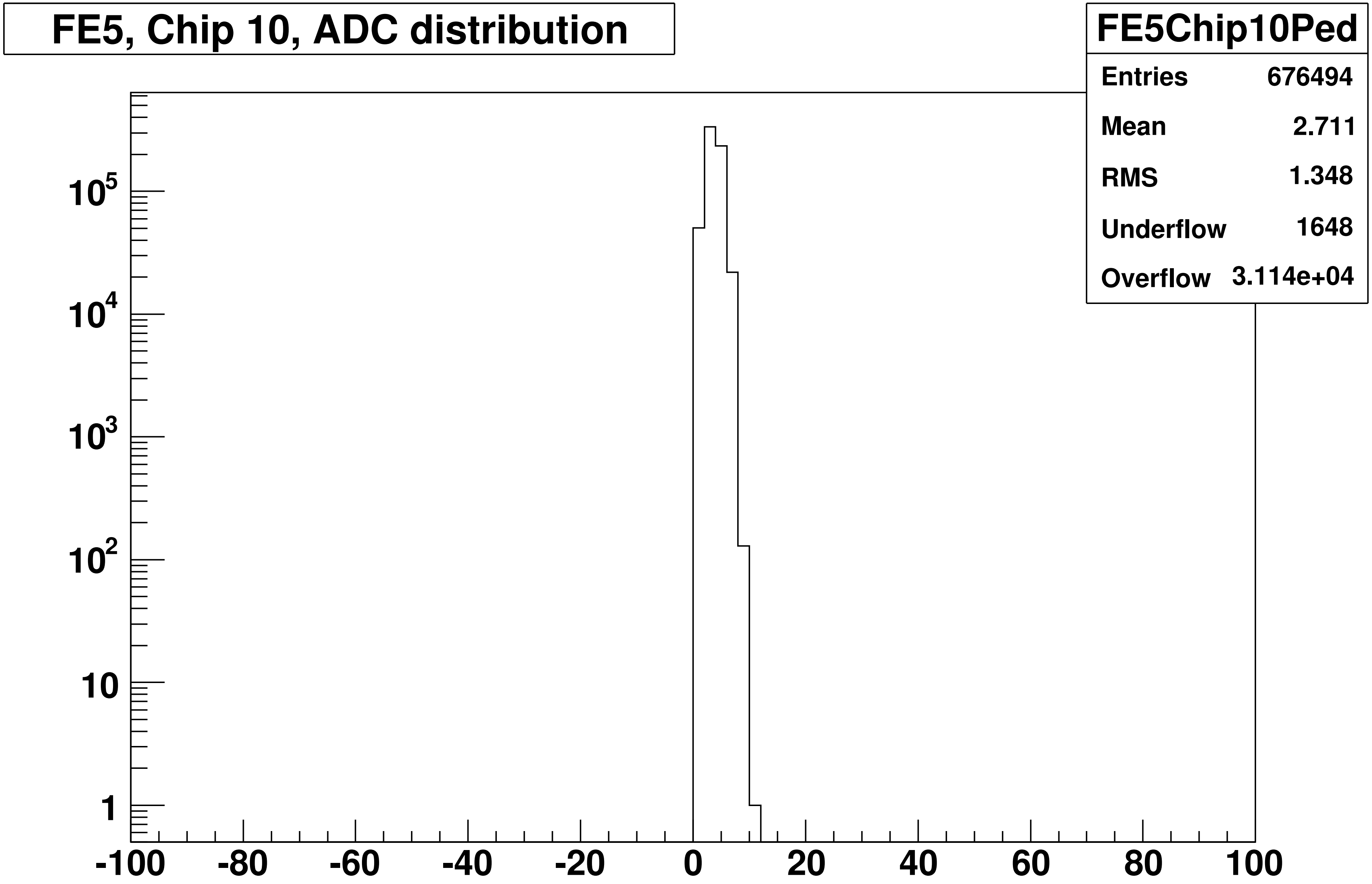
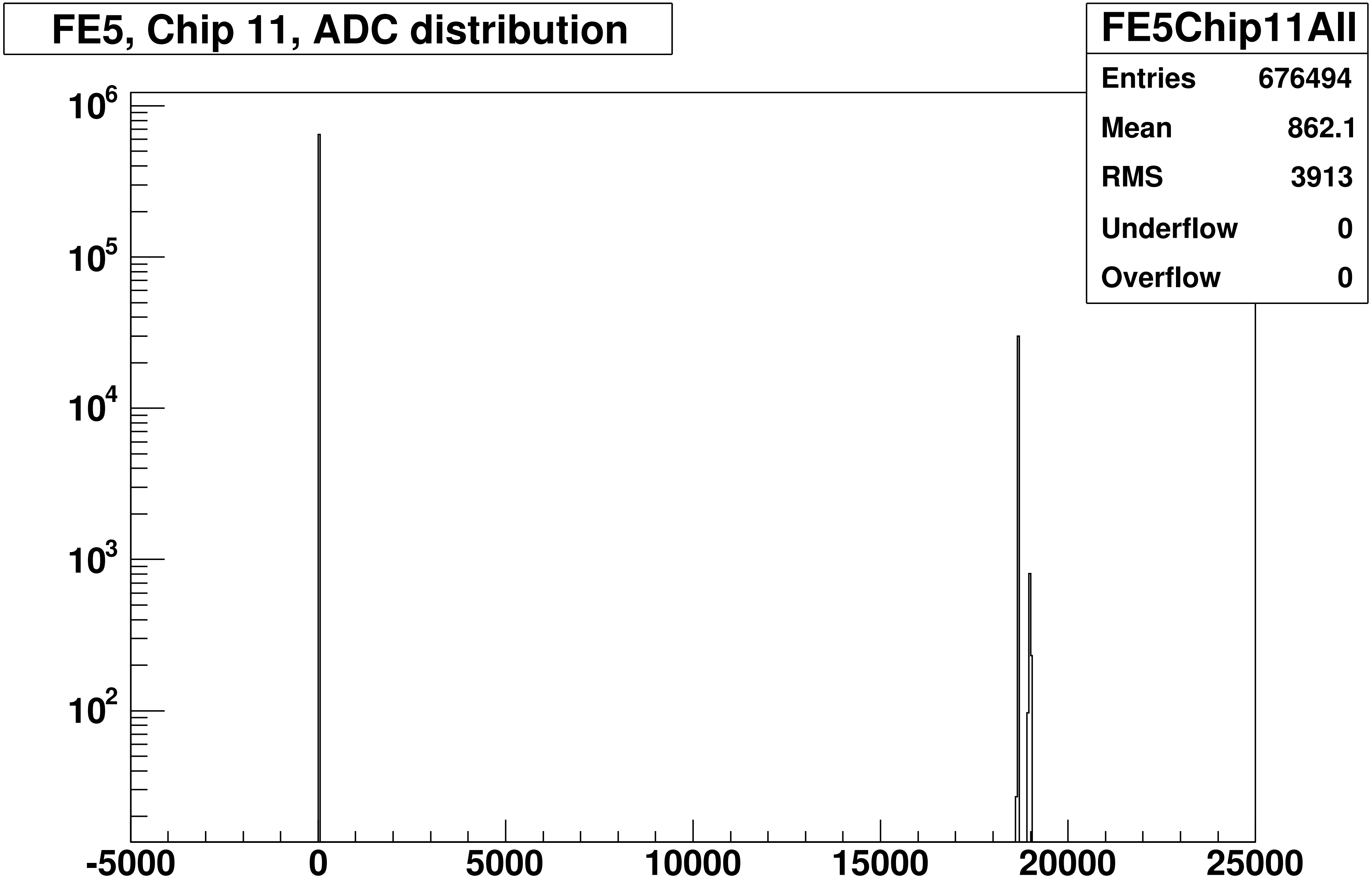
20000
25000

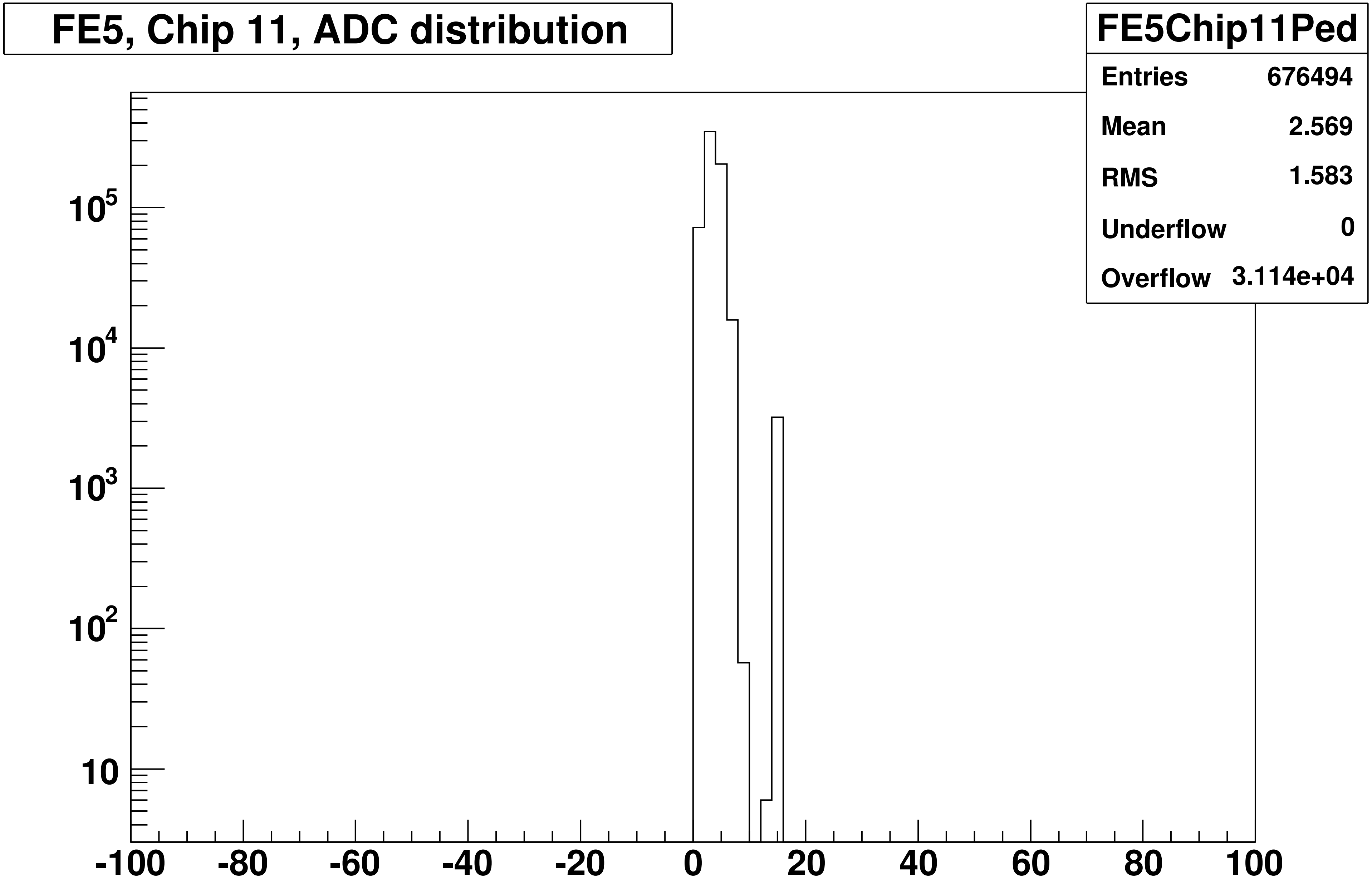

FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 676494
Mean 835.4
RMS 3878
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 676494
Mean 2.541
RMS 1.333
Underflow 2487
Overflow 3.112e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 676494
Mean 840.7
RMS 3872
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 676494
Mean 2.711
RMS 1.348
Underflow 1648
Overflow 3.114e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 676494
Mean 862.1
RMS 3913
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 676494
Mean 2.569
RMS 1.583
Underflow 0
Overflow 3.114e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
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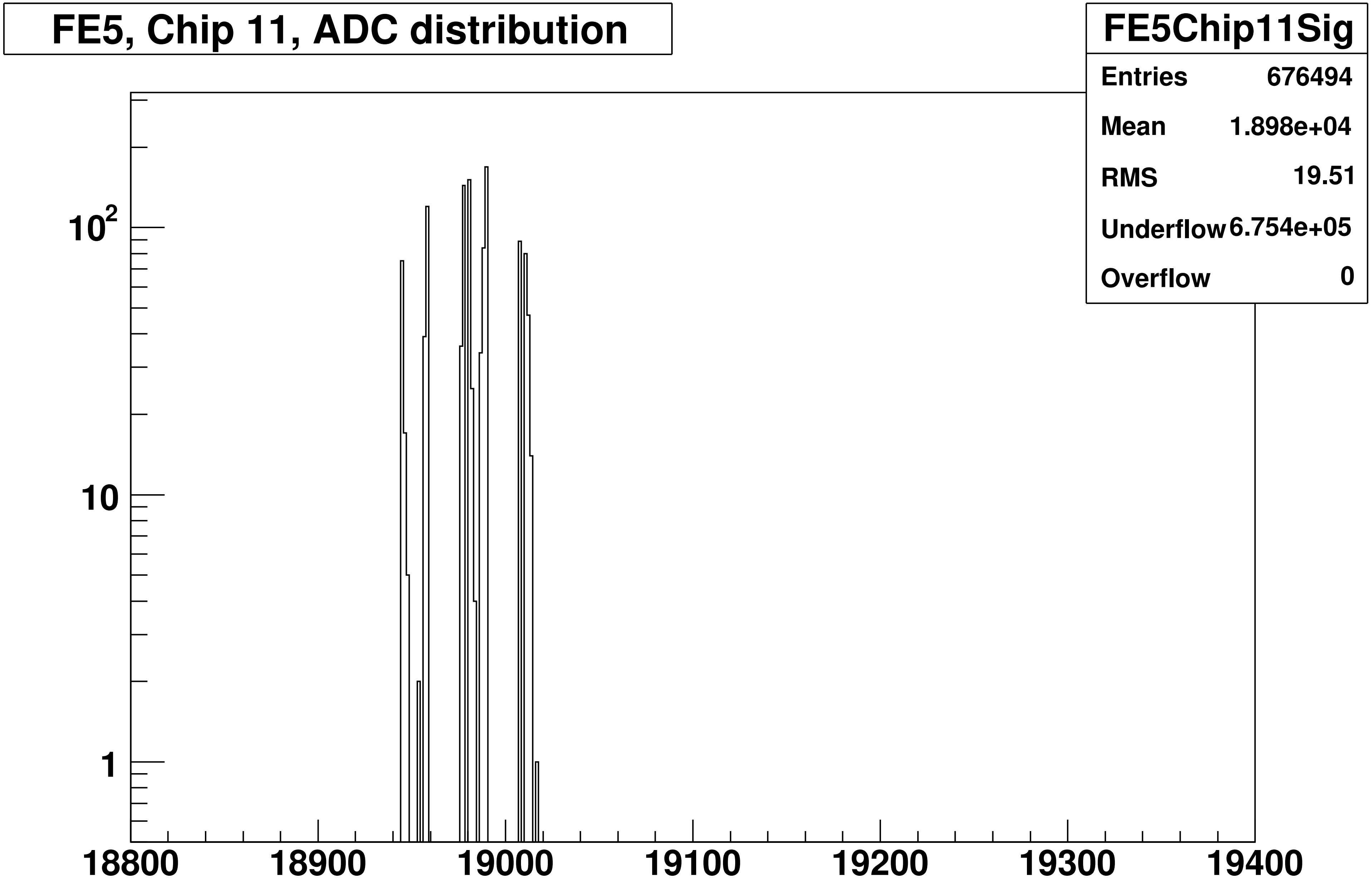
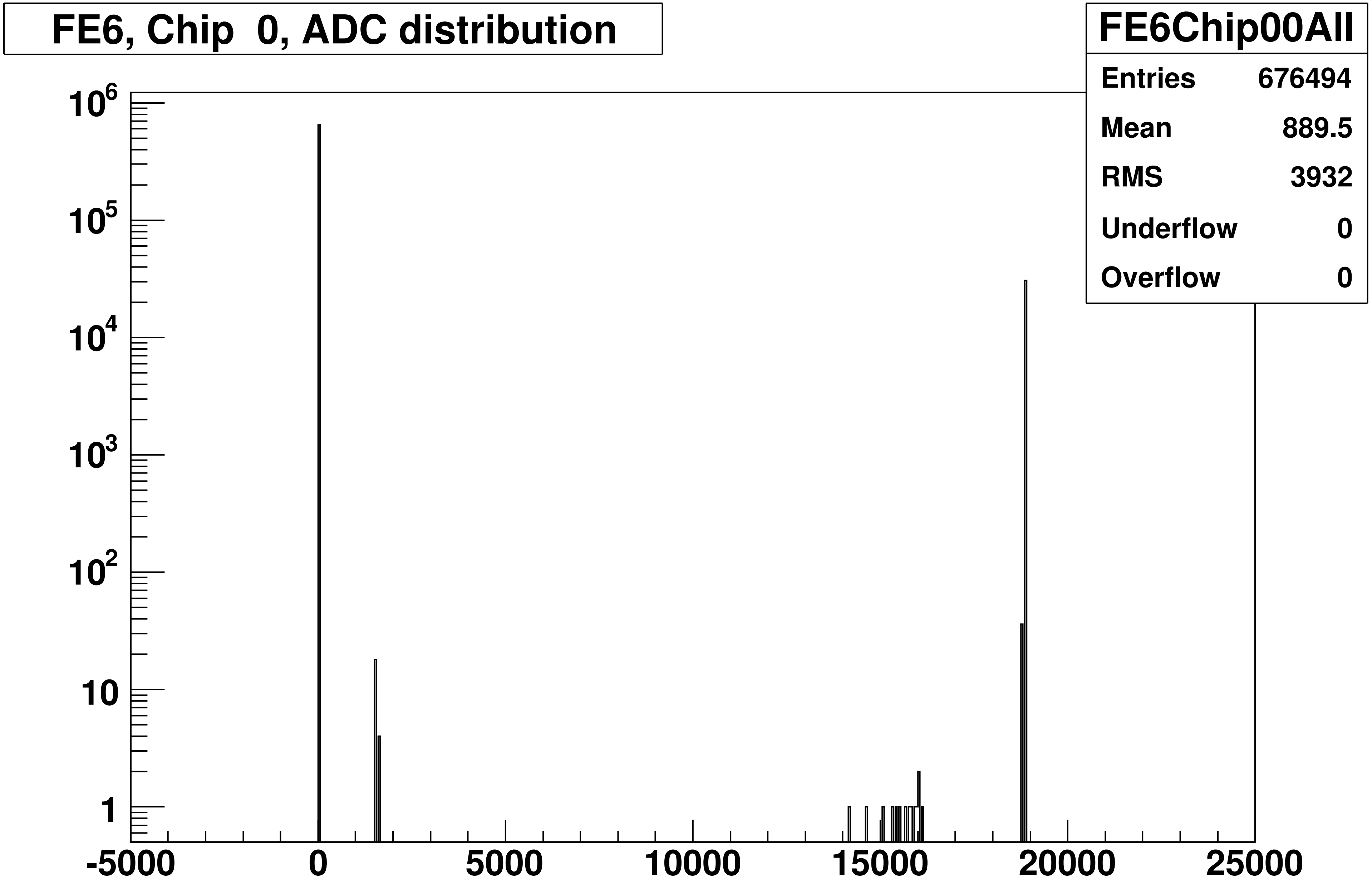
40
60
80
100


FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 676494
Mean 1.898e+04
RMS 19.51
Underflow 6.754e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 676494
Mean 889.5
RMS 3932
Underflow 0
Overflow 0
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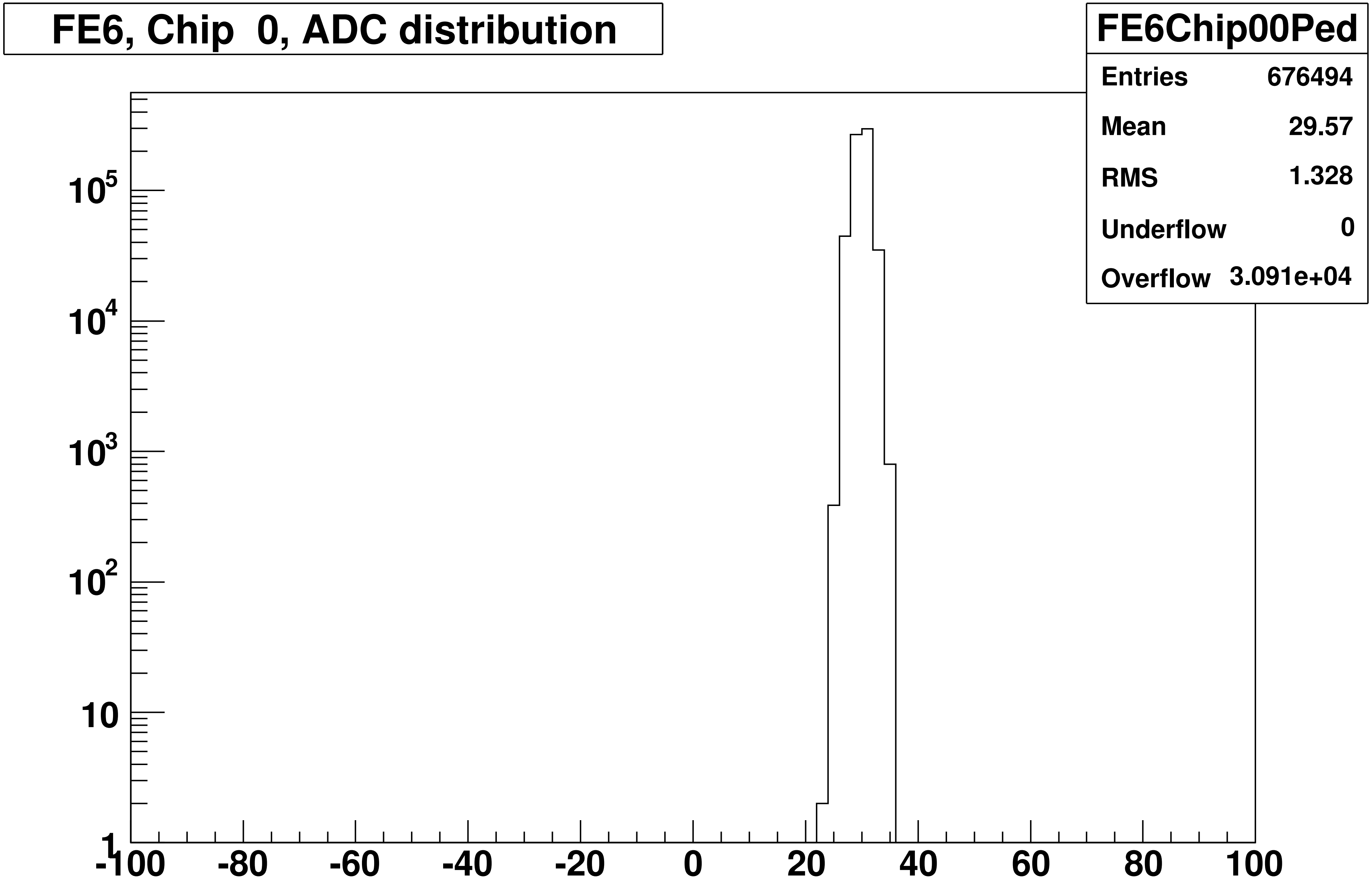
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 676494
Mean 29.57
RMS 1.328
Underflow 0
Overflow 3.091e+04
10^5
10^4
10^3
10^2
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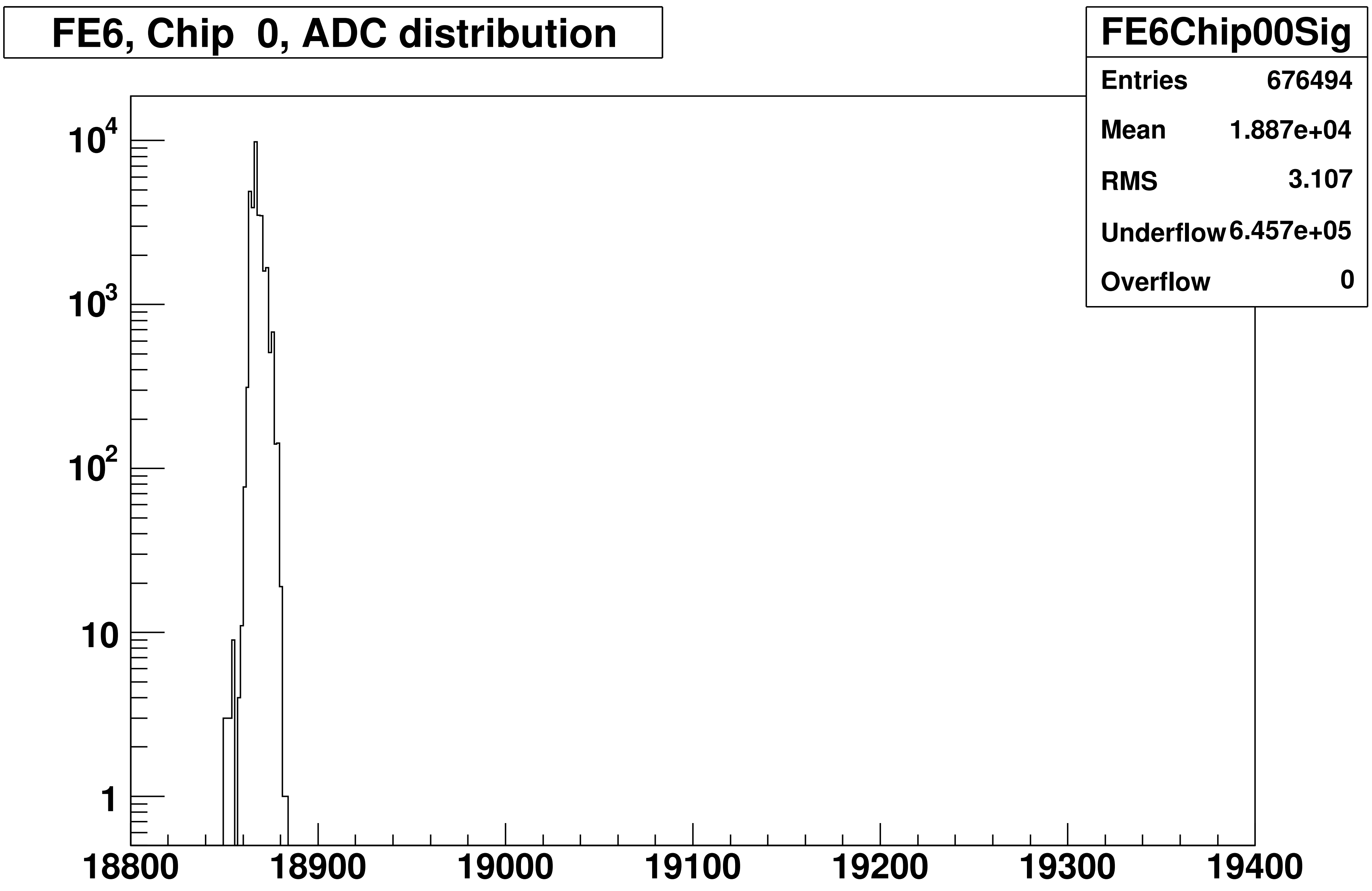
FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 676494
Mean 1.887e+04
RMS 3.107
Underflow 6.457e+05
Overflow 0
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10^3
10^2
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18800
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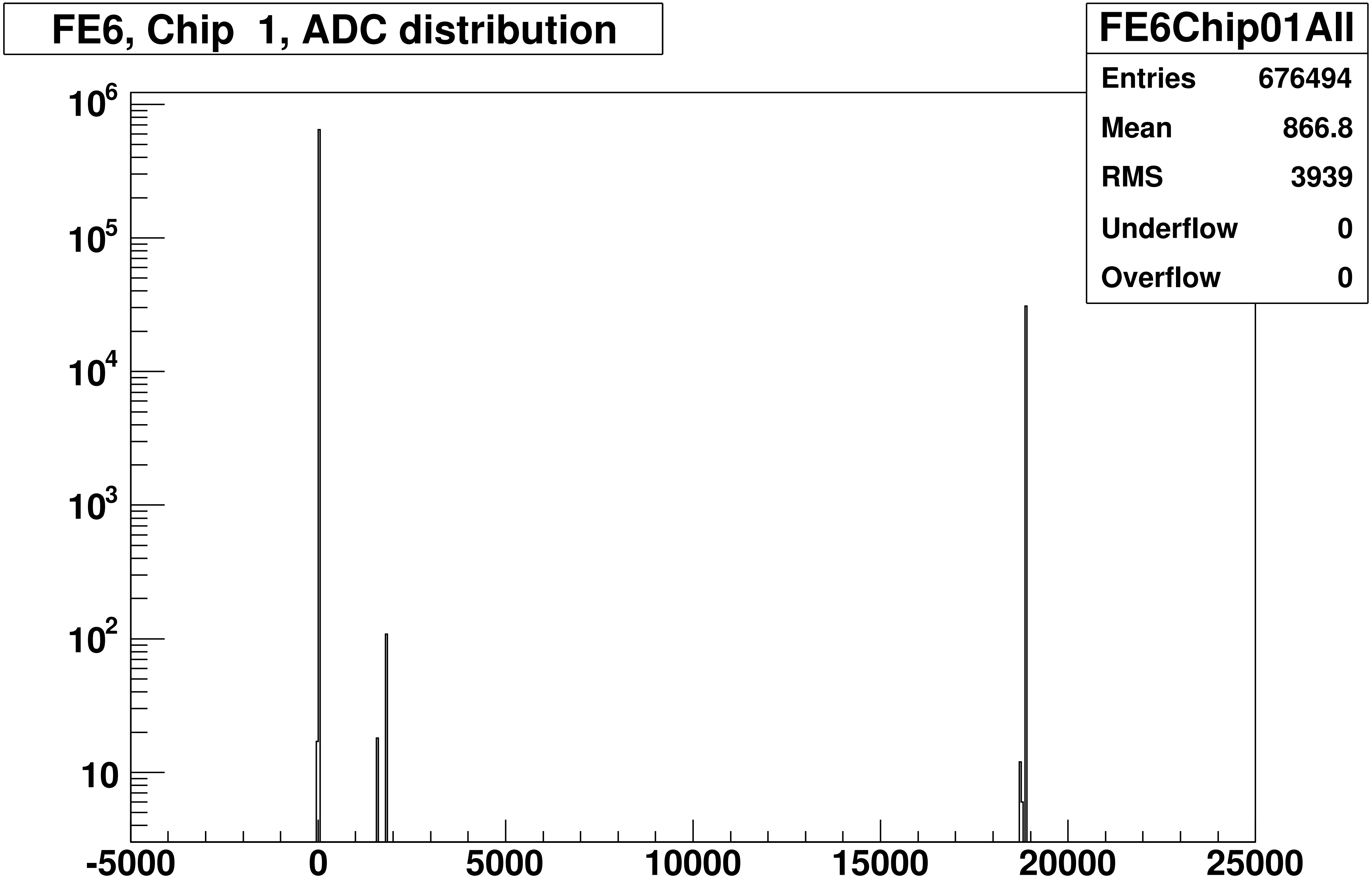
FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 676494
Mean 866.8
RMS 3939
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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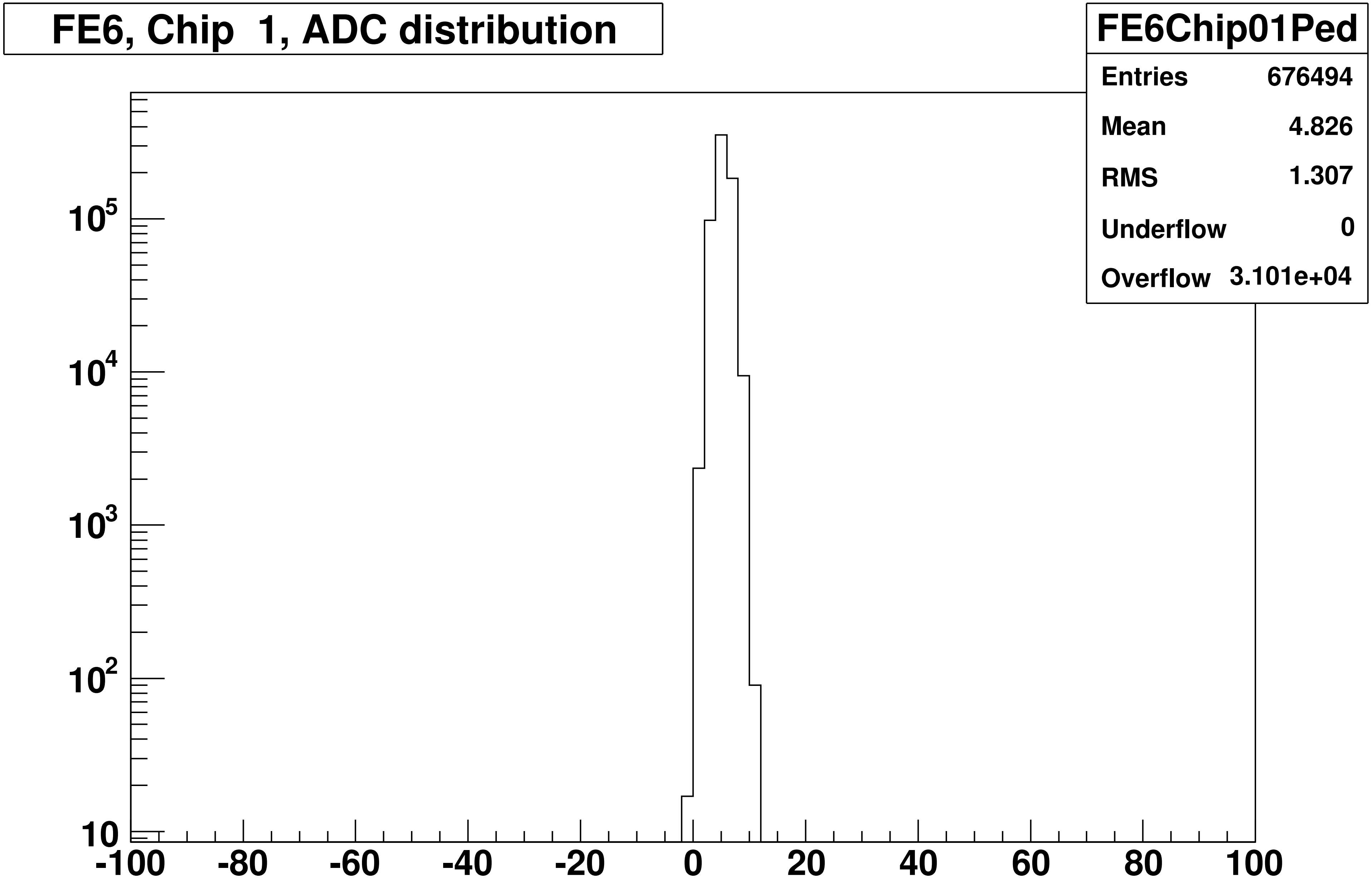
FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 676494
Mean 4.826
RMS 1.307
Underflow 0
Overflow 3.101e+04
10^5
10^4
10^3
10^2
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-80
-60
-40
-20
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40
60
80
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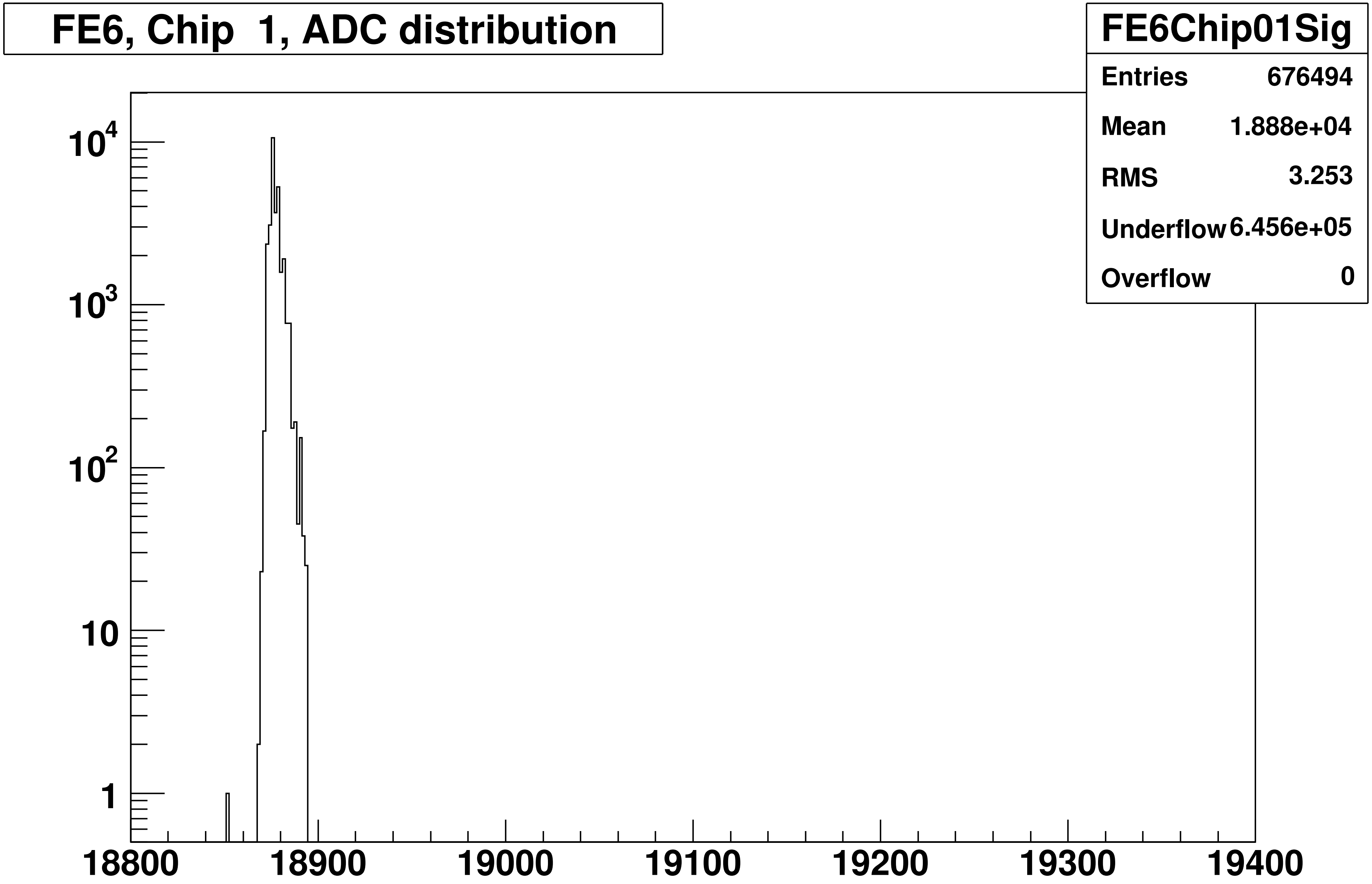

FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 676494
Mean 1.888e+04
RMS 3.253
Underflow 6.456e+05
Overflow 0
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10^3
10^2
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18900
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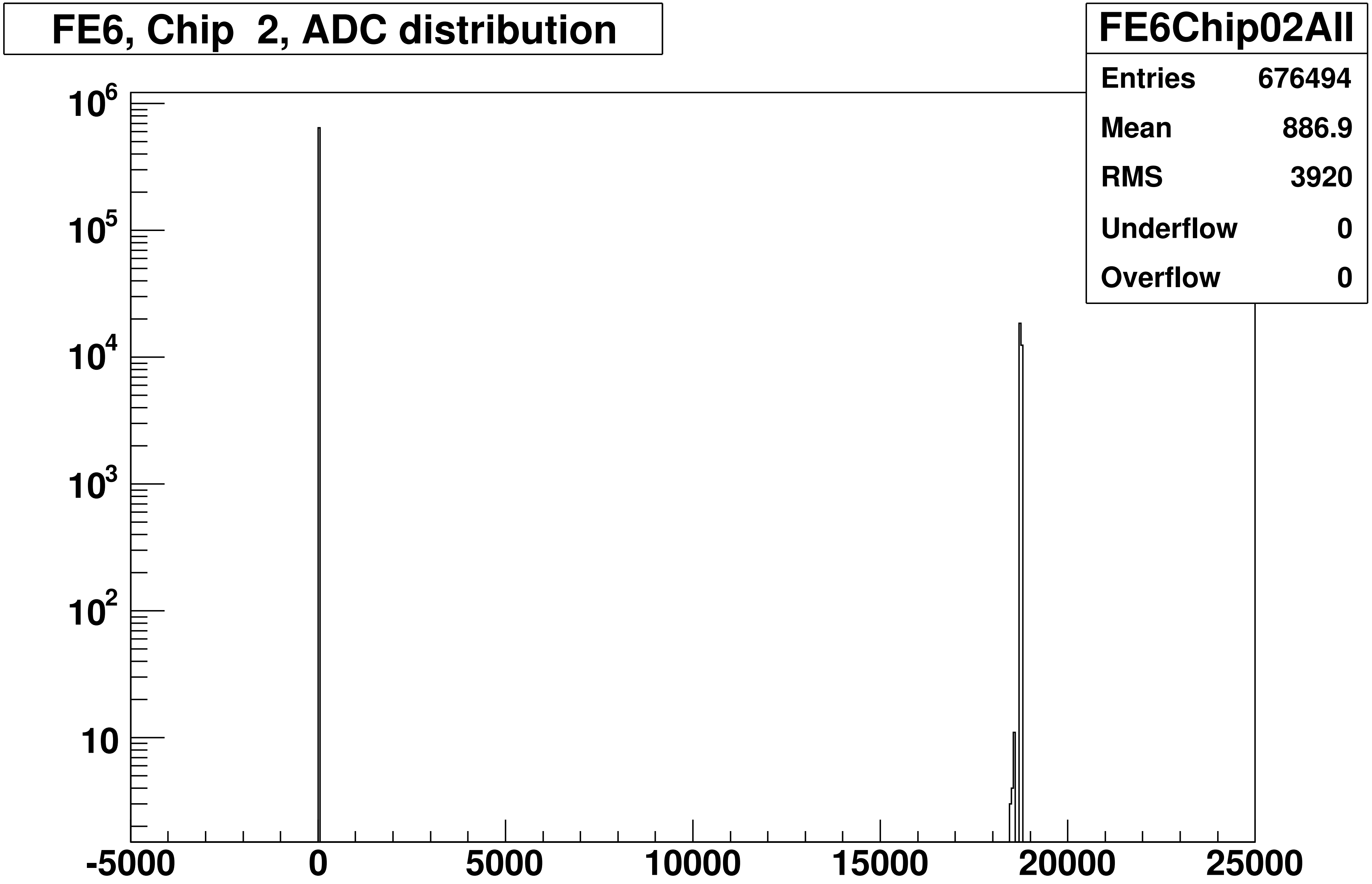
FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 676494
Mean 886.9
RMS 3920
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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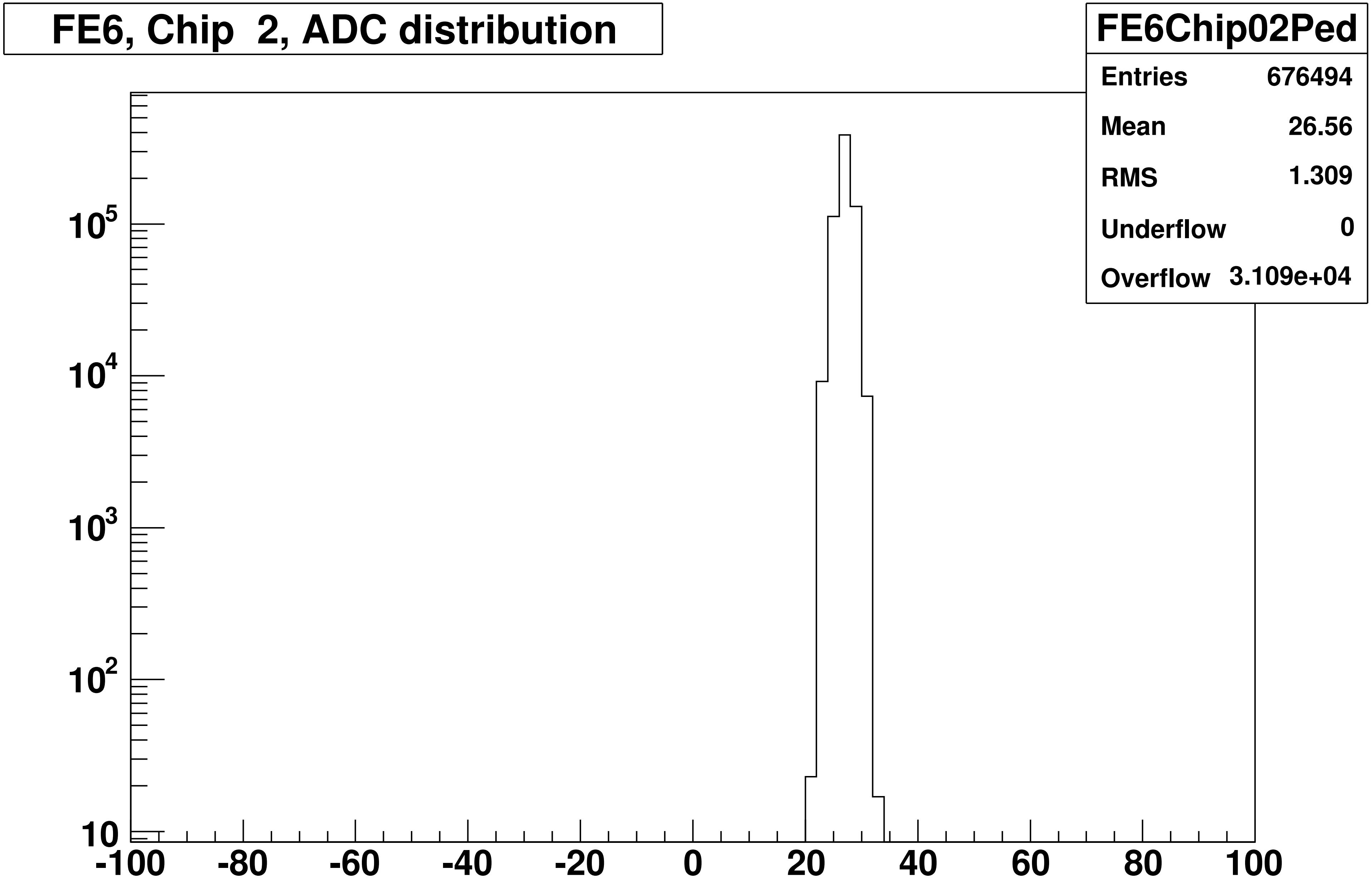
FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 676494
Mean 26.56
RMS 1.309
Underflow 0
Overflow 3.109e+04
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10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
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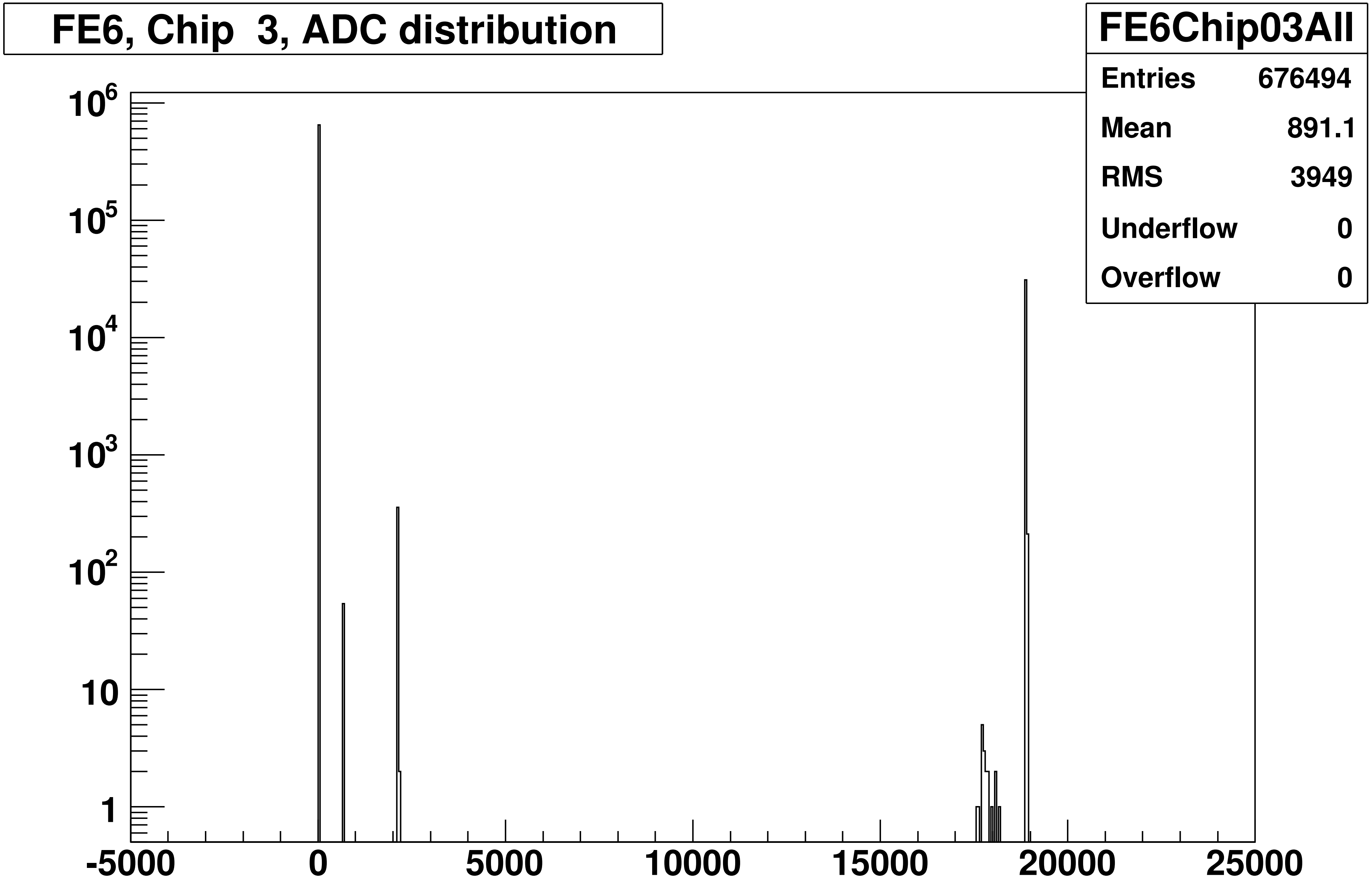

FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 676494
Mean 891.1
RMS 3949
Underflow 0
Overflow 0
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10^4
10^3
10^2
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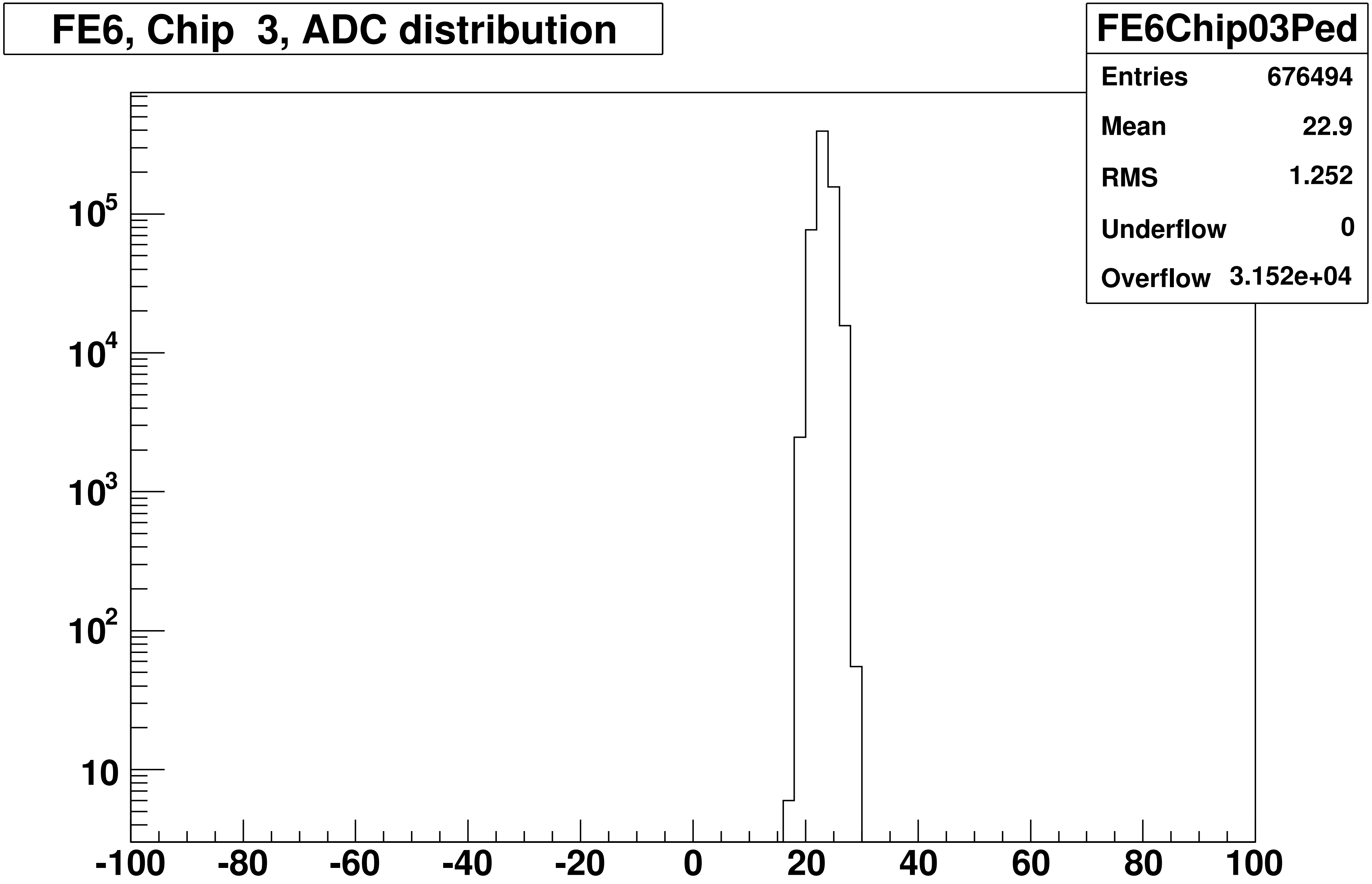
FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 676494
Mean 22.9
RMS 1.252
Underflow 0
Overflow 3.152e+04
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10^4
10^3
10^2
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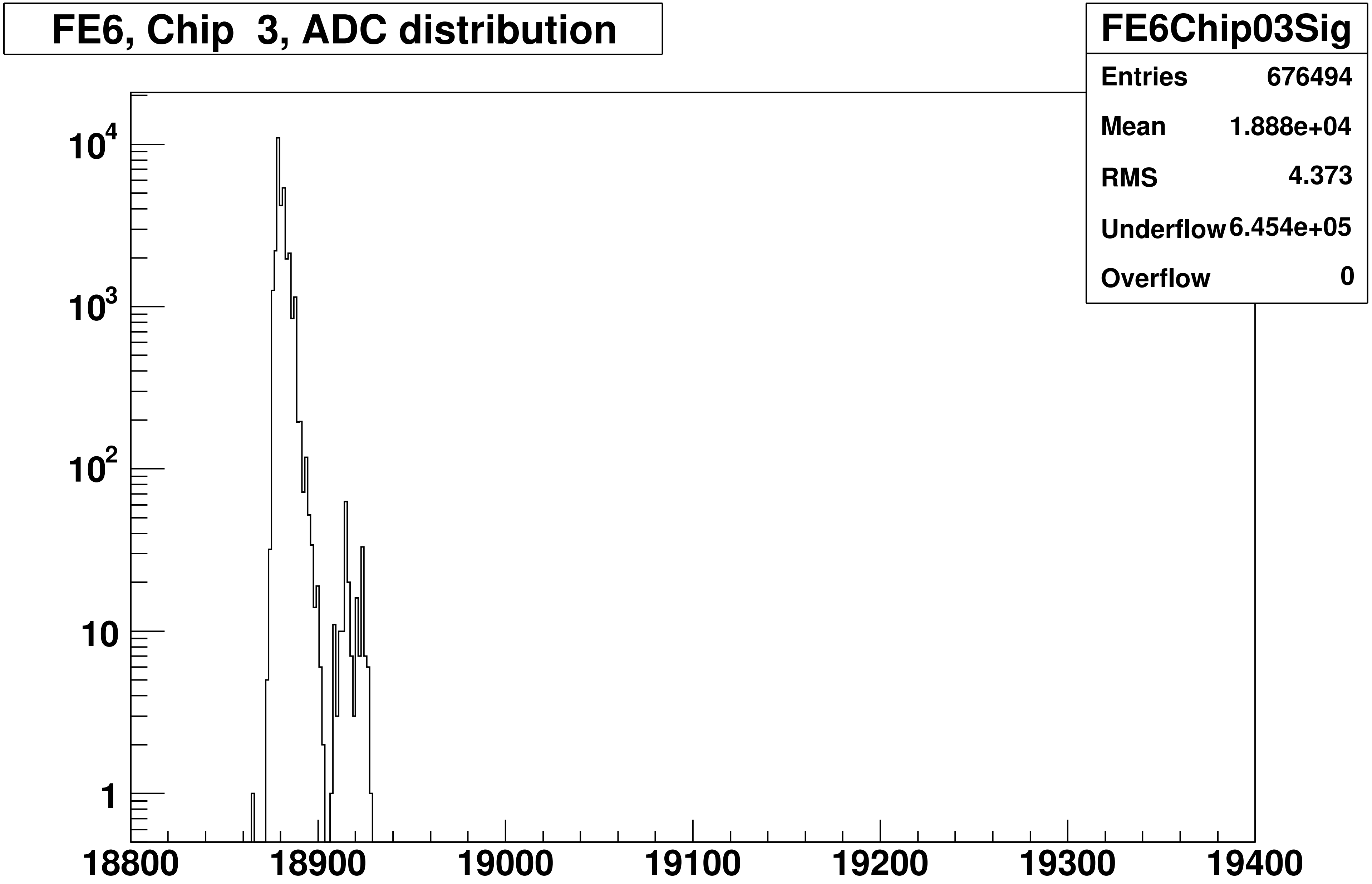

FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 676494
Mean 1.888e+04
RMS 4.373
Underflow 6.454e+05
Overflow 0
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10^3
10^2
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18900
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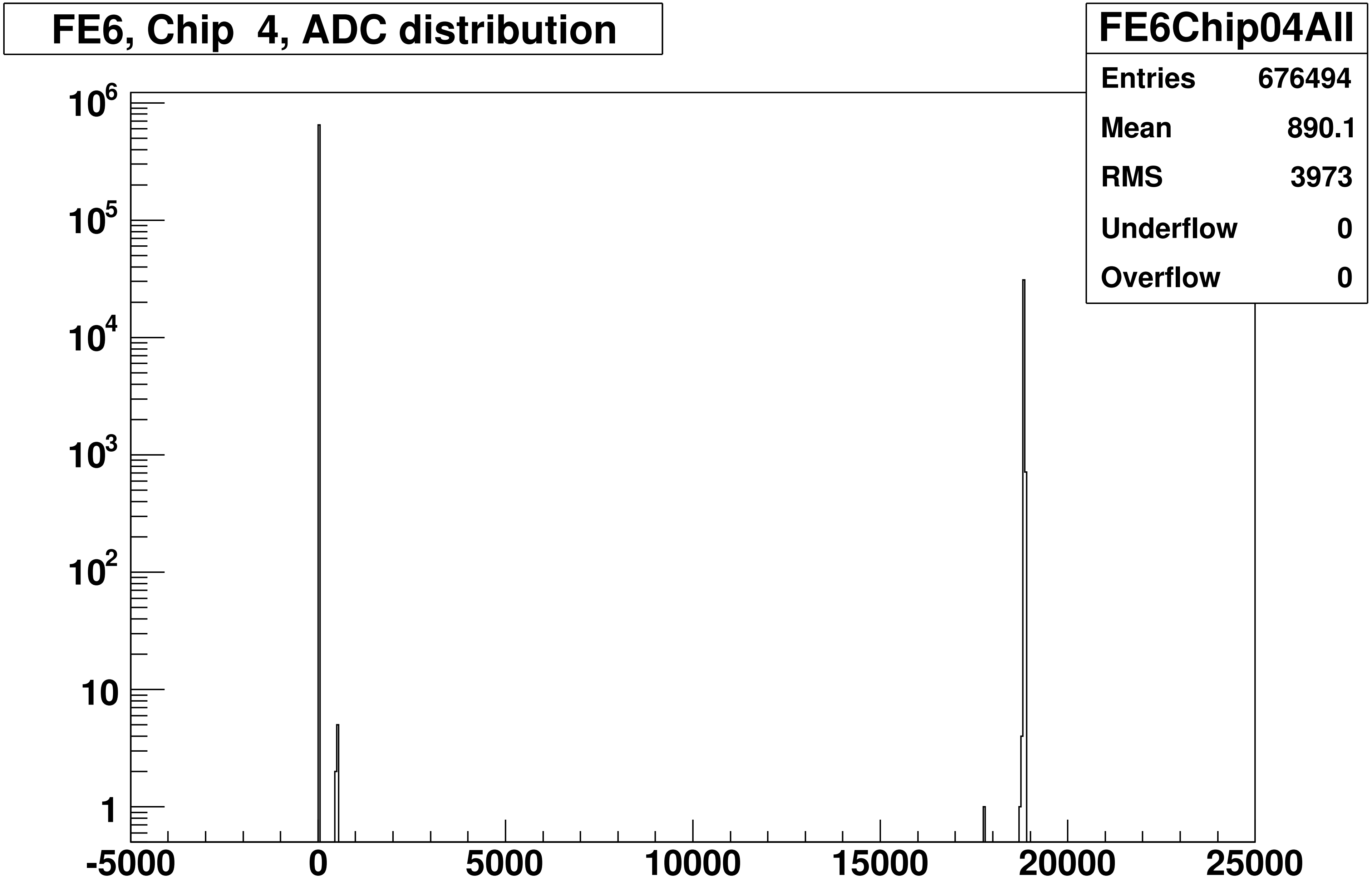
FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 676494
Mean 890.1
RMS 3973
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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-5000
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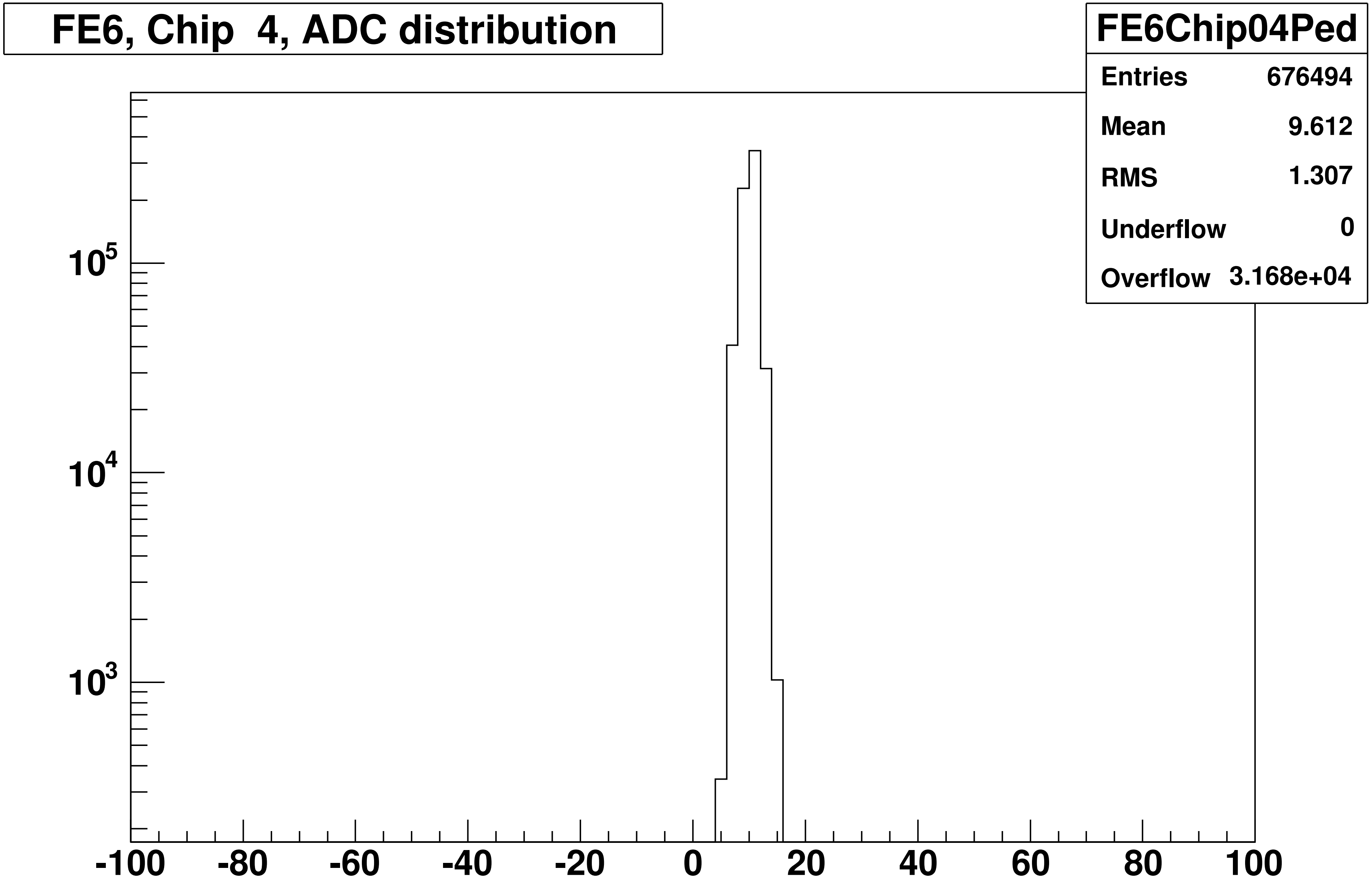
FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 676494
Mean 9.612
RMS 1.307
Underflow 0
Overflow 3.168e+04
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10^4
10^3
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-60
-40
-20
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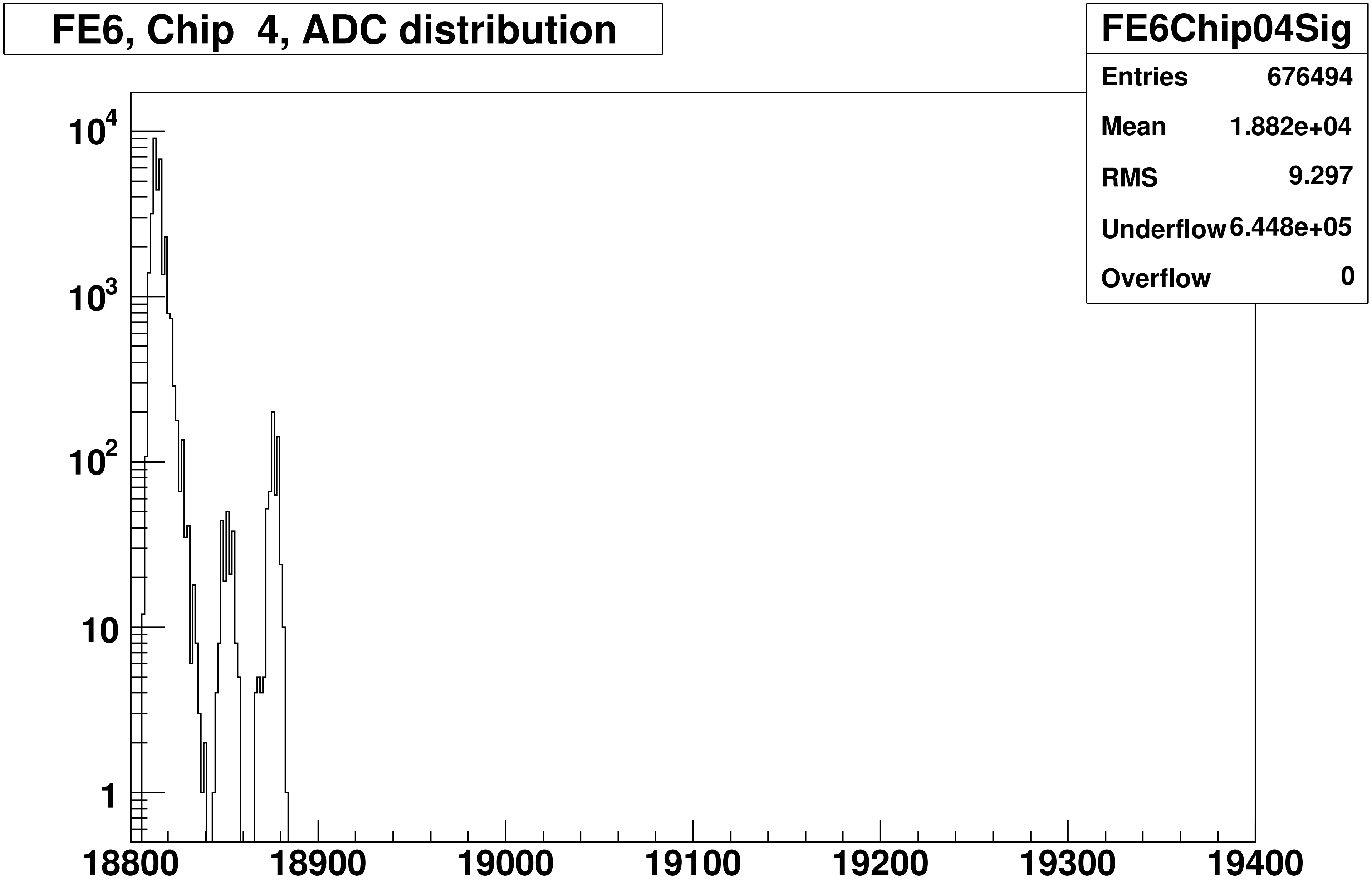
FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 676494
Mean 1.882e+04
RMS 9.297
Underflow 6.448e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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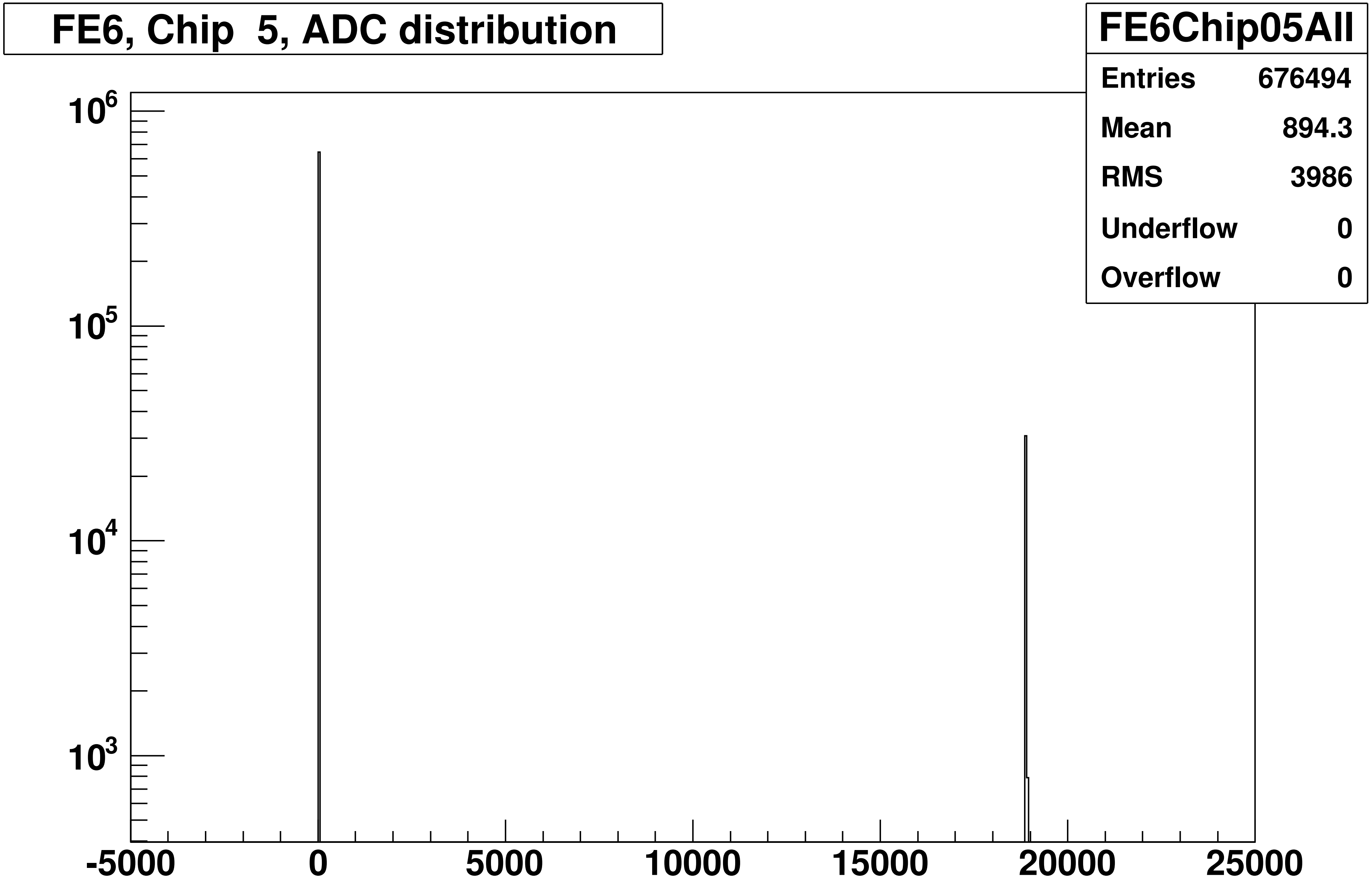
19100
19200
19300
19400



FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 676494
Mean 894.3
RMS 3986
Underflow 0
Overflow 0
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10^5
10^4
10^3
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5000
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20000
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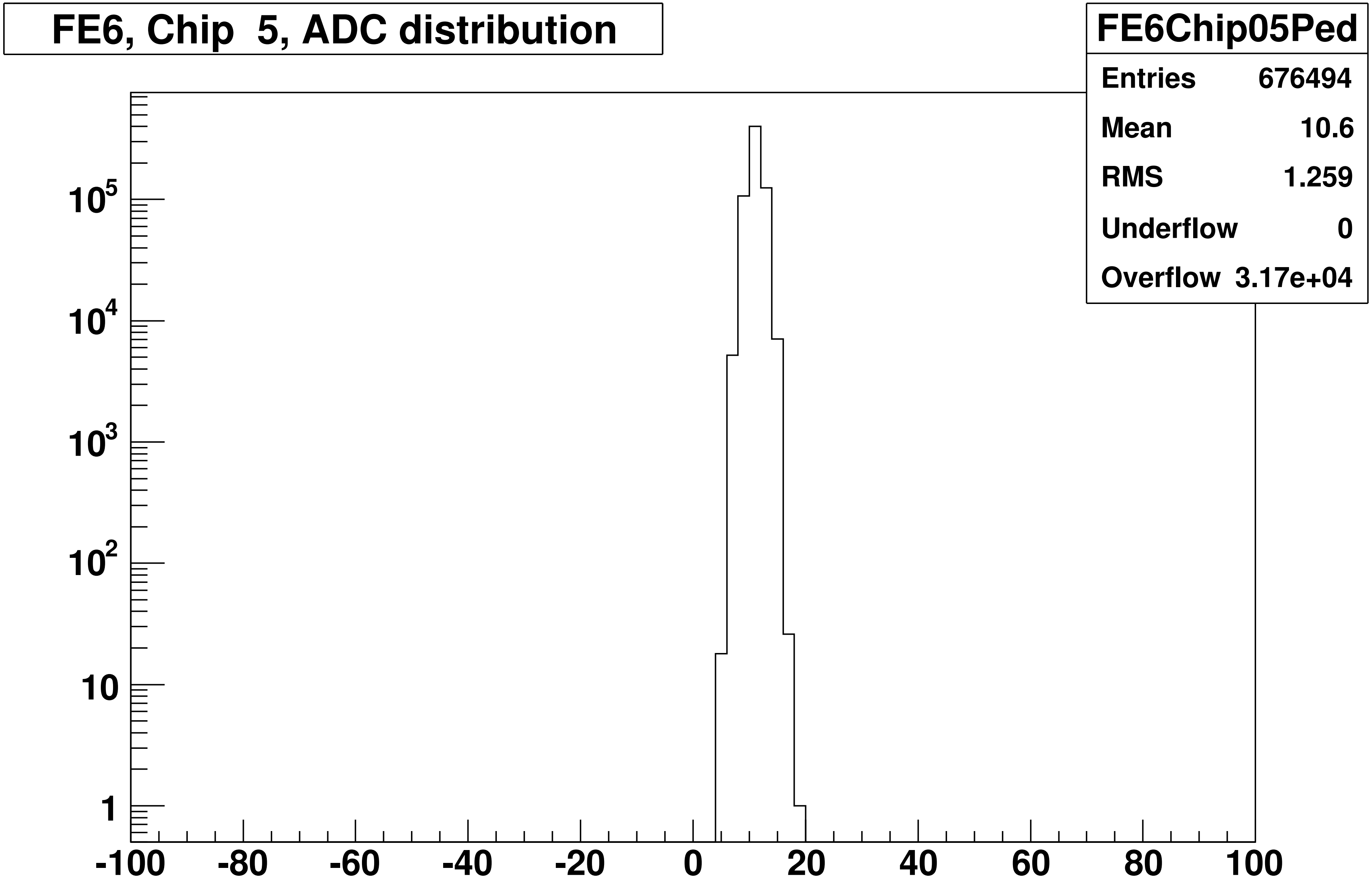

FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 676494
Mean 10.6
RMS 1.259
Underflow 0
Overflow 3.17e+04
10^5
10^4
10^3
10^2
10
1
-100
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80
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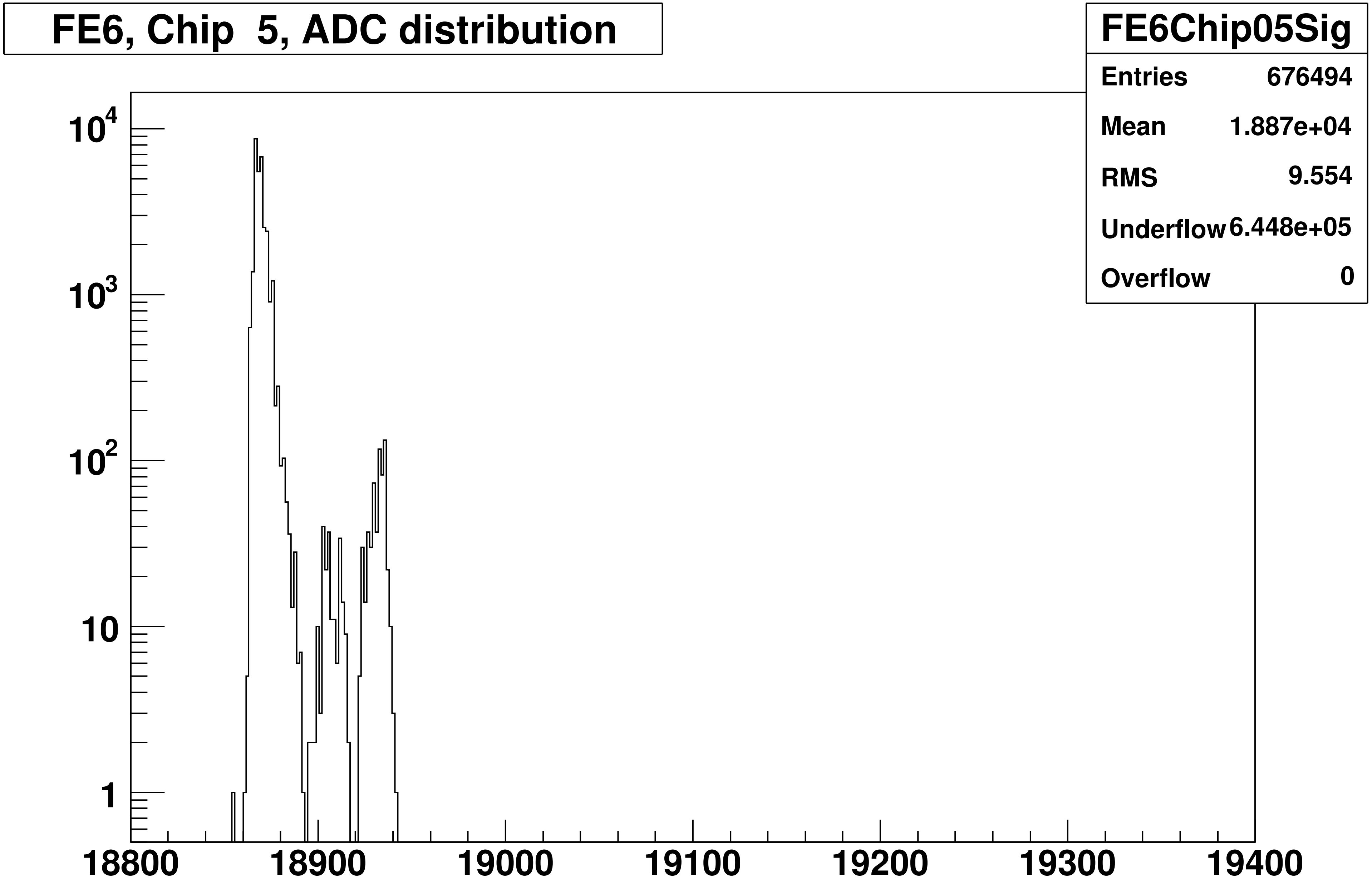
FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 676494
Mean 1.887e+04
RMS 9.554
Underflow 6.448e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
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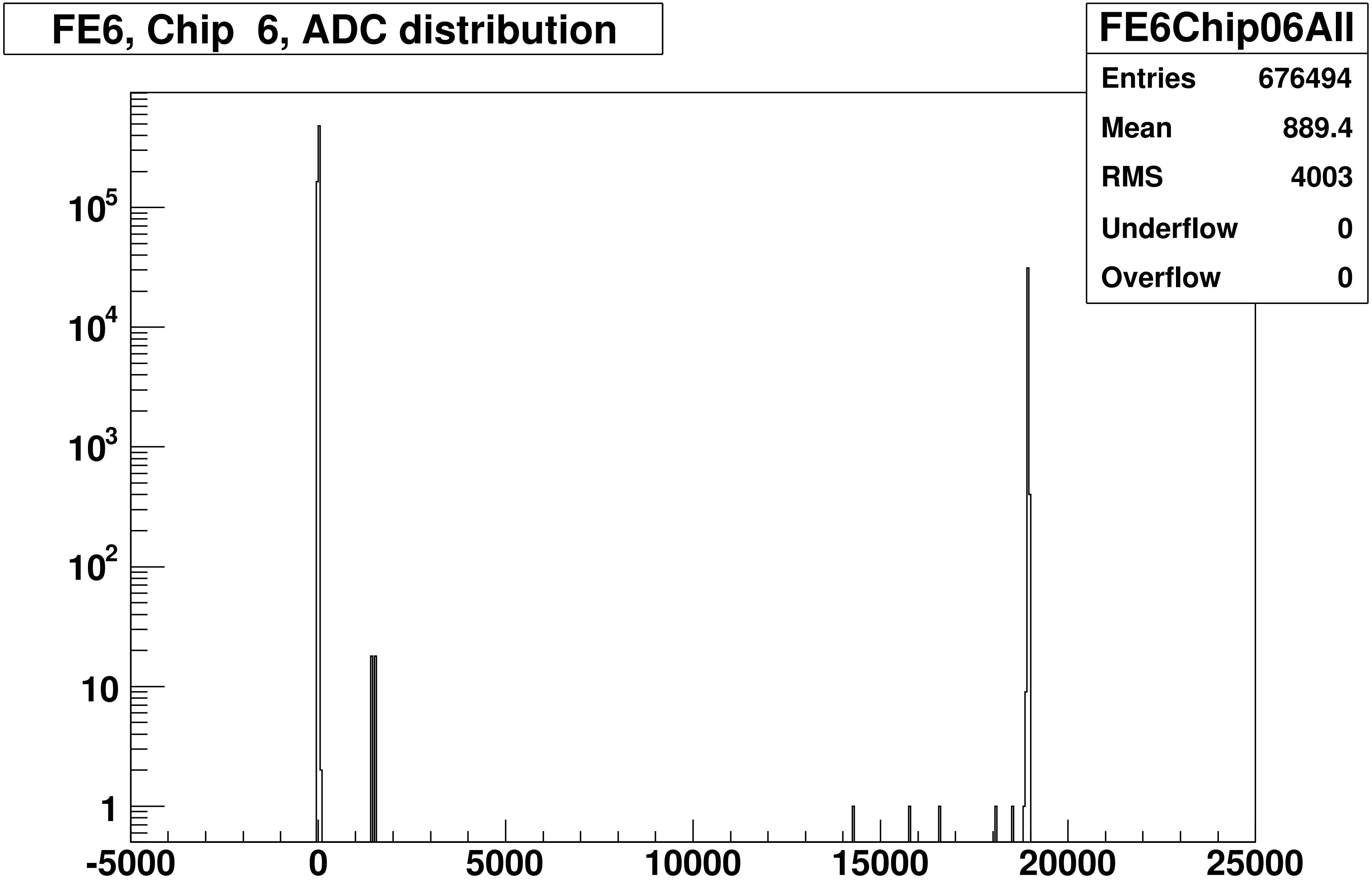
FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 676494
Mean 889.4
RMS 4003
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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10000
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20000
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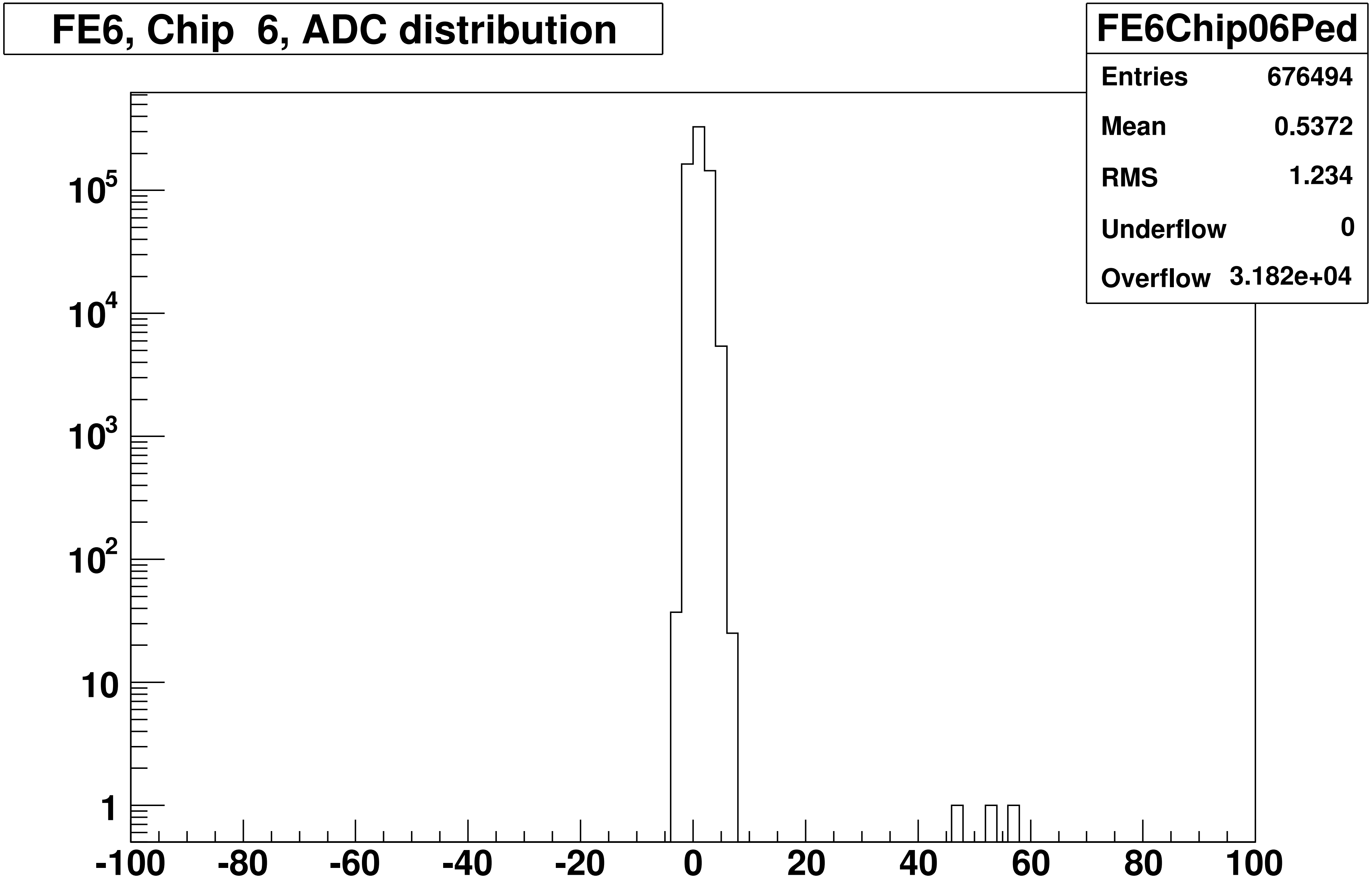
FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 676494
Mean 0.5372
RMS 1.234
Underflow 0
Overflow 3.182e+04
10^5
10^4
10^3
10^2
10
1
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-40
-20
0
20
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80
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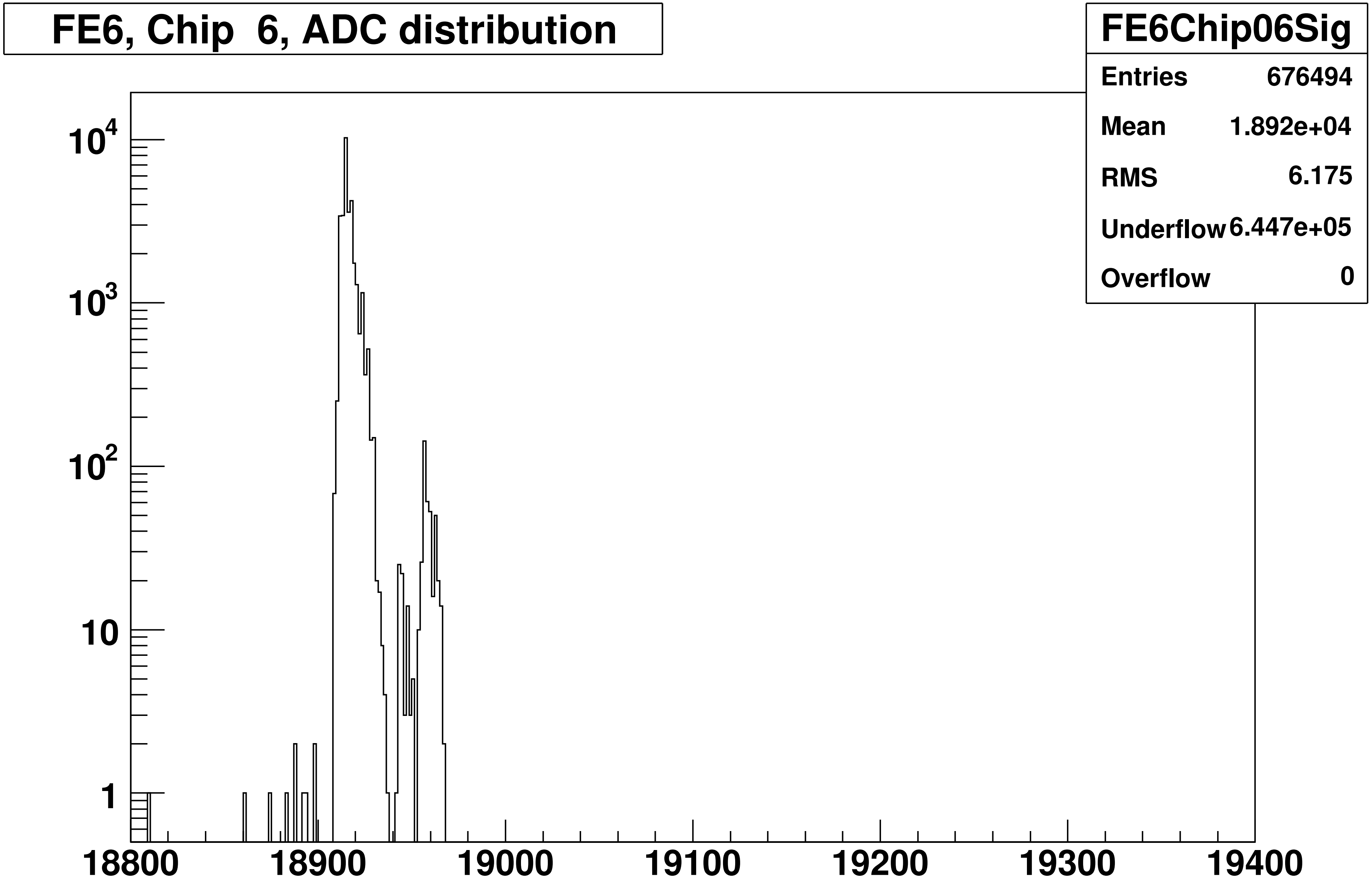

FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 676494
Mean 1.892e+04
RMS 6.175
Underflow 6.447e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
19100
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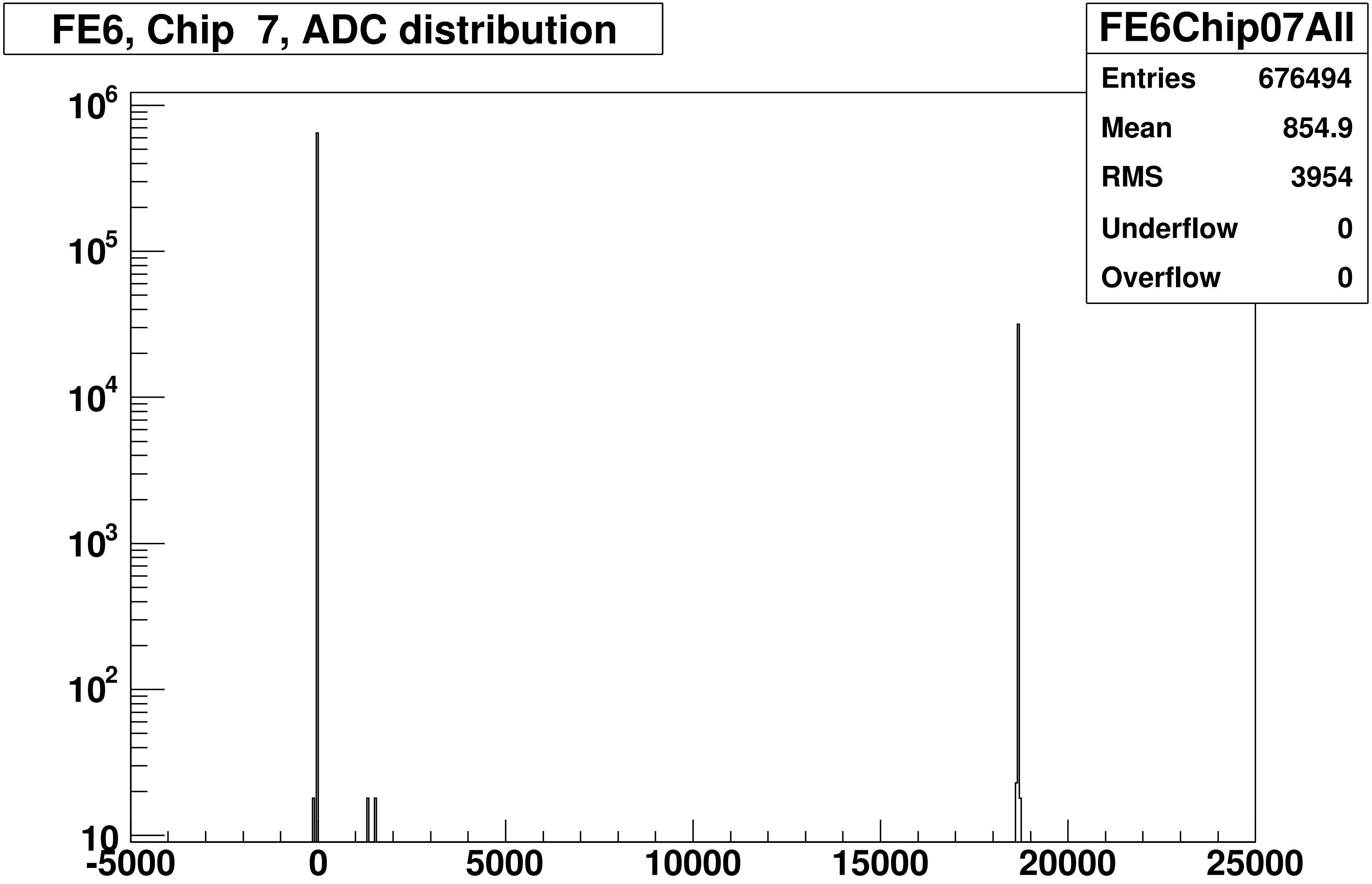
19400



FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 676494
Mean 854.9
RMS 3954
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
-5000
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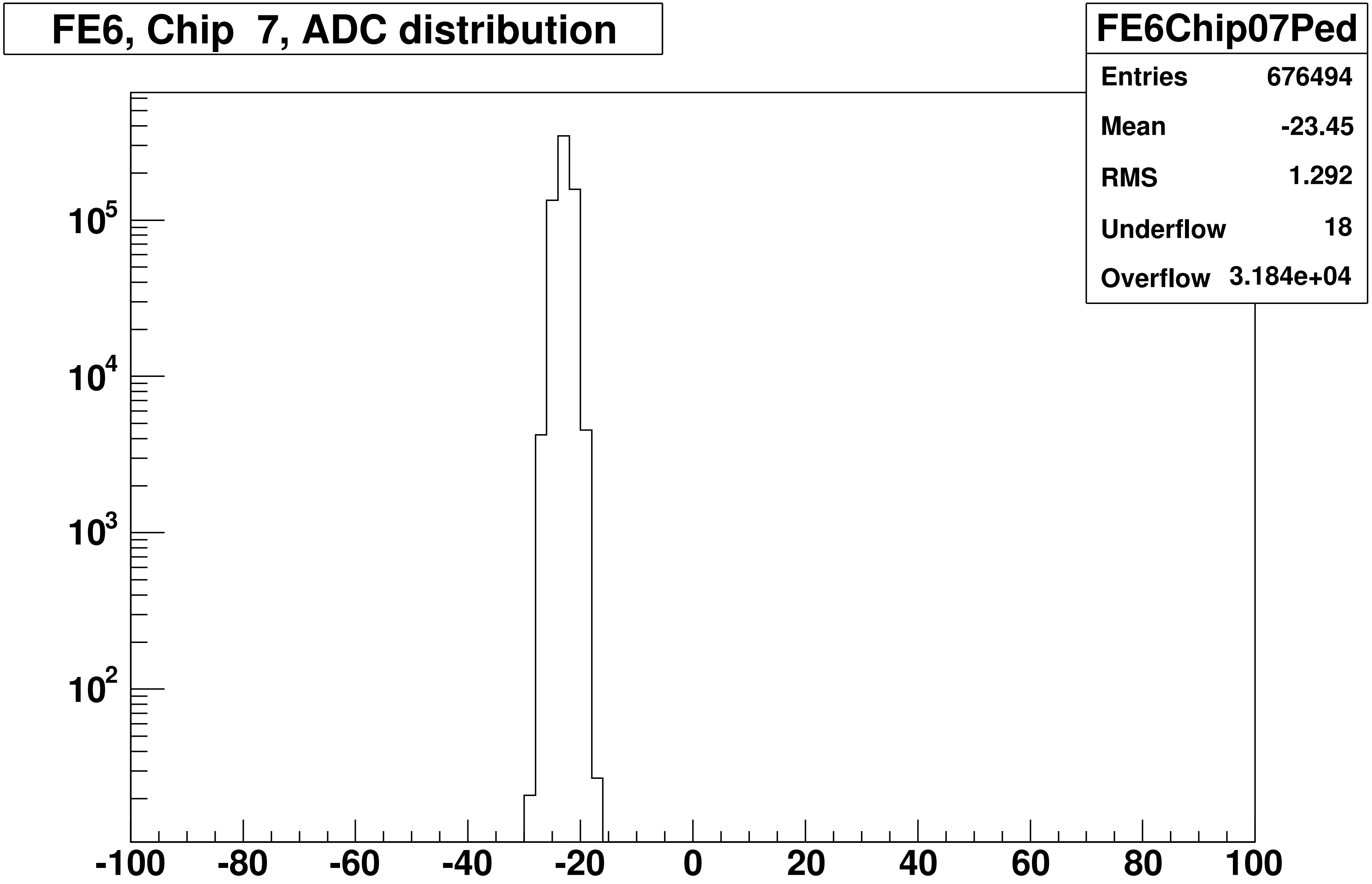
10000
15000
20000
25000


FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 676494
Mean -23.45
RMS 1.292
Underflow 18
Overflow 3.184e+04
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10^4
10^3
10^2
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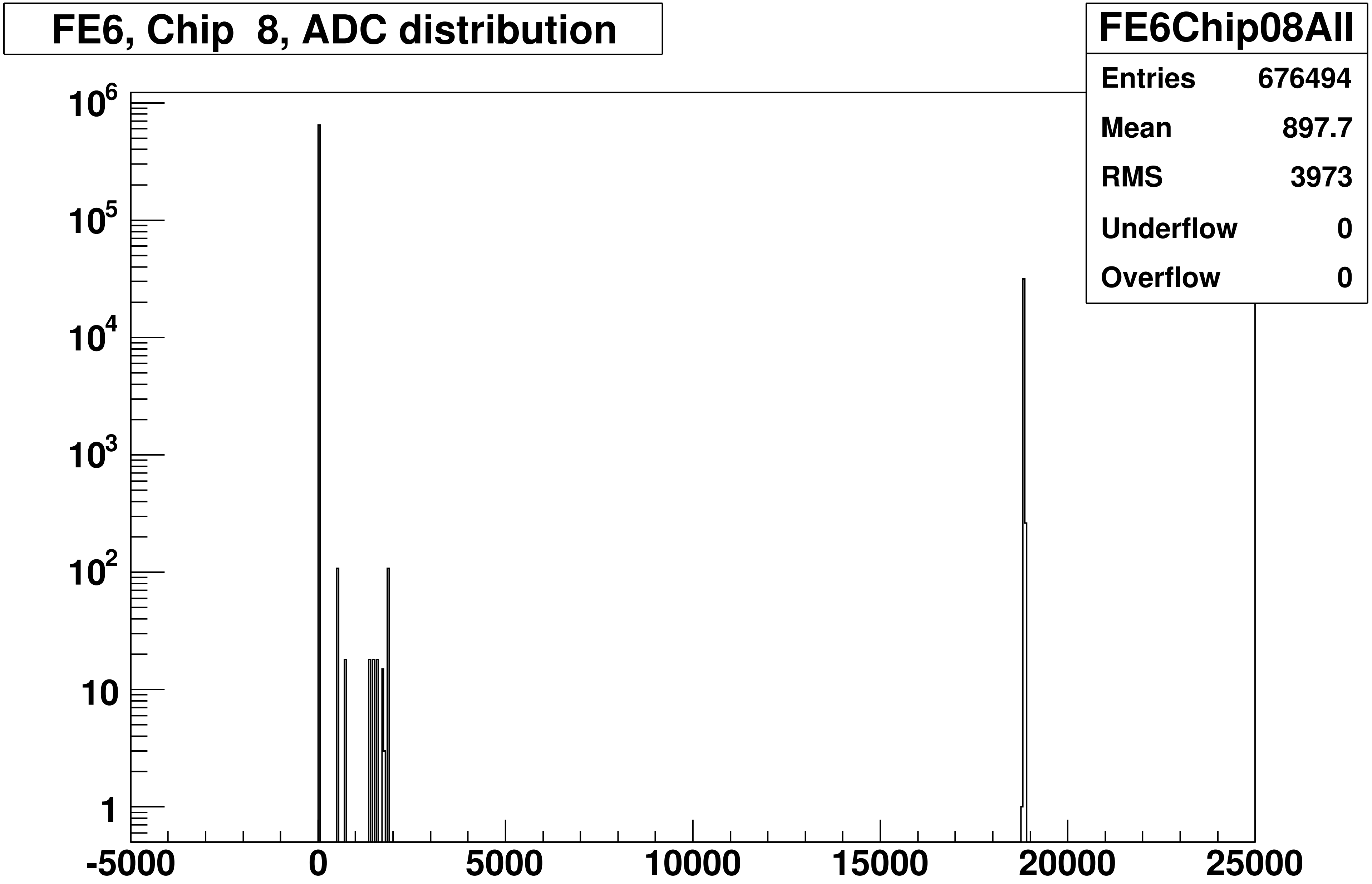
FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 676494
Mean 897.7
RMS 3973
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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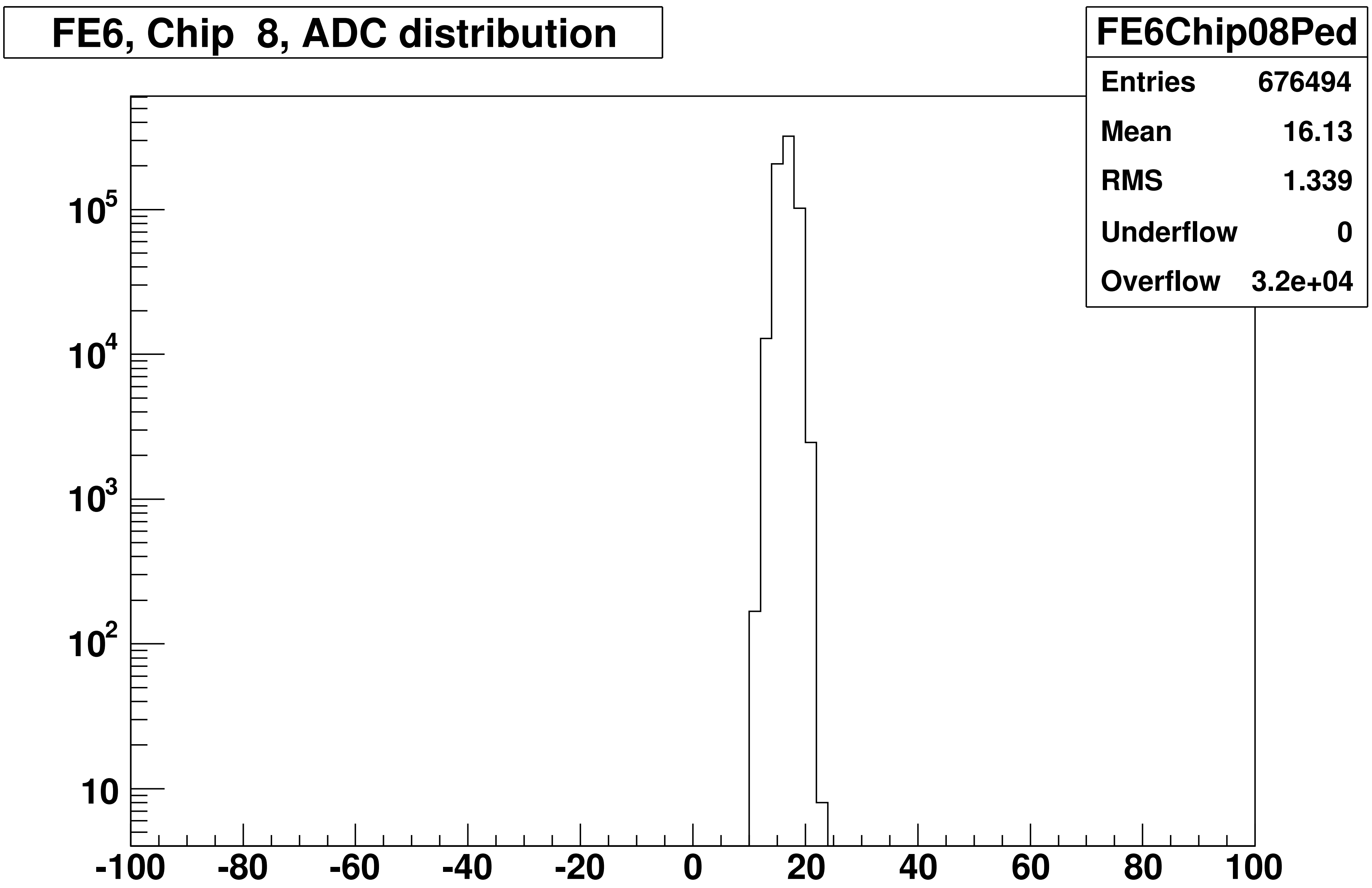
FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 676494
Mean 16.13
RMS 1.339
Underflow 0
Overflow 3.2e+04
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10^4
10^3
10^2
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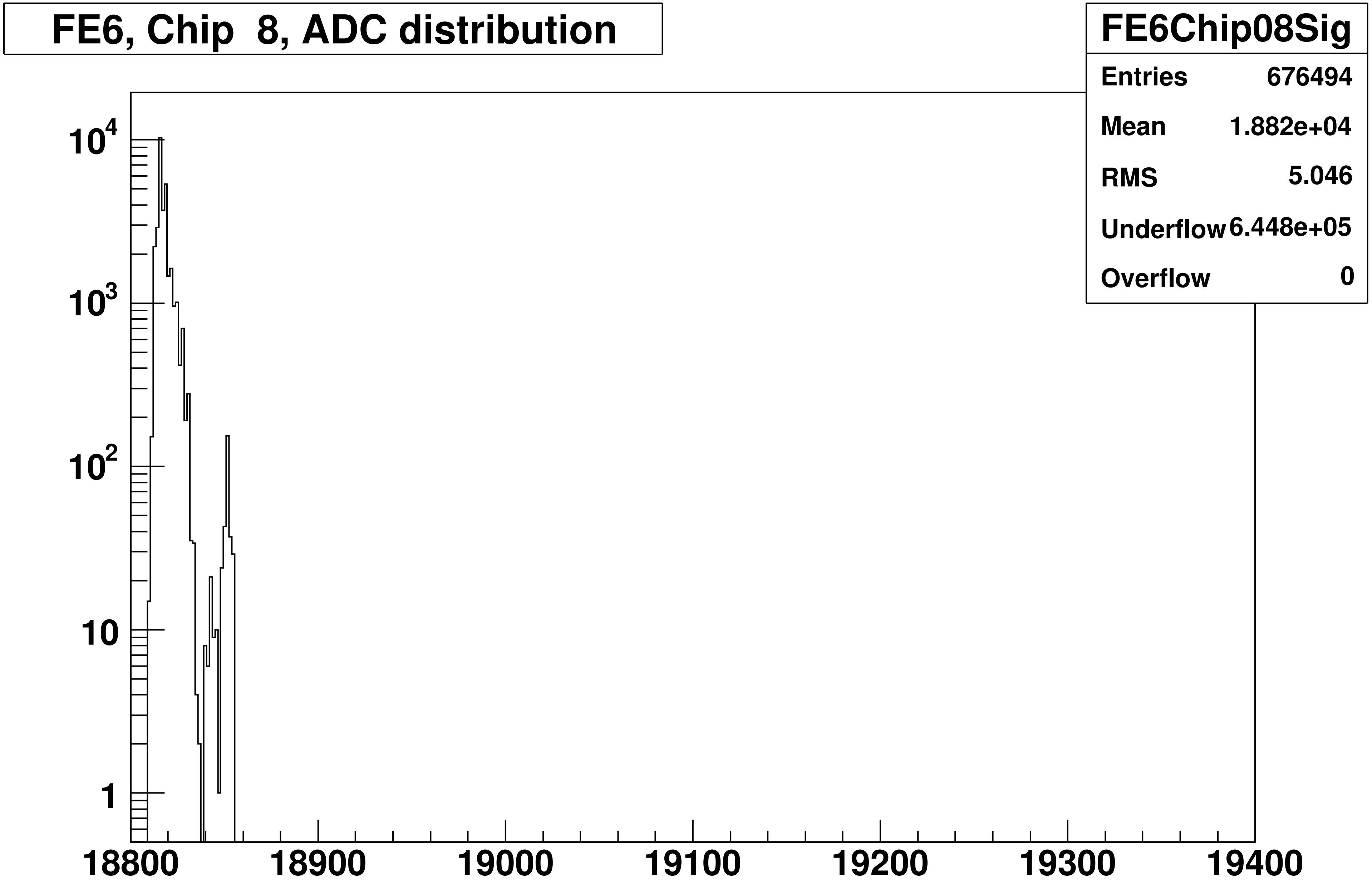

FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 676494
Mean 1.882e+04
RMS 5.046
Underflow 6.448e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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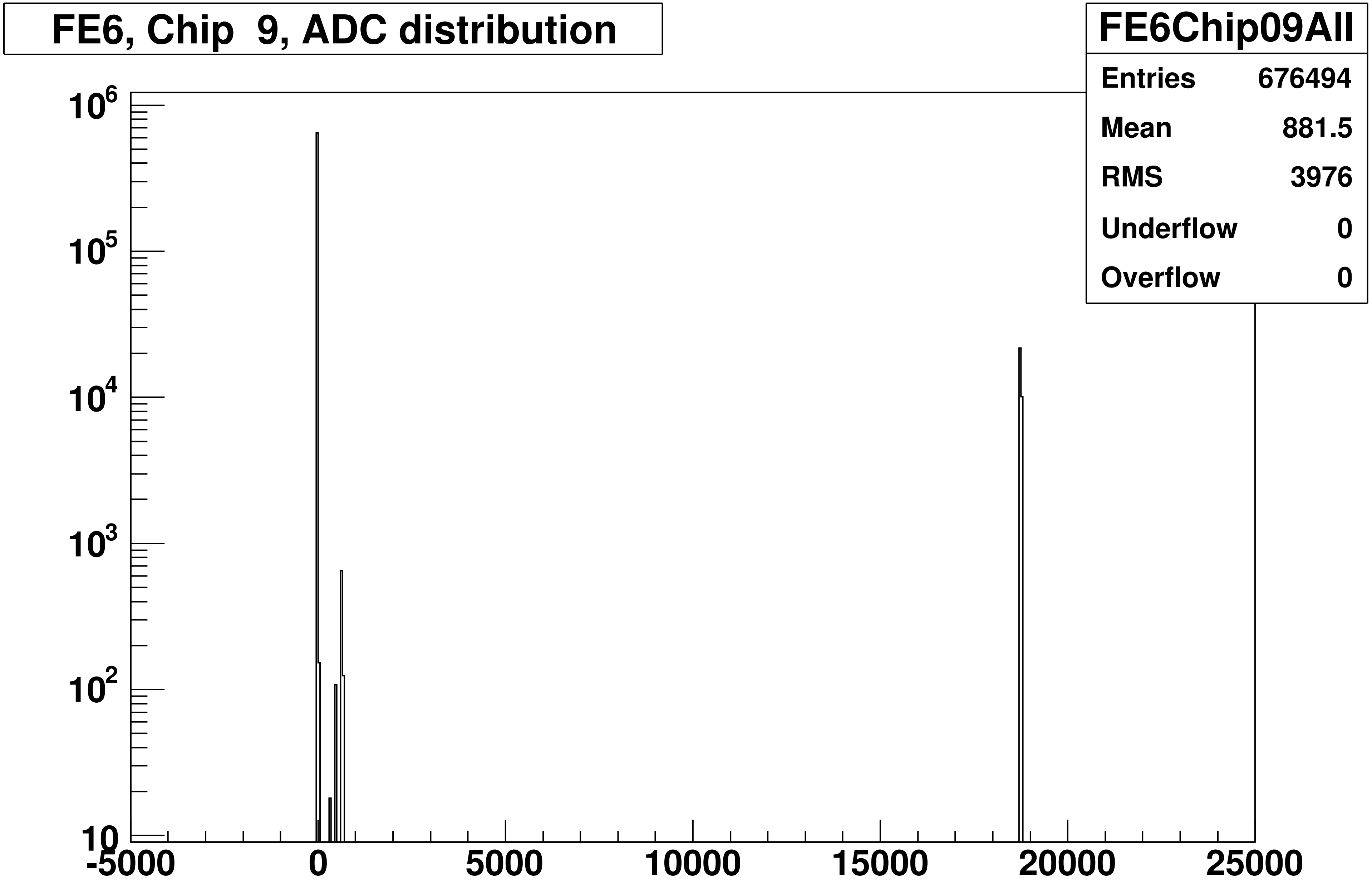
FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 676494
Mean 881.5
RMS 3976
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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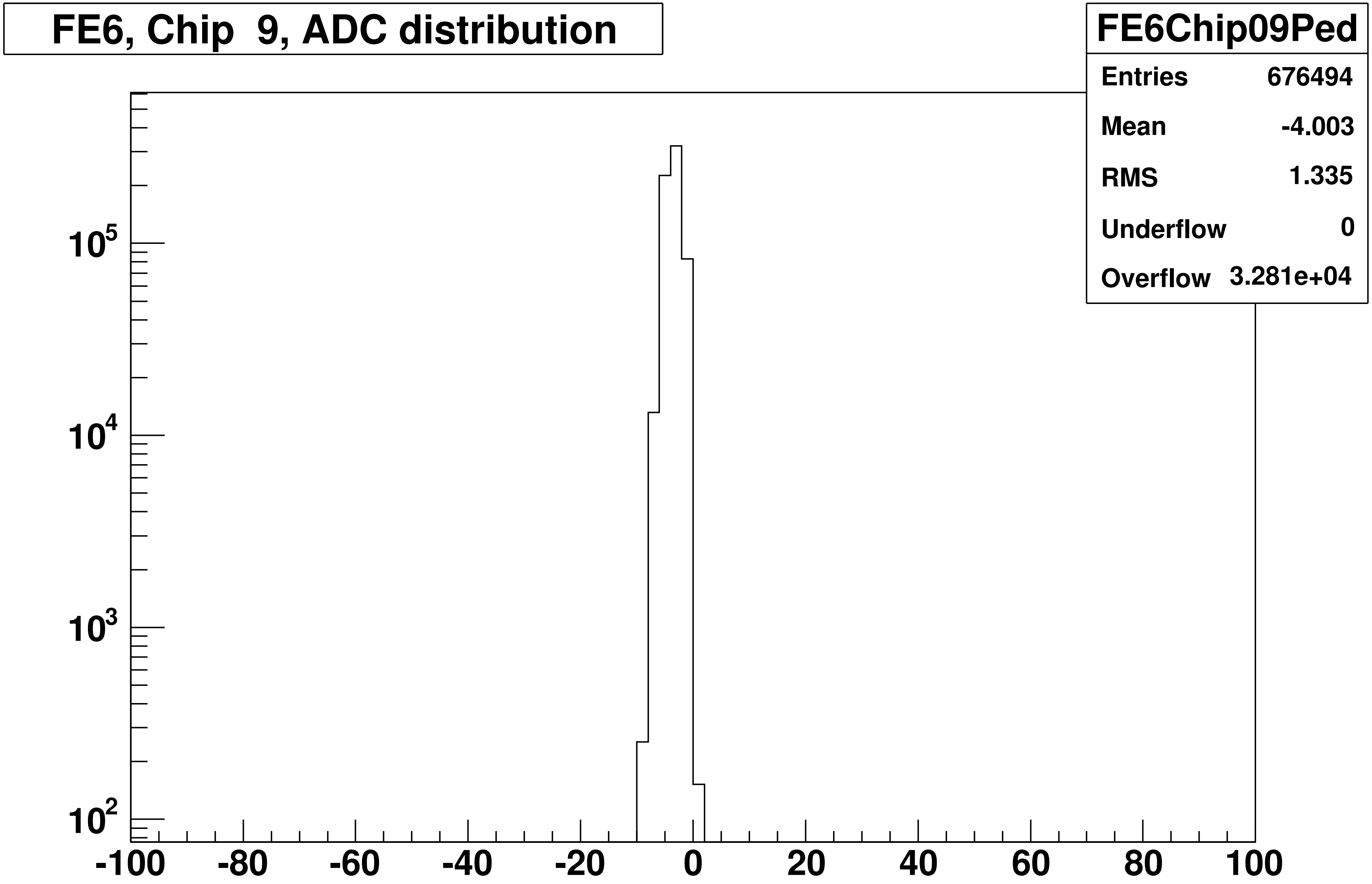
25000


FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 676494
Mean -4.003
RMS 1.335
Underflow 0
Overflow 3.281e+04
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10^4
10^3
10^2
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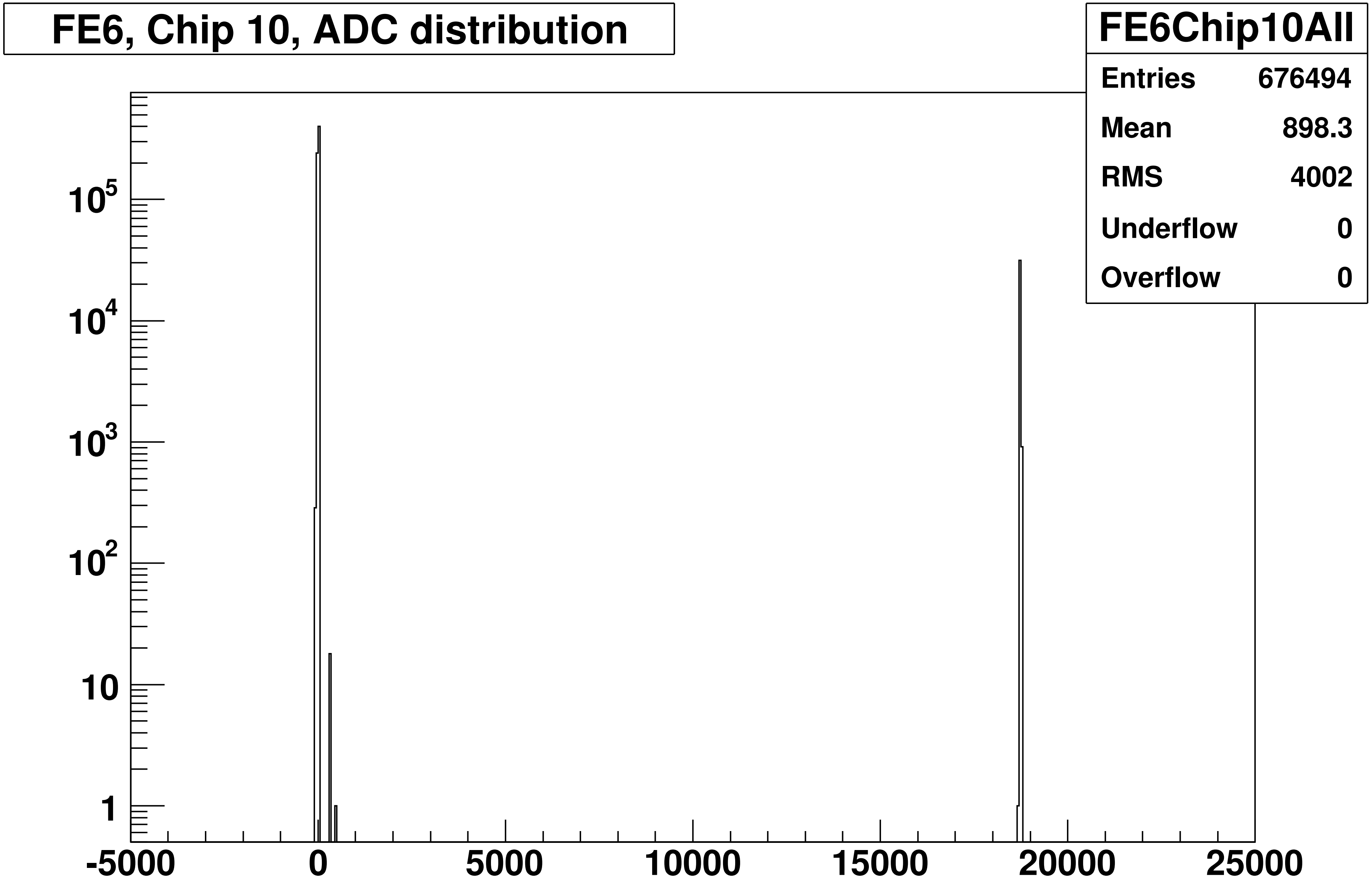
FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 676494
Mean 898.3
RMS 4002
Underflow 0
Overflow 0
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10^4
10^3
10^2
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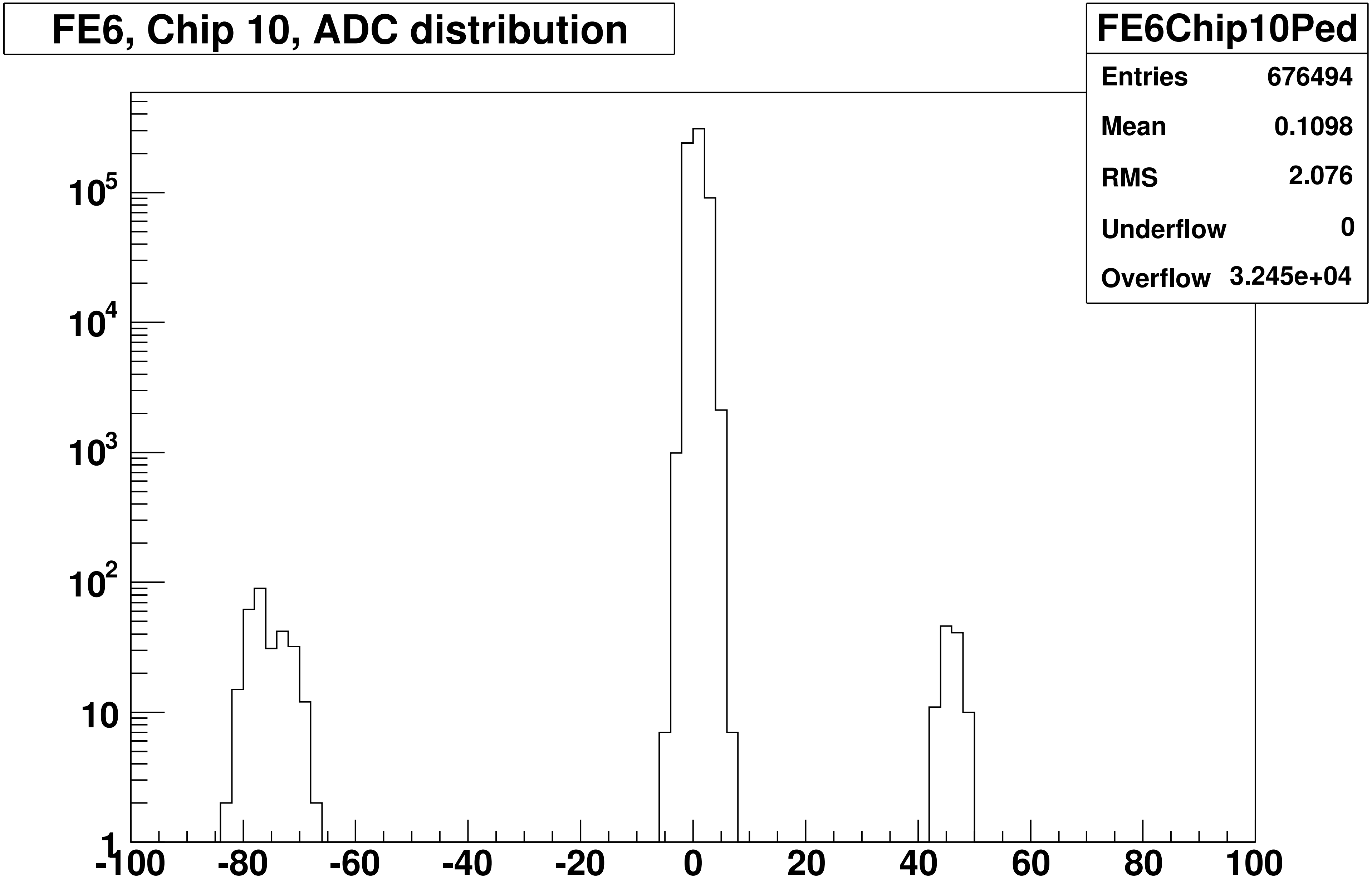
FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 676494
Mean 0.1098
RMS 2.076
Underflow 0
Overflow 3.245e+04
10^5
10^4
10^3
10^2
10
1
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-80
-60
-40
-20
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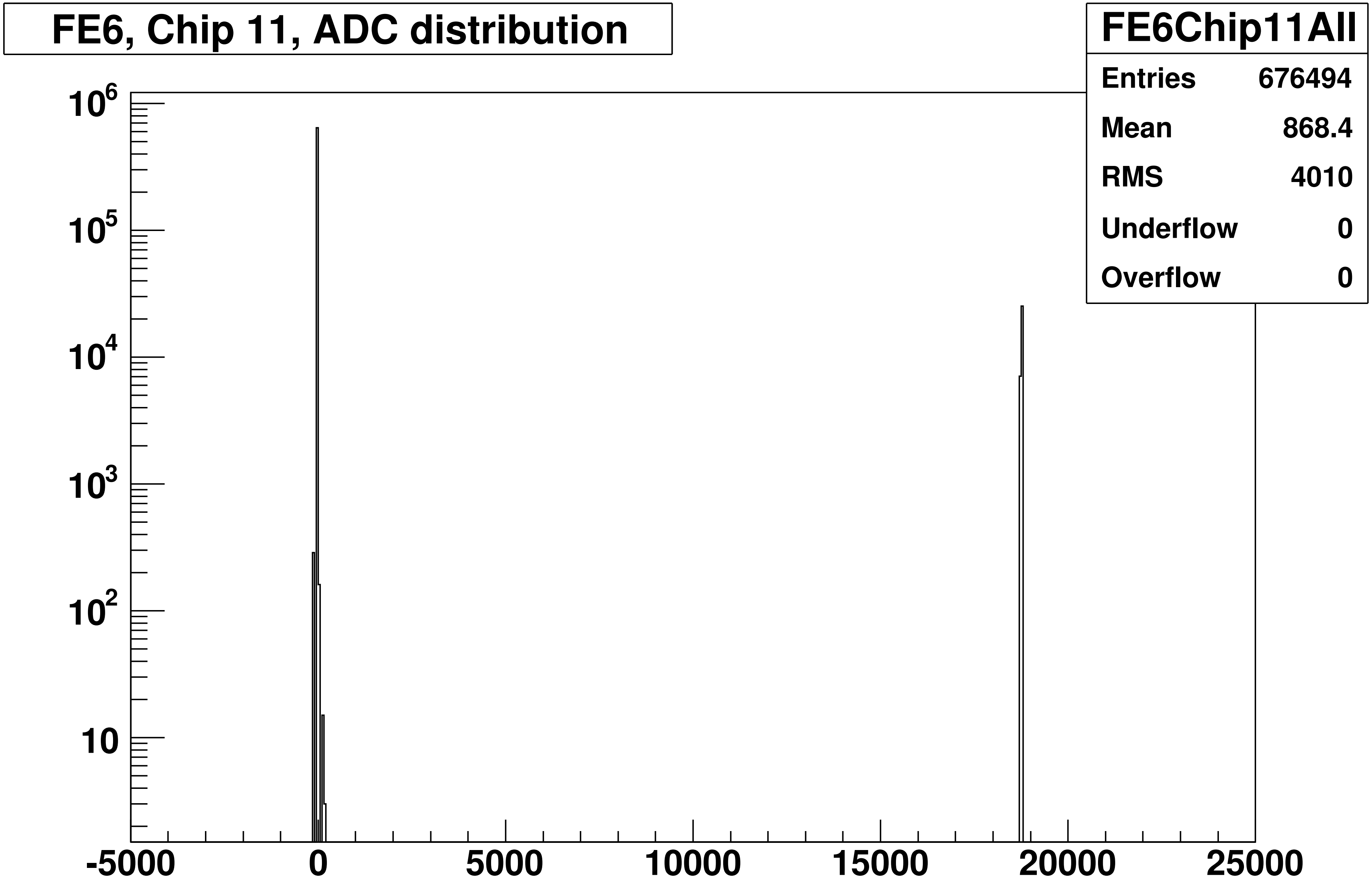
FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 676494
Mean 868.4
RMS 4010
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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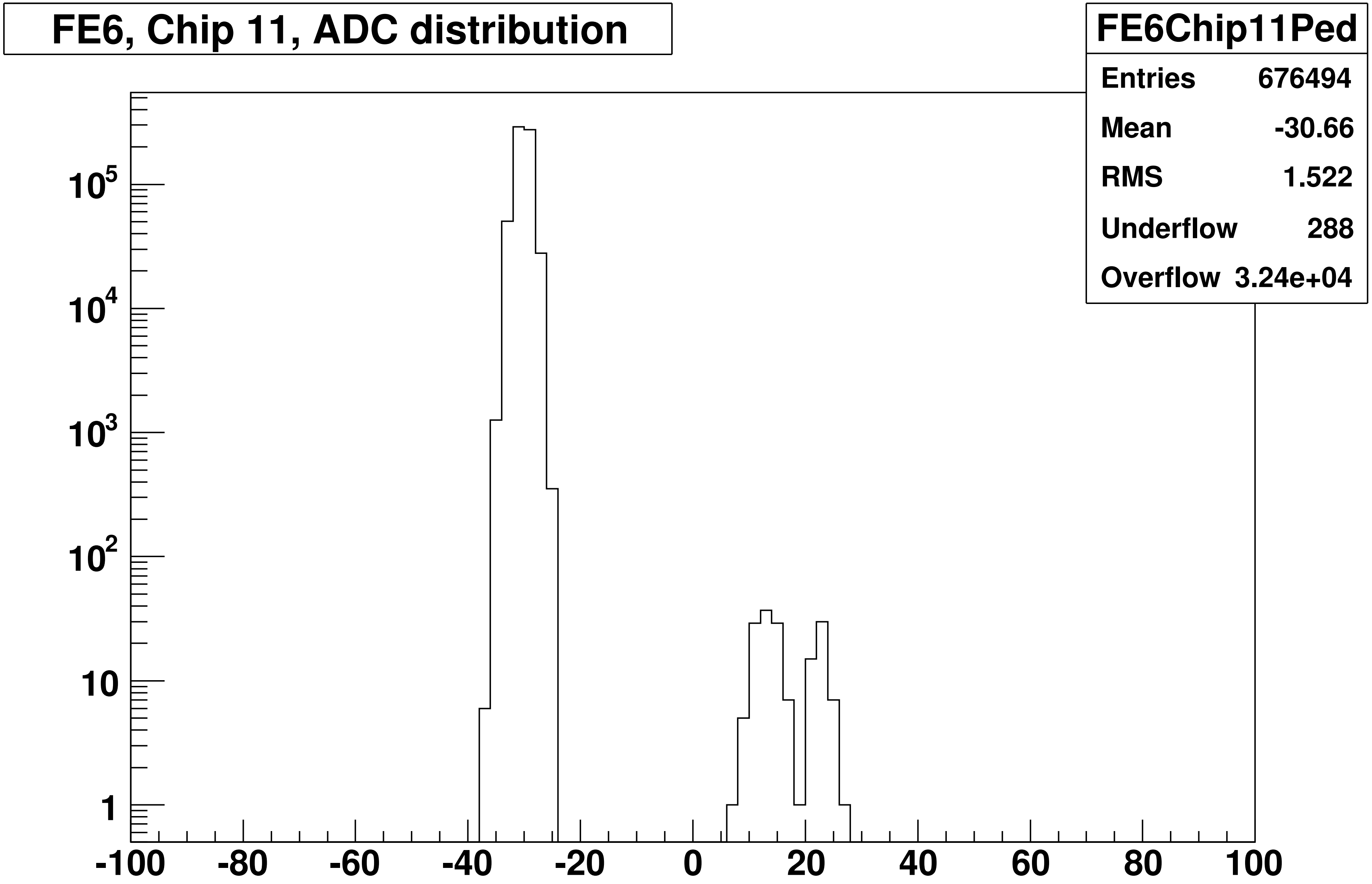
25000


FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 676494
Mean -30.66
RMS 1.522
Underflow 288
Overflow 3.24e+04
1
10
10^2
10^3
10^4
10^5
-100
-80
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80
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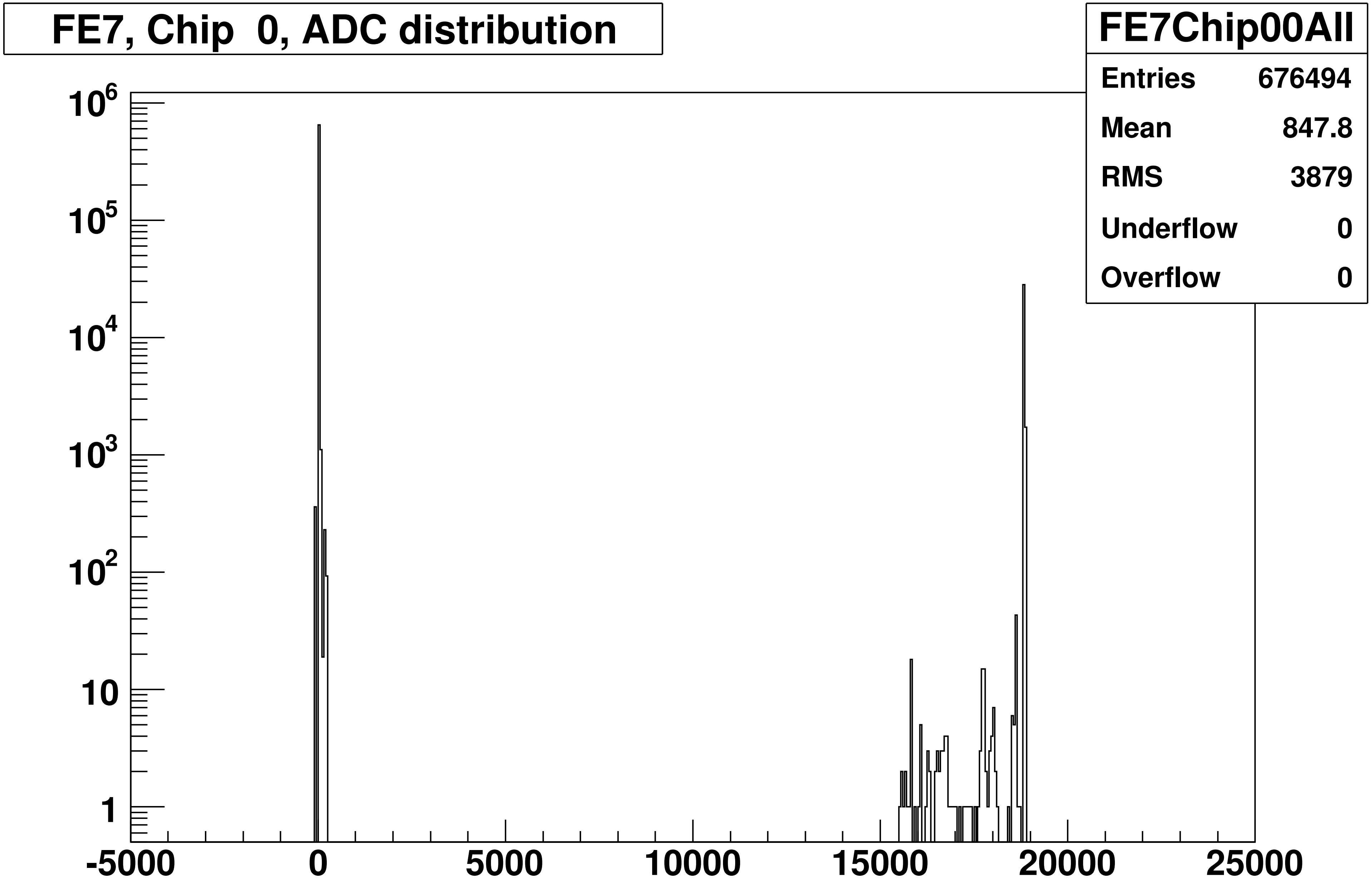
FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 676494
Mean 847.8
RMS 3879
Underflow 0
Overflow 0
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10^4
10^3
10^2
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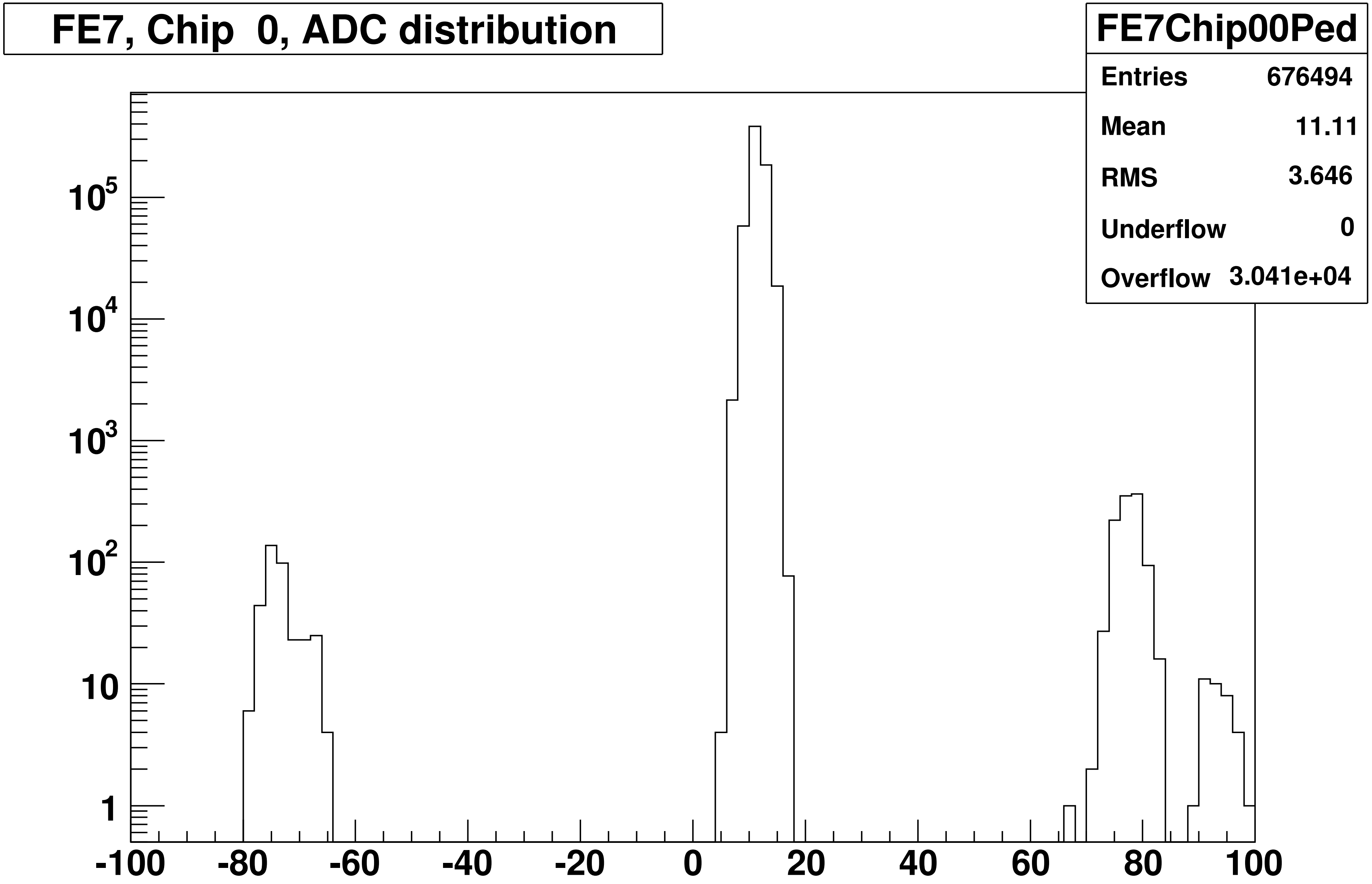

FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 676494
Mean 11.11
RMS 3.646
Underflow 0
Overflow 3.041e+04
10^5
10^4
10^3
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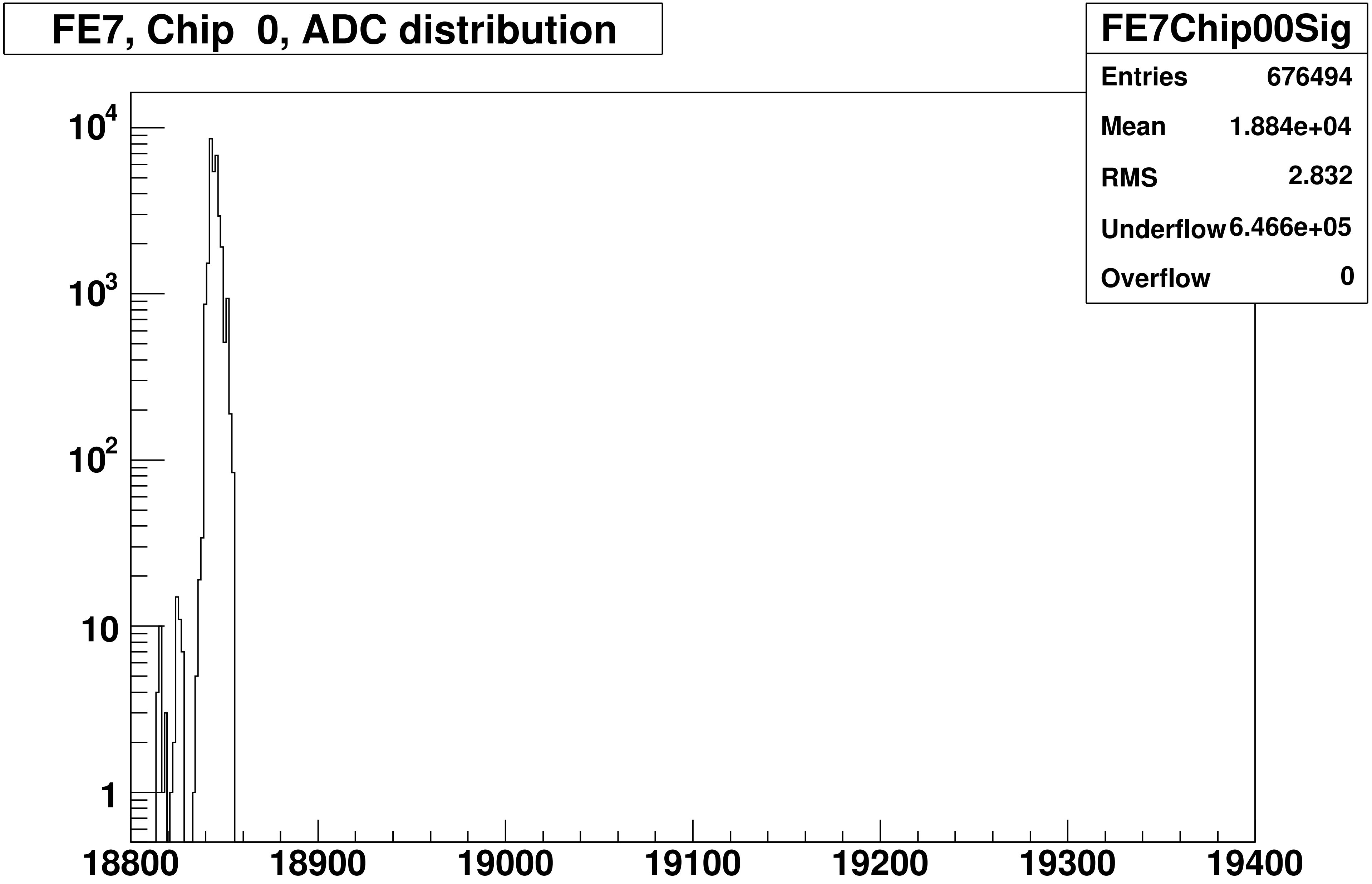
FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 676494
Mean 1.884e+04
RMS 2.832
Underflow 6.466e+05
Overflow 0
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10^3
10^2
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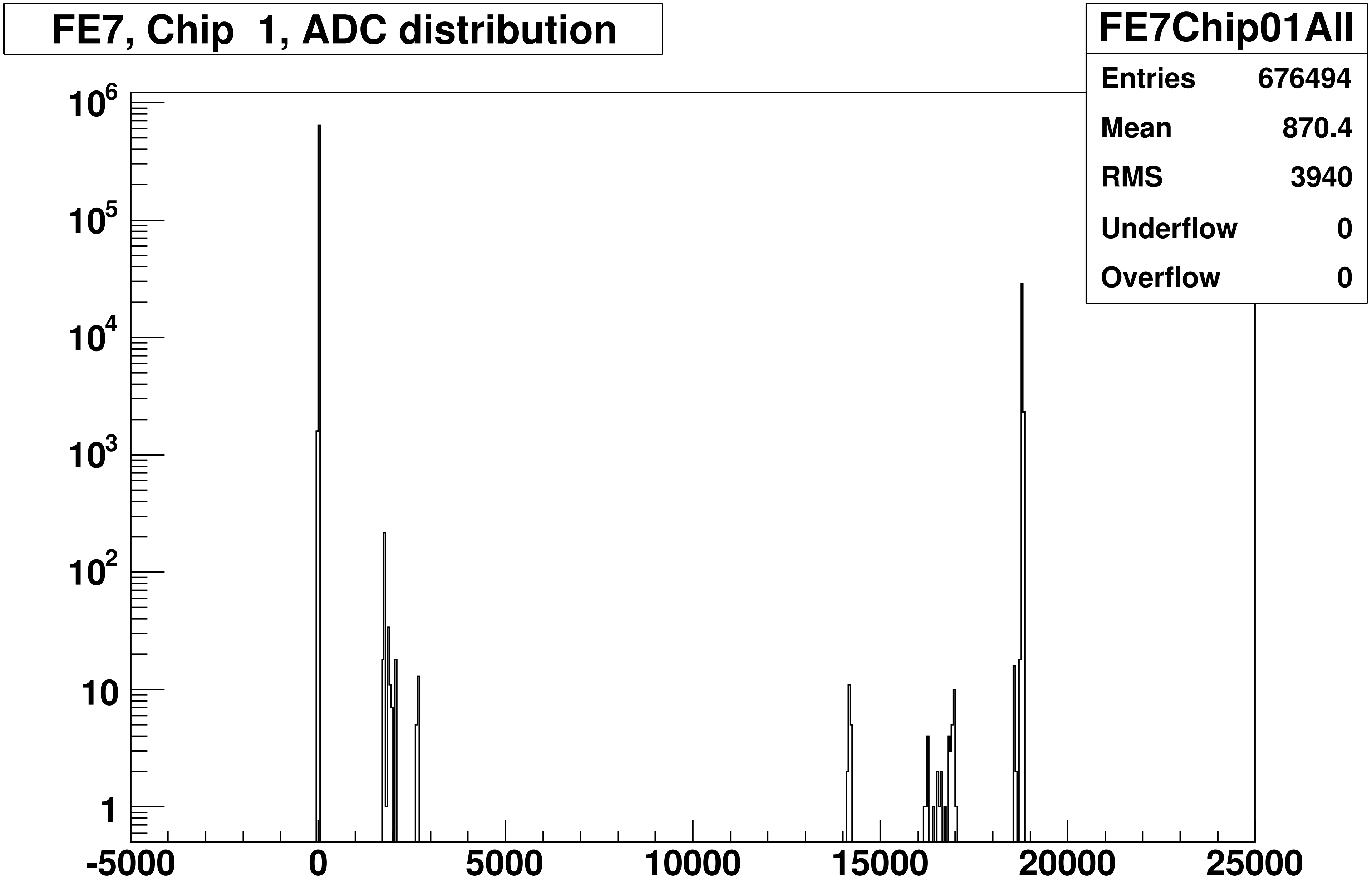
FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 676494
Mean 870.4
RMS 3940
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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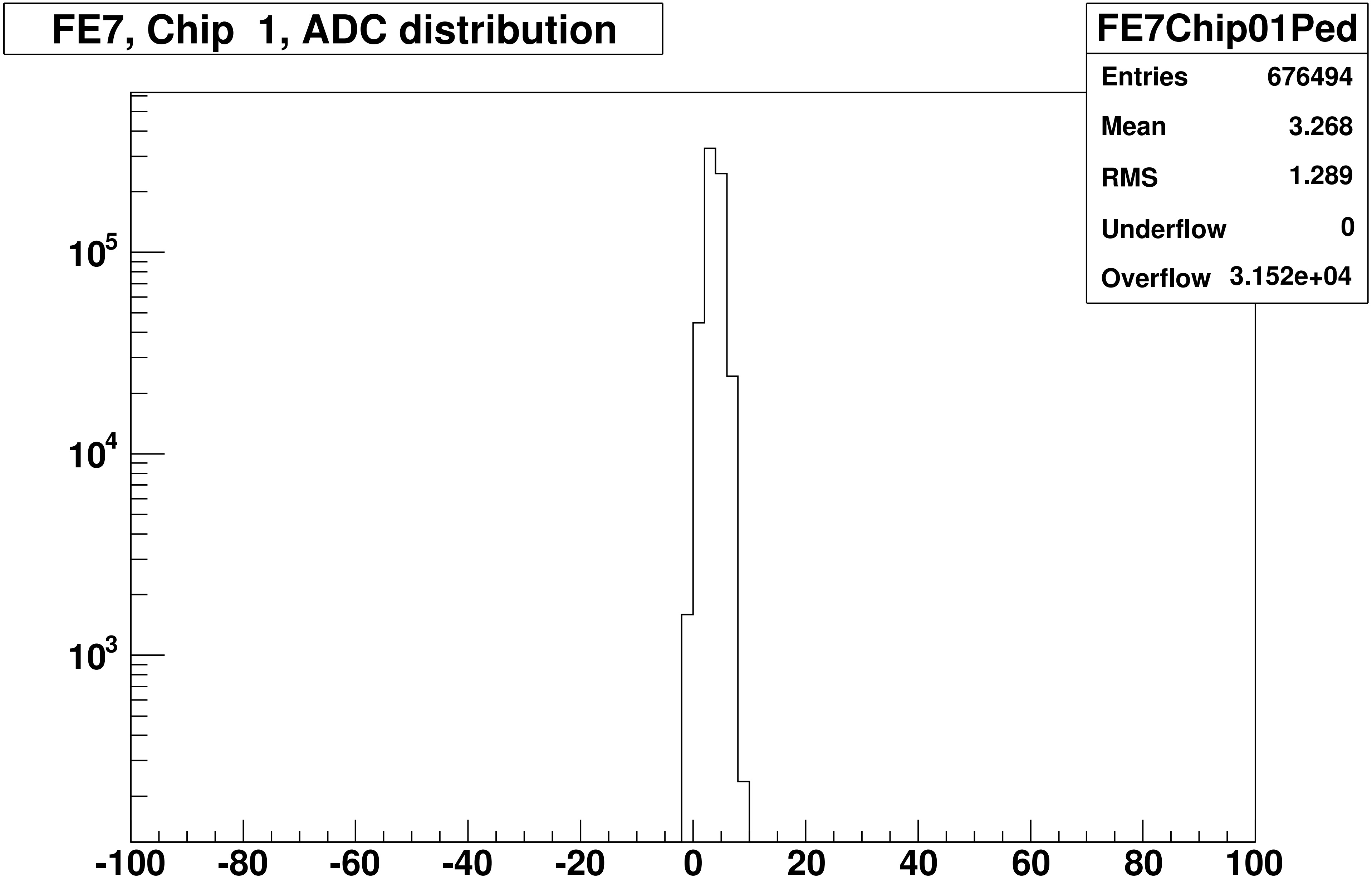
FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 676494
Mean 3.268
RMS 1.289
Underflow 0
Overflow 3.152e+04
10^5
10^4
10^3
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-40
-20
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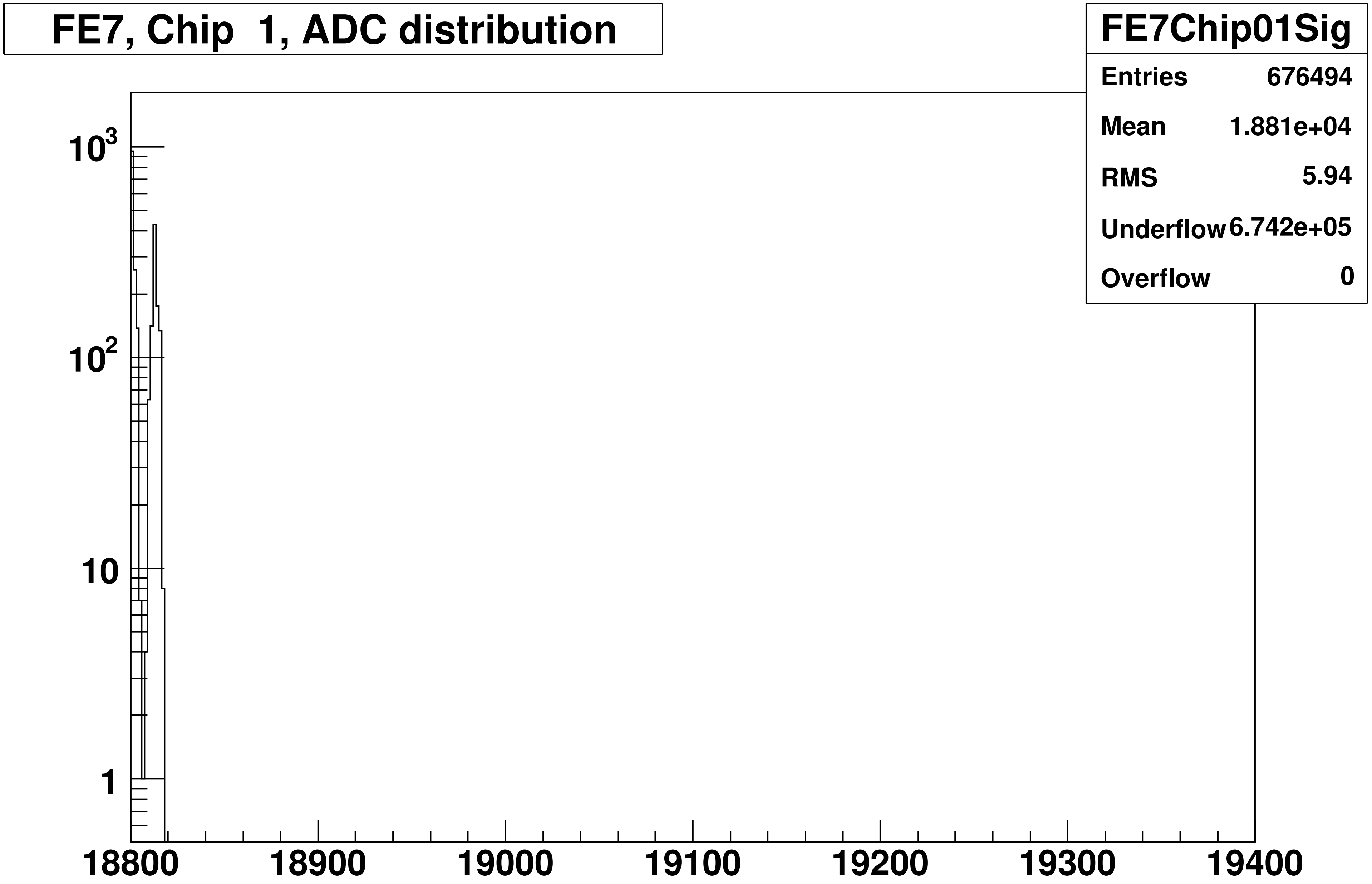
FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 676494
Mean 1.881e+04
RMS 5.94
Underflow 6.742e+05
Overflow 0
10^3
10^2
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18800
18900
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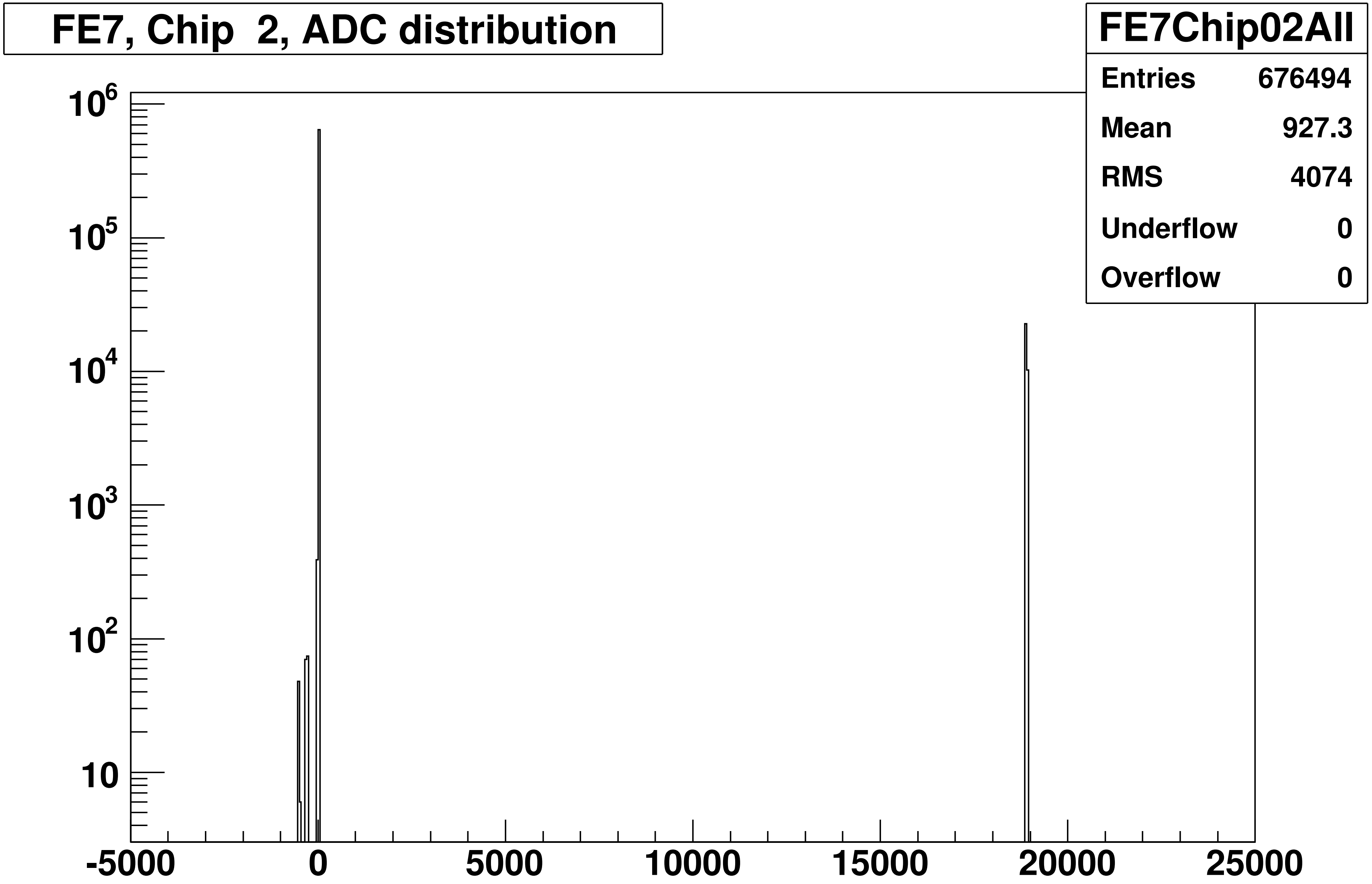
19400


FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 676494
Mean 927.3
RMS 4074
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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5000
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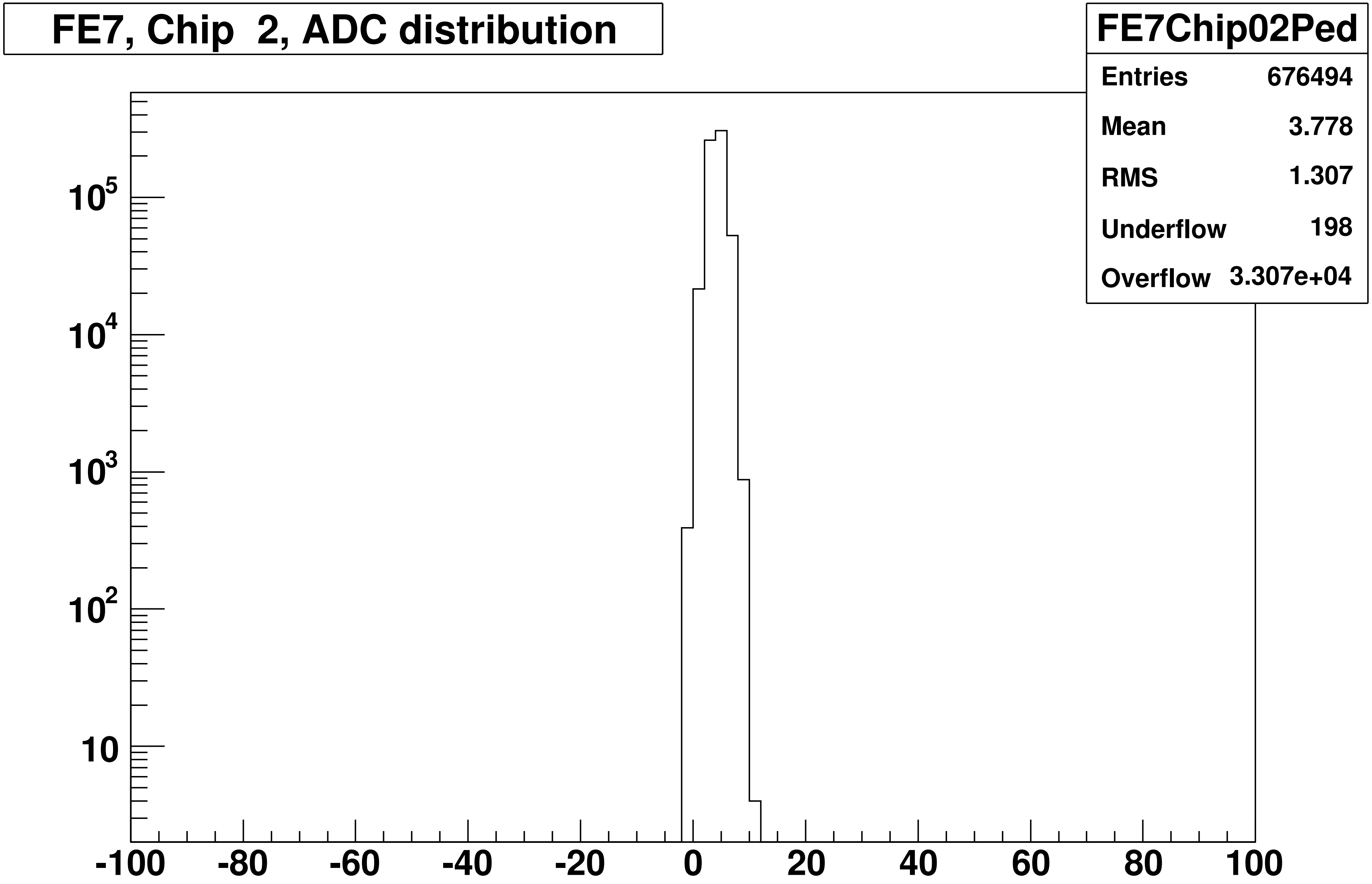
FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 676494
Mean 3.778
RMS 1.307
Underflow 198
Overflow 3.307e+04
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10^3
10^4
10^5
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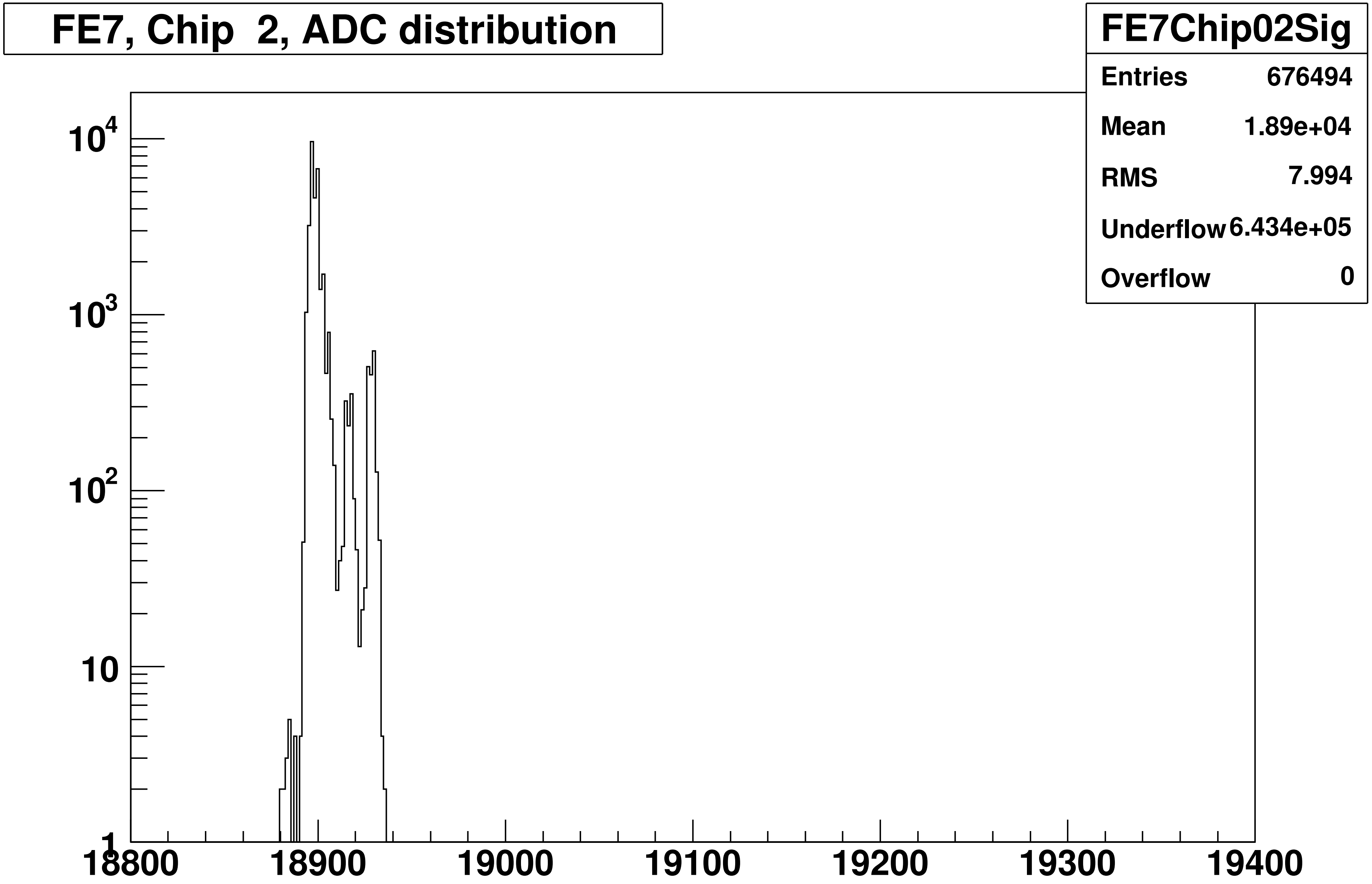
FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 676494
Mean 1.89e+04
RMS 7.994
Underflow 6.434e+05
Overflow 0
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10^3
10^2
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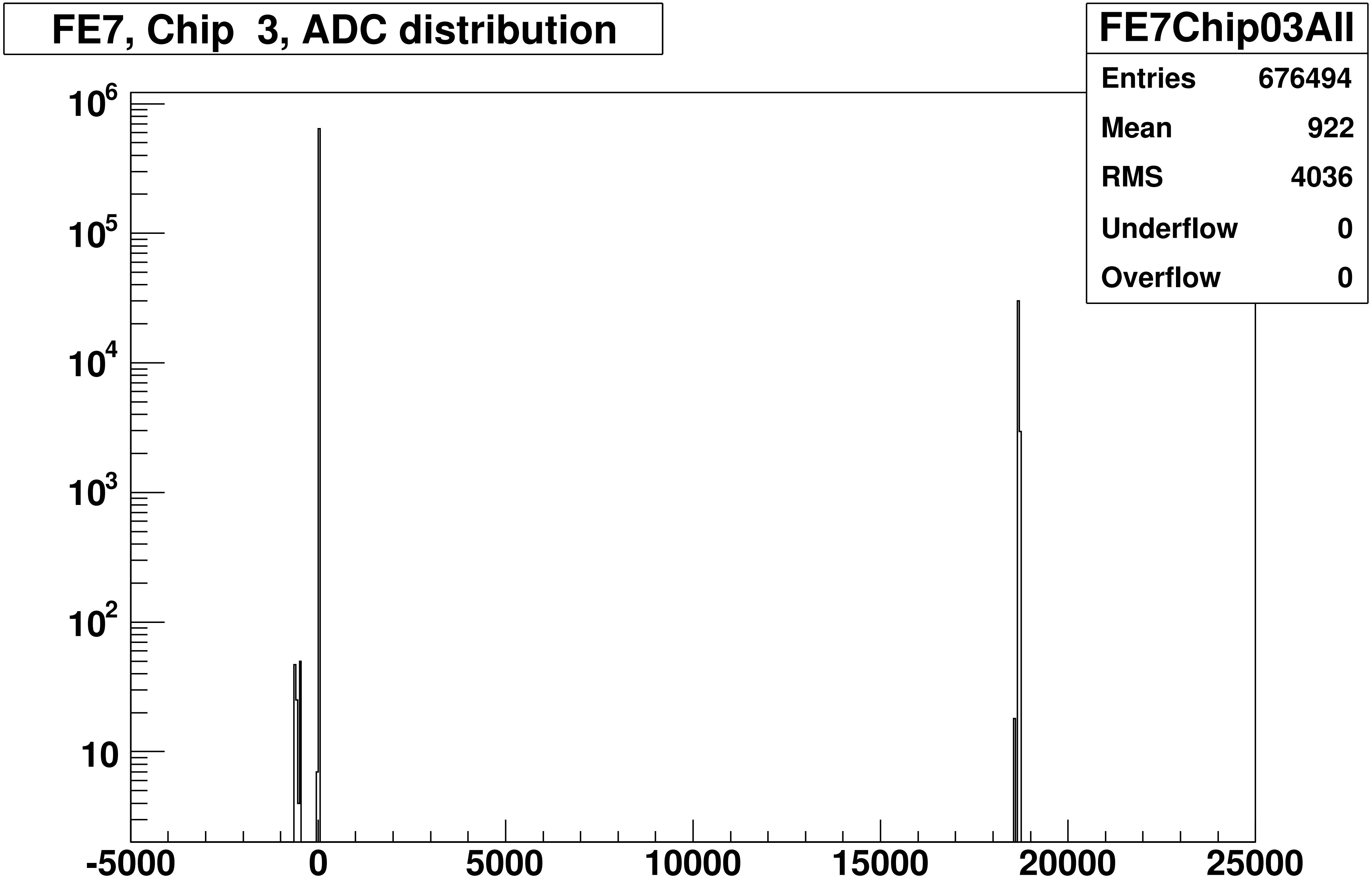
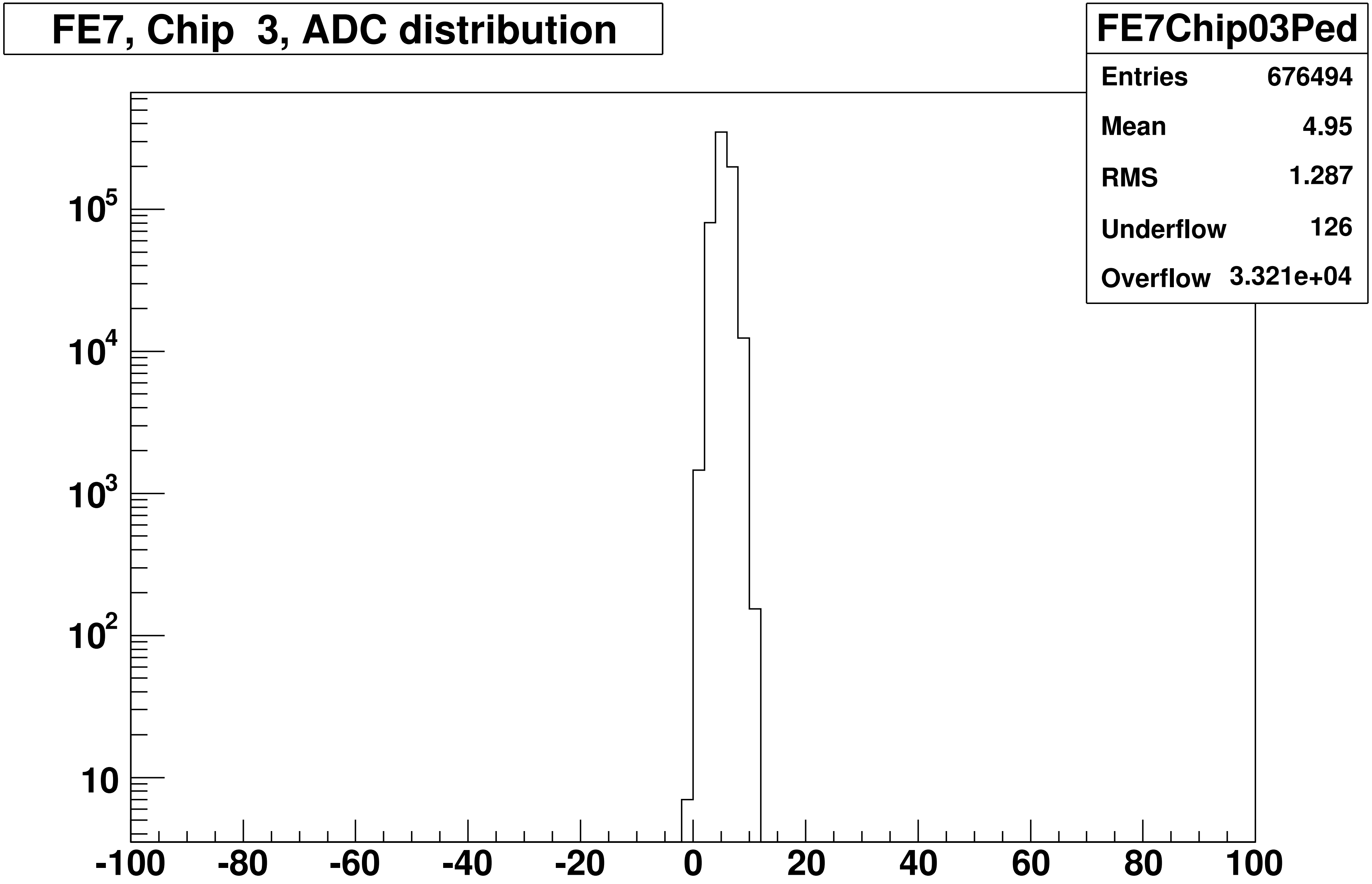
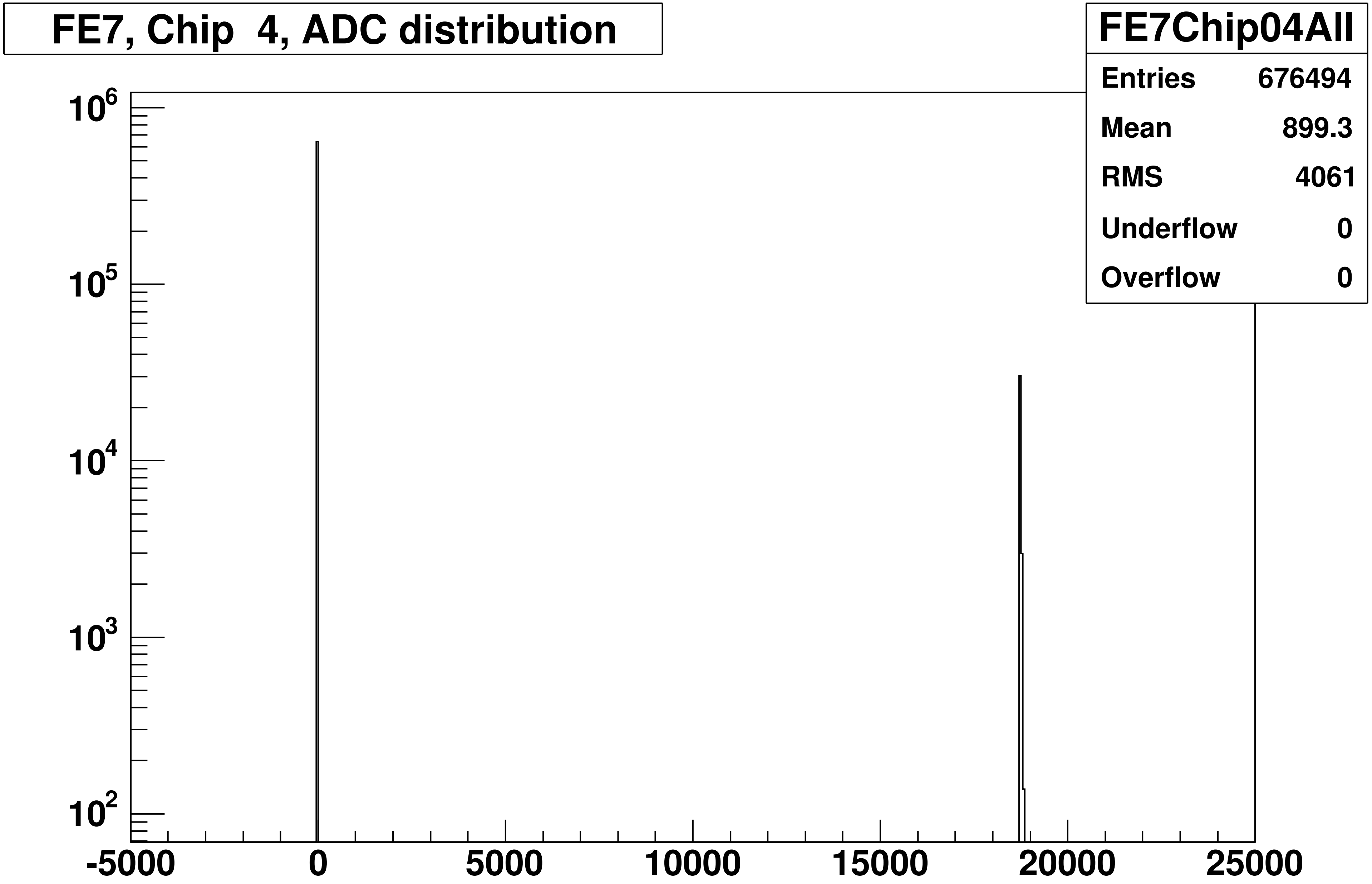
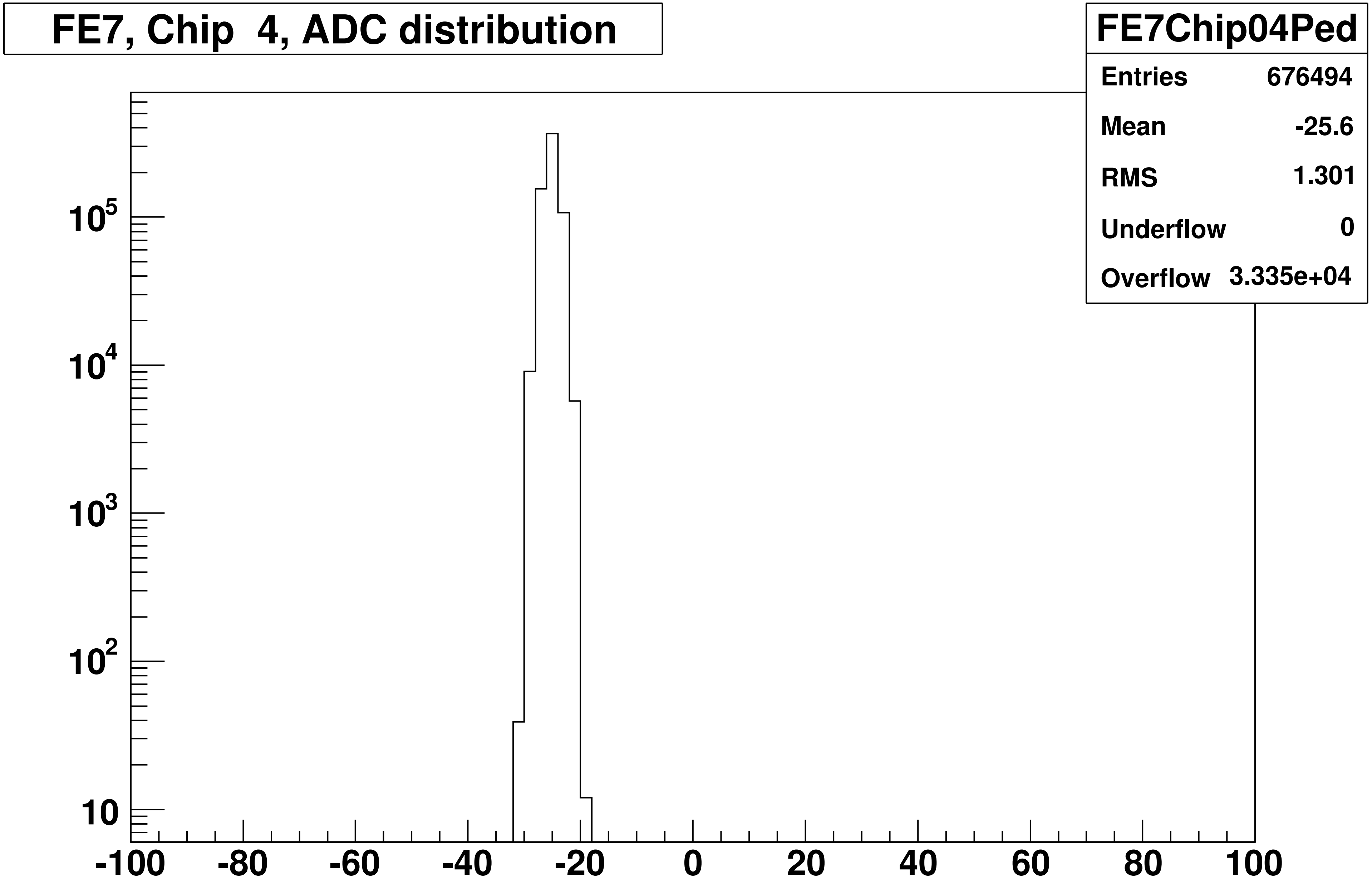


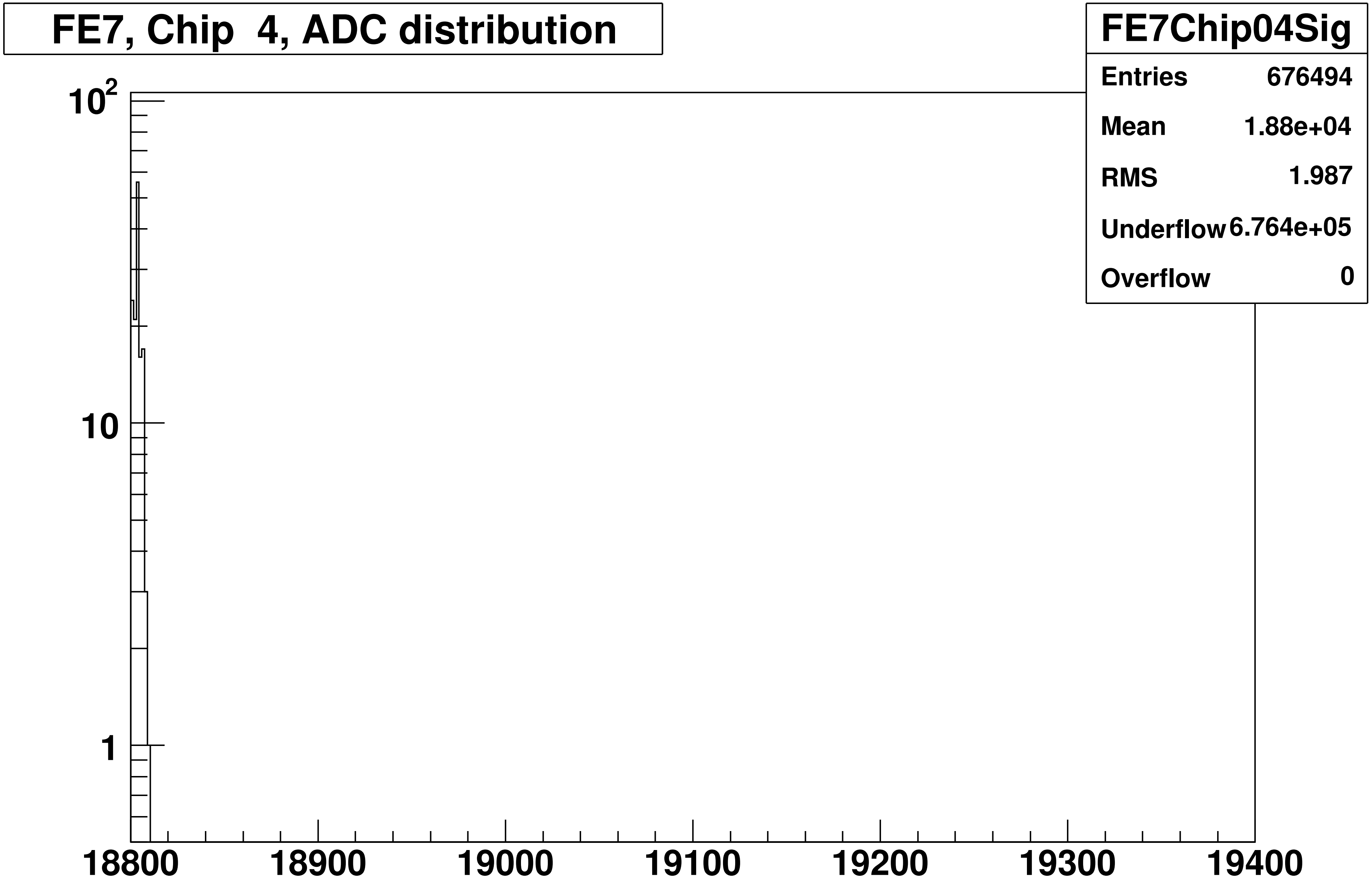
FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 676494
Mean 922
RMS 4036
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

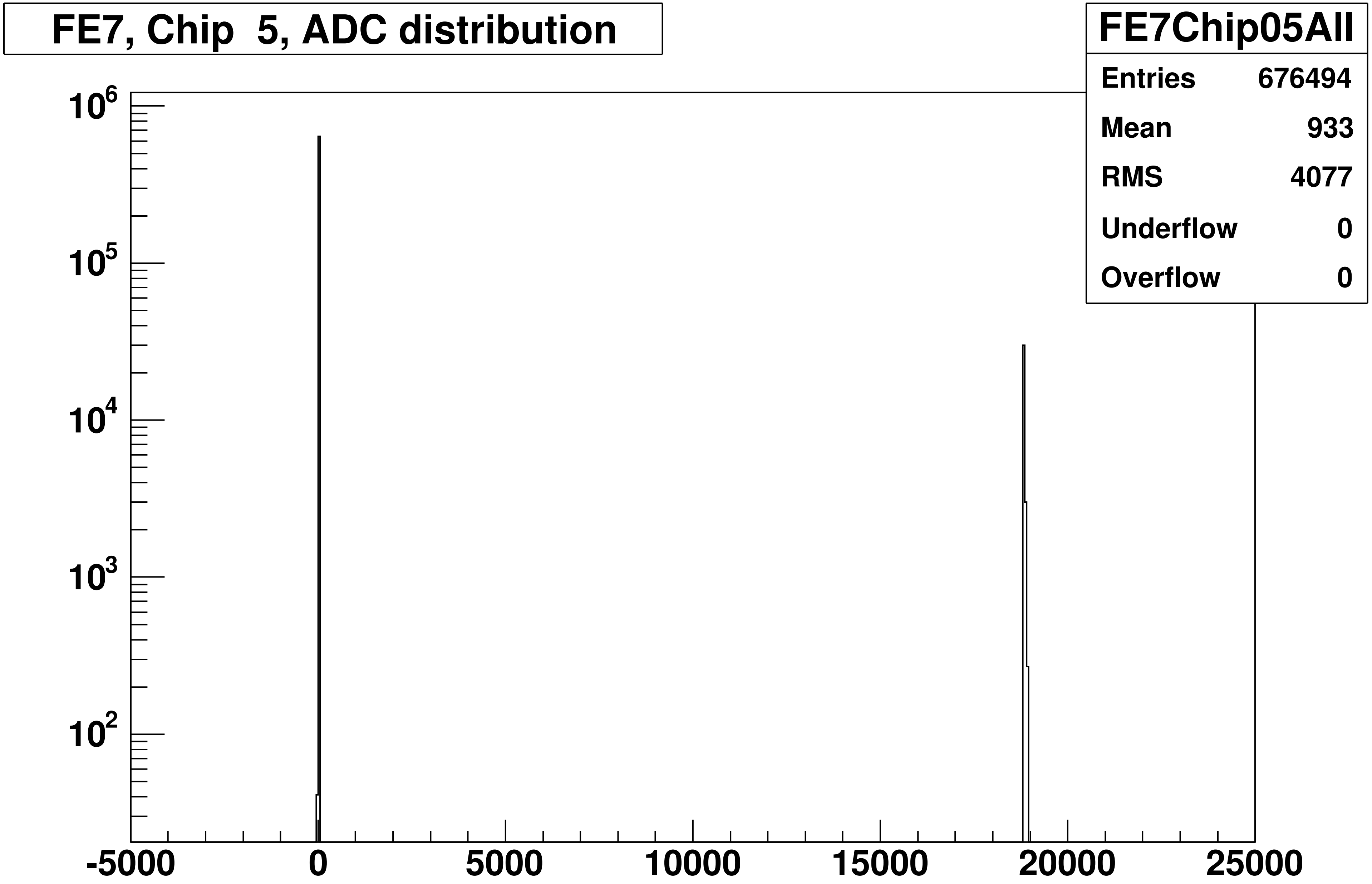

FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 676494
Mean 4.95
RMS 1.287
Underflow 126
Overflow 3.321e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

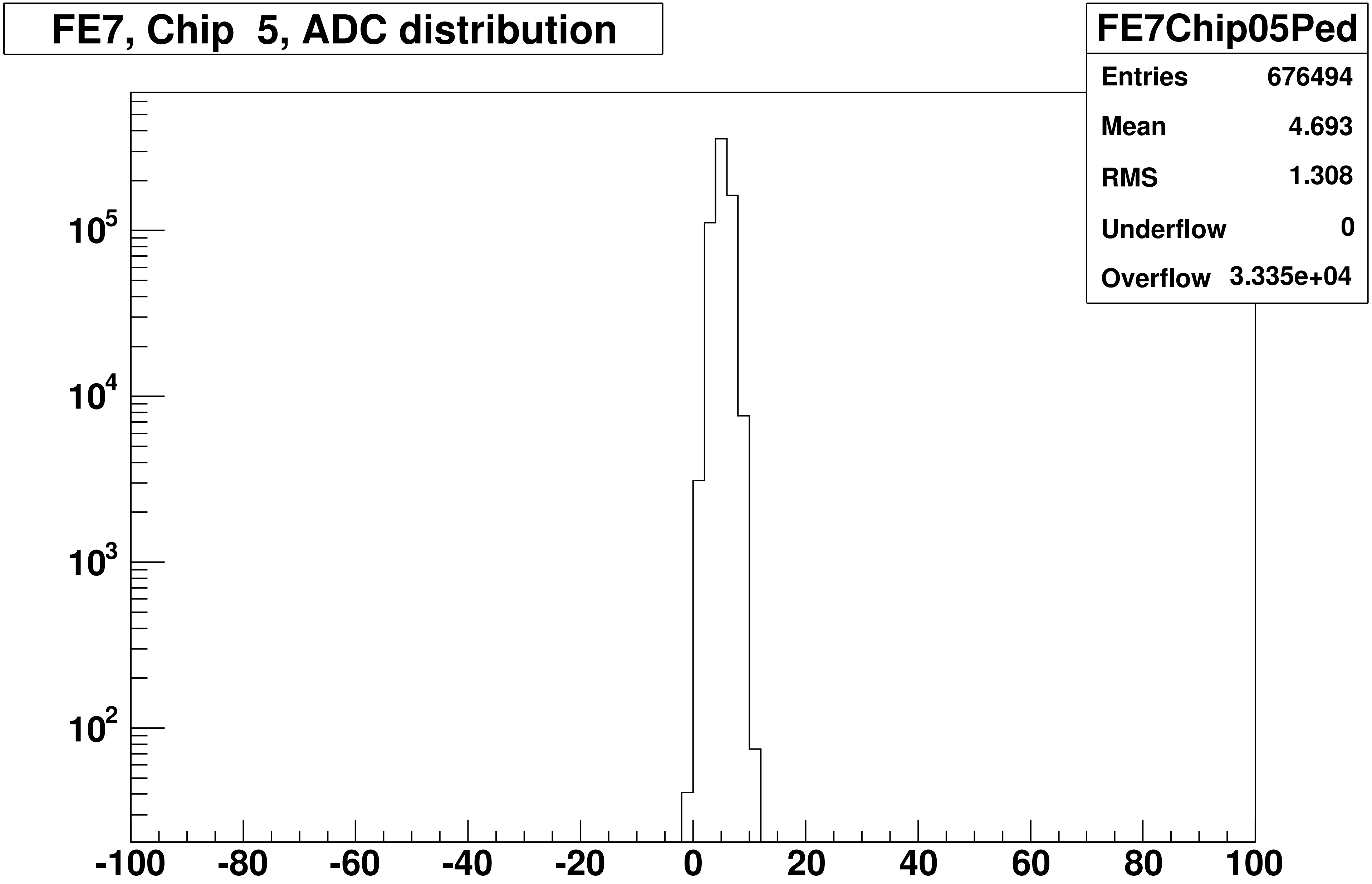


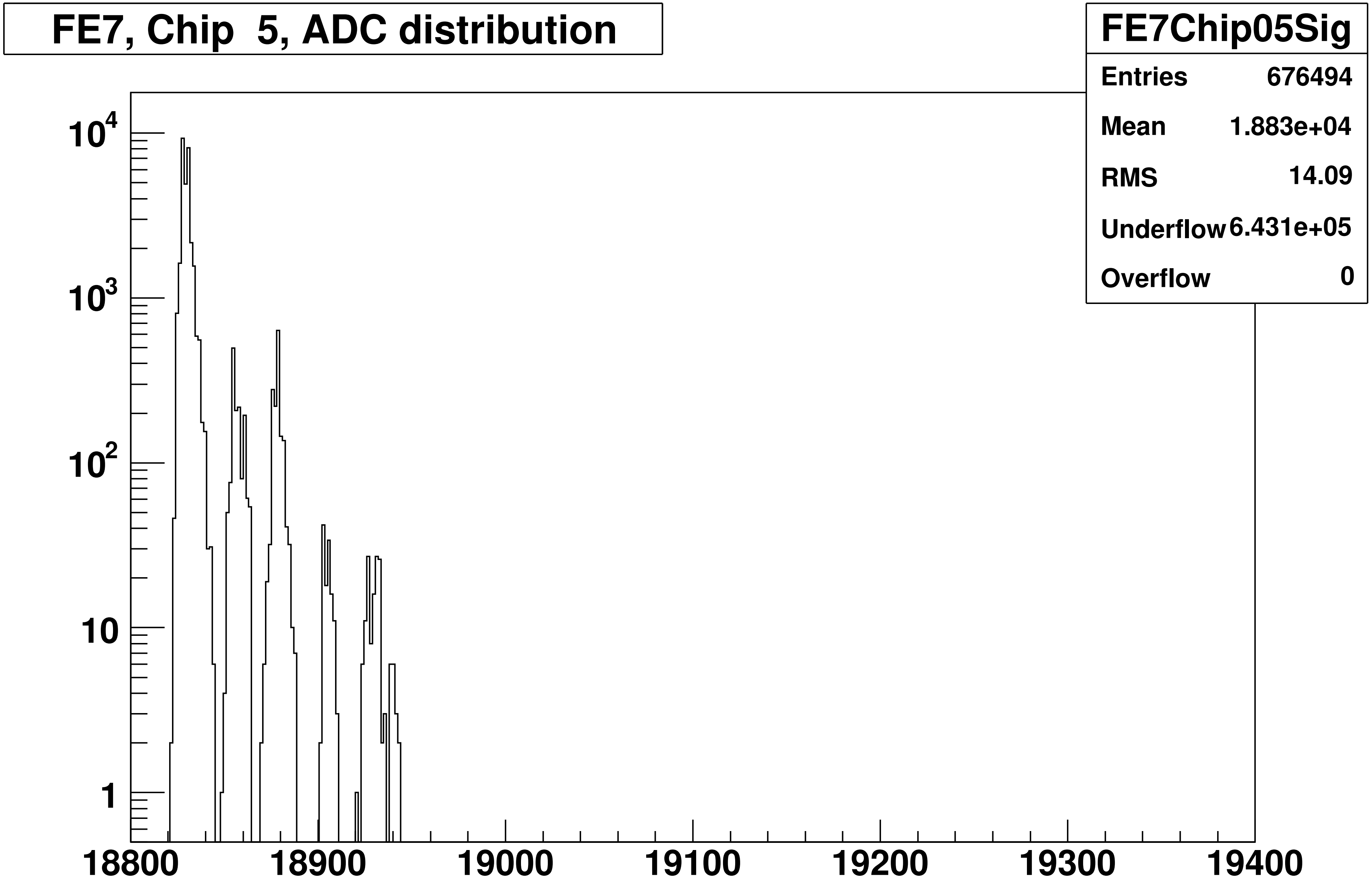
FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 676494
Mean 899.3
RMS 4061
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

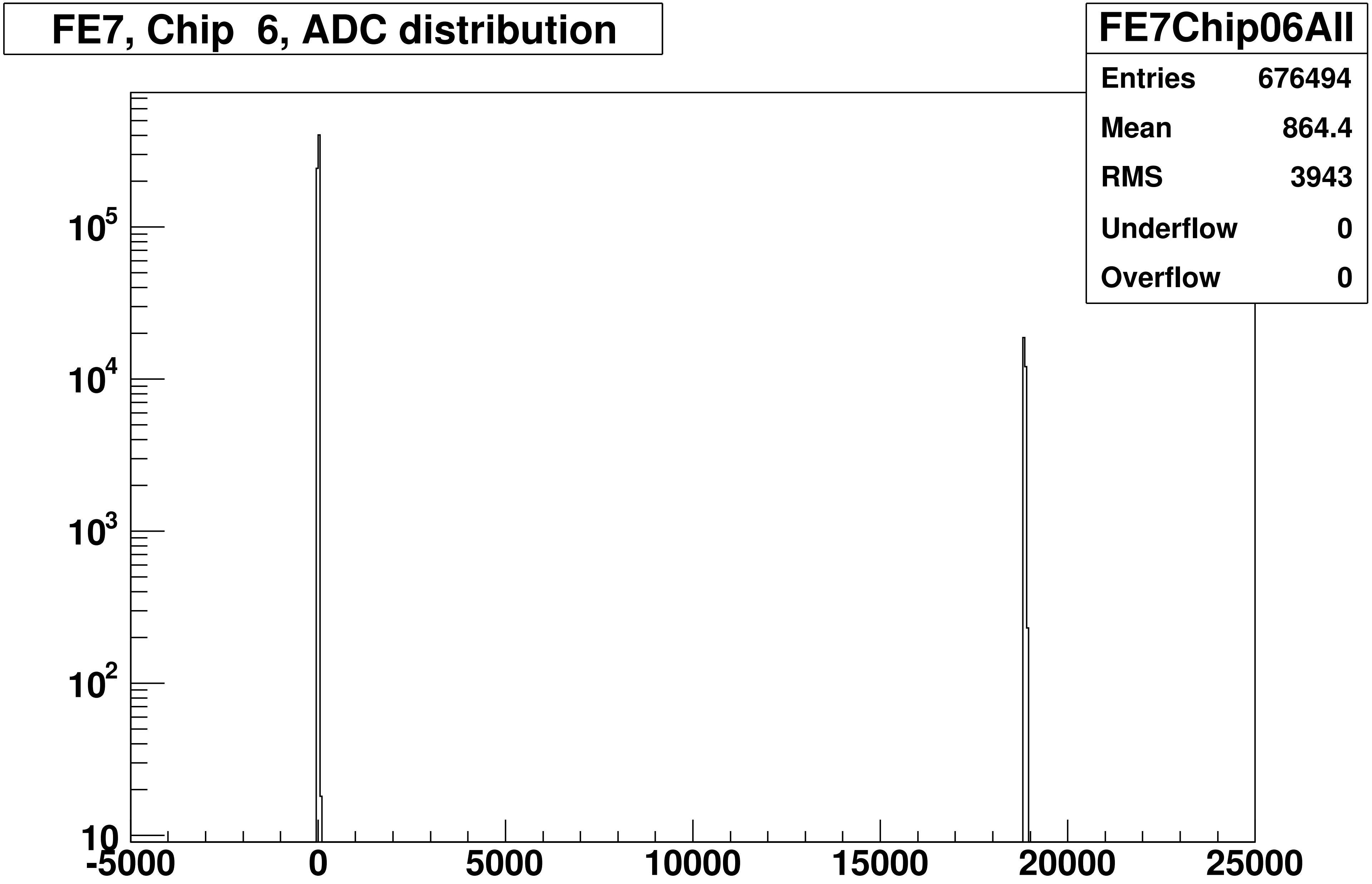

FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 676494
Mean -25.6
RMS 1.301
Underflow 0
Overflow 3.335e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

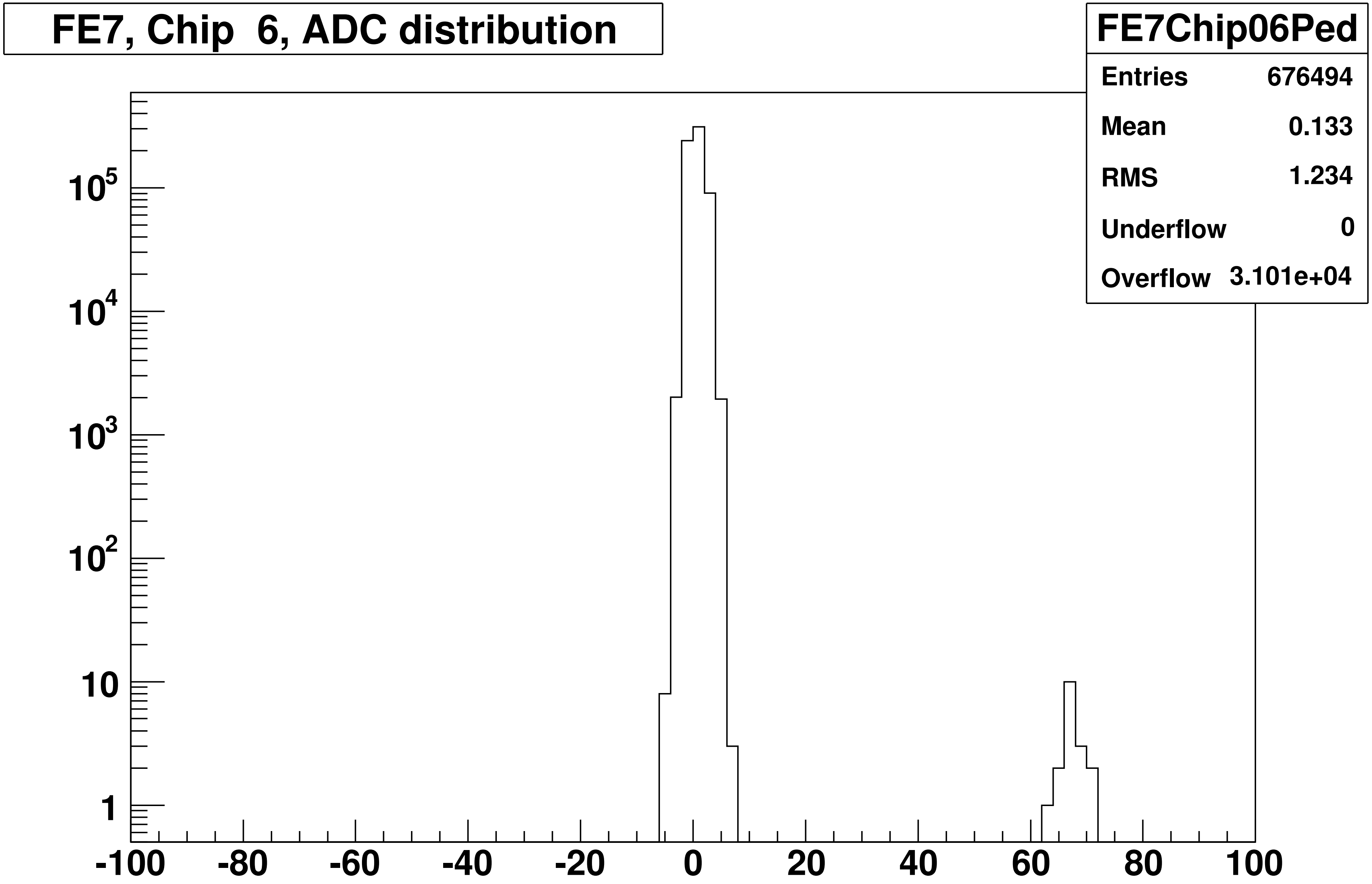

FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 676494
Mean 1.88e+04
RMS 1.987
Underflow 6.764e+05
Overflow 0
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 676494
Mean 933
RMS 4077
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

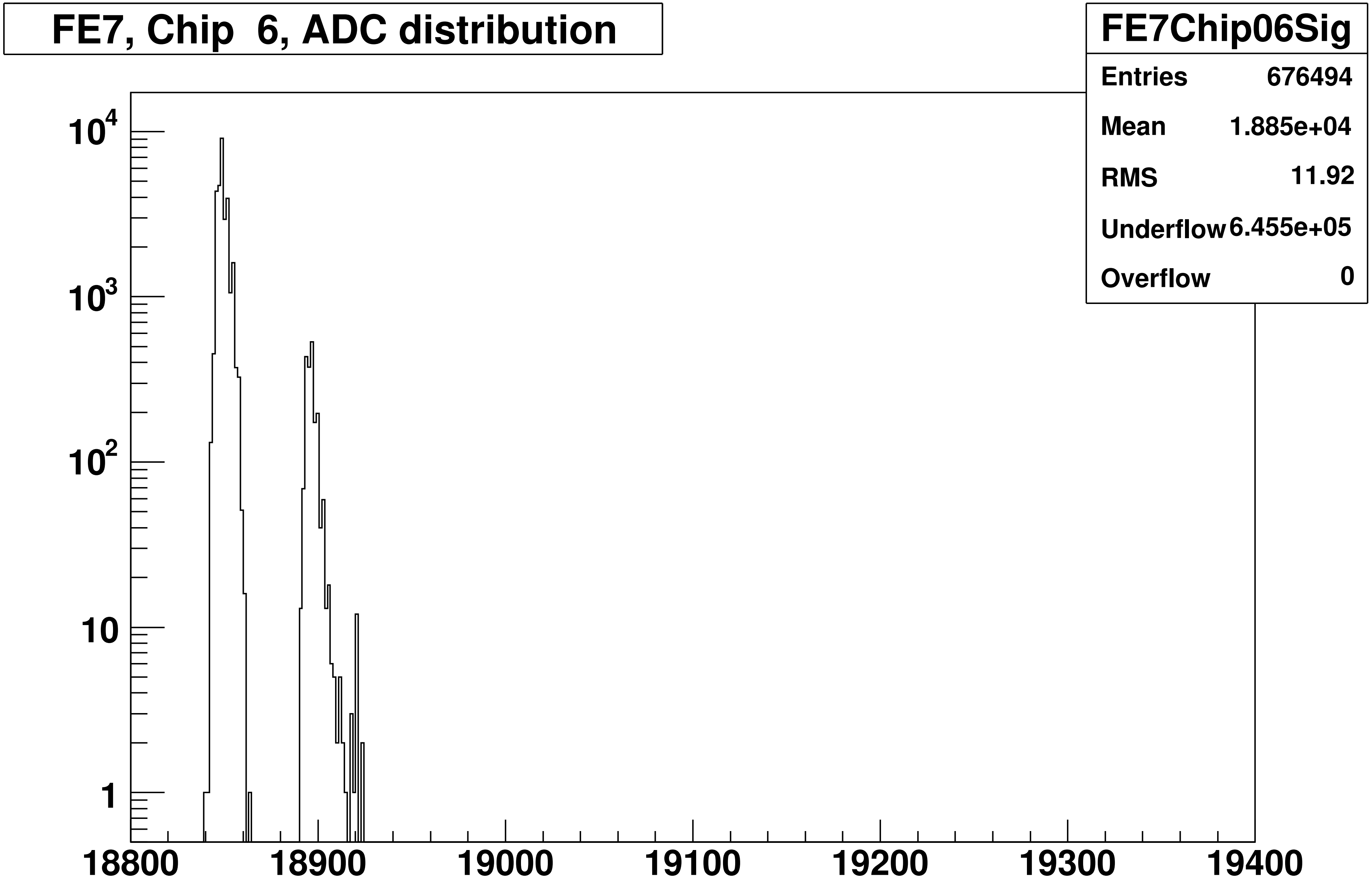

FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 676494
Mean 4.693
RMS 1.308
Underflow 0
Overflow 3.335e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

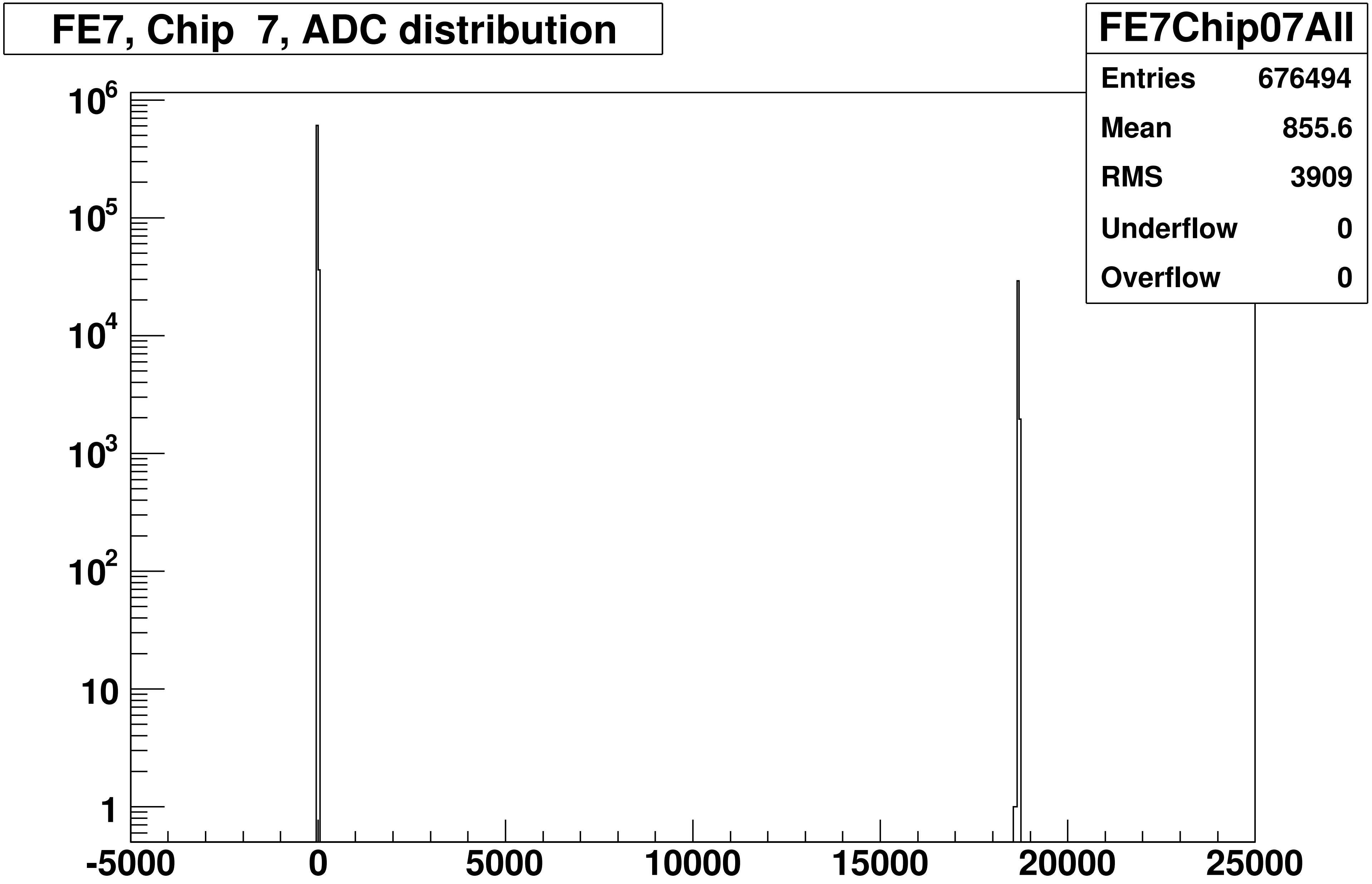

FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 676494
Mean 1.883e+04
RMS 14.09
Underflow 6.431e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

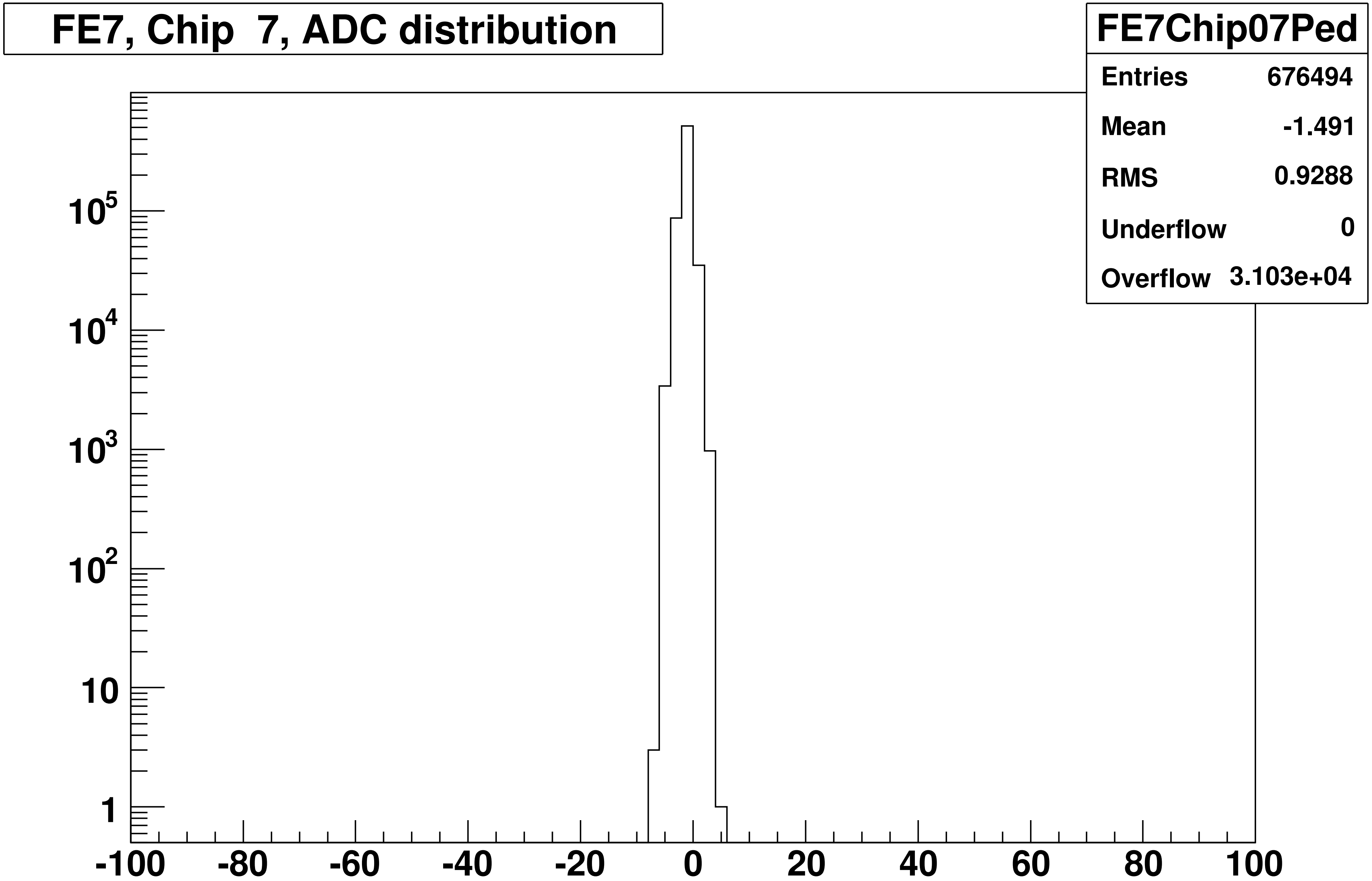

FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 676494
Mean 864.4
RMS 3943
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

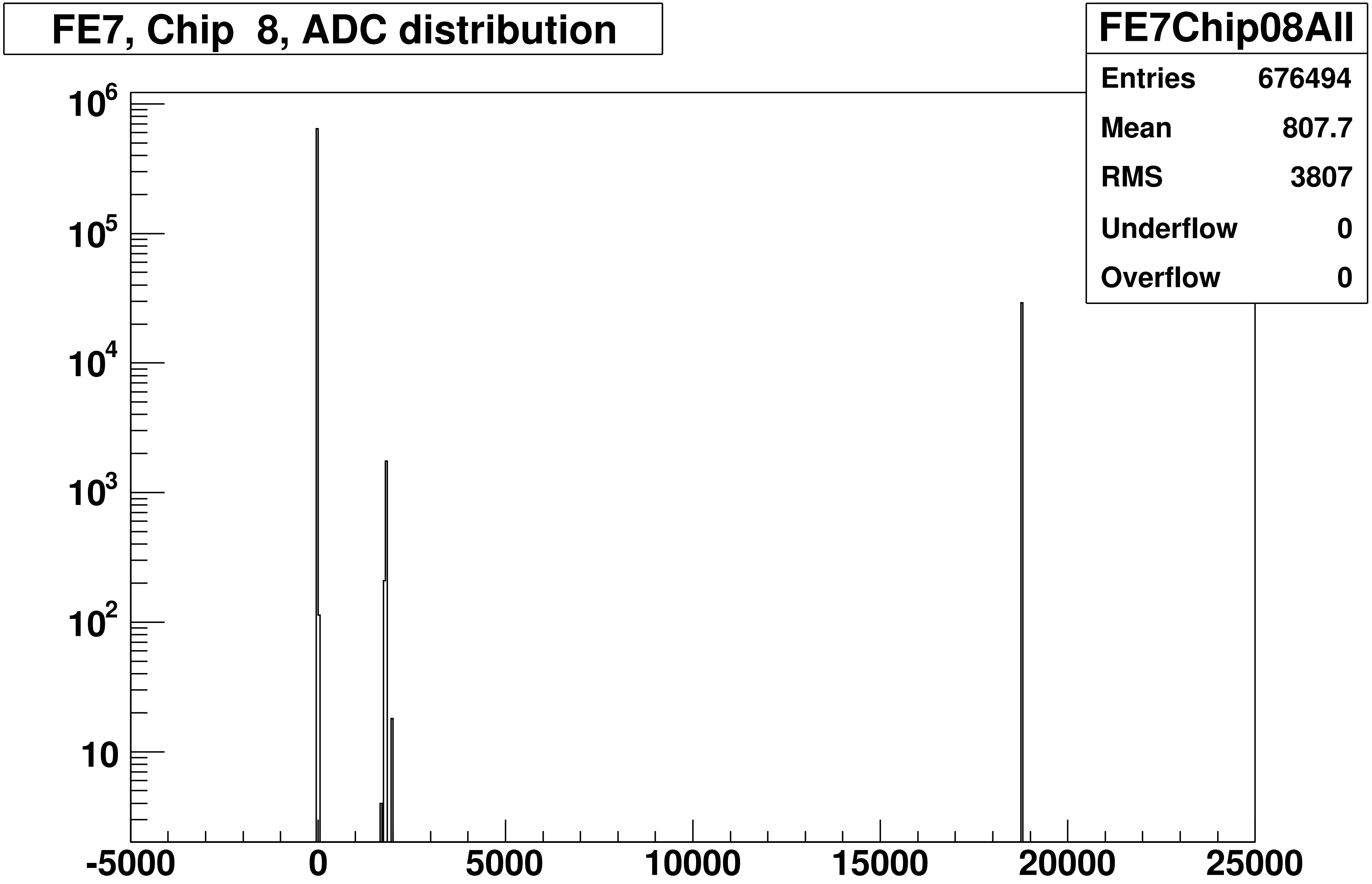


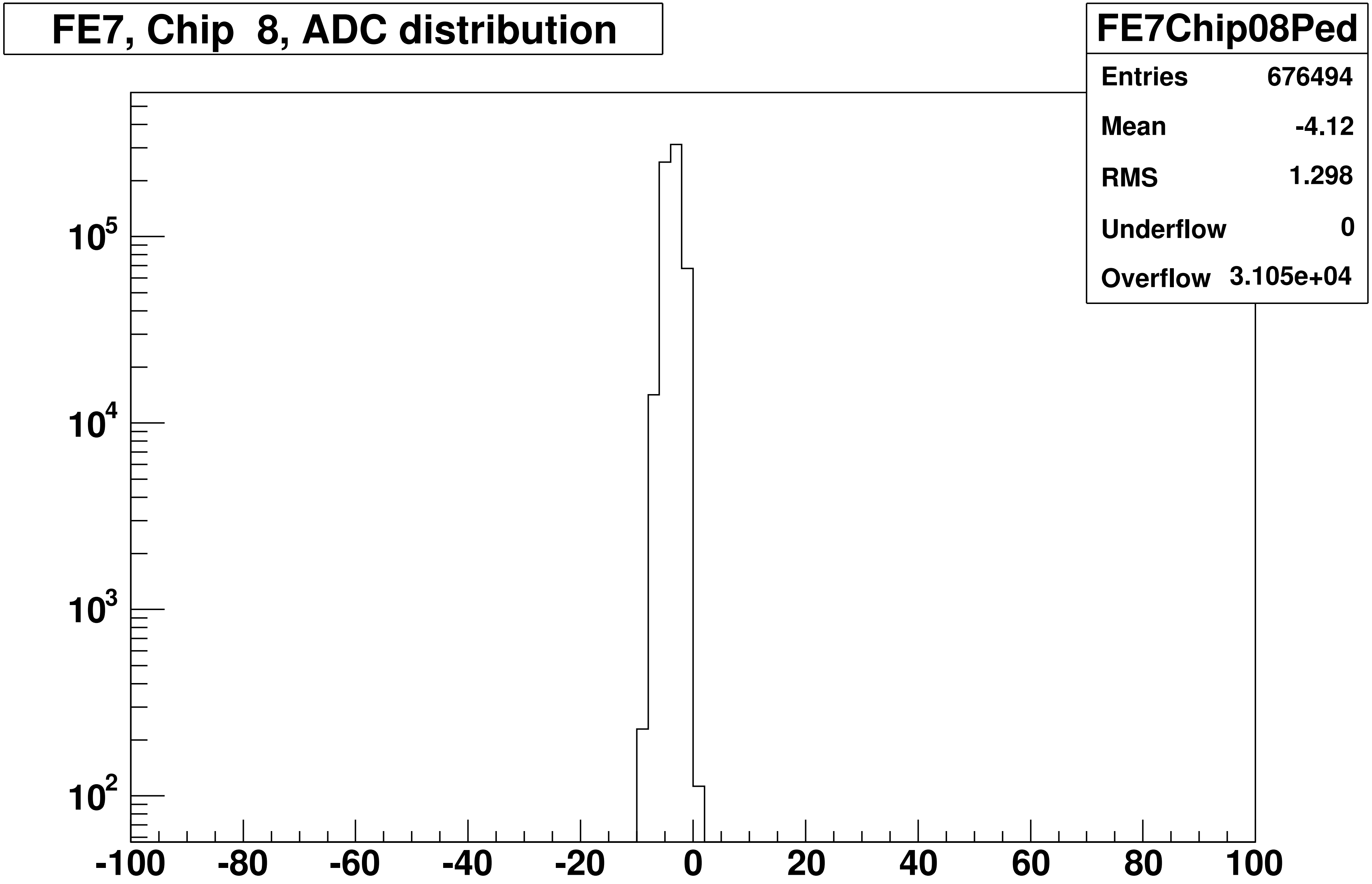
FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 676494
Mean 0.133
RMS 1.234
Underflow 0
Overflow 3.101e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

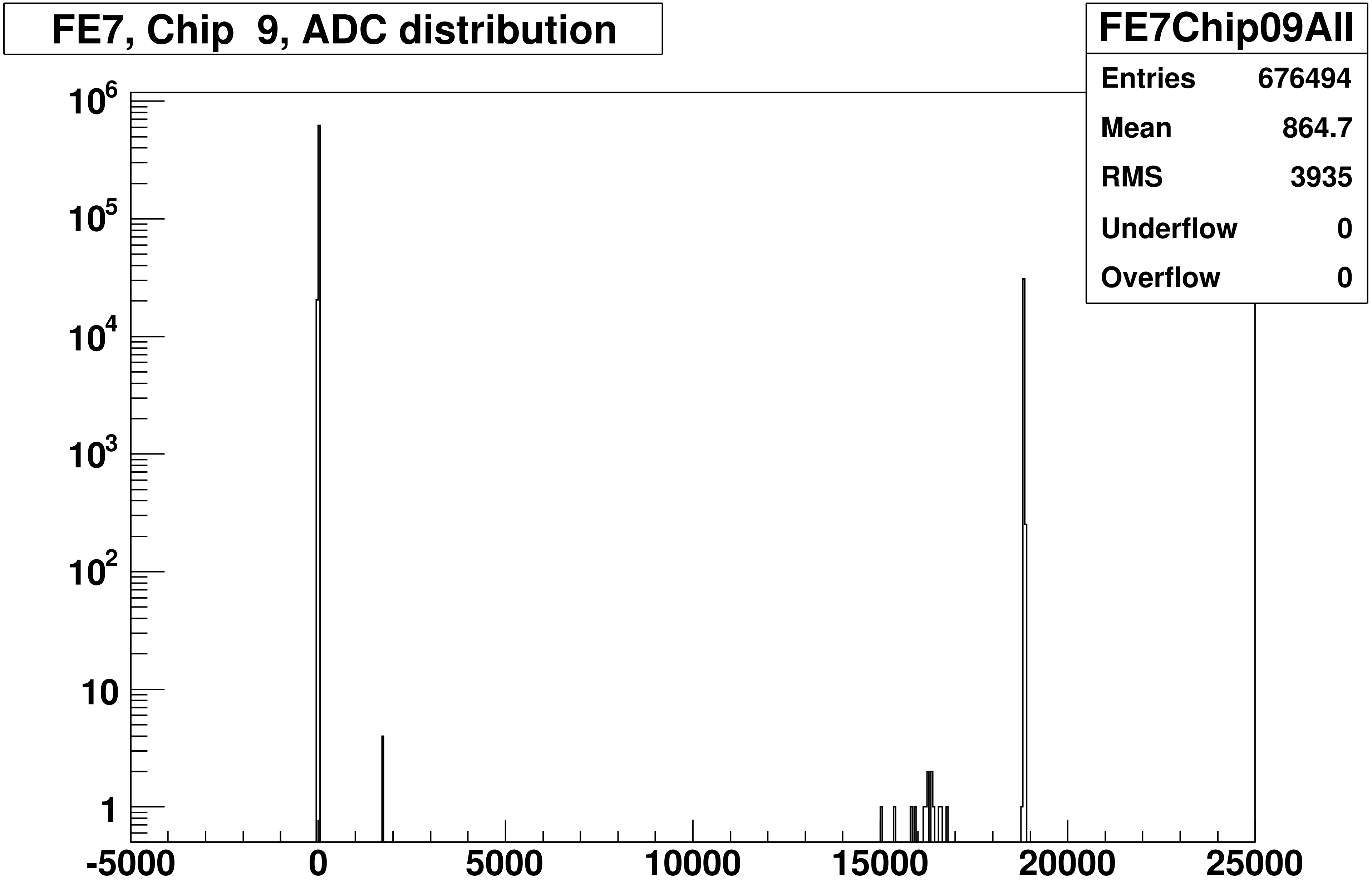

FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 676494
Mean 1.885e+04
RMS 11.92
Underflow 6.455e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 676494
Mean 855.6
RMS 3909
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

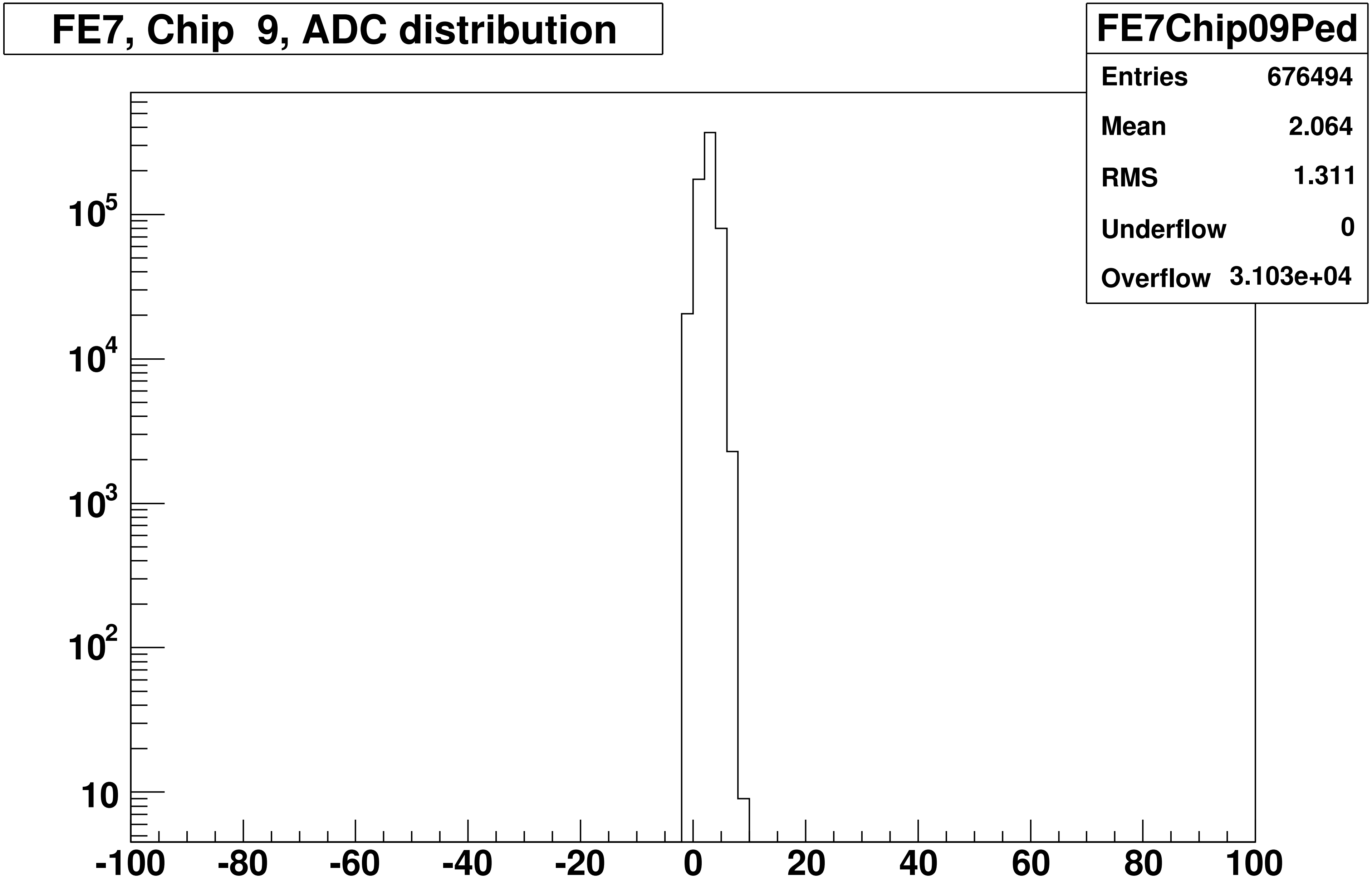

FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 676494
Mean -1.491
RMS 0.9288
Underflow 0
Overflow 3.103e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

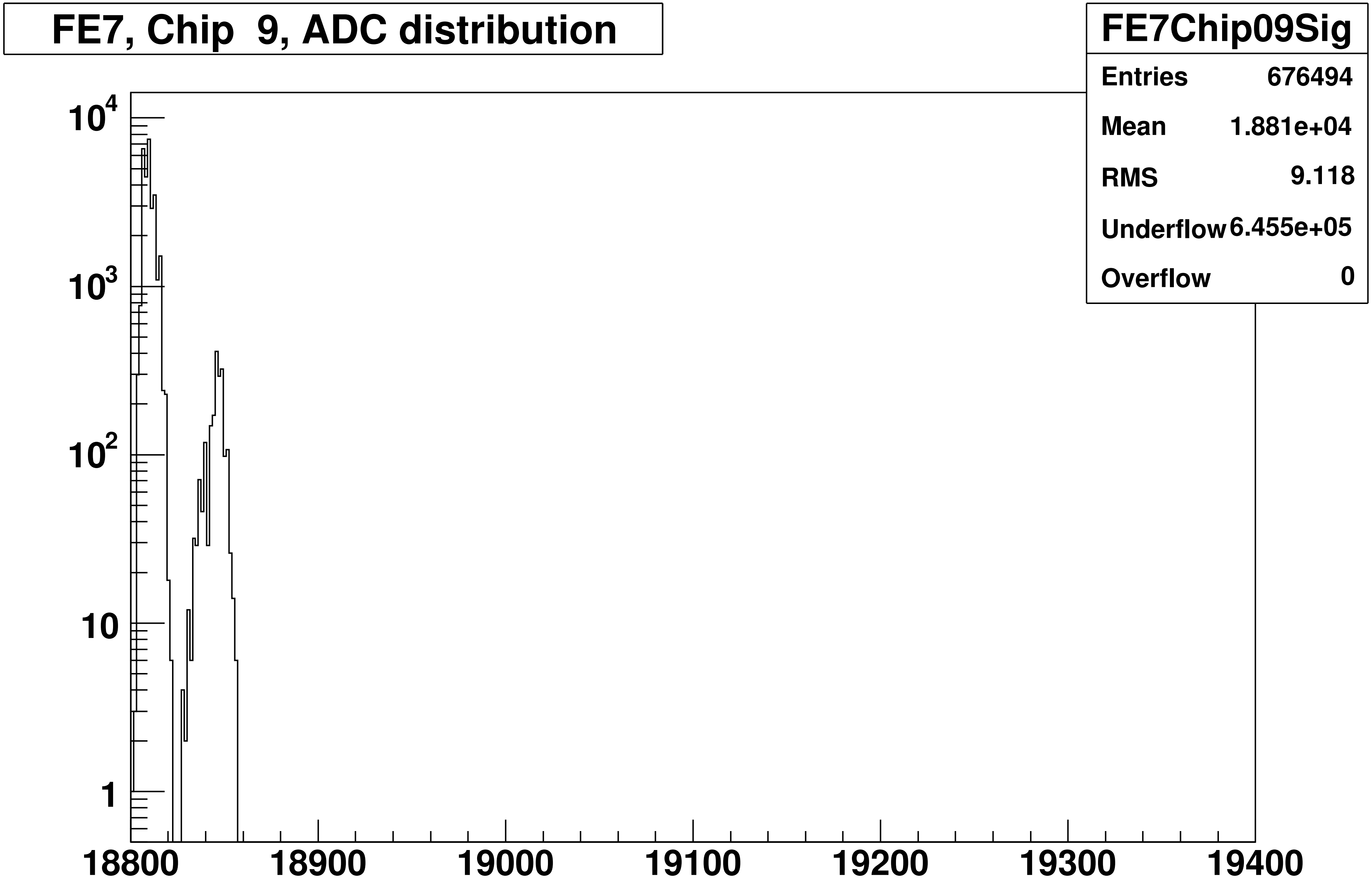

FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 676494
Mean 807.7
RMS 3807
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 676494
Mean -4.12
RMS 1.298
Underflow 0
Overflow 3.105e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

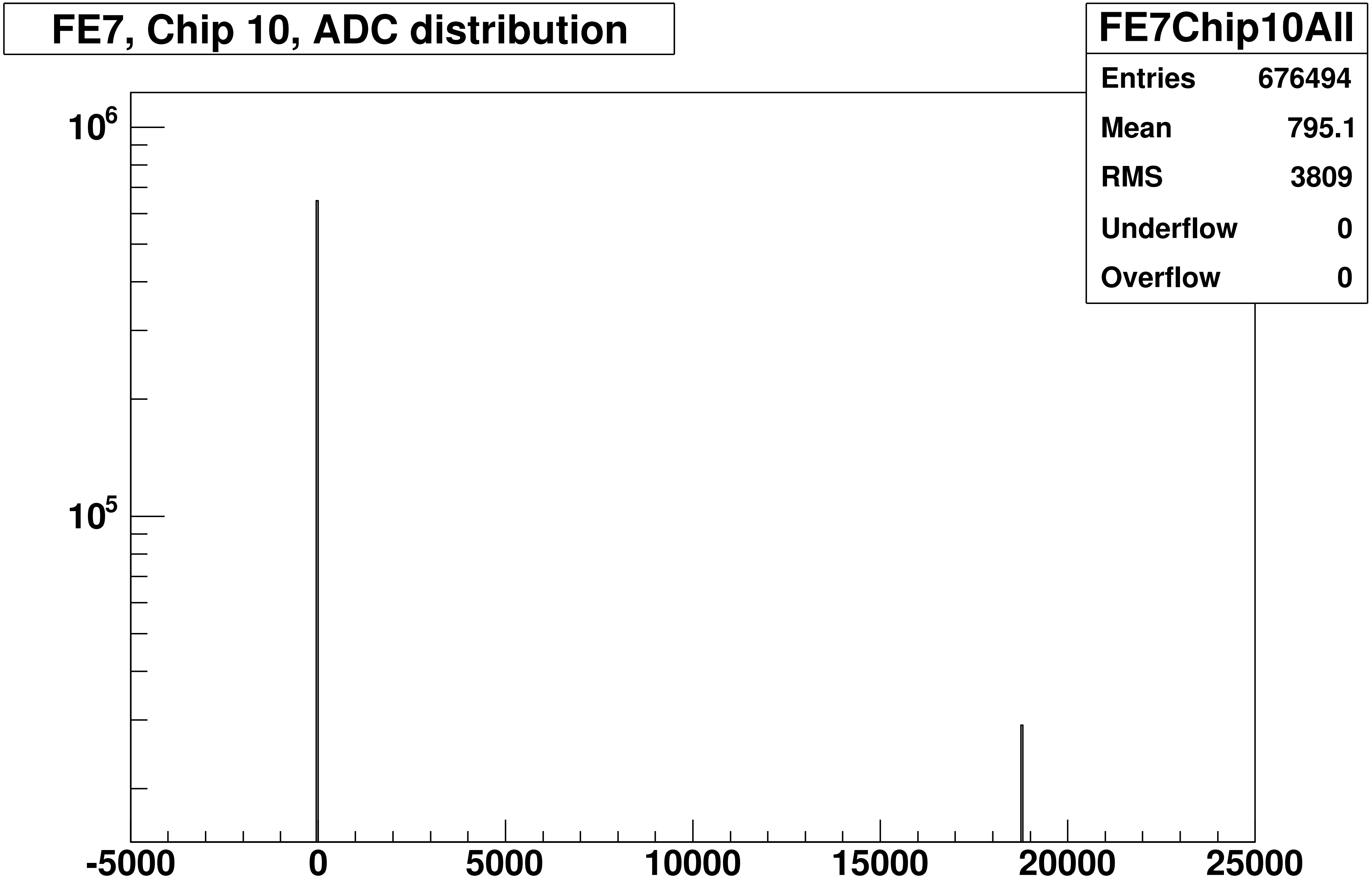


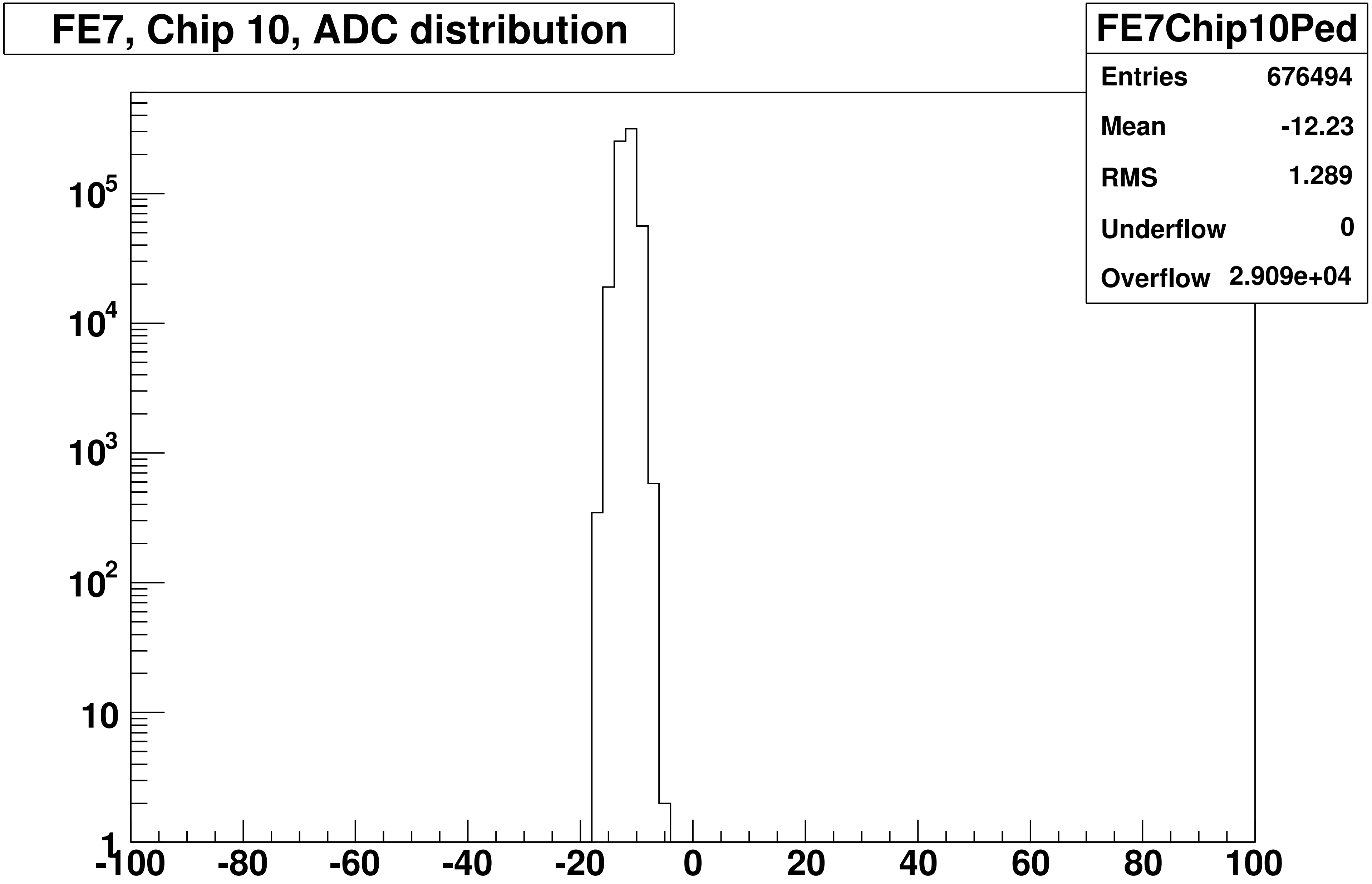
FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 676494
Mean 864.7
RMS 3935
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 676494
Mean 2.064
RMS 1.311
Underflow 0
Overflow 3.103e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

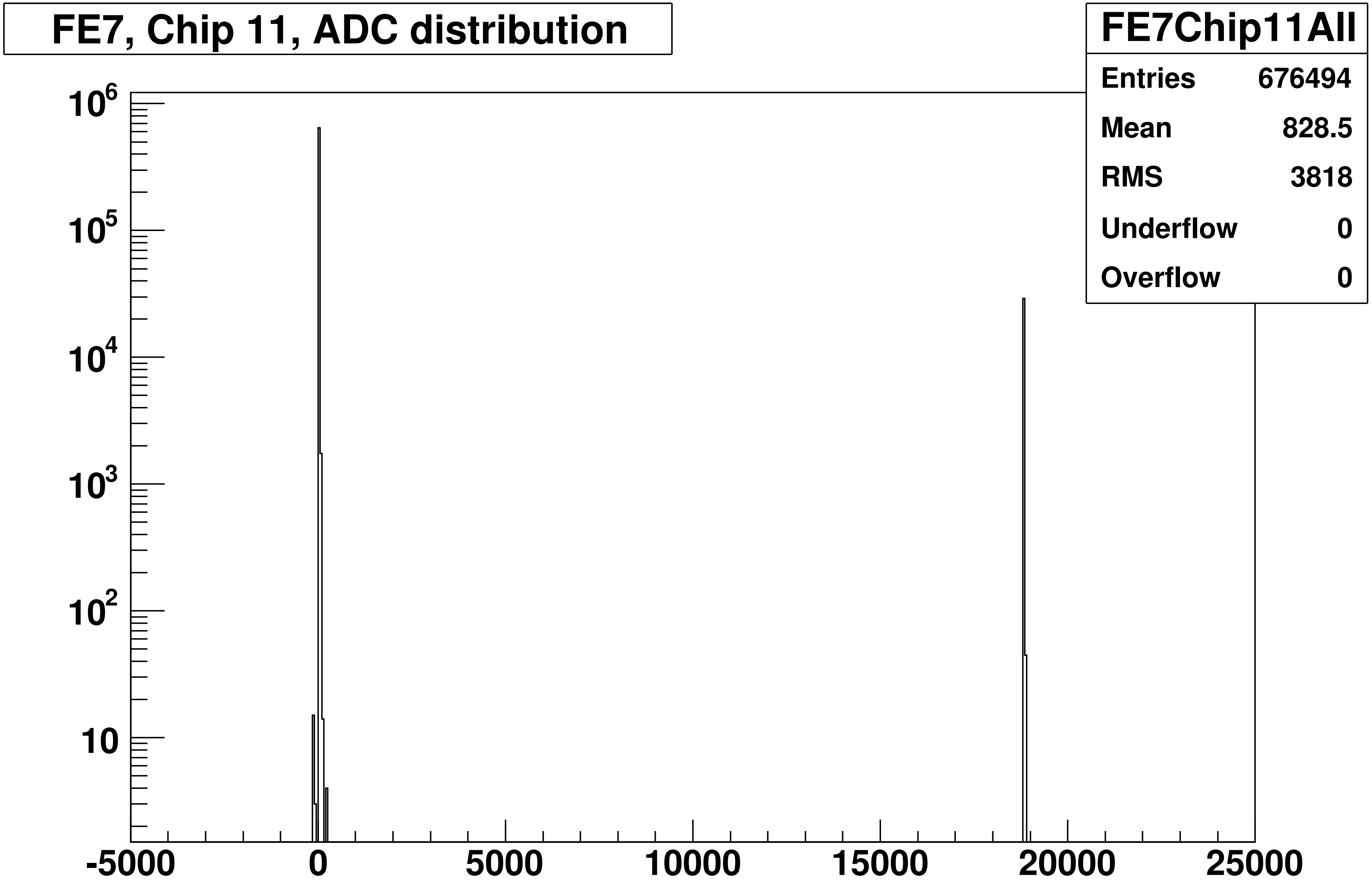


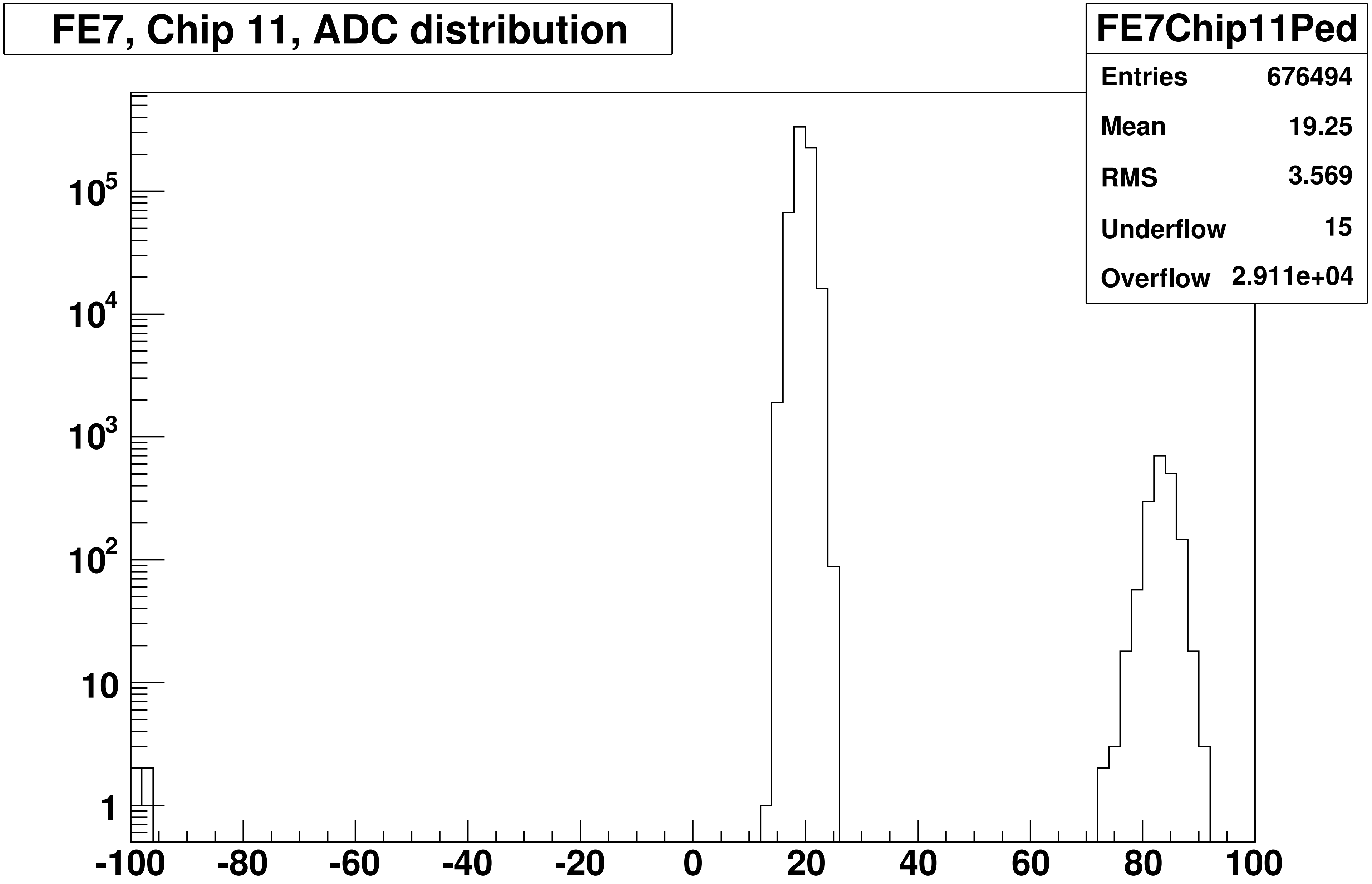
FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 676494
Mean 1.881e+04
RMS 9.118
Underflow 6.455e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 676494
Mean 795.1
RMS 3809
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000

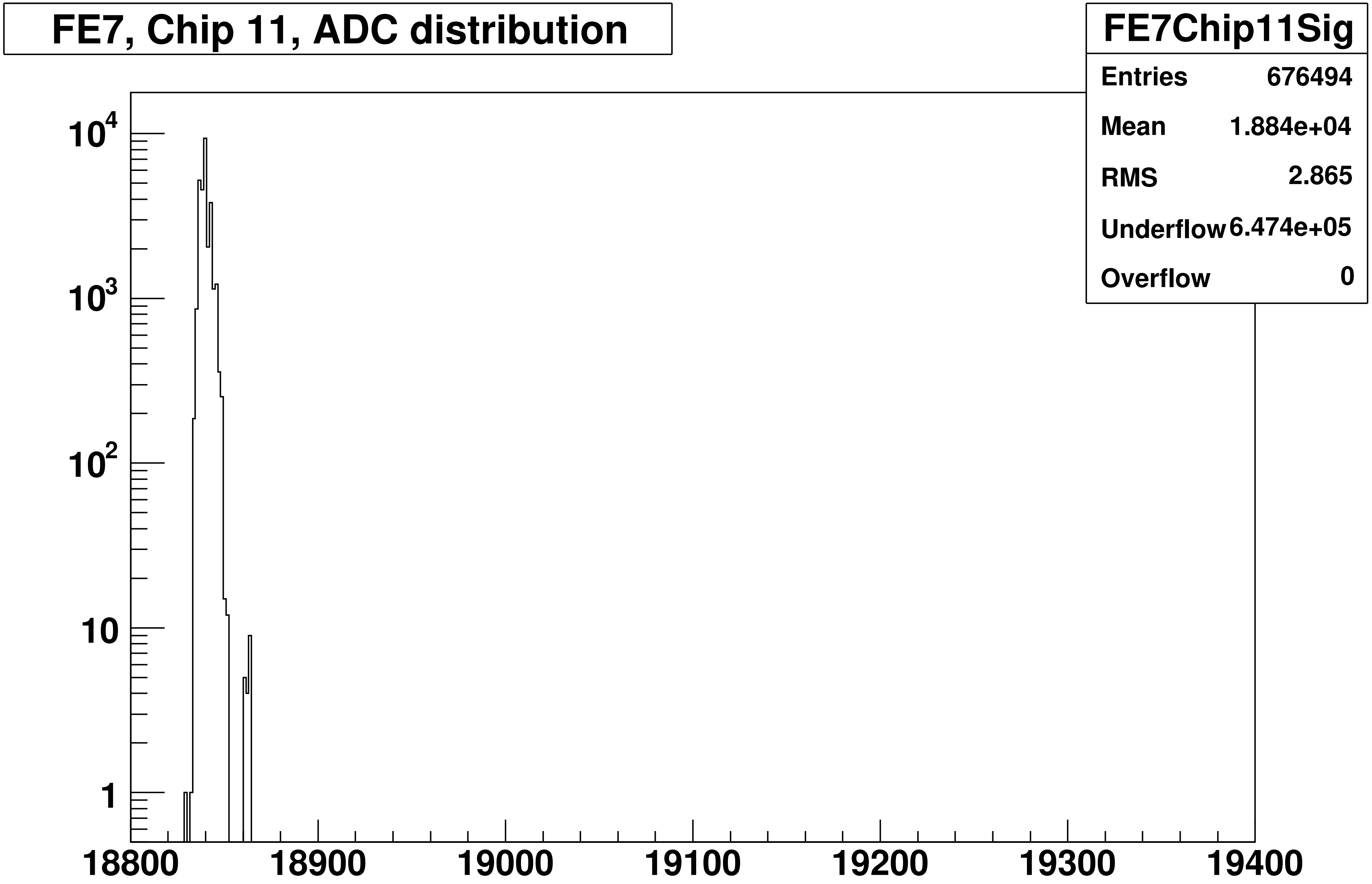


FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 676494
Mean -12.23
RMS 1.289
Underflow 0
Overflow 2.909e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

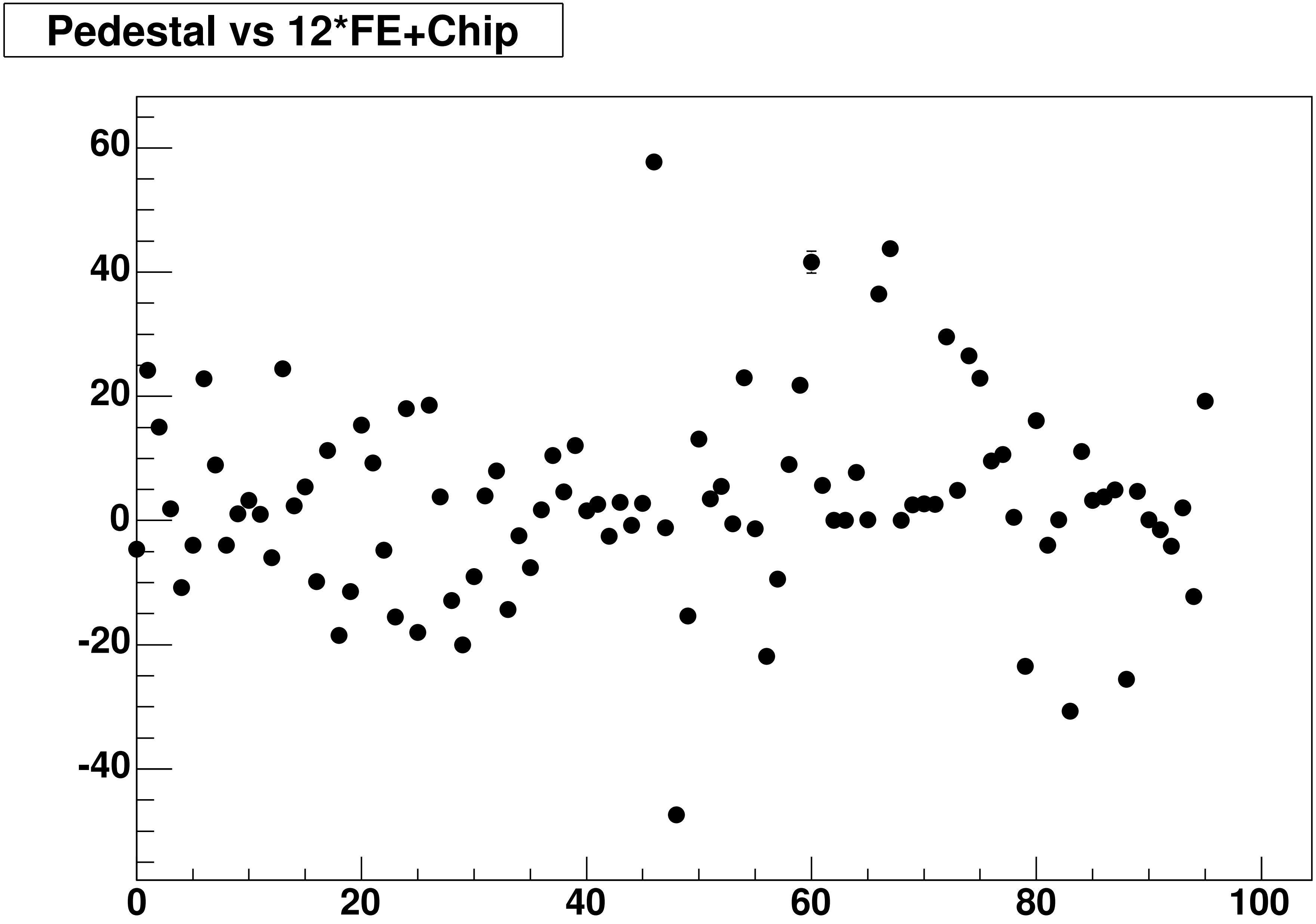

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 676494
Mean 828.5
RMS 3818
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

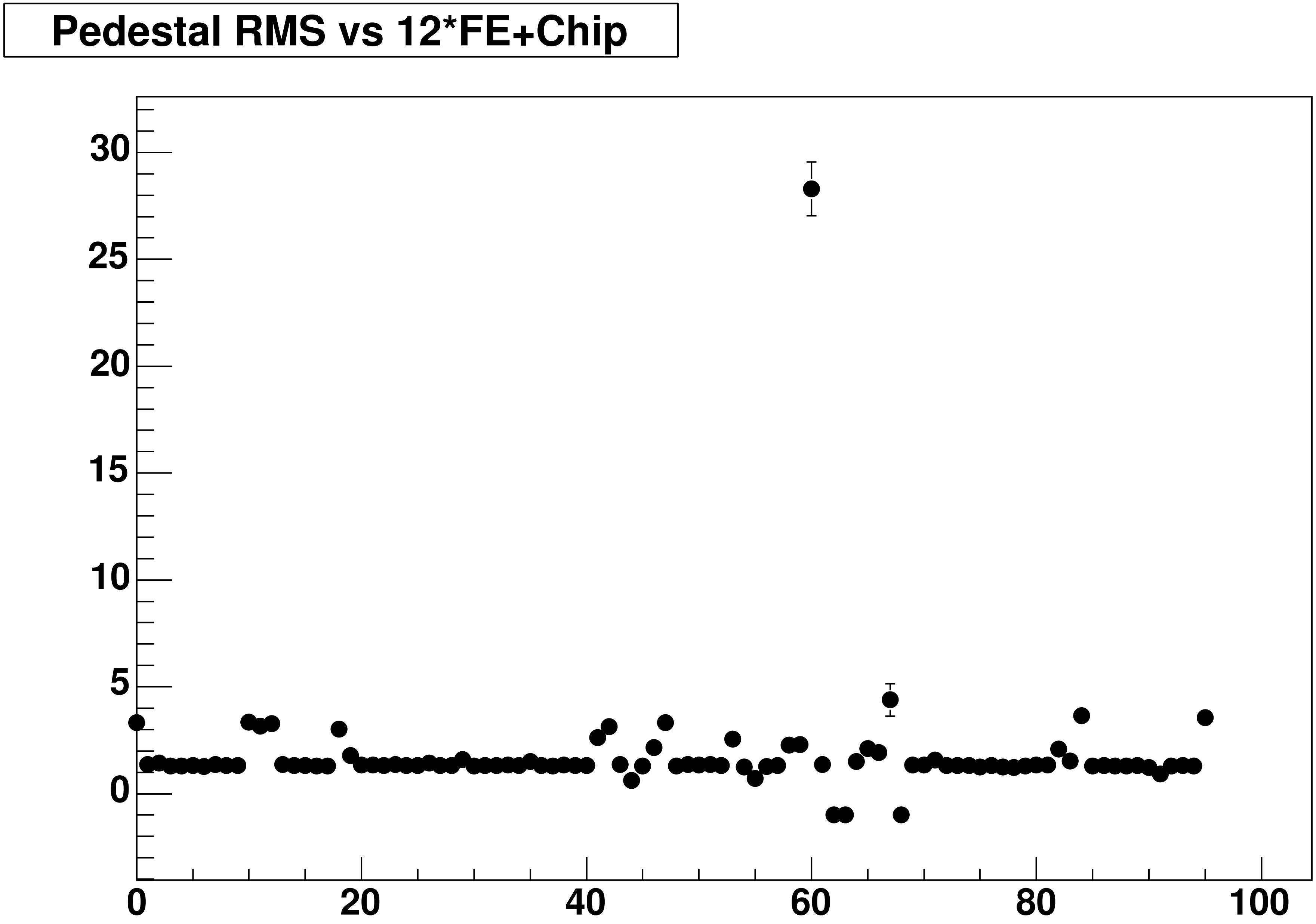


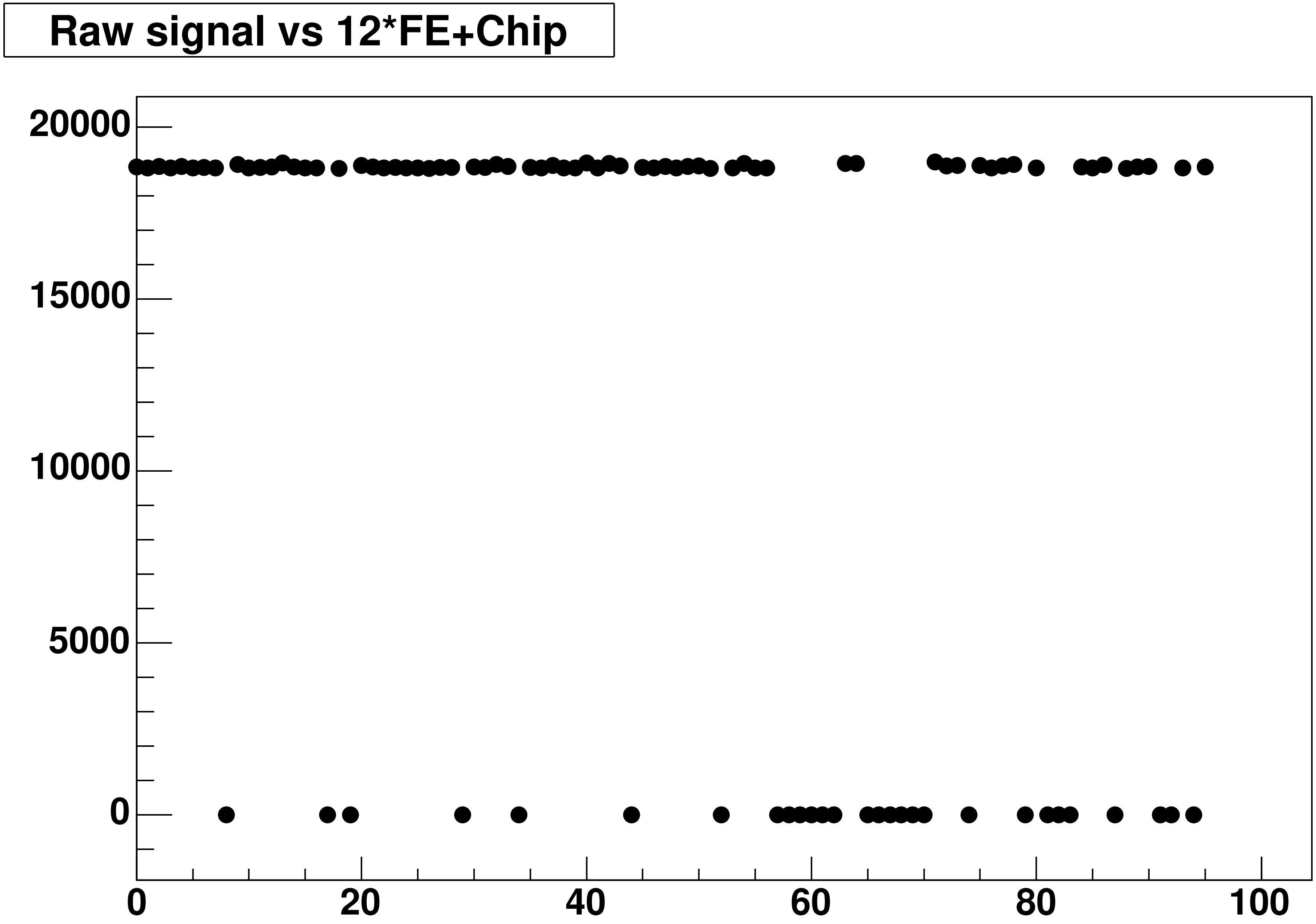
FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 676494
Mean 19.25
RMS 3.569
Underflow 15
Overflow 2.911e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

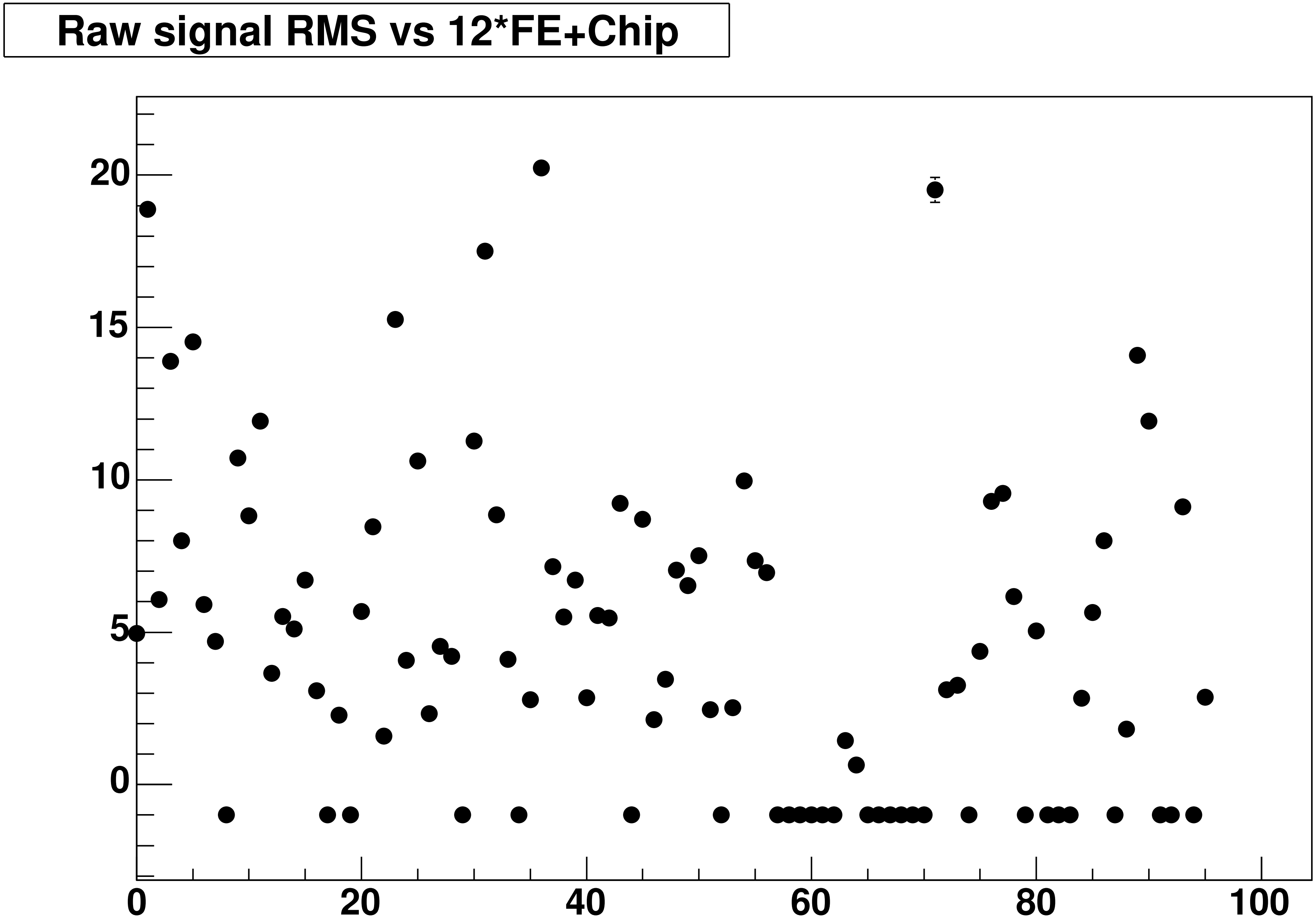


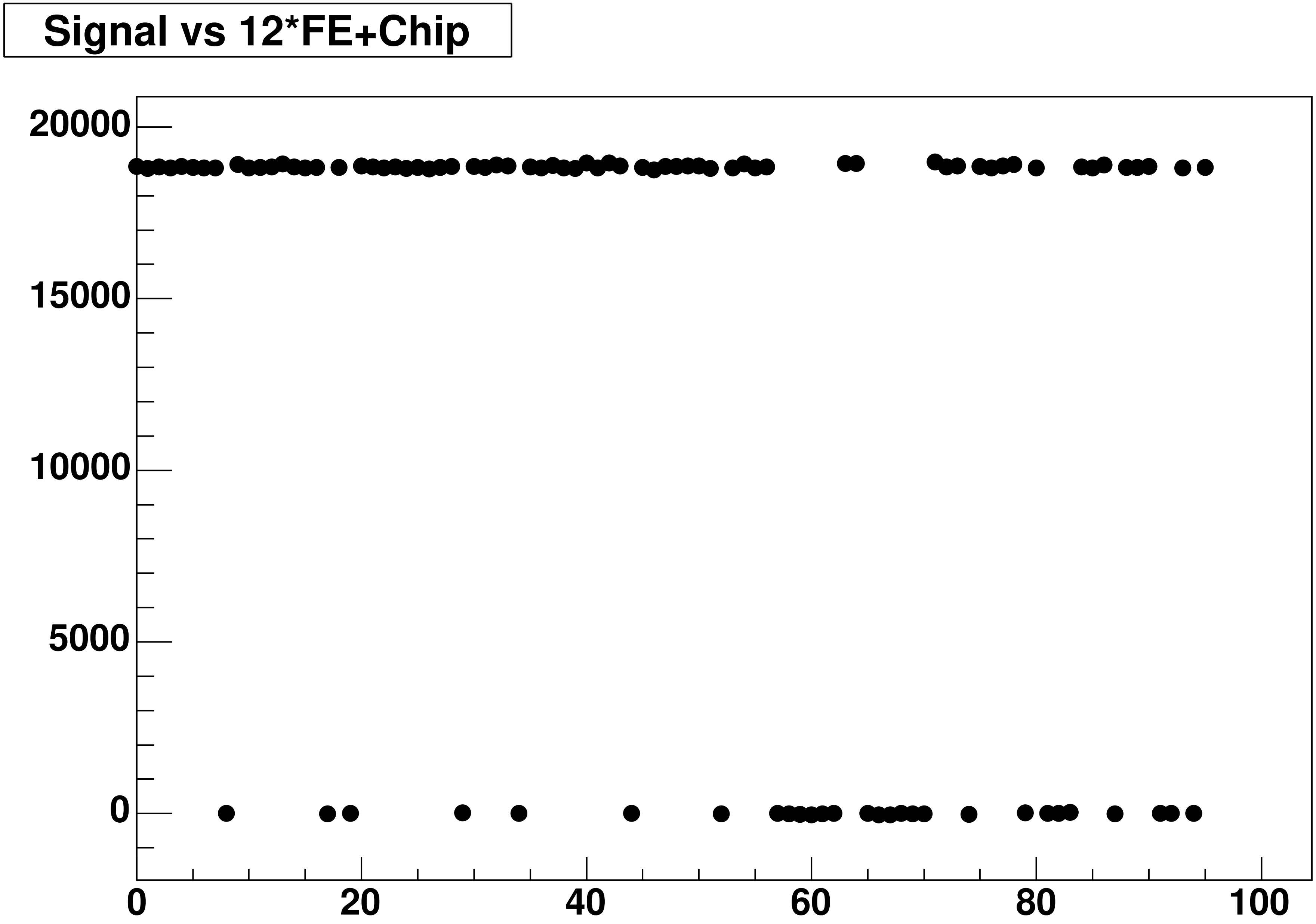
FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 676494
Mean 1.884e+04
RMS 2.865
Underflow 6.474e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

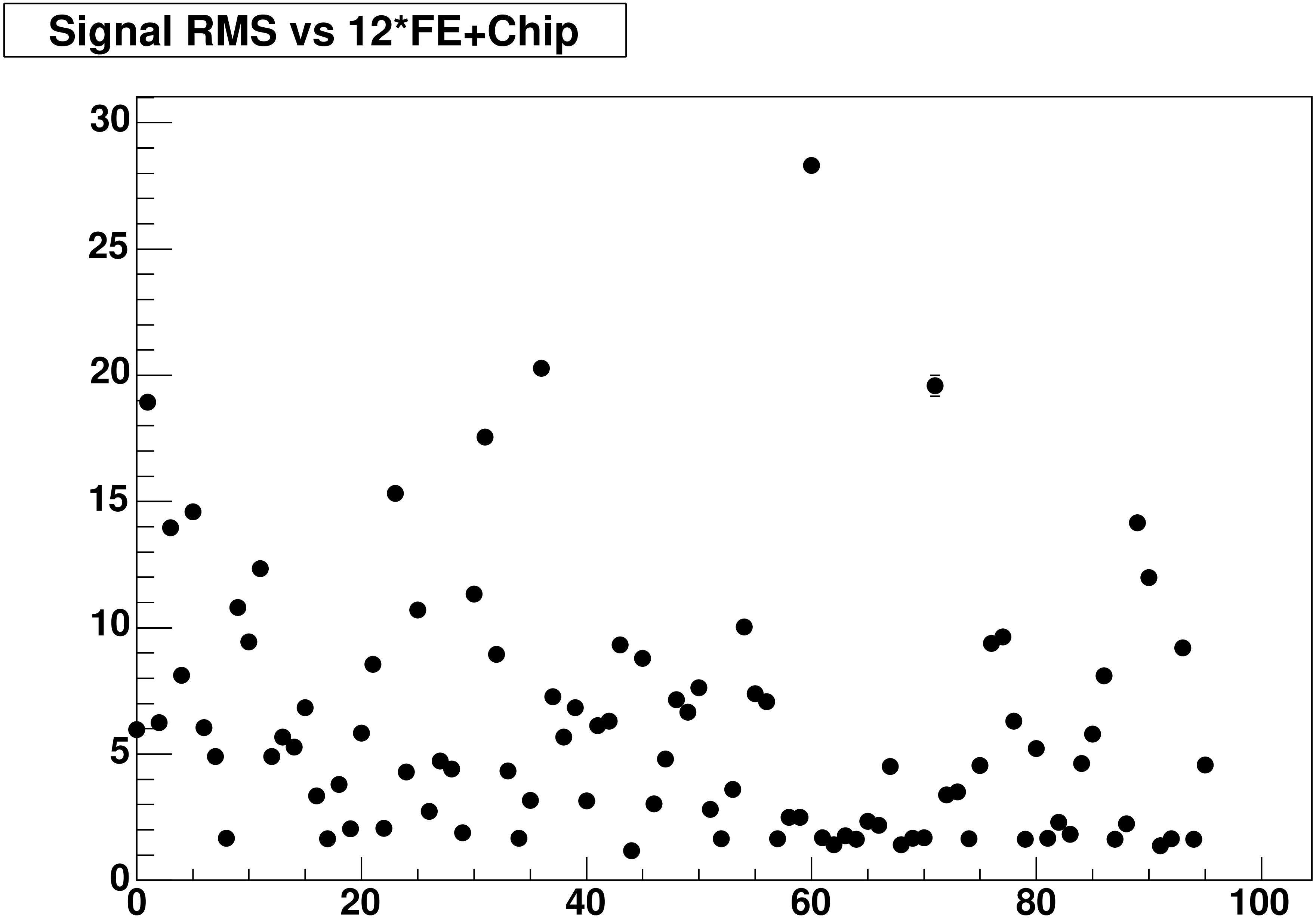

Pedestal vs 12*FE+Chip
60
40
20
0
-20
-40
0
20
40
60
80
100


Pedestal RMS vs 12*FE+Chip
0
5
10
15
20
25
30
0
20
40
60
80
100


Raw signal vs 12*FE+Chip
20000
15000
10000
5000
0
0
20
40
60
80
100


Raw signal RMS vs 12*FE+Chip
20
15
10
5
0
0
20
40
60
80
100


Signal vs 12*FE+Chip
20000
15000
10000
5000
0
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
0
5
10
15
20
25
30
0
20
40
60
80
100