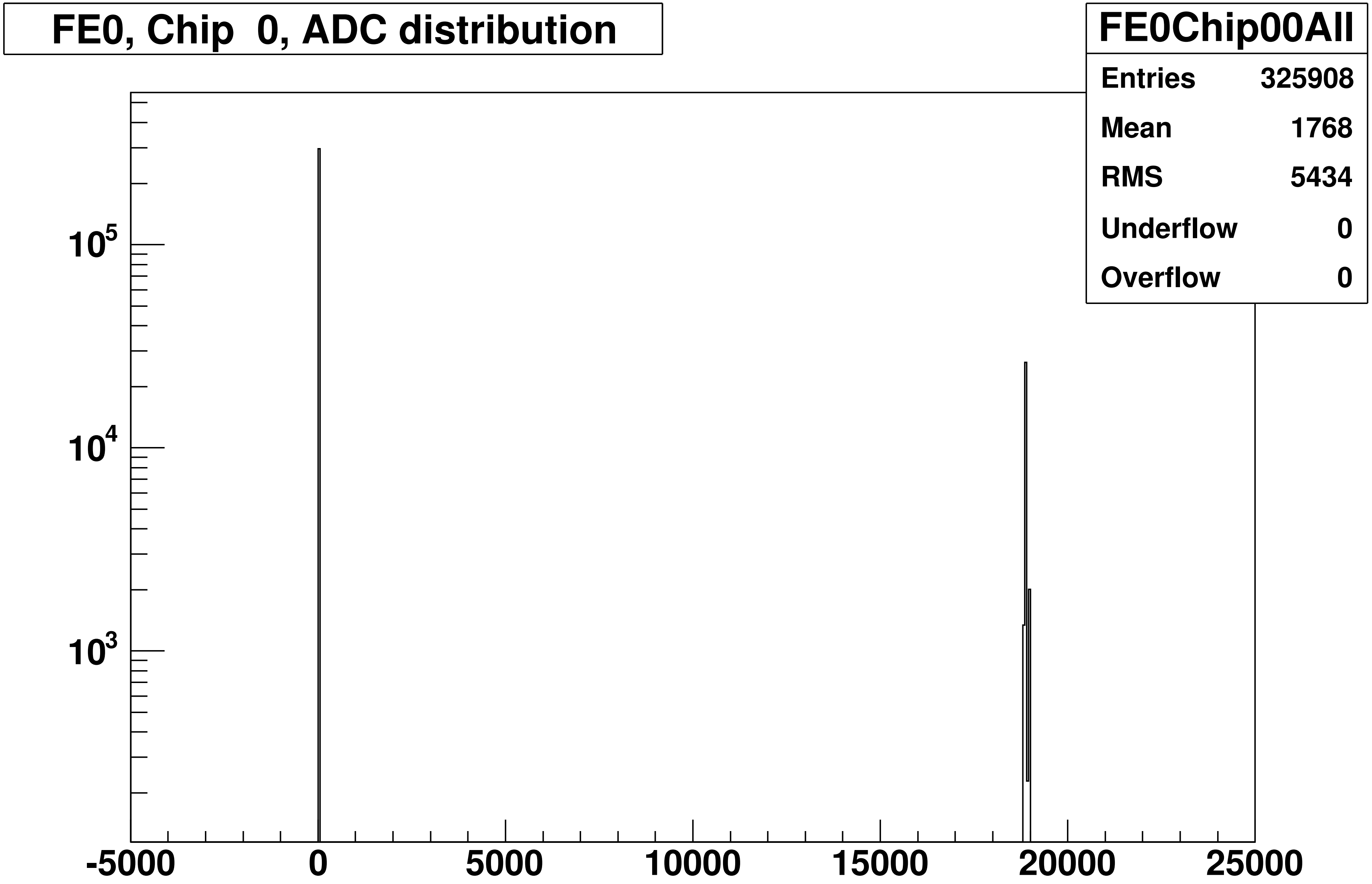
FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 325908
Mean 1768
RMS 5434
Underflow 0
Overflow 0
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10^4
10^3
-5000
0
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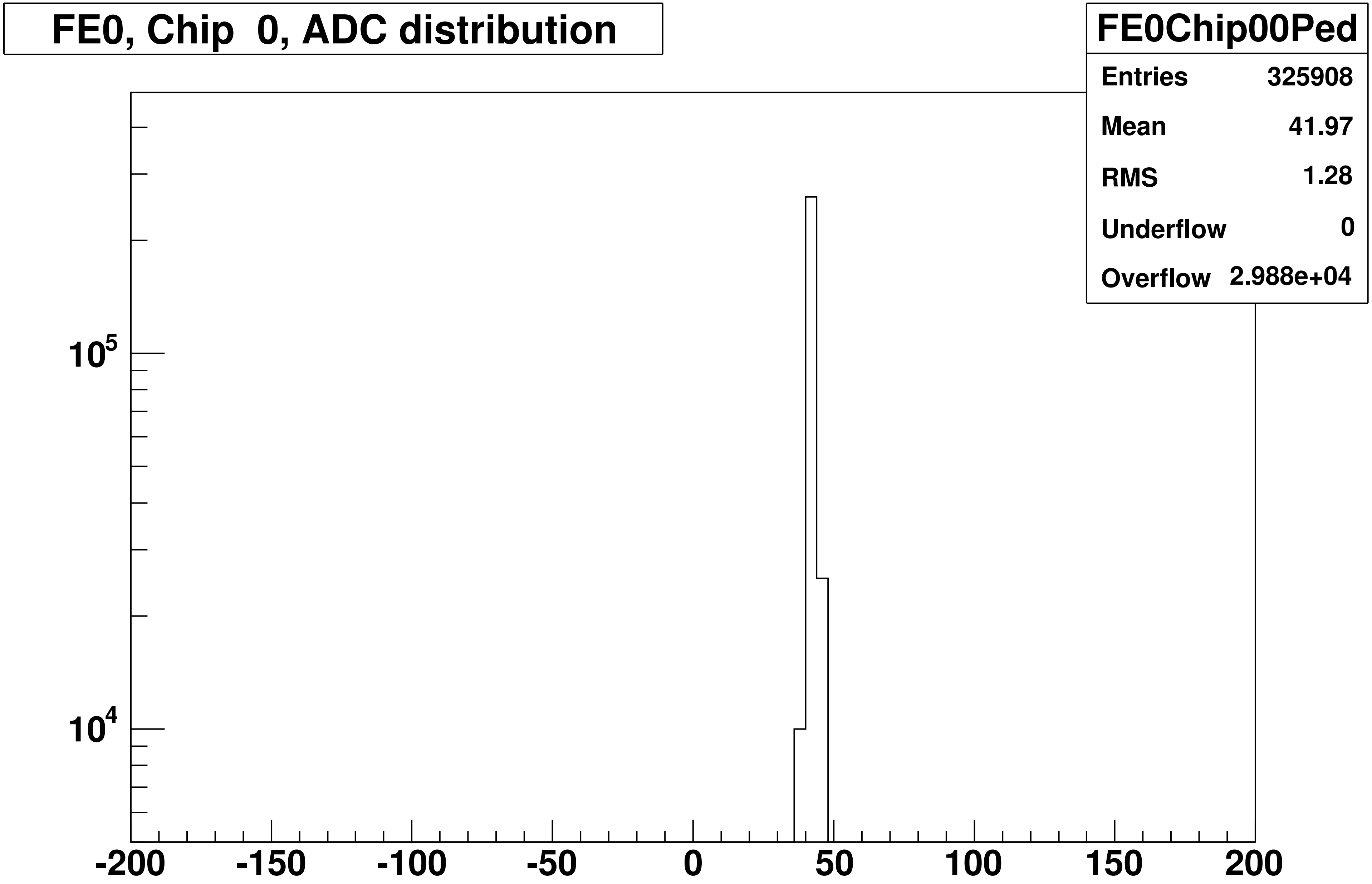
10000
15000
20000
25000


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 325908
Mean 41.97
RMS 1.28
Underflow 0
Overflow 2.988e+04
10^5
10^4
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-100
-50
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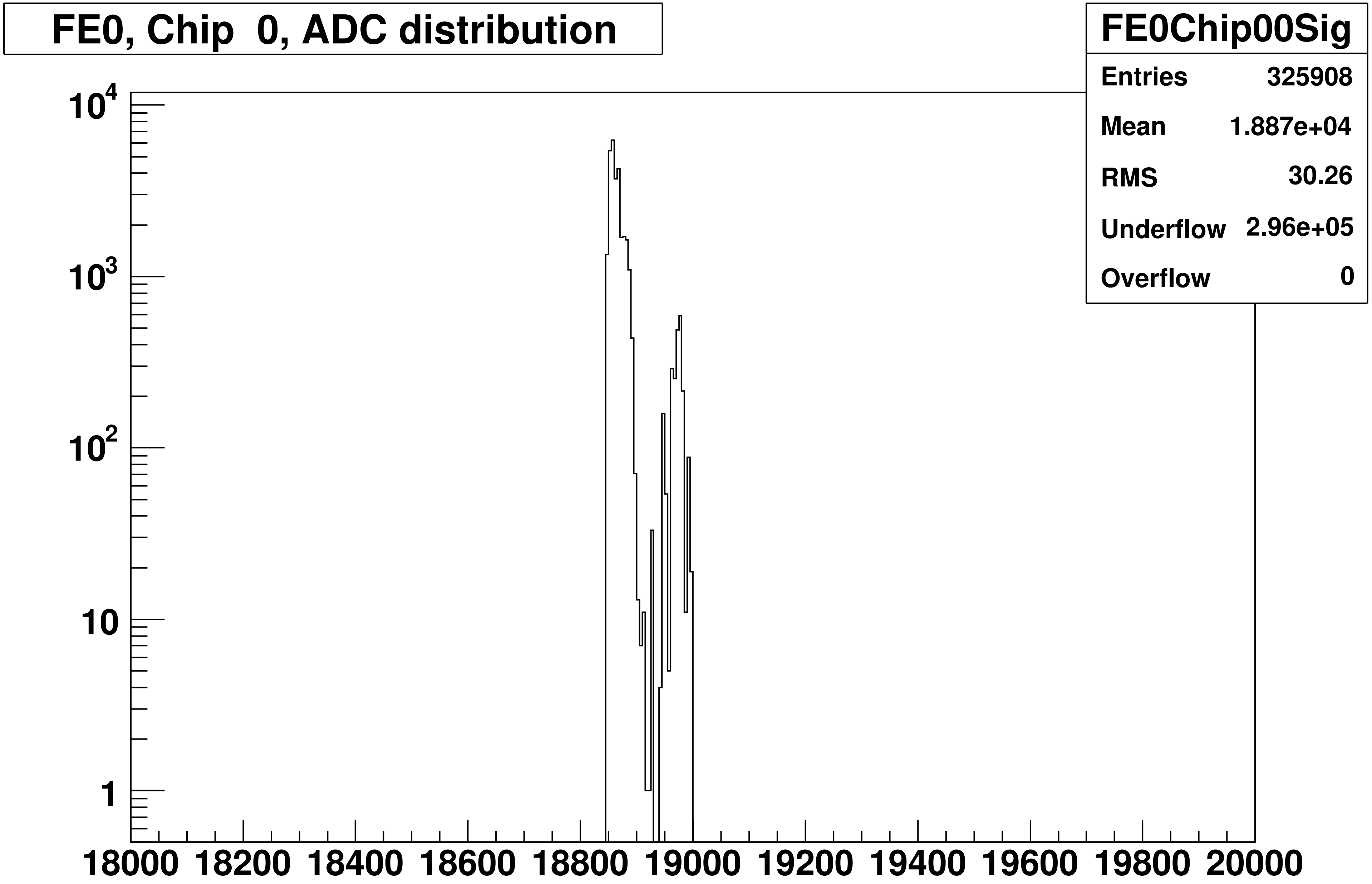
100
150
200


FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 325908
Mean 1.887e+04
RMS 30.26
Underflow 2.96e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
18400
18600
18800
19000
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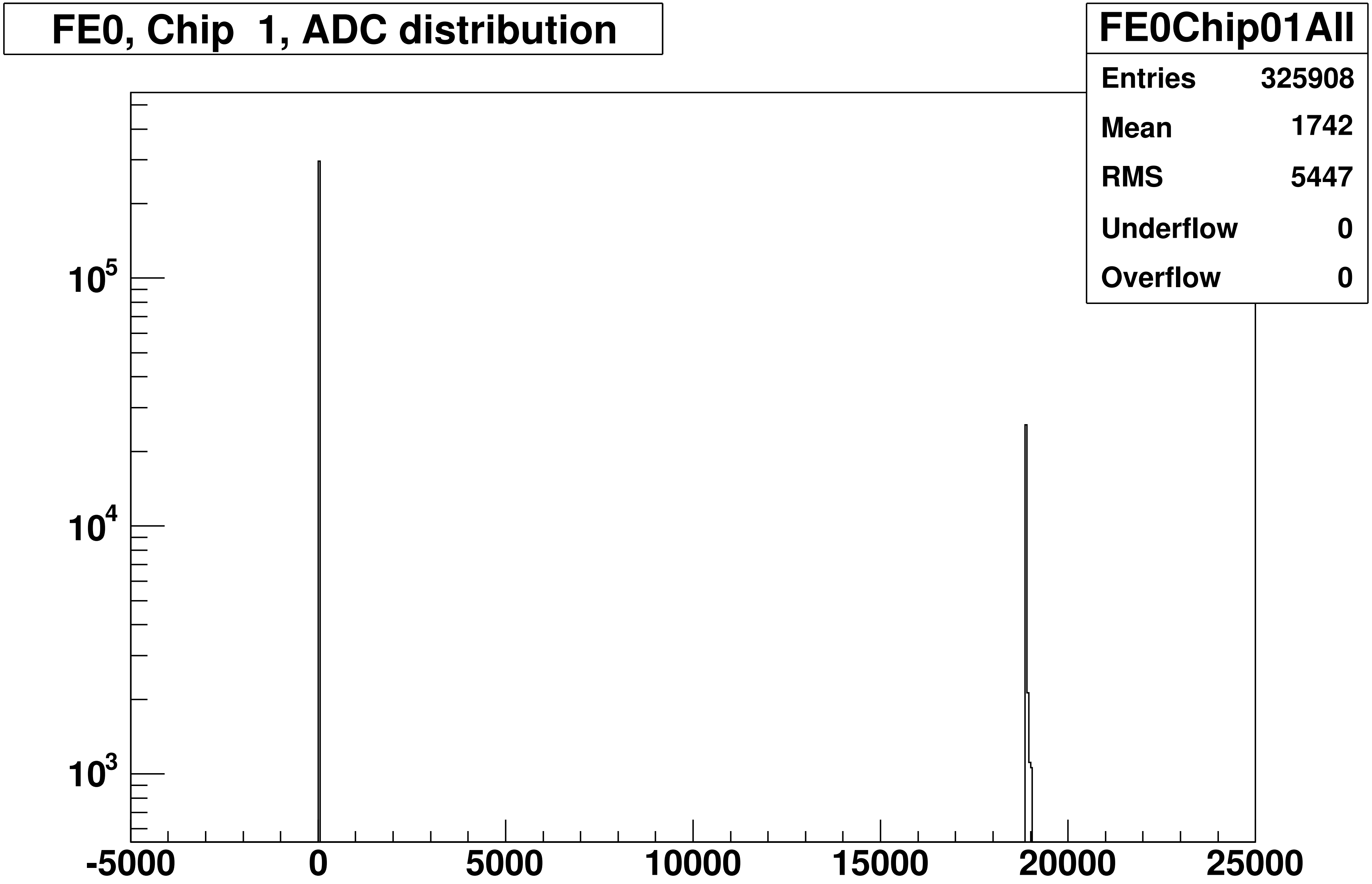
19800
20000


FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 325908
Mean 1742
RMS 5447
Underflow 0
Overflow 0
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10^4
10^3
-5000
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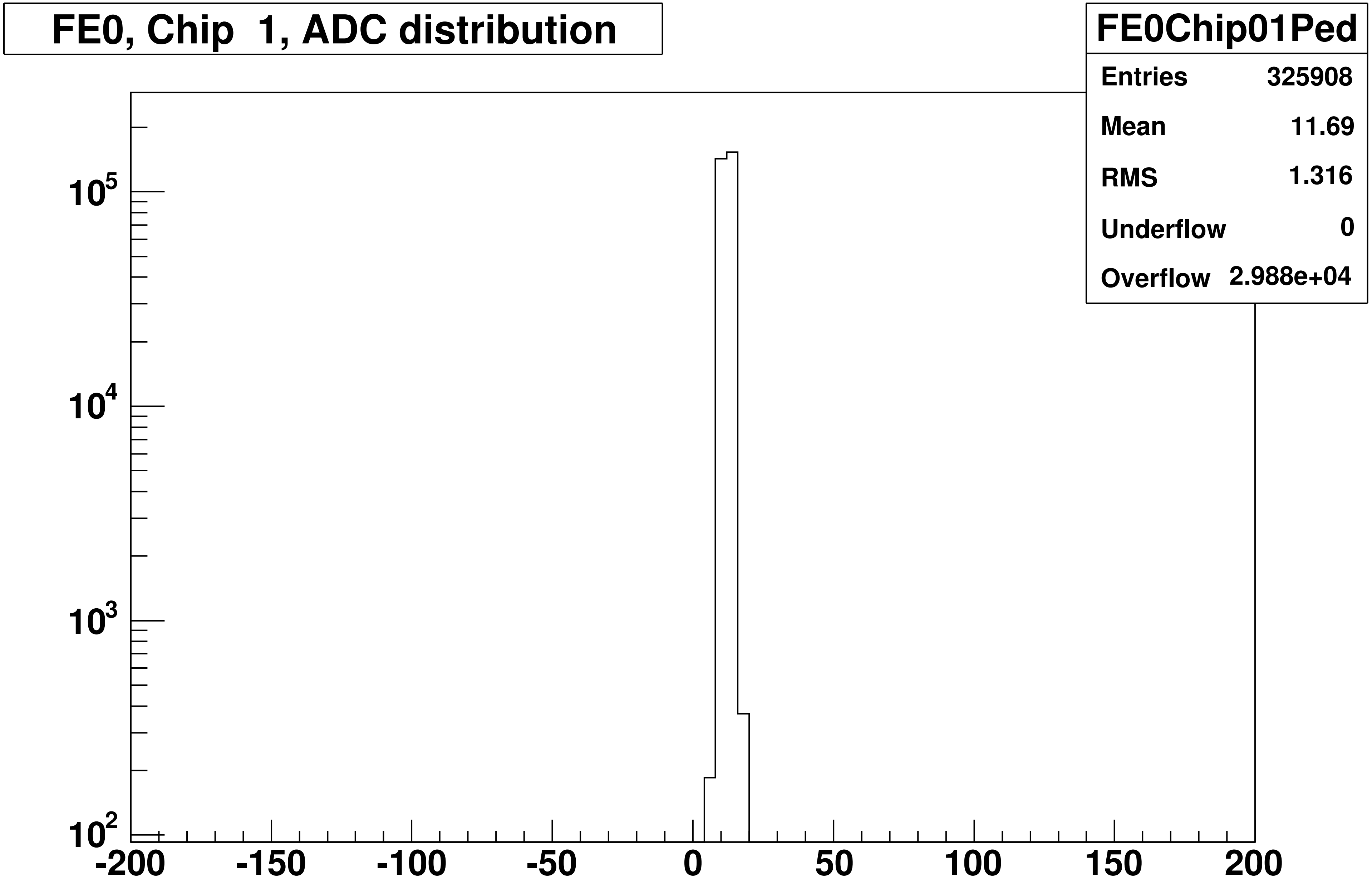
10000
15000
20000
25000


FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 325908
Mean 11.69
RMS 1.316
Underflow 0
Overflow 2.988e+04
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10^4
10^3
10^2
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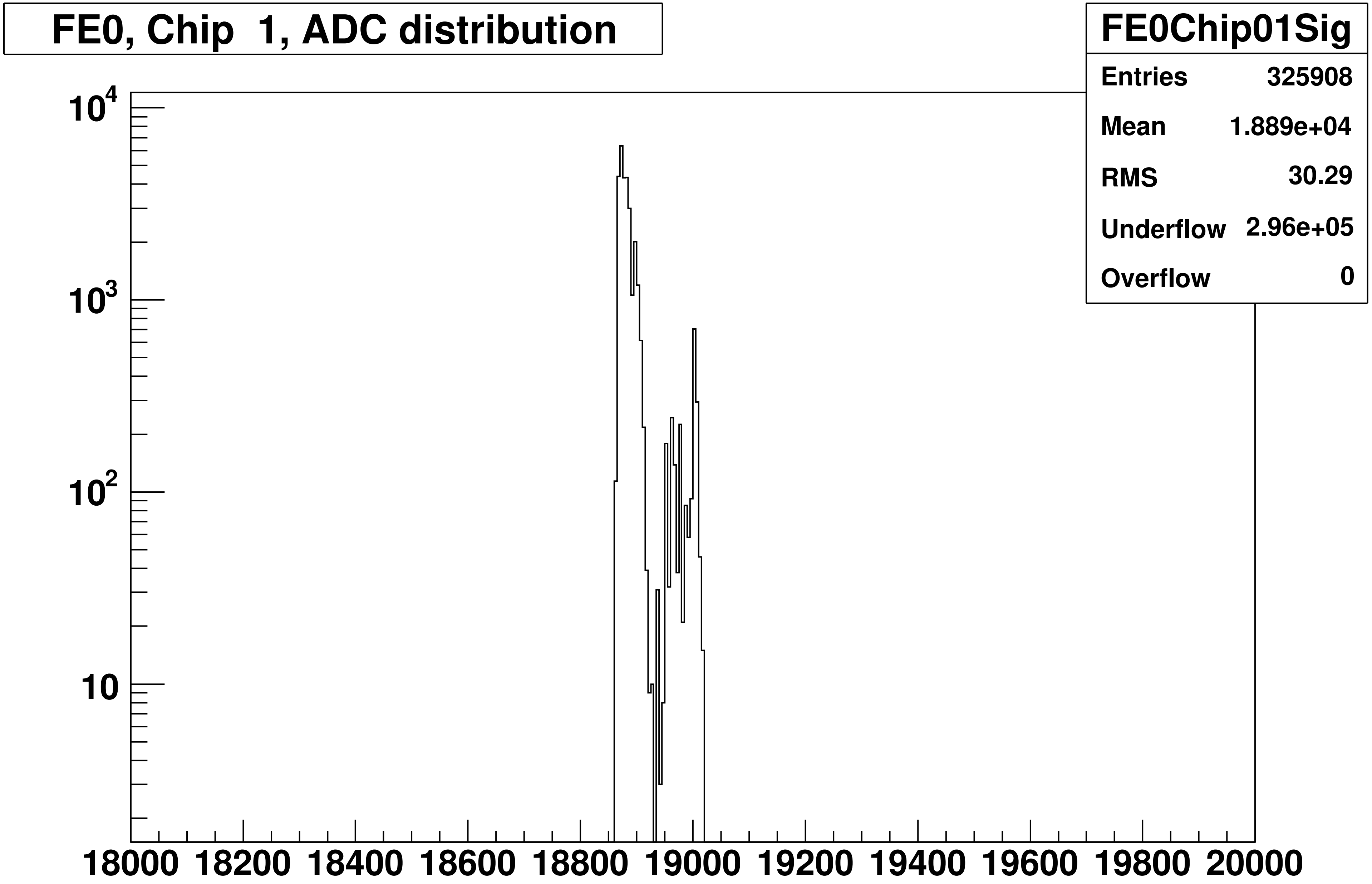
150
200


FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 325908
Mean 1.889e+04
RMS 30.29
Underflow 2.96e+05
Overflow 0
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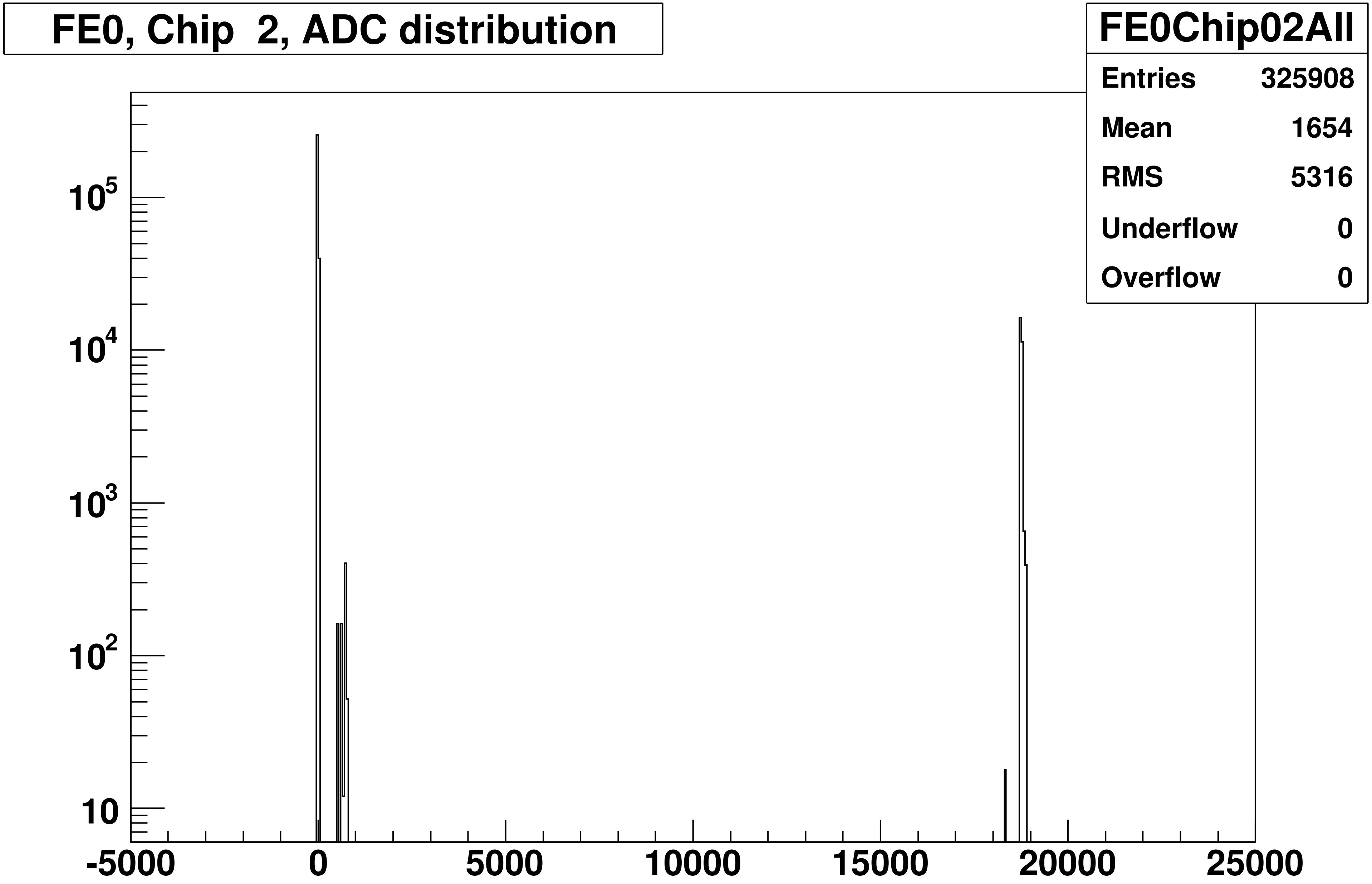
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 325908
Mean 1654
RMS 5316
Underflow 0
Overflow 0
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10^4
10^3
10^2
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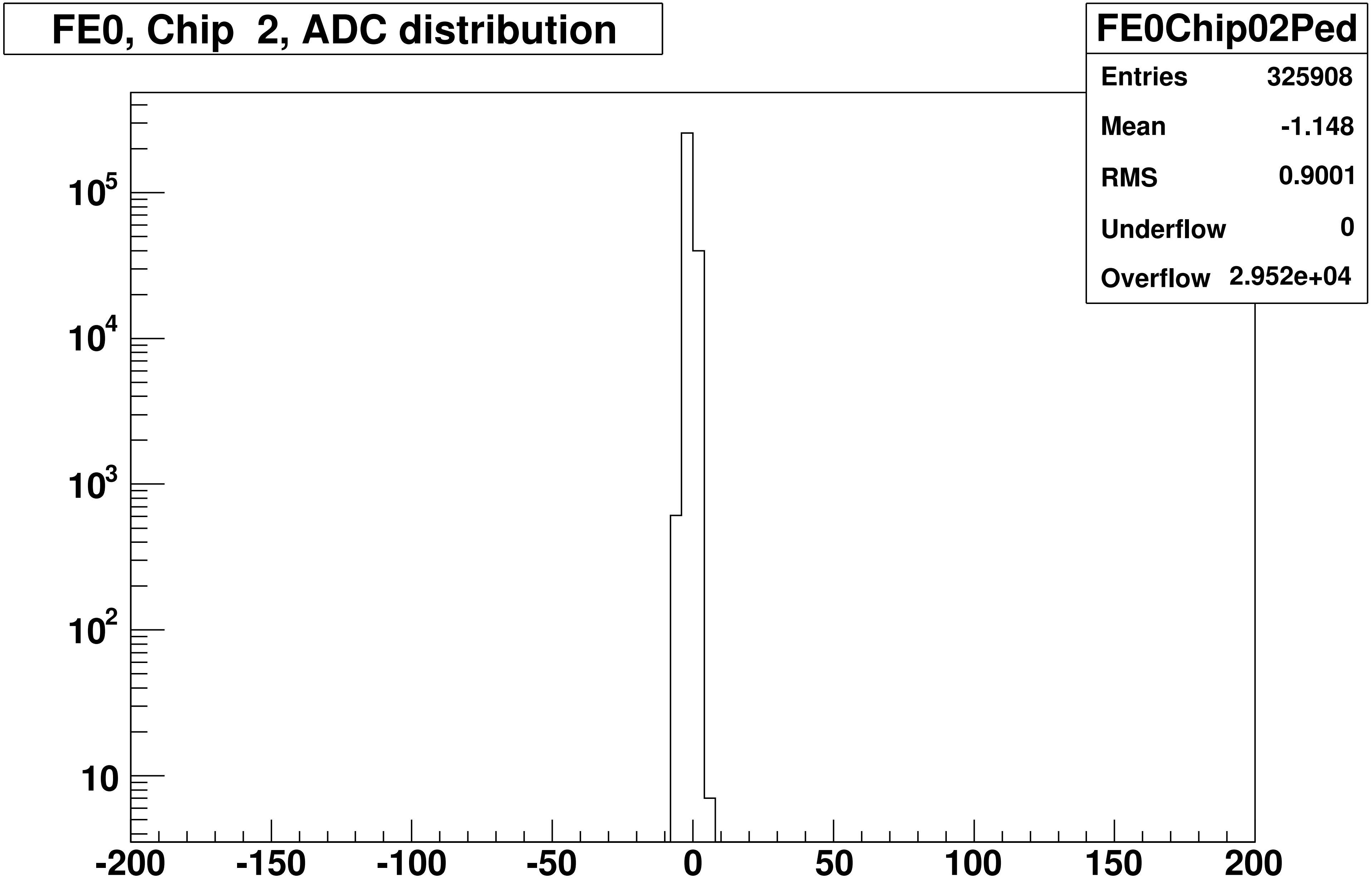
10000
15000
20000
25000



FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 325908
Mean -1.148
RMS 0.9001
Underflow 0
Overflow 2.952e+04
10^5
10^4
10^3
10^2
10
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-100
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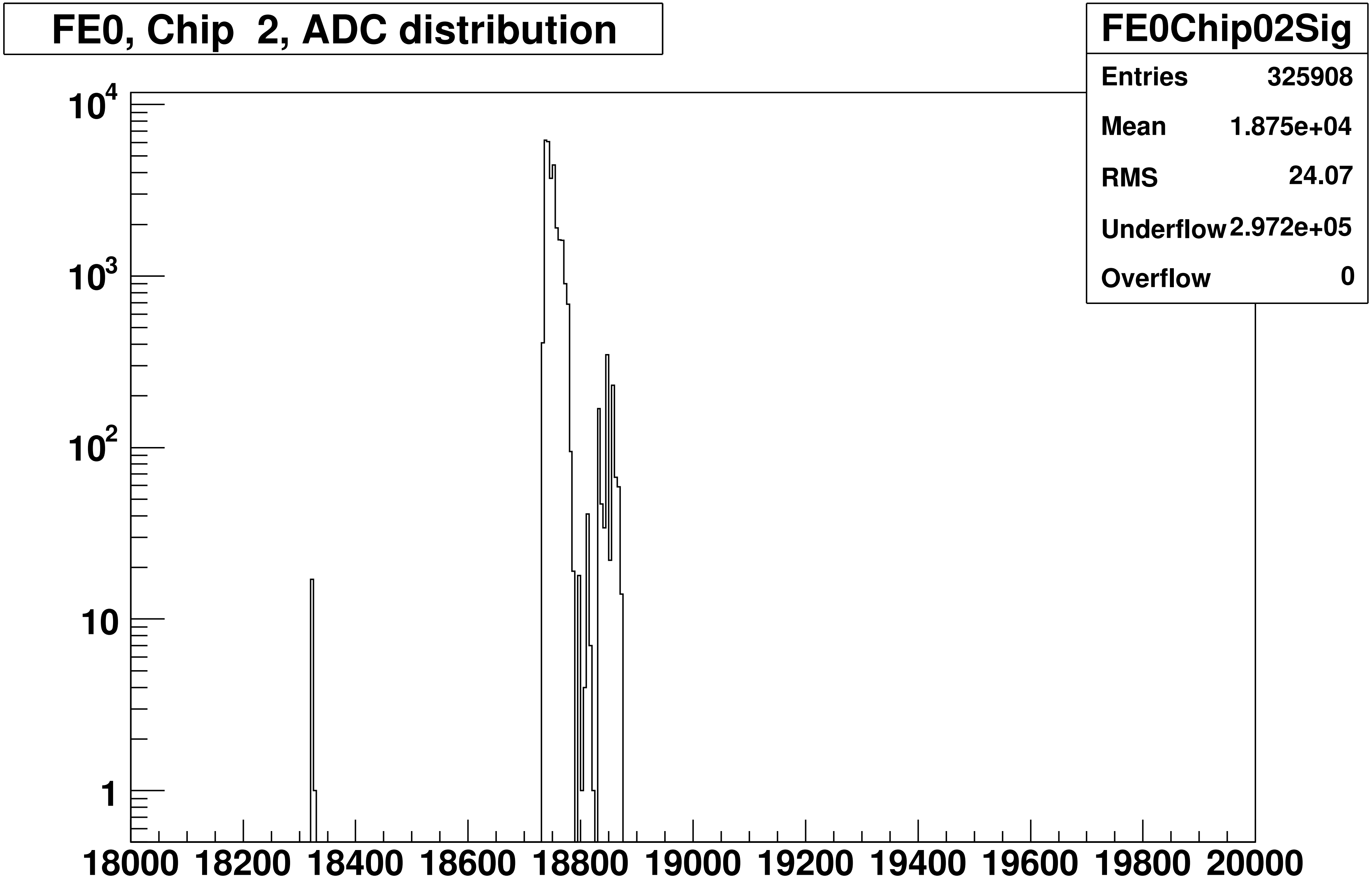
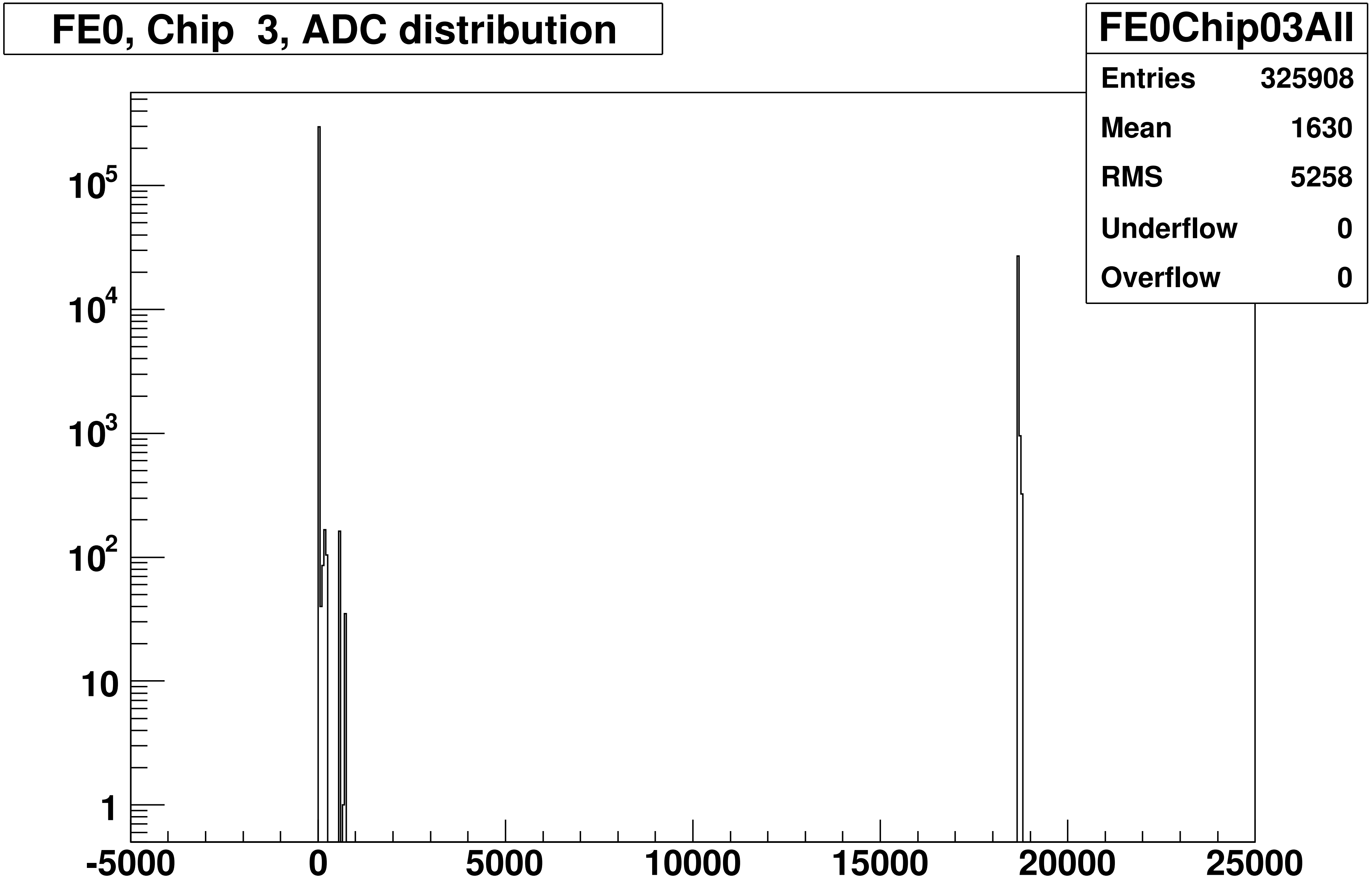
100
150
200


FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 325908
Mean 1.875e+04
RMS 24.07
Underflow 2.972e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 325908
Mean 1630
RMS 5258
Underflow 0
Overflow 0
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10^4
10^3
10^2
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-5000
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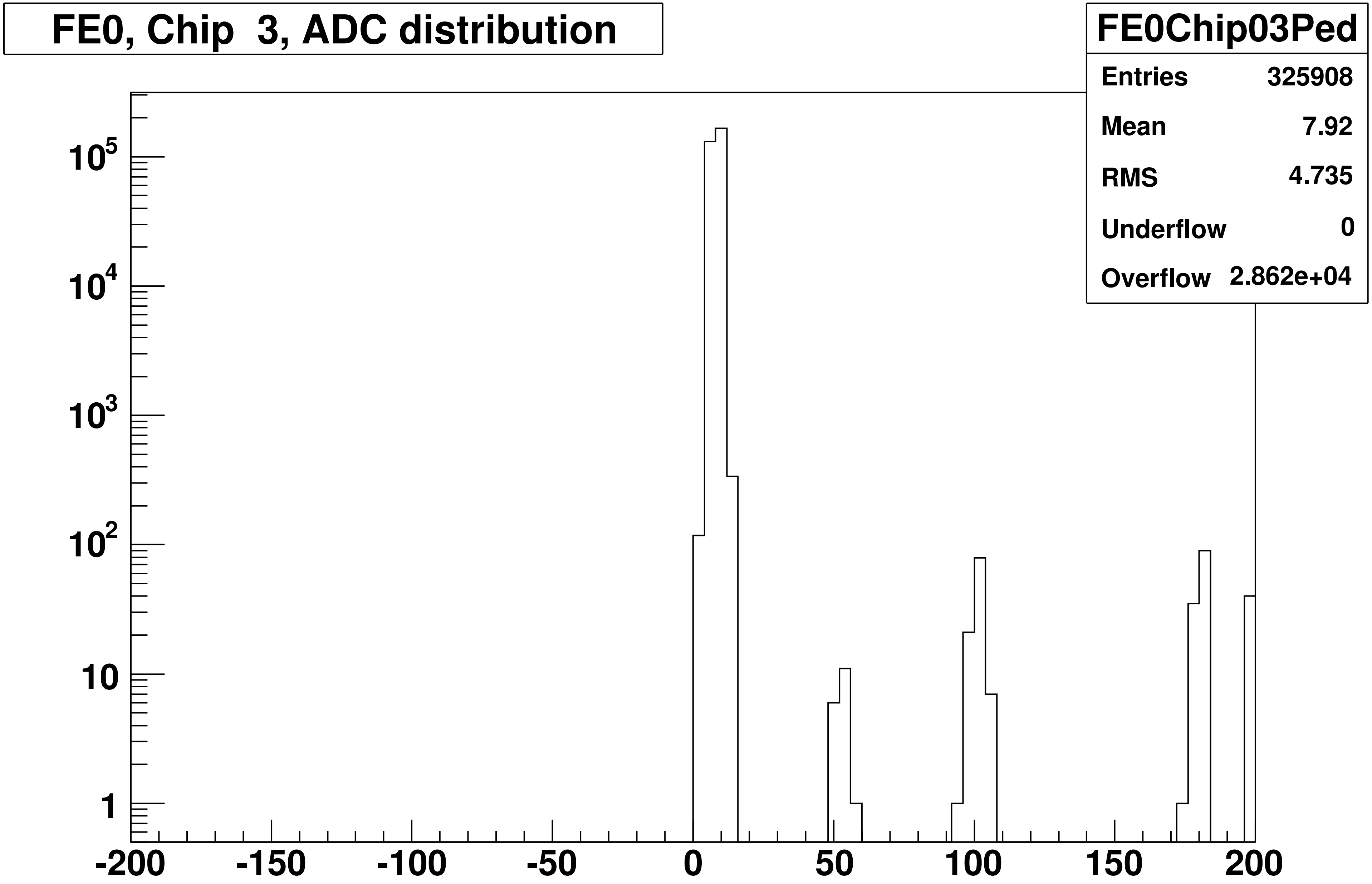
10000
15000
20000
25000



FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 325908
Mean 7.92
RMS 4.735
Underflow 0
Overflow 2.862e+04
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10^4
10^3
10^2
10
1
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-150
-100
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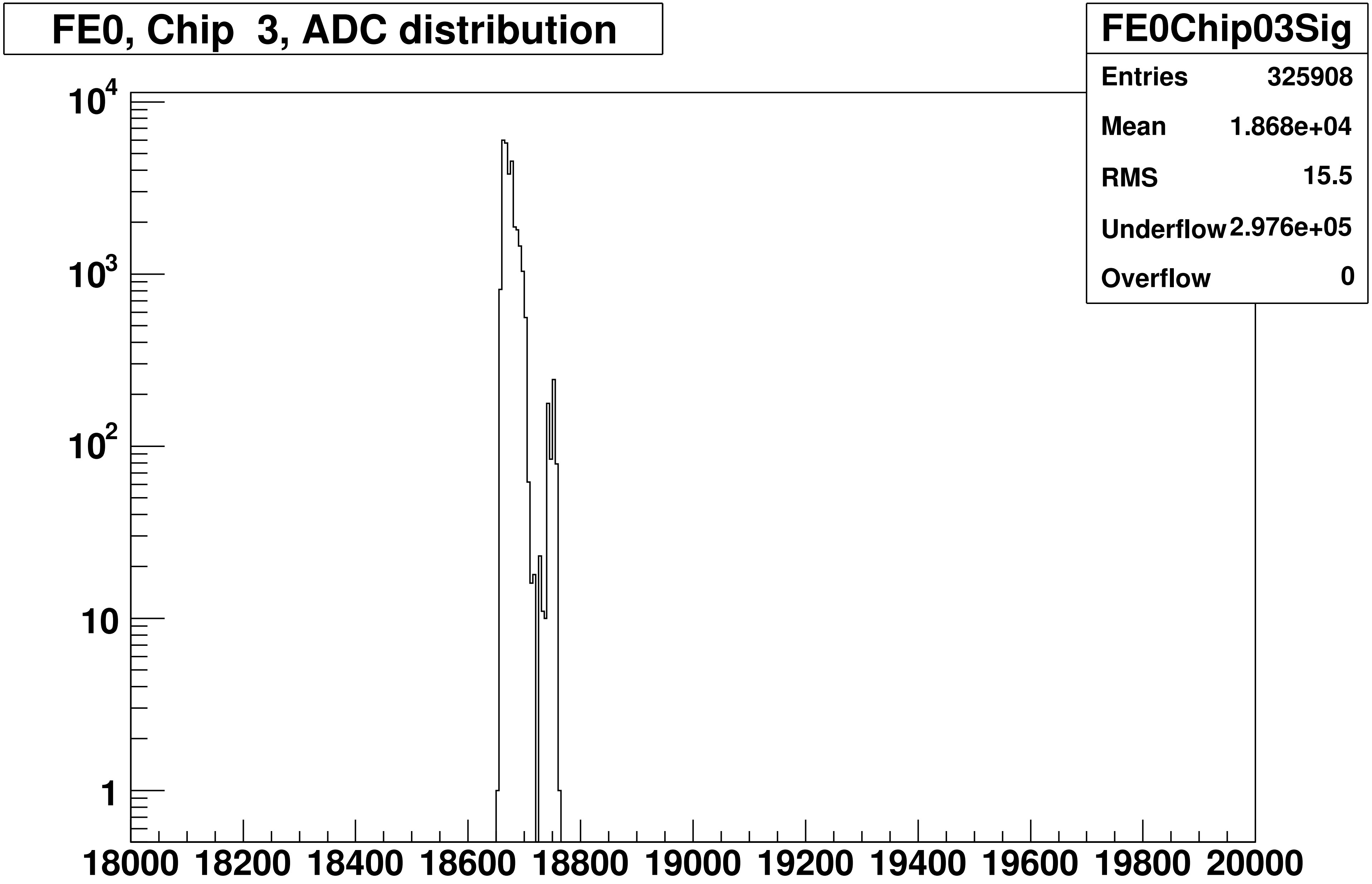
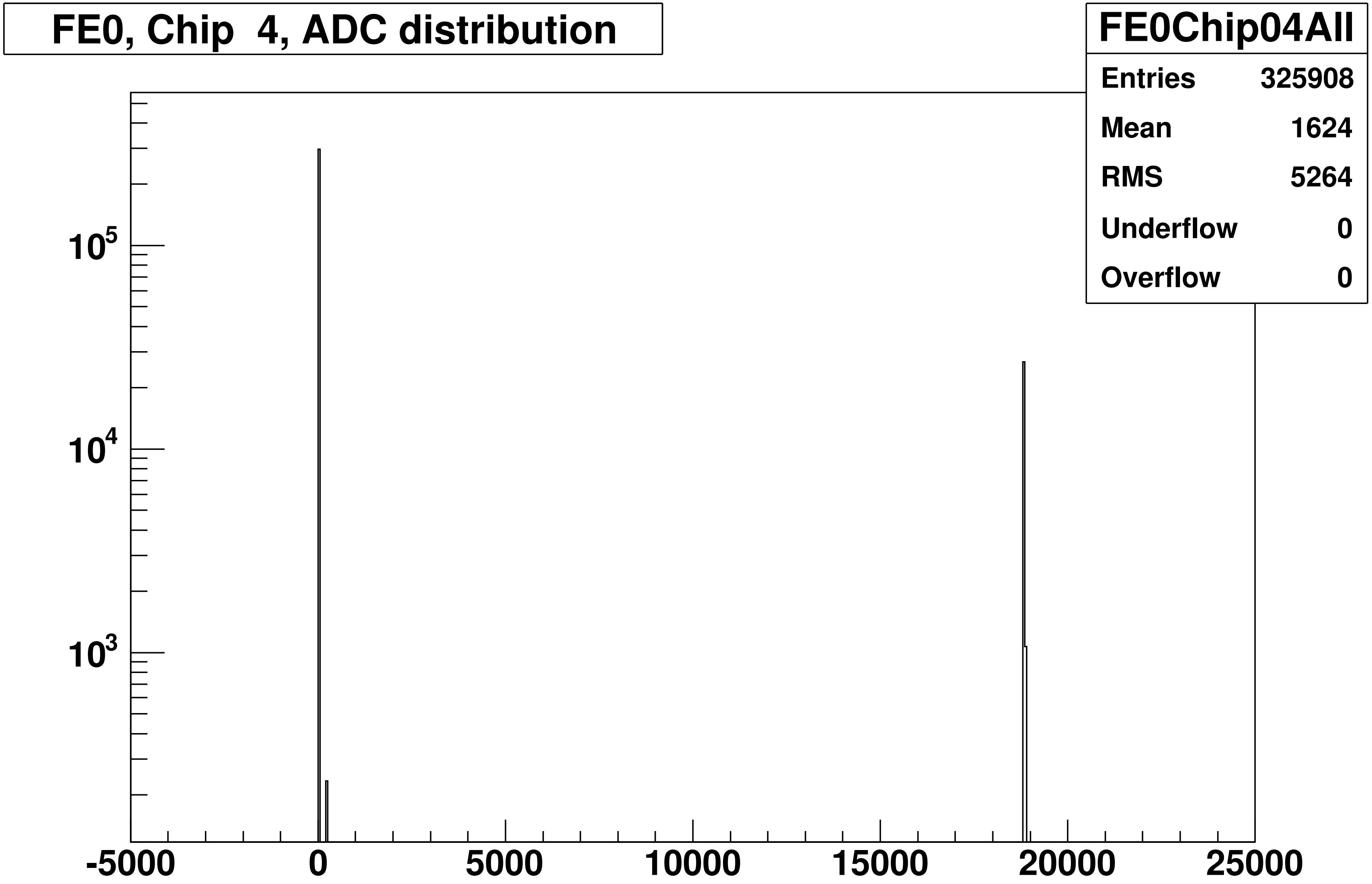
0
50
100
150
200



FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 325908
Mean 1.868e+04
RMS 15.5
Underflow 2.976e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 325908
Mean 1624
RMS 5264
Underflow 0
Overflow 0
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10^4
10^3
-5000
0
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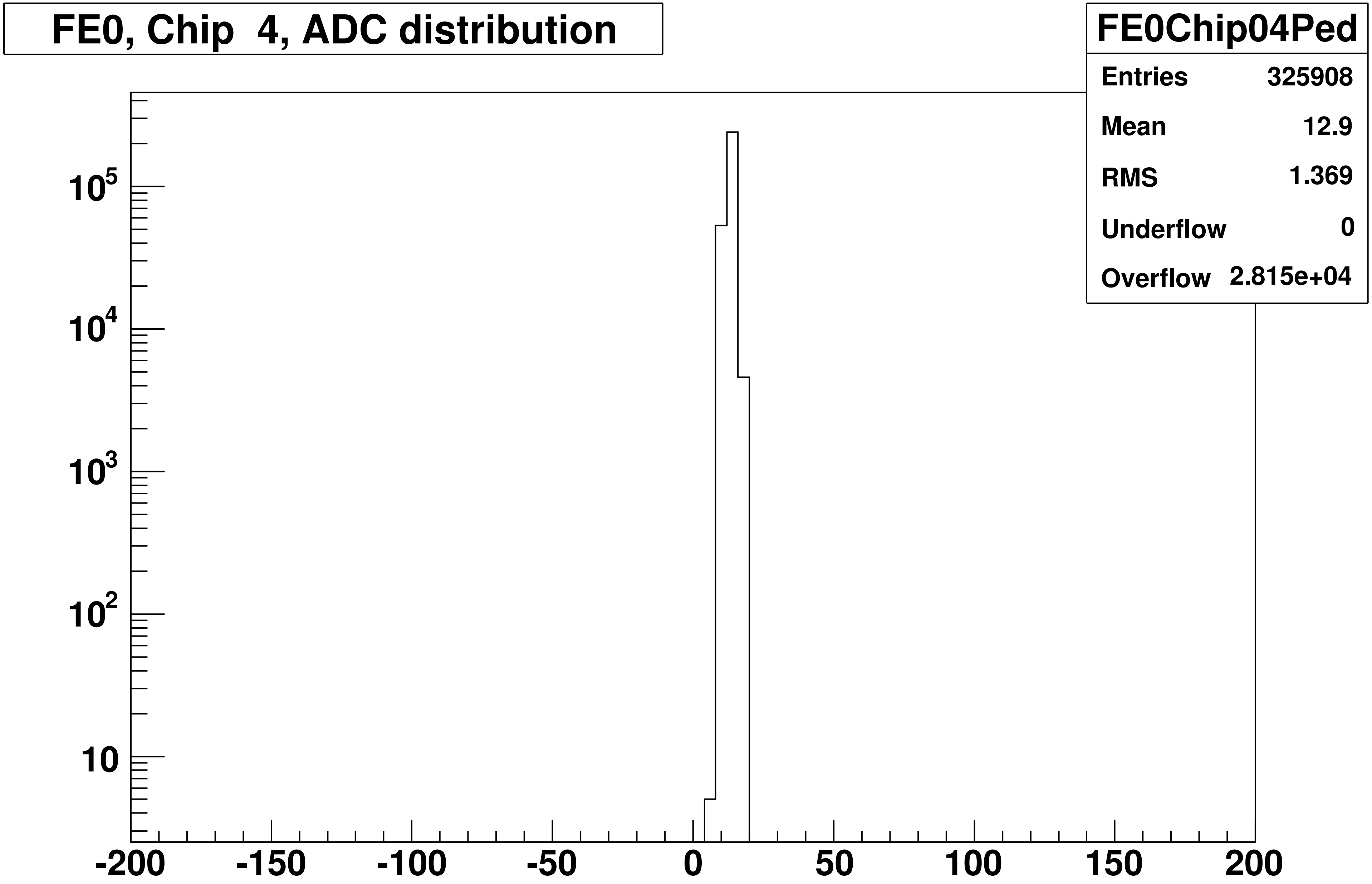
10000
15000
20000
25000


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 325908
Mean 12.9
RMS 1.369
Underflow 0
Overflow 2.815e+04
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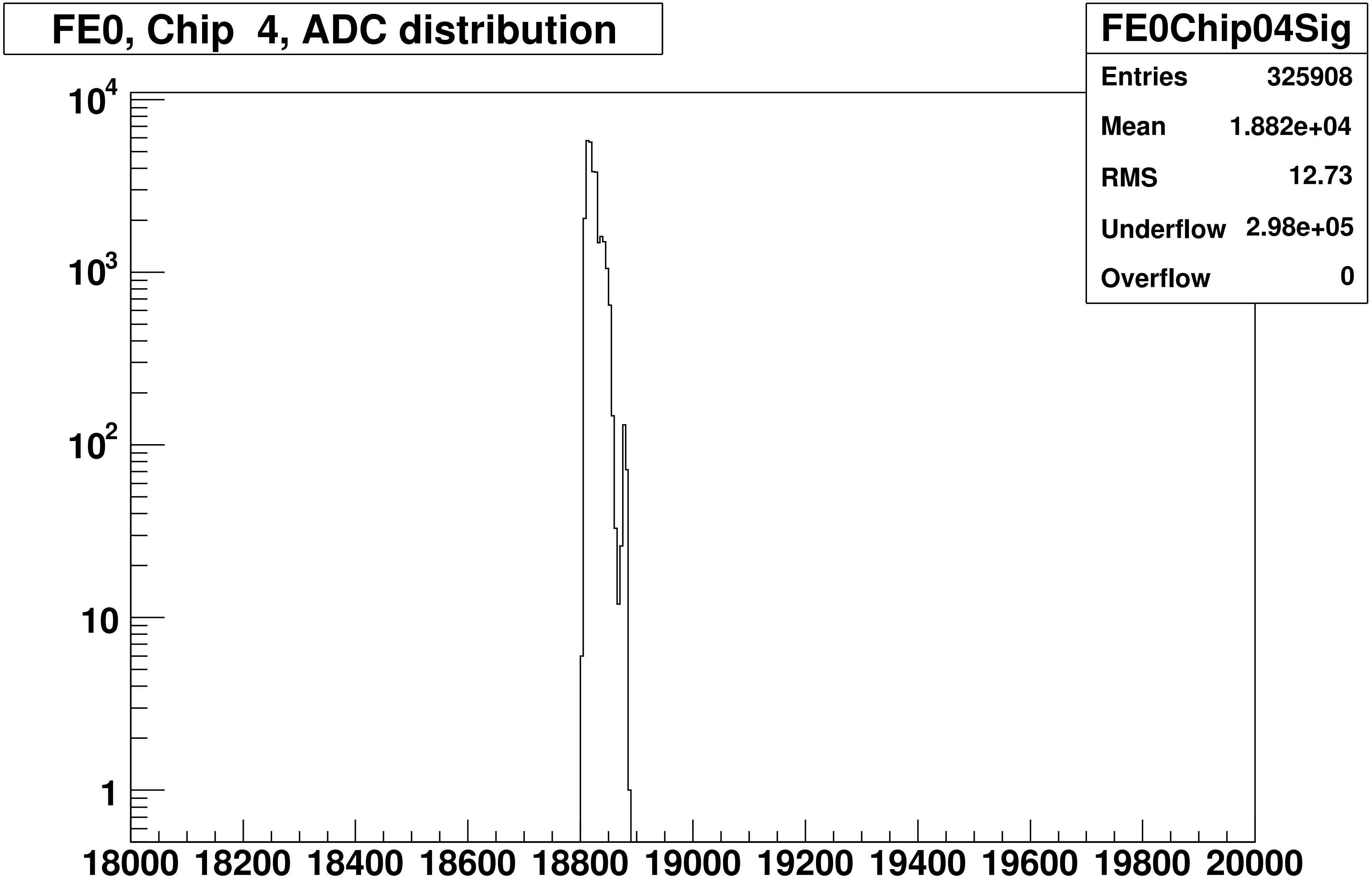
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200


FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 325908
Mean 1.882e+04
RMS 12.73
Underflow 2.98e+05
Overflow 0
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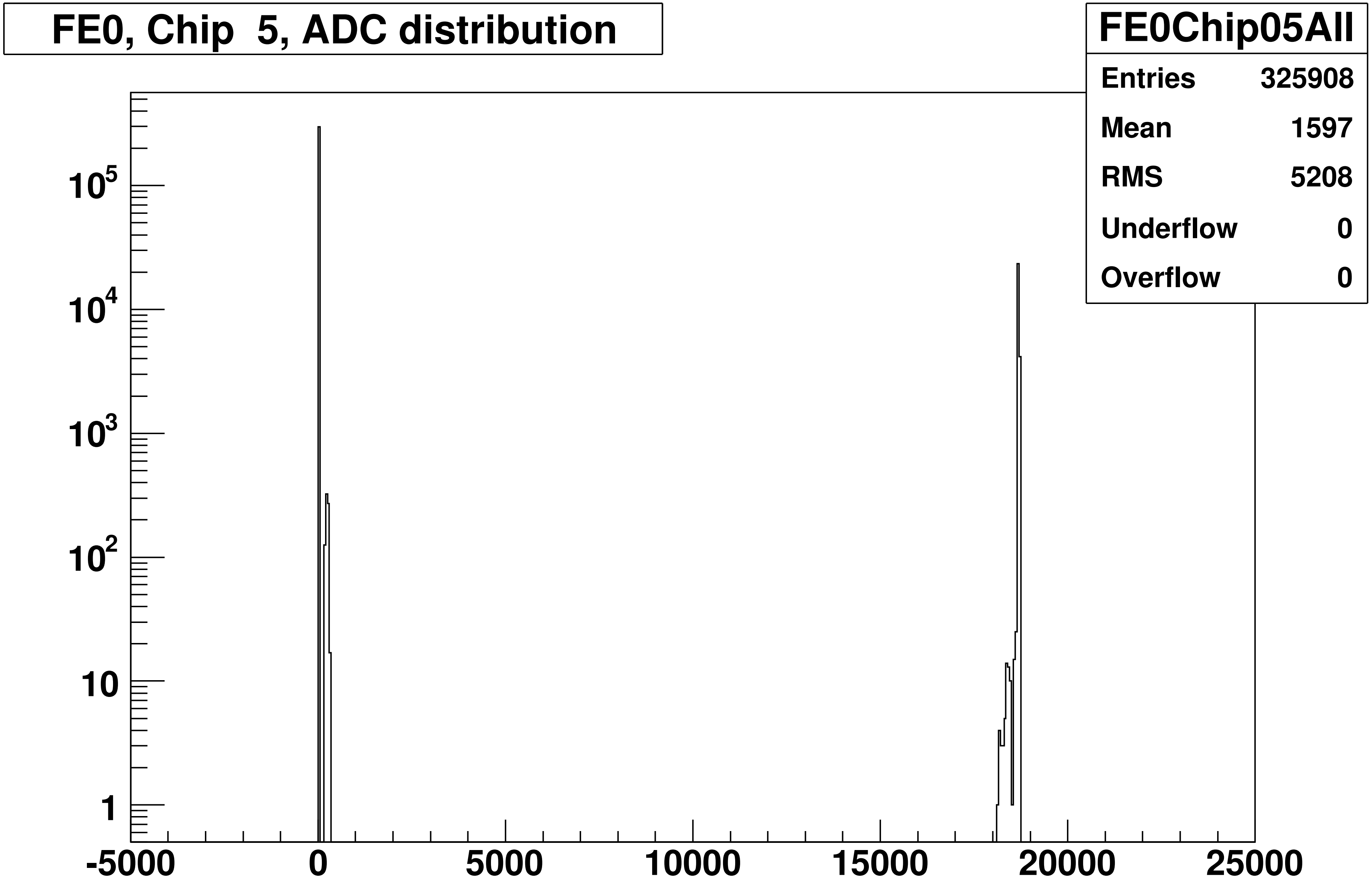
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 325908
Mean 1597
RMS 5208
Underflow 0
Overflow 0
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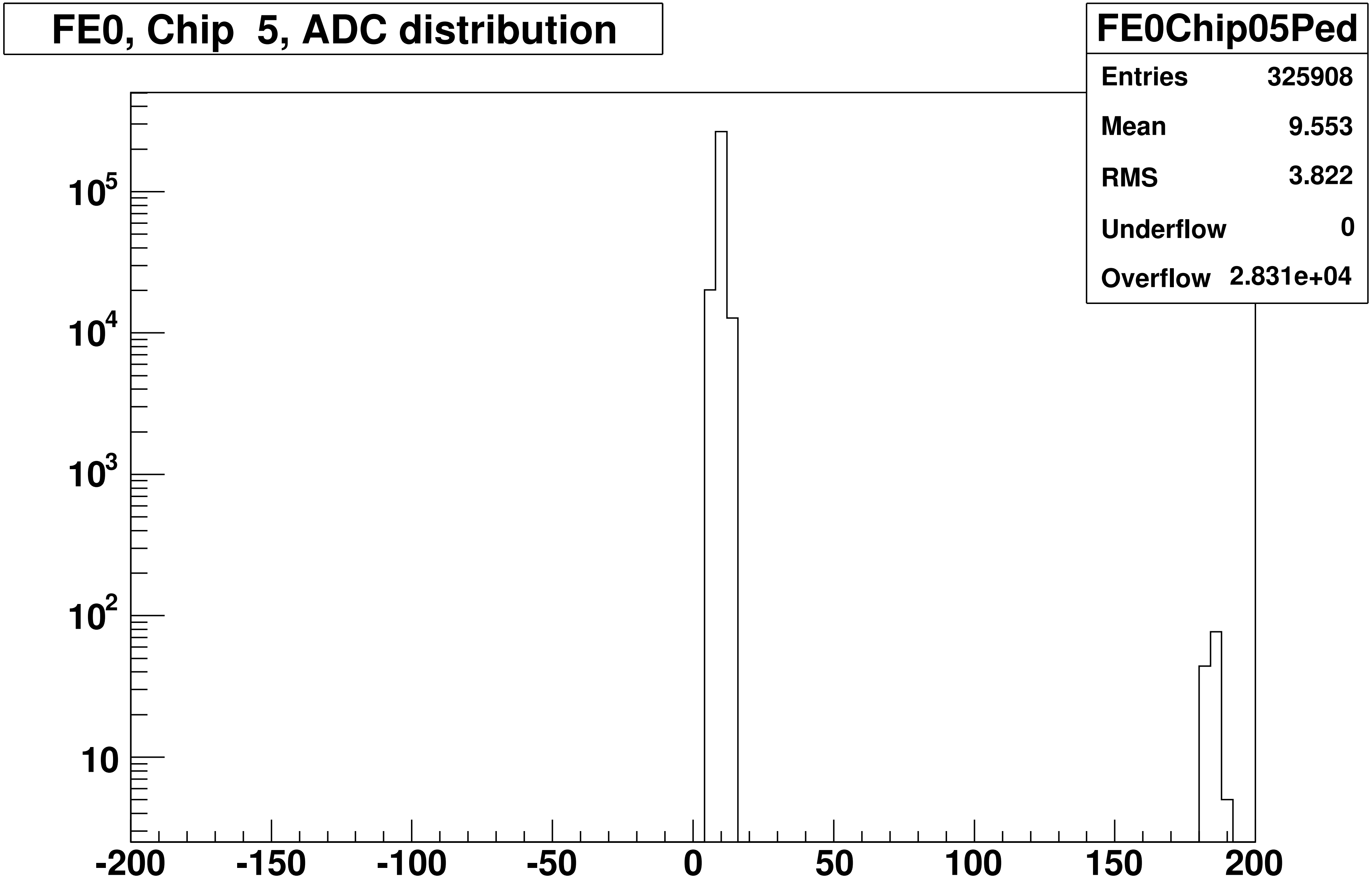
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 325908
Mean 9.553
RMS 3.822
Underflow 0
Overflow 2.831e+04
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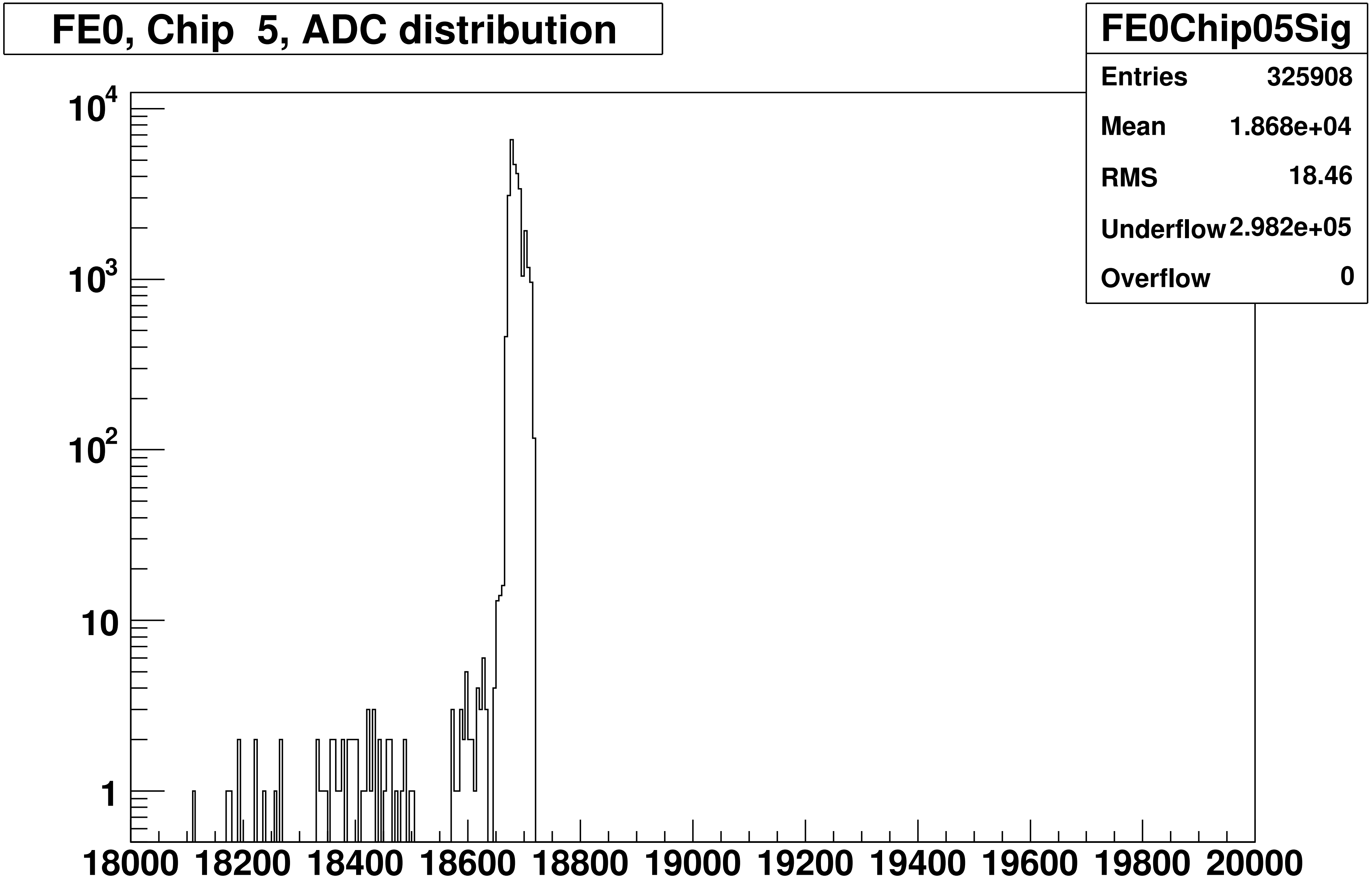
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 325908
Mean 1.868e+04
RMS 18.46
Underflow 2.982e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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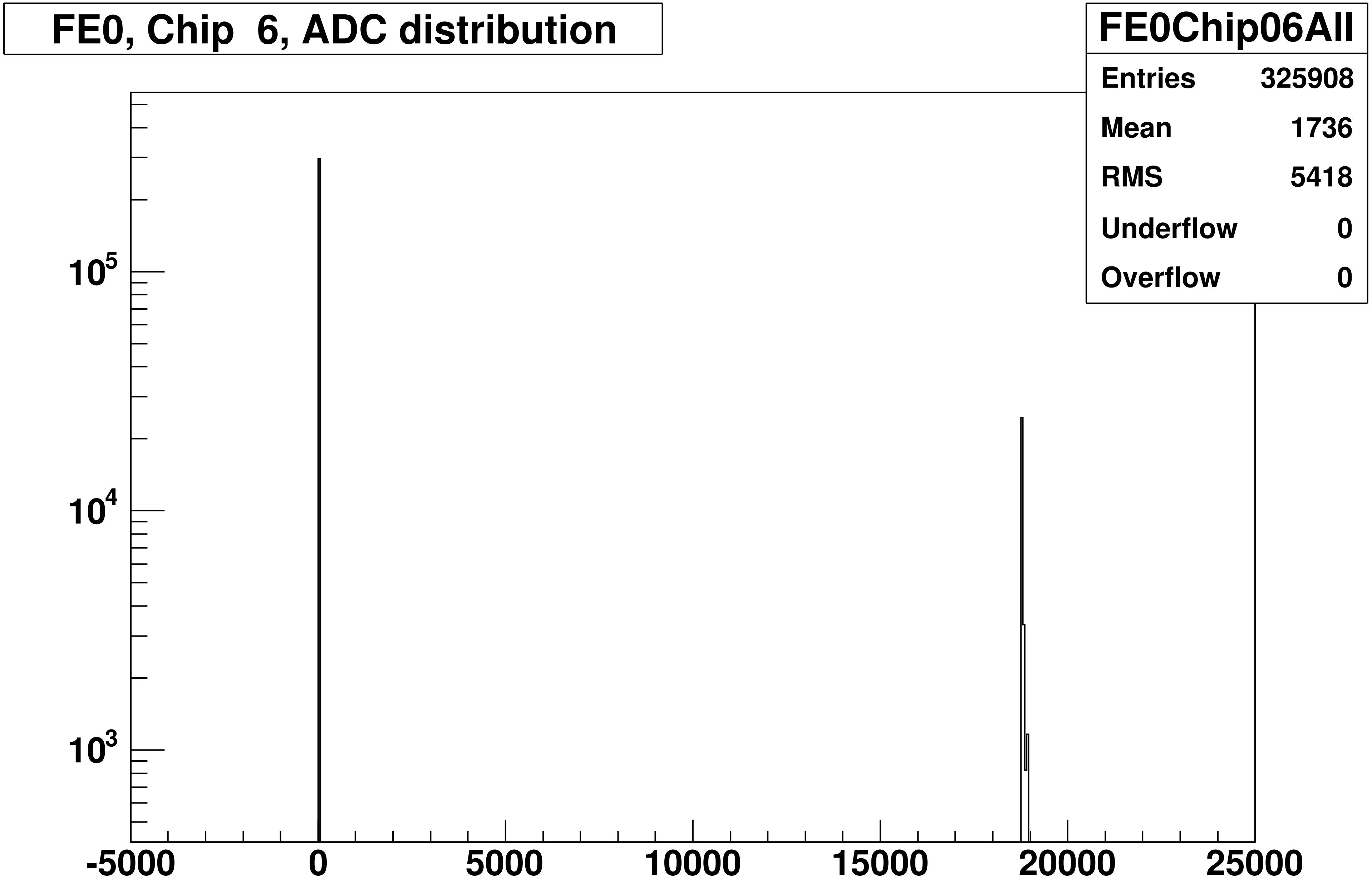
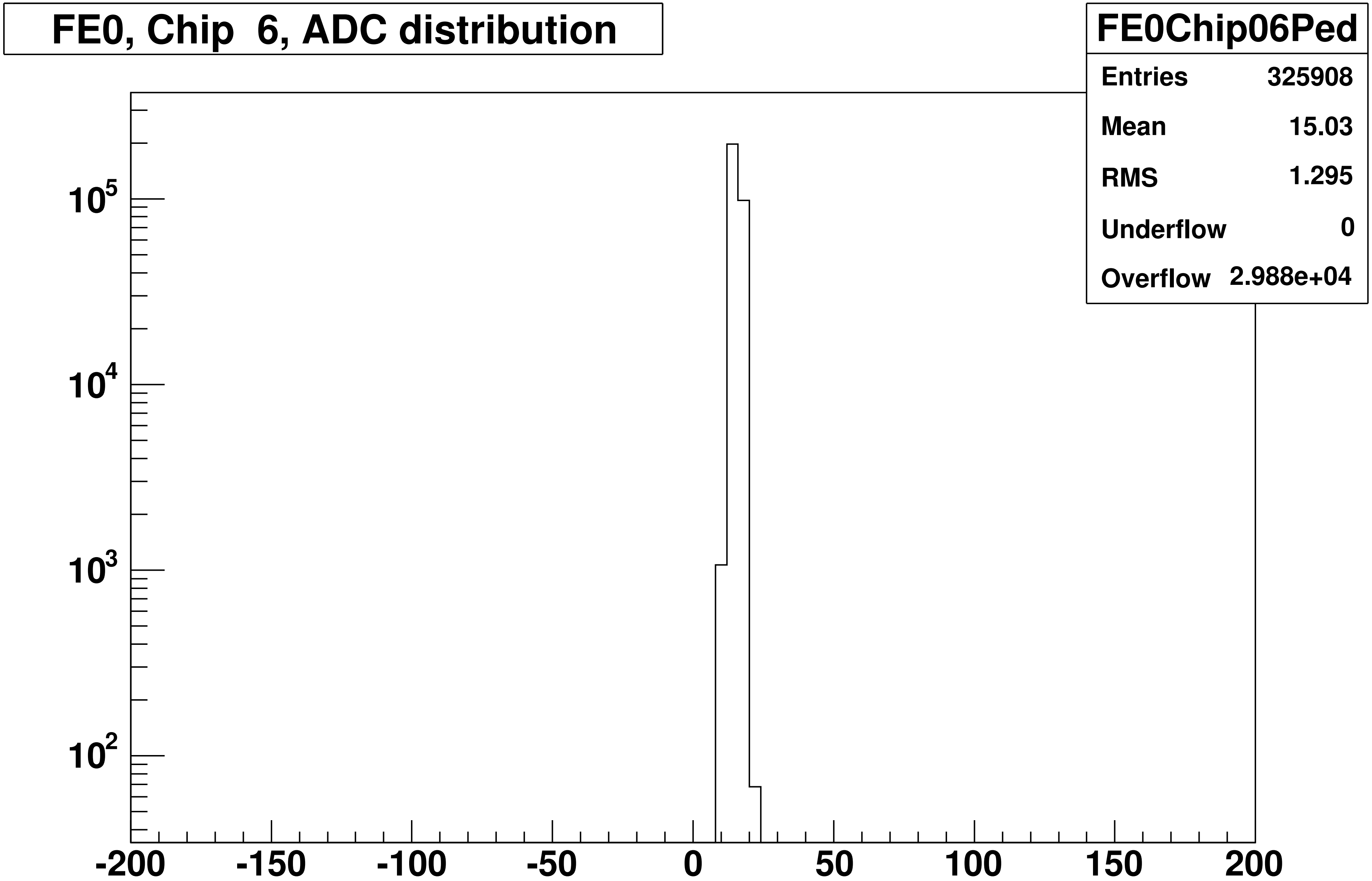
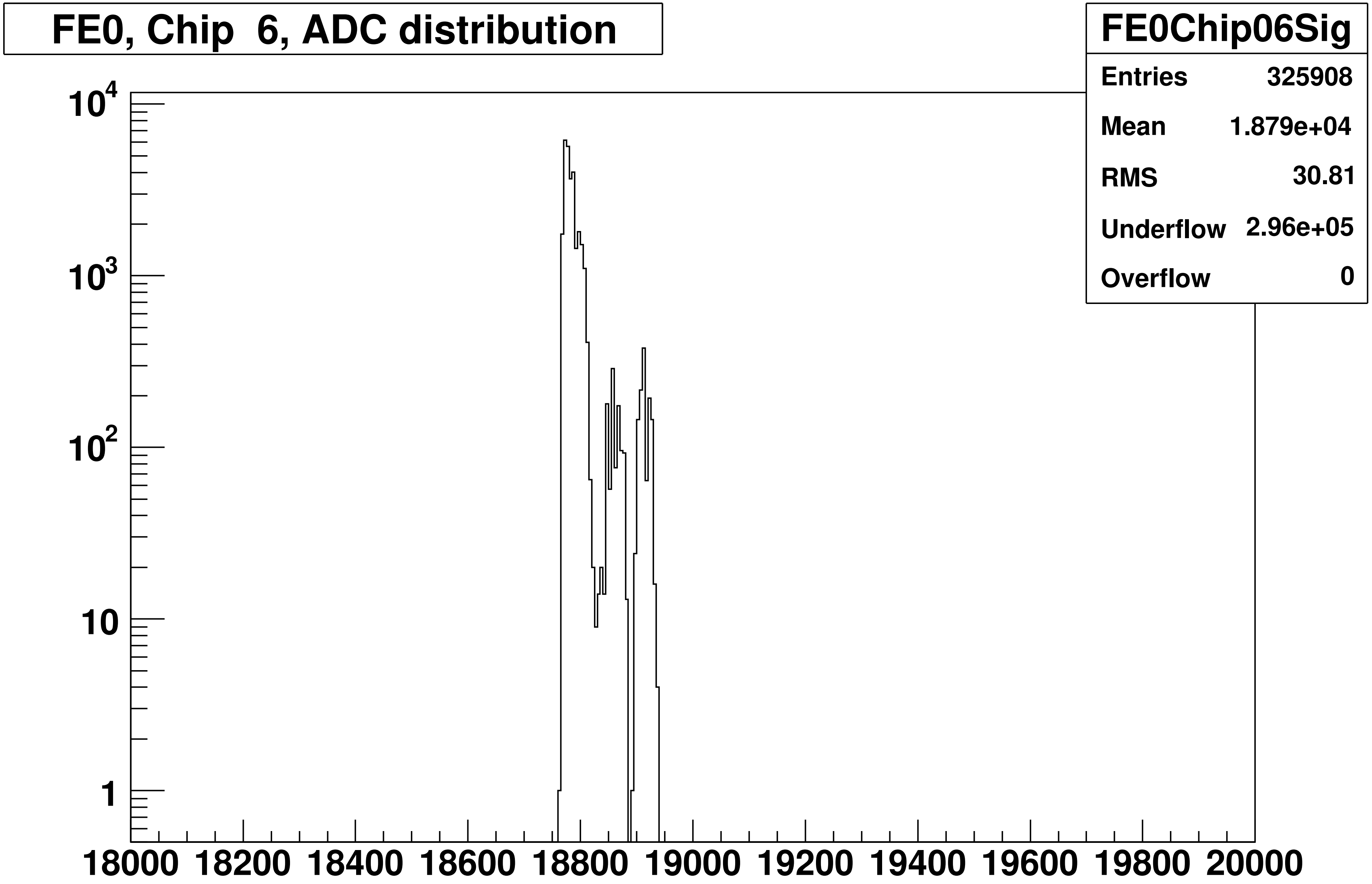
19400
19600
19800
20000


FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 325908
Mean 1736
RMS 5418
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 325908
Mean 15.03
RMS 1.295
Underflow 0
Overflow 2.988e+04
10^5
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200



FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 325908
Mean 1.879e+04
RMS 30.81
Underflow 2.96e+05
Overflow 0
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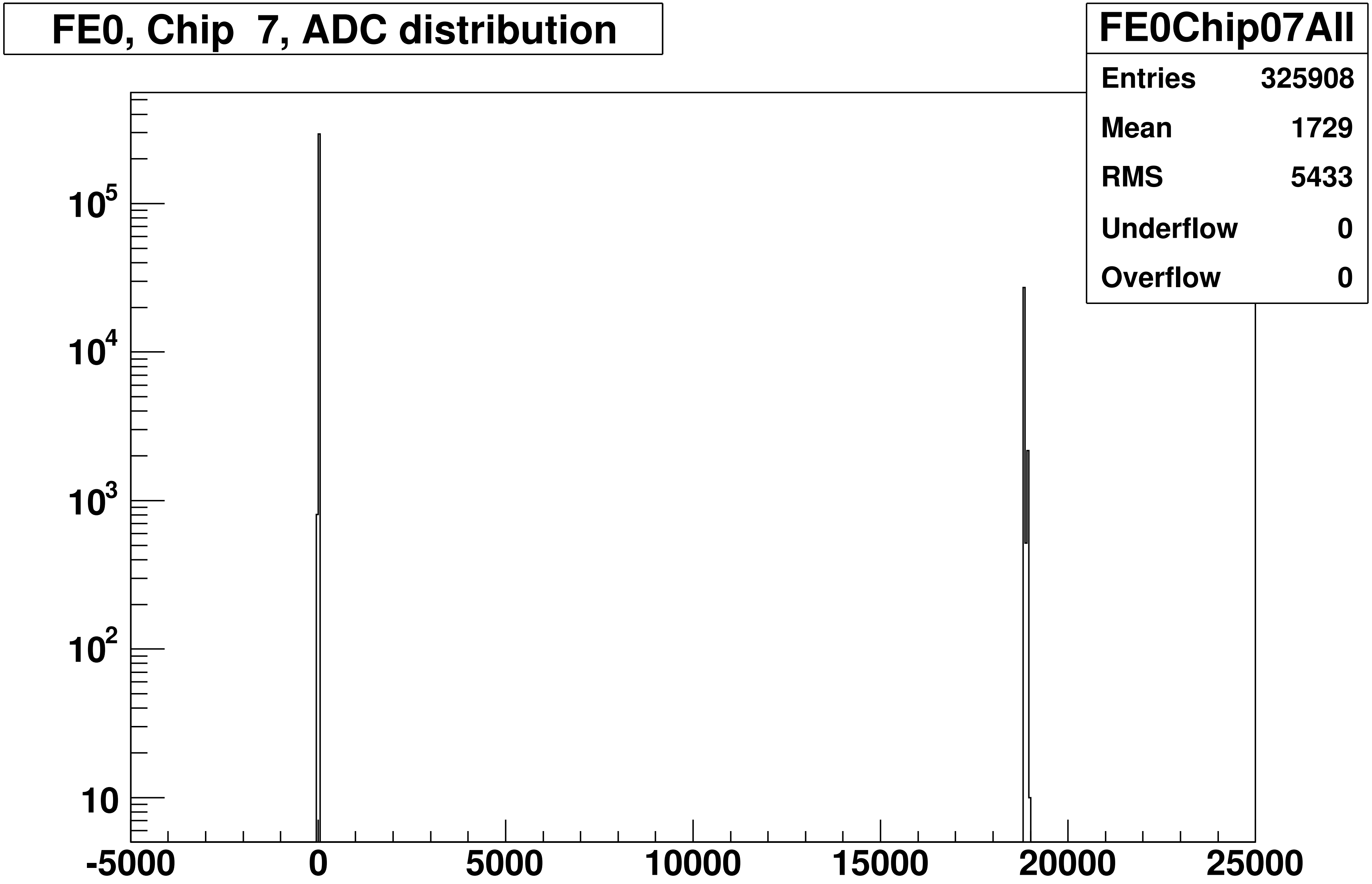
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 325908
Mean 1729
RMS 5433
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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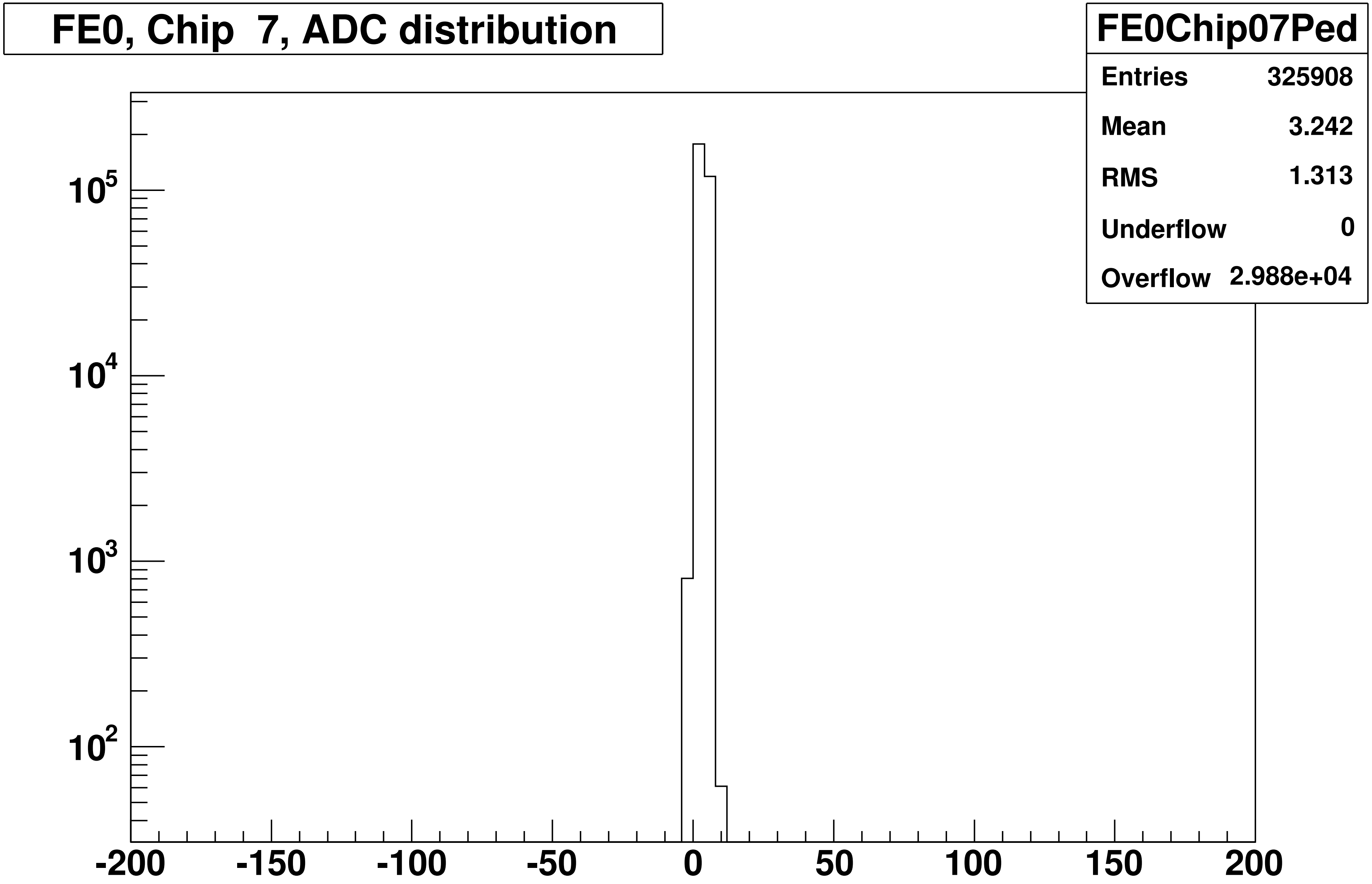
-5000
0
5000
10000
15000
20000
25000



FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 325908
Mean 3.242
RMS 1.313
Underflow 0
Overflow 2.988e+04
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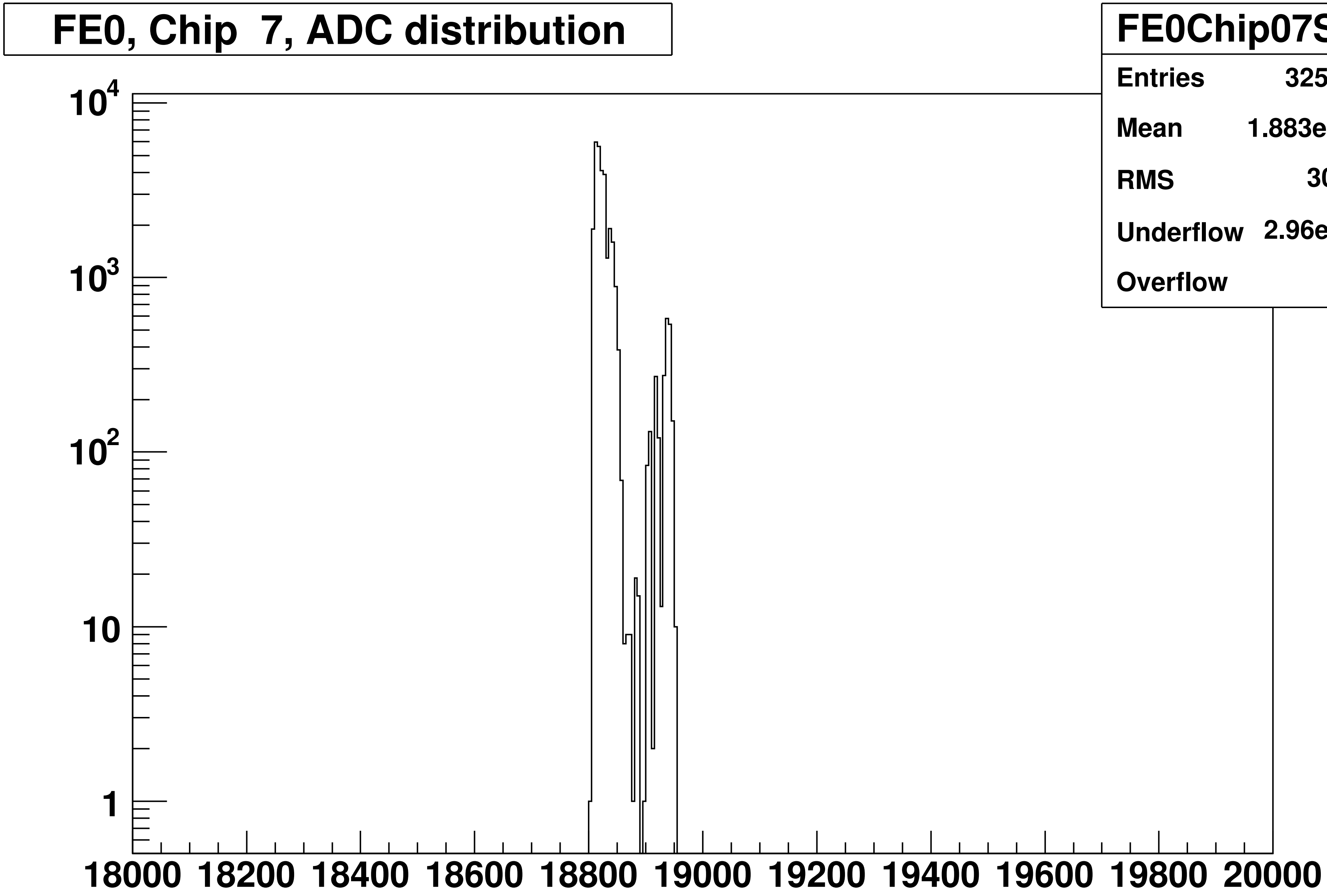
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200


FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 325908
Mean 1.883e+04
RMS 30.63
Underflow 2.96e+05
Overflow 0
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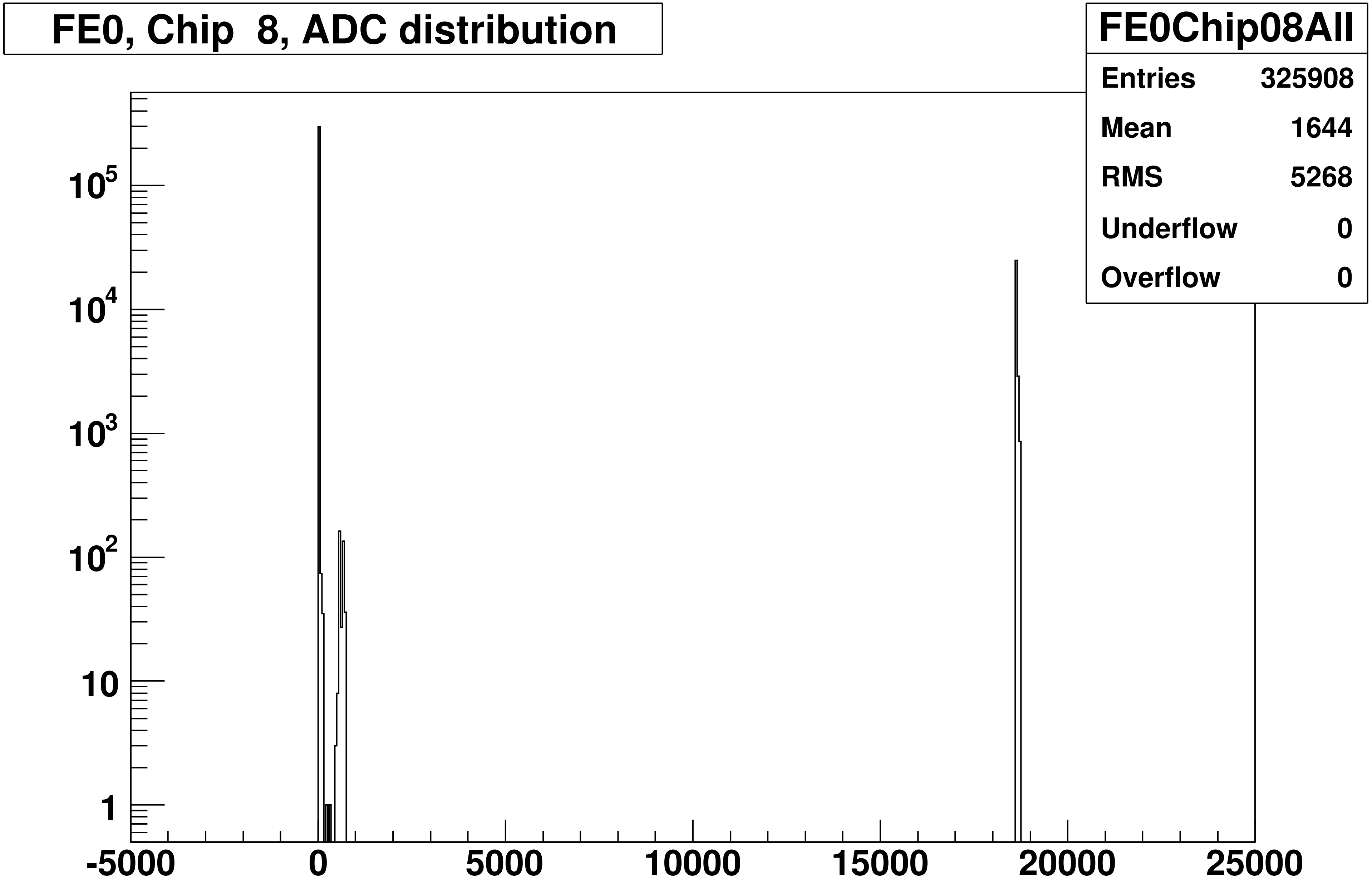
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 325908
Mean 1644
RMS 5268
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
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-5000
0
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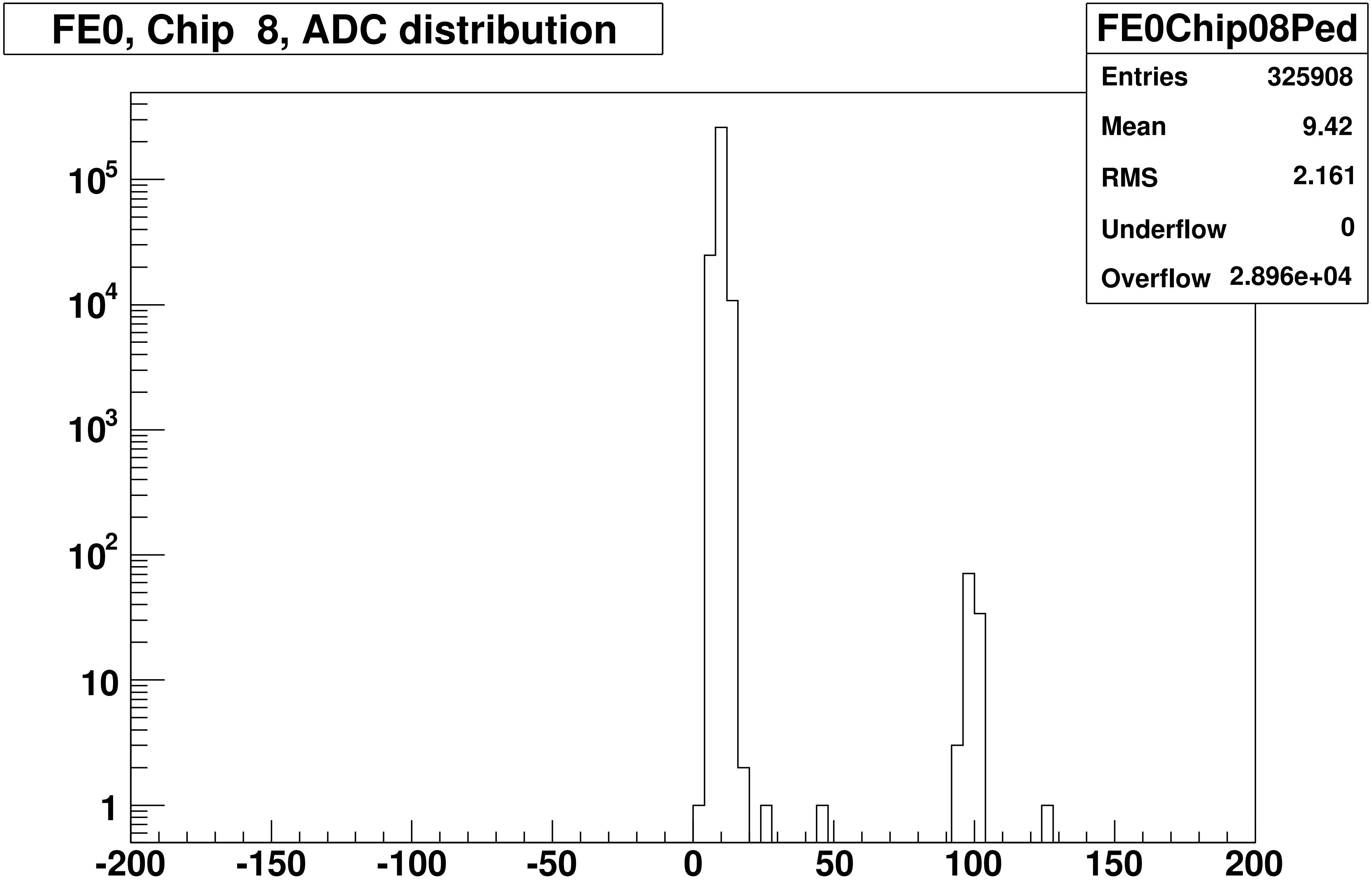
10000
15000
20000
25000


FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 325908
Mean 9.42
RMS 2.161
Underflow 0
Overflow 2.896e+04
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10^3
10^2
10
1
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-150
-100
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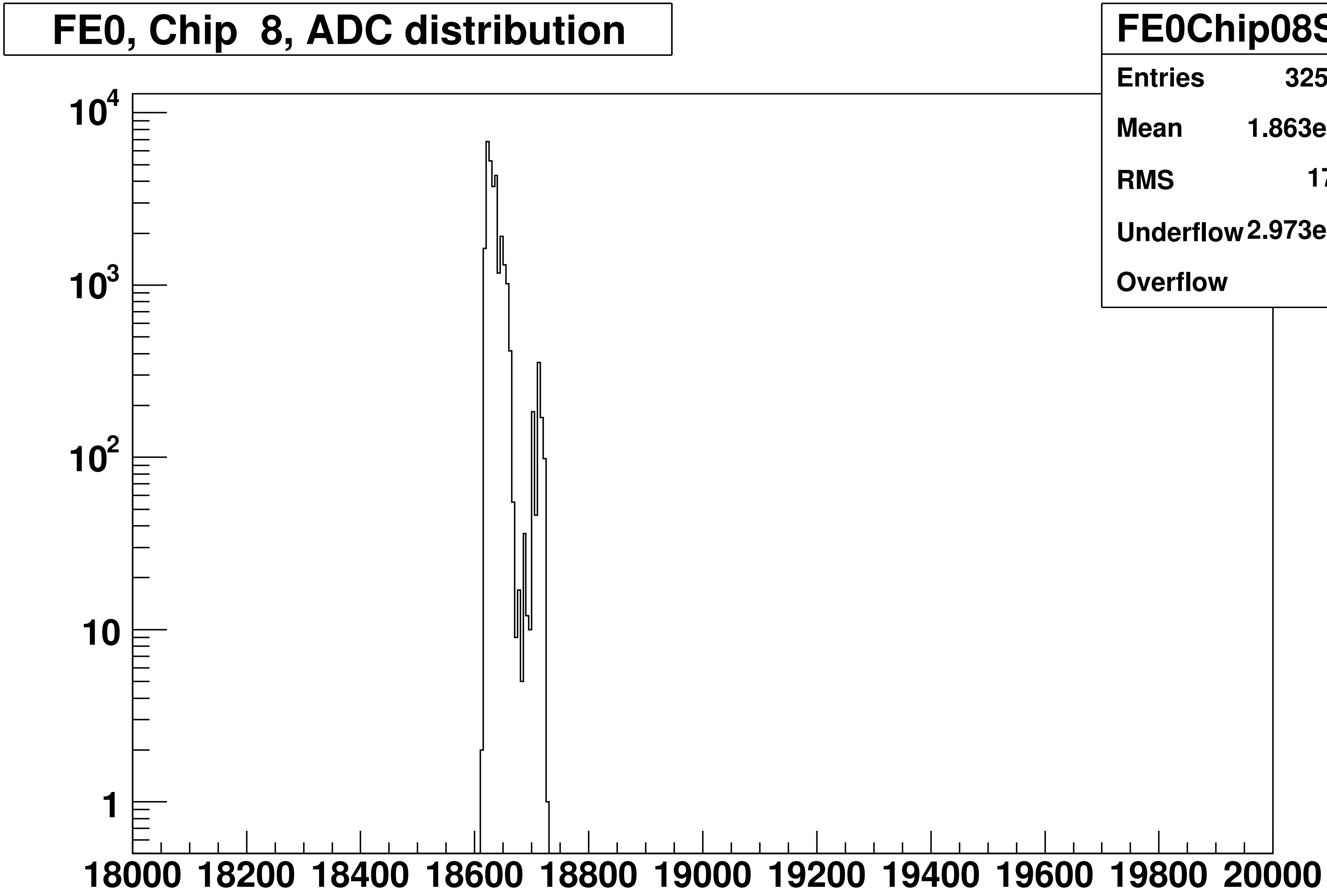
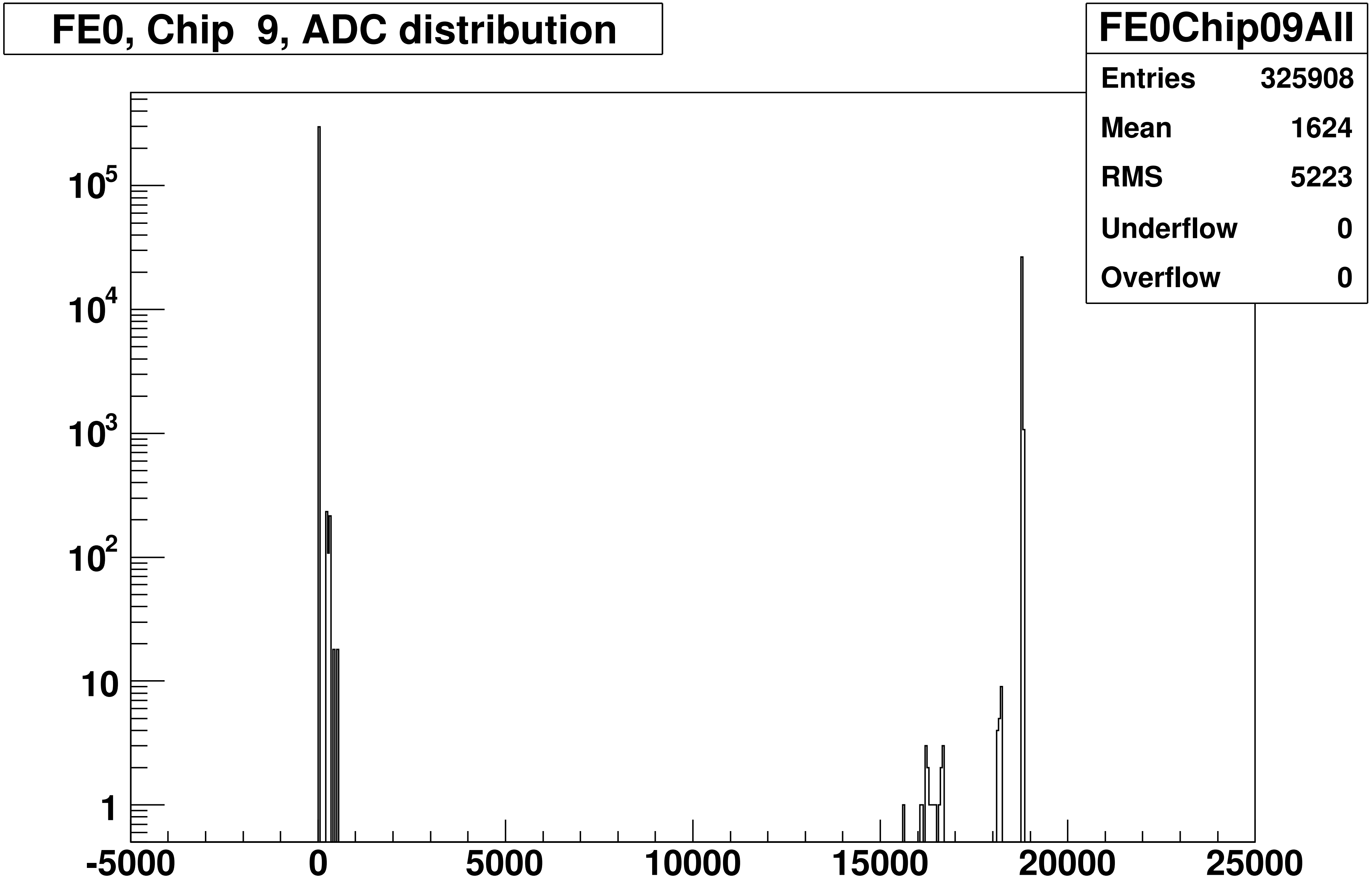
100
150
200



FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 325908
Mean 1.863e+04
RMS 17.63
Underflow 2.973e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 325908
Mean 1624
RMS 5223
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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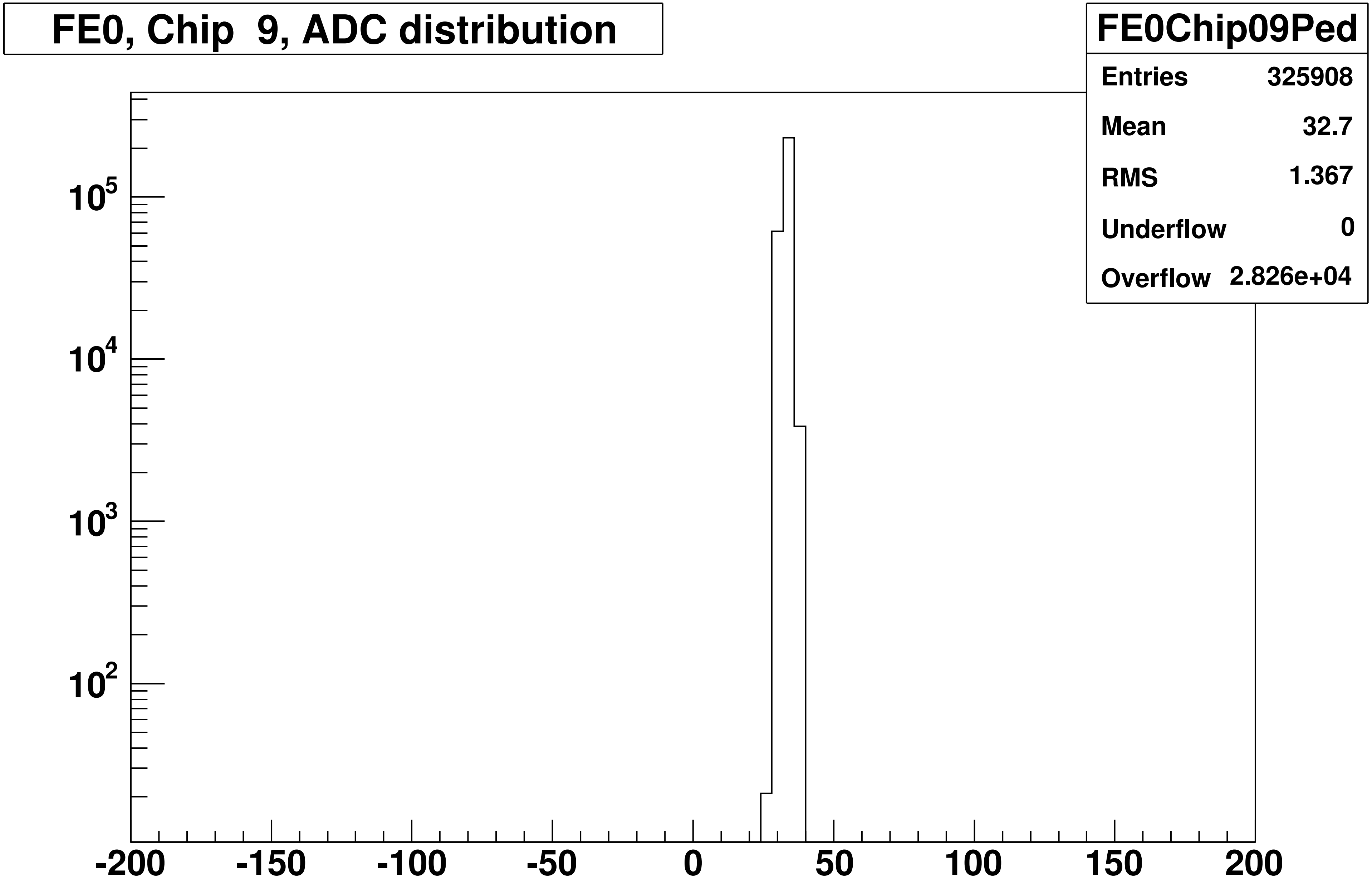
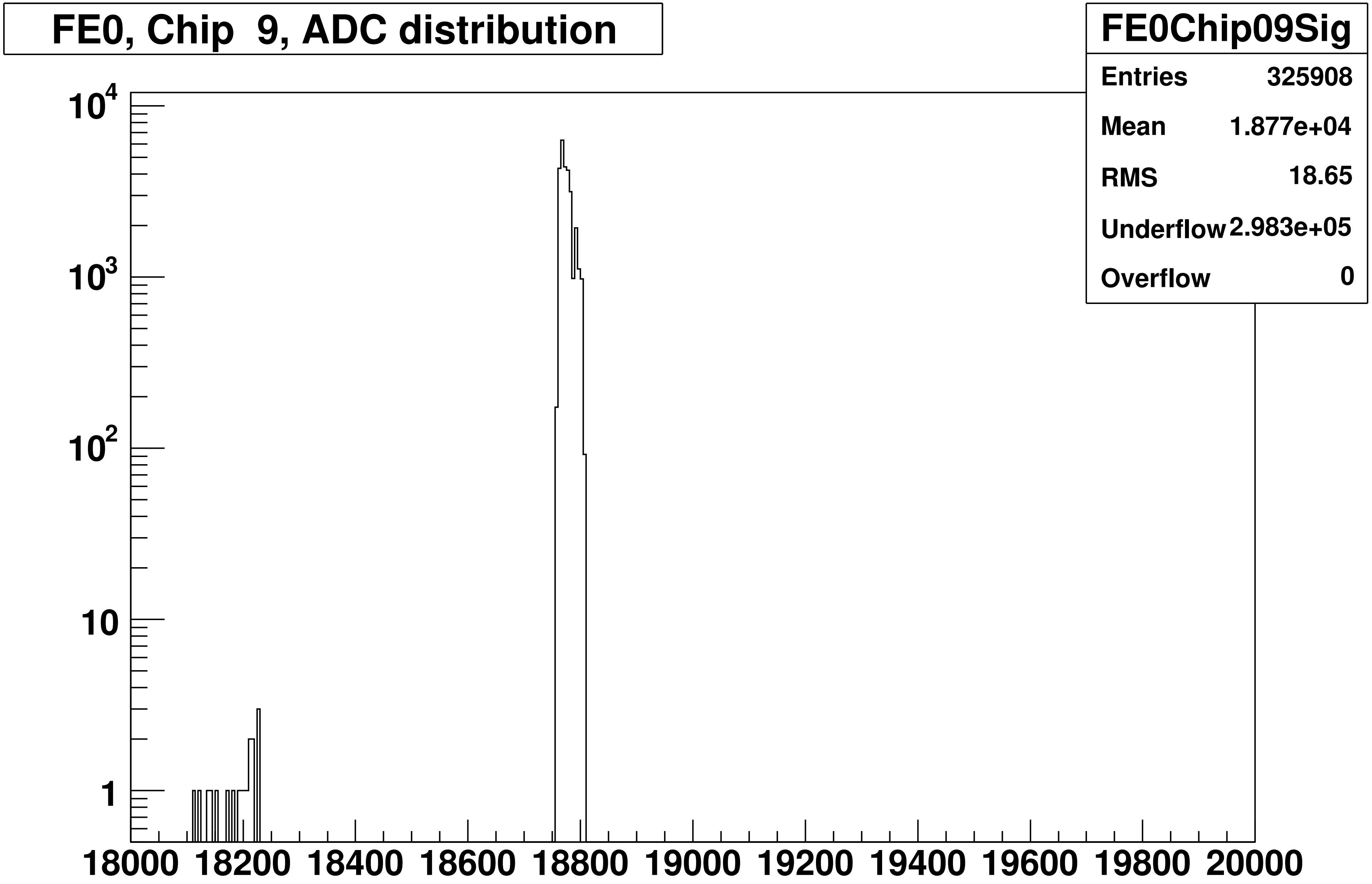
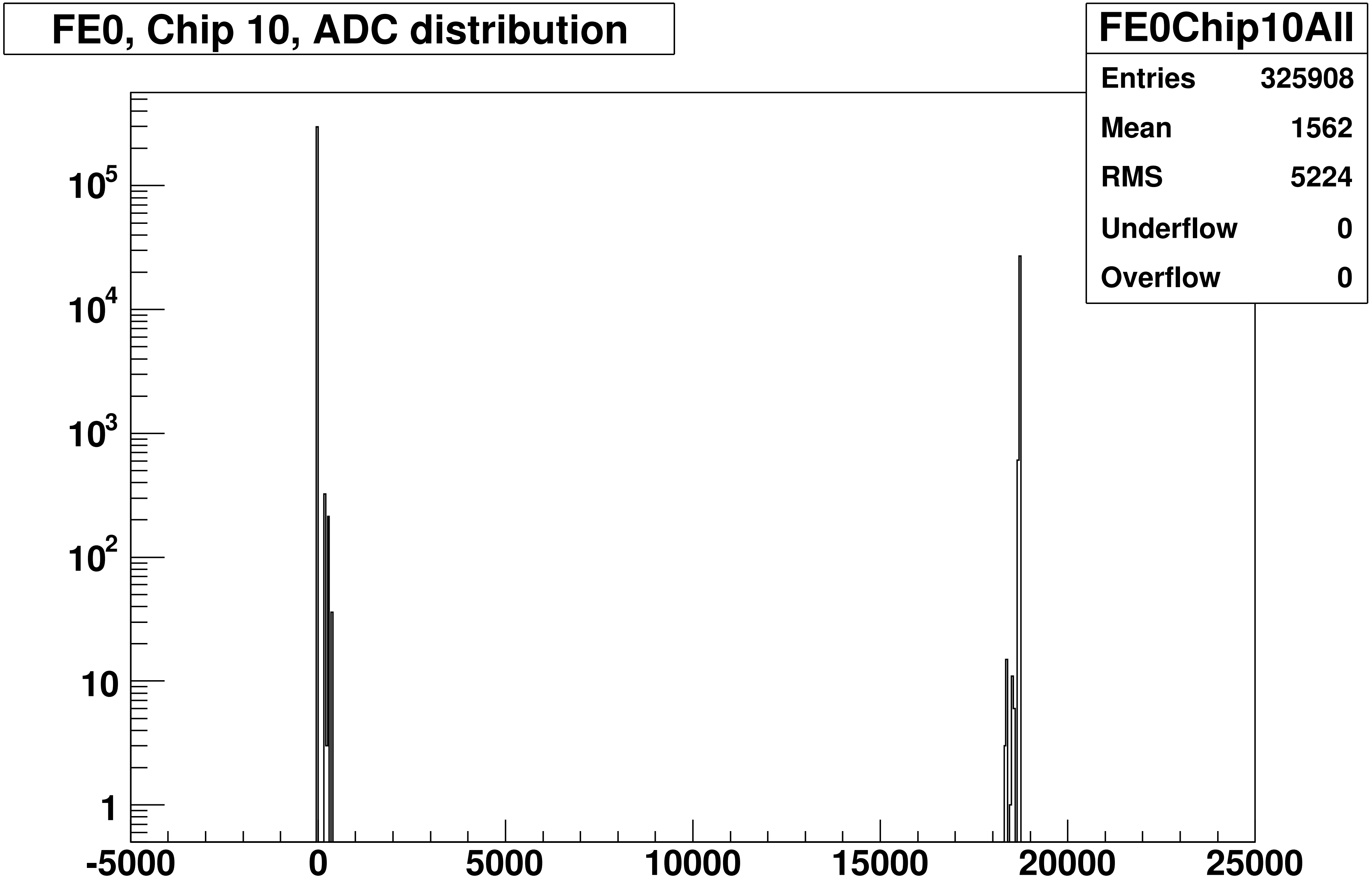
15000
20000
25000


FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 325908
Mean 32.7
RMS 1.367
Underflow 0
Overflow 2.826e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 325908
Mean 1.877e+04
RMS 18.65
Underflow 2.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 325908
Mean 1562
RMS 5224
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
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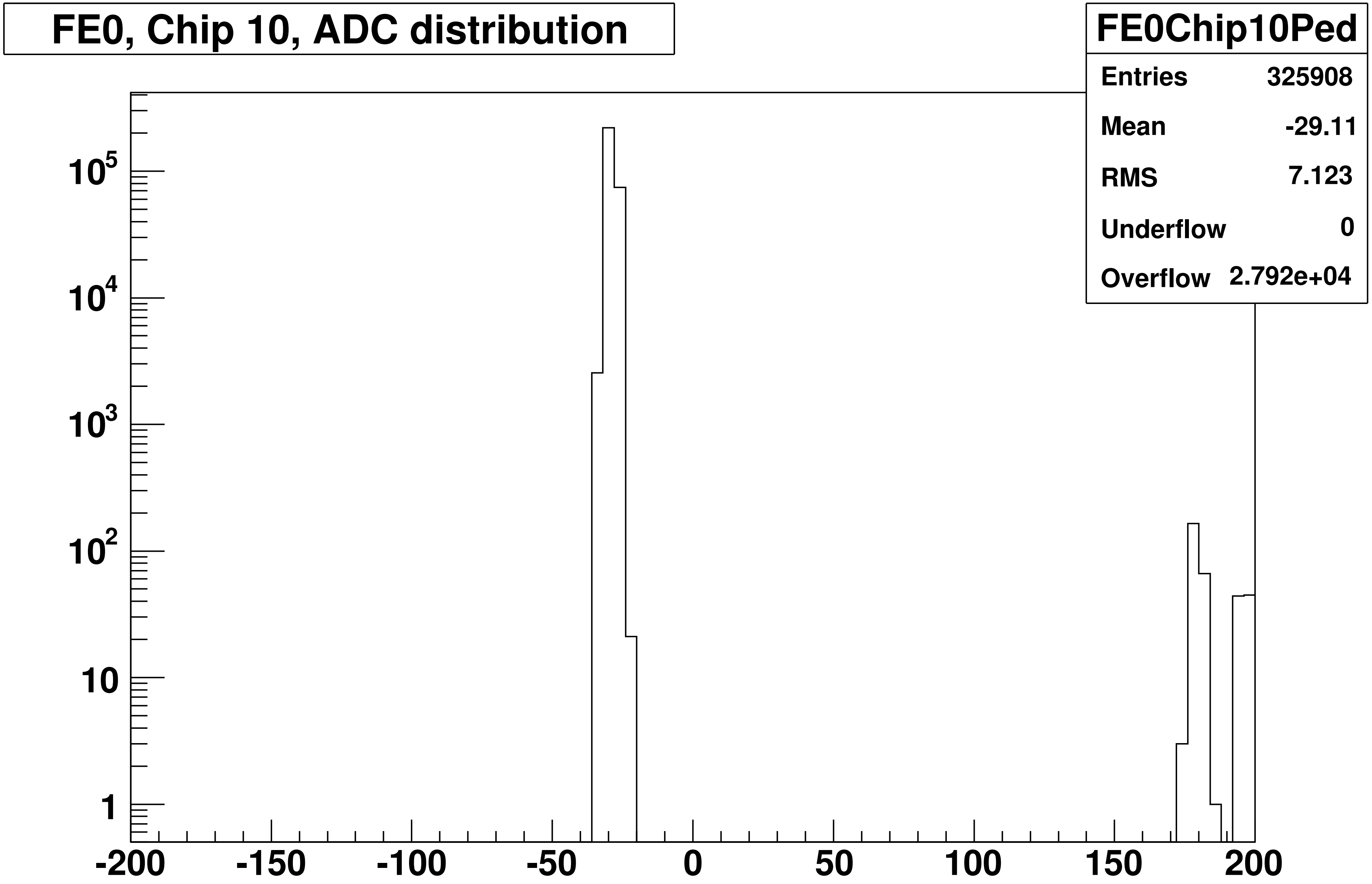
10000
15000
20000
25000


FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 325908
Mean -29.11
RMS 7.123
Underflow 0
Overflow 2.792e+04
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10^3
10^2
10
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-100
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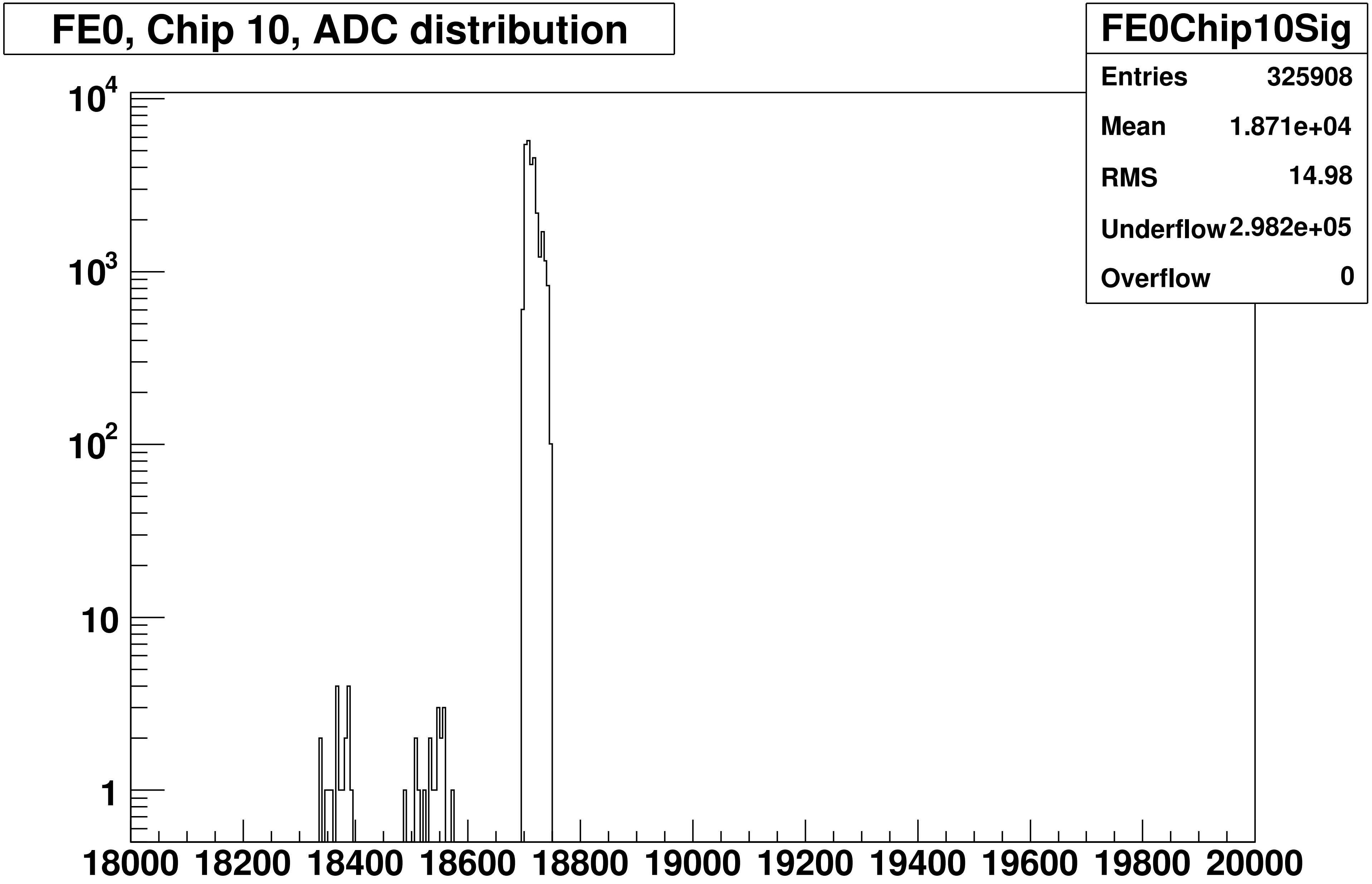
0
50
100
150
200


FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 325908
Mean 1.871e+04
RMS 14.98
Underflow 2.982e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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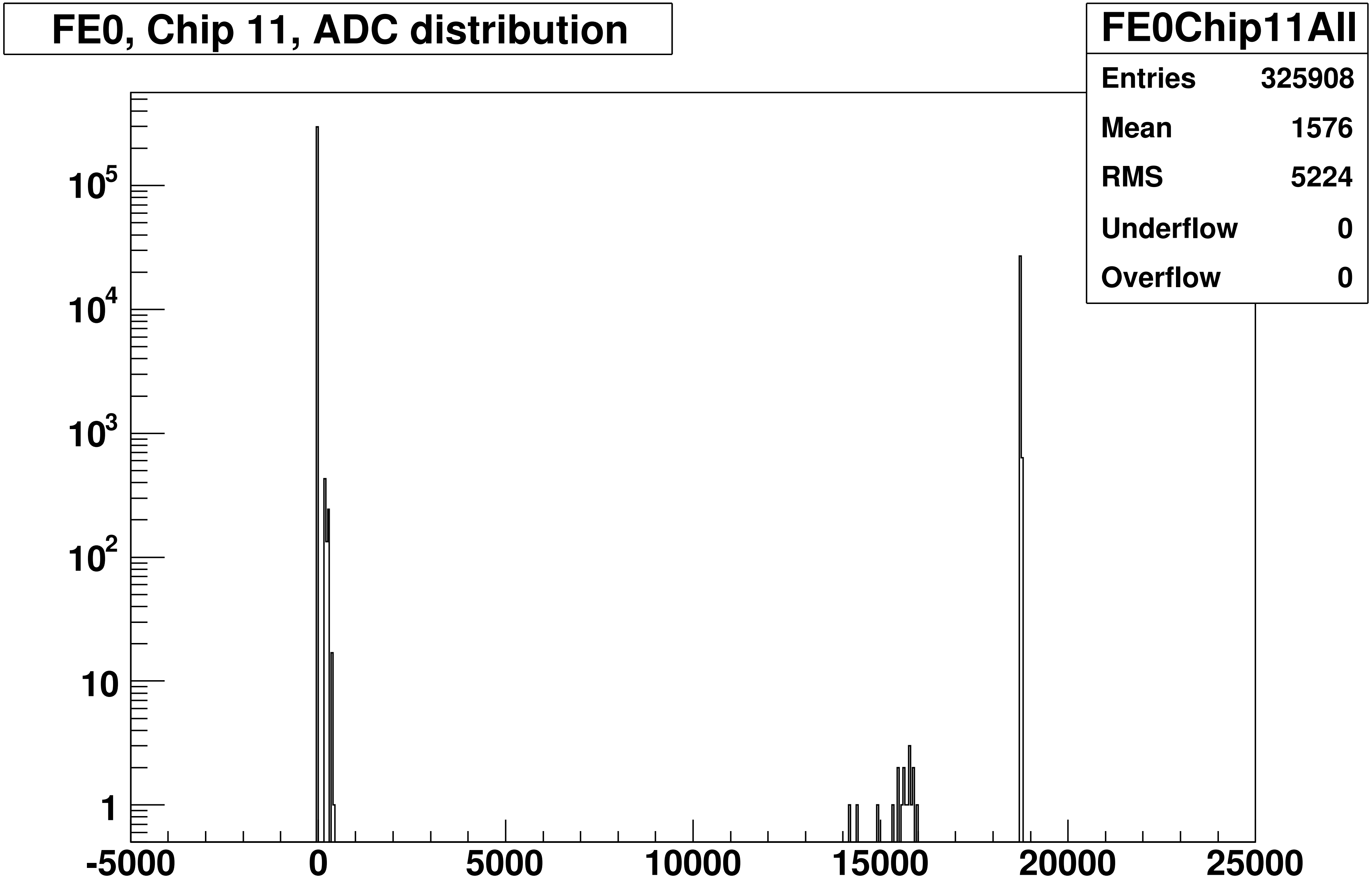
19000
19200
19400
19600
19800
20000


FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 325908
Mean 1576
RMS 5224
Underflow 0
Overflow 0
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10^4
10^3
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10
1
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0
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10000
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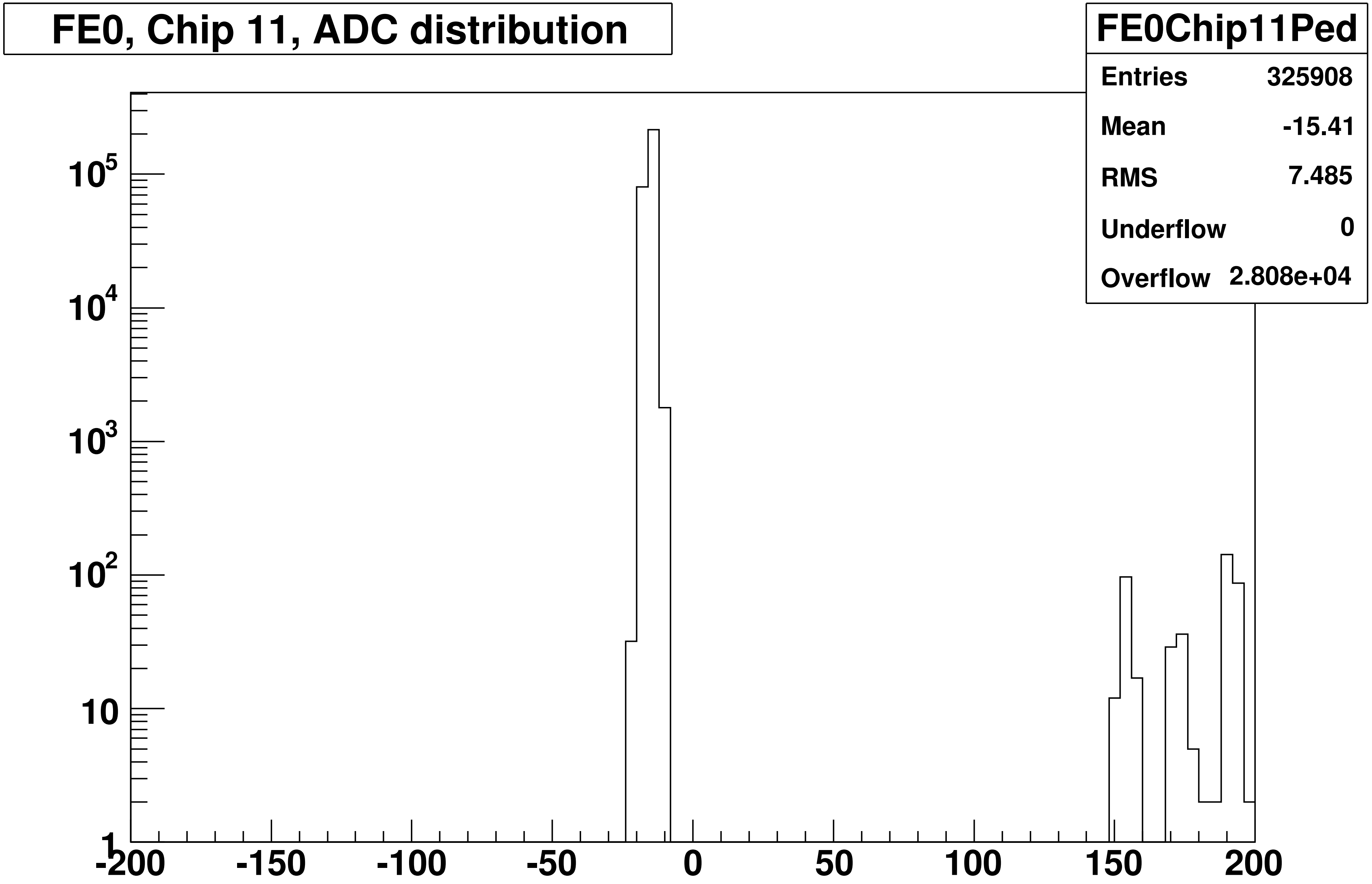
20000
25000


FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 325908
Mean -15.41
RMS 7.485
Underflow 0
Overflow 2.808e+04
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10^3
10^2
10
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-100
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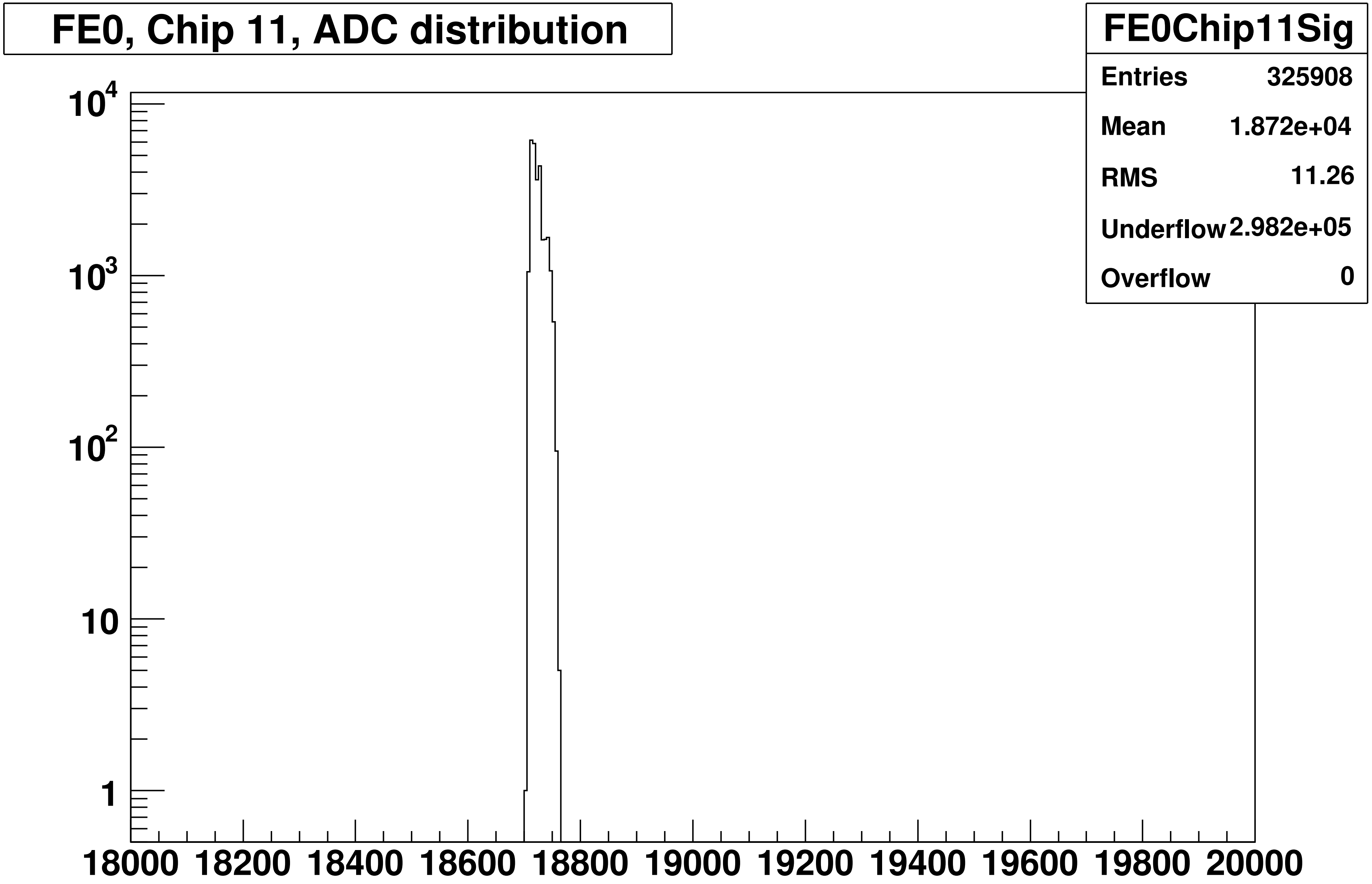
100
150
200


FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 325908
Mean 1.872e+04
RMS 11.26
Underflow 2.982e+05
Overflow 0
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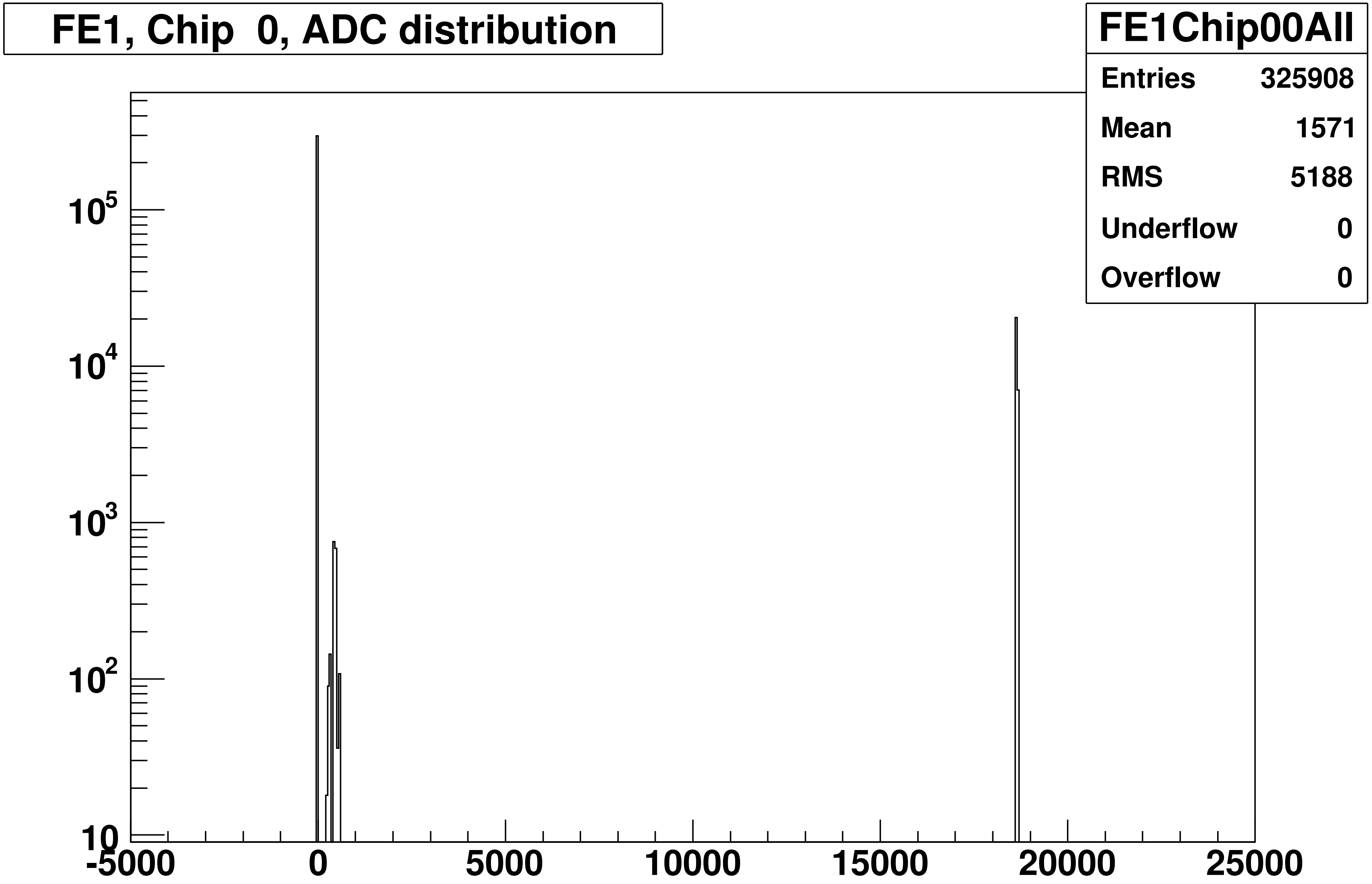
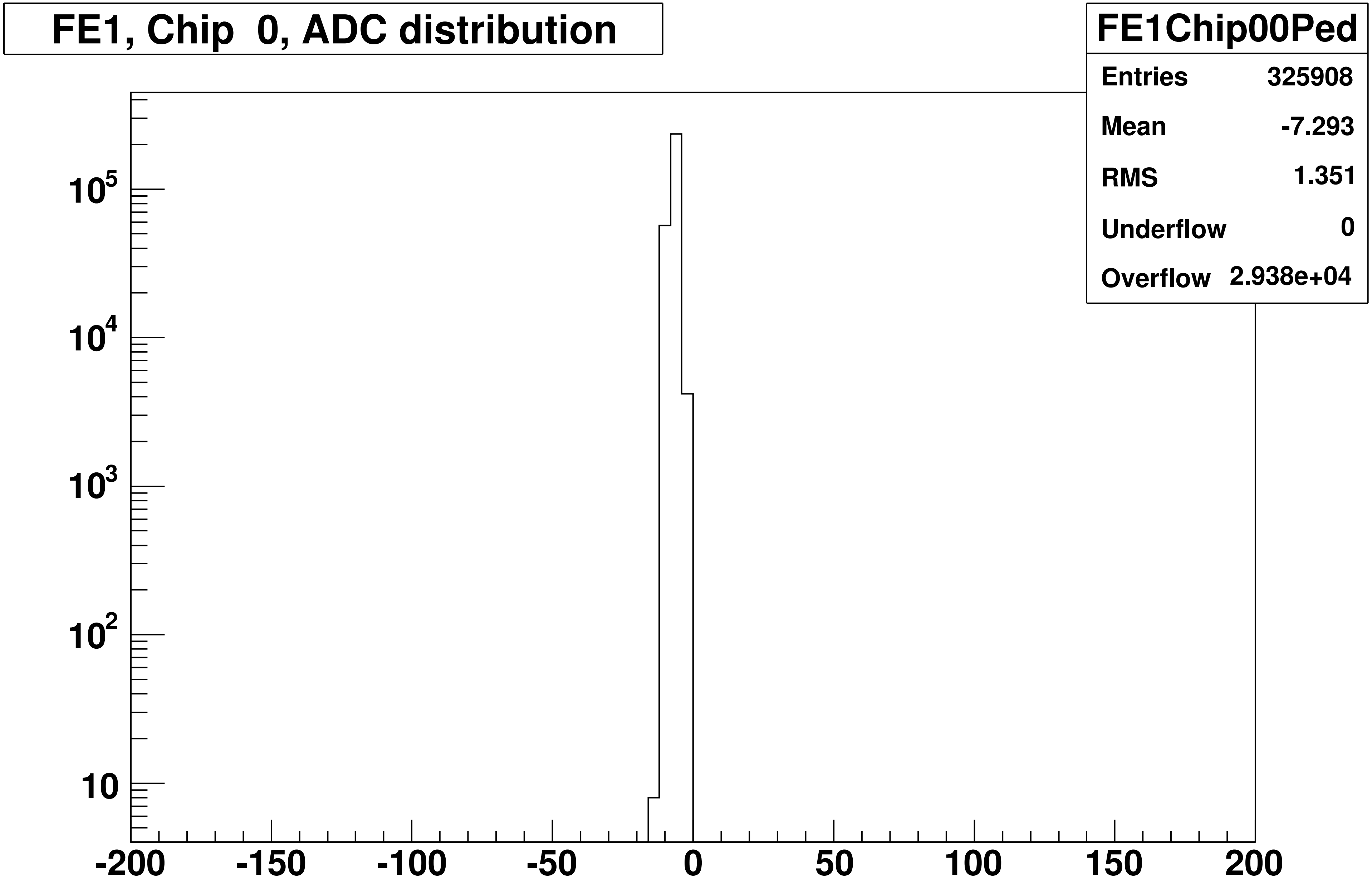
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 325908
Mean 1571
RMS 5188
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 325908
Mean -7.293
RMS 1.351
Underflow 0
Overflow 2.938e+04
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10^4
10^3
10^2
10
-200
-150
-100
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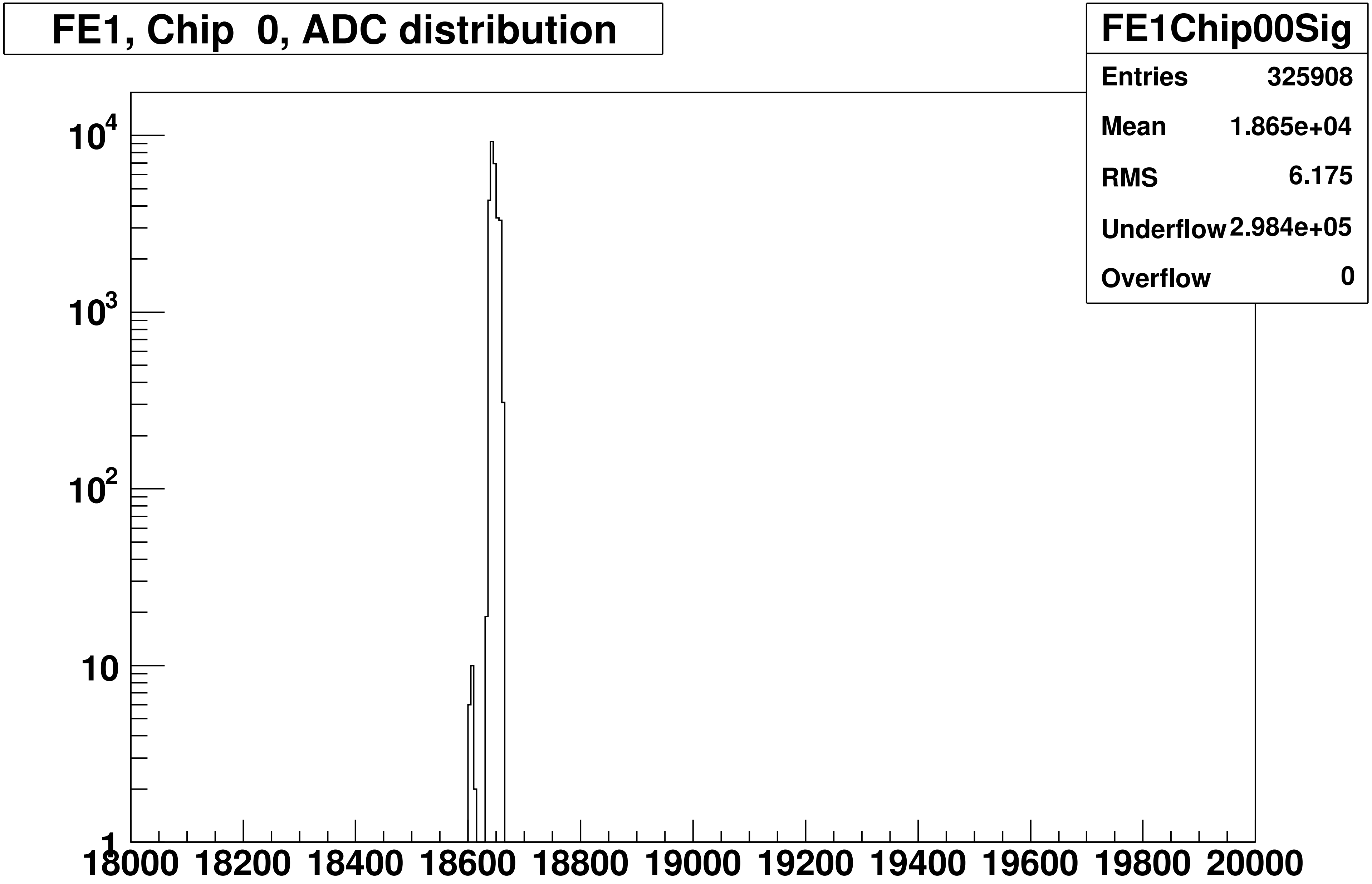
50
100
150
200


FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 325908
Mean 1.865e+04
RMS 6.175
Underflow 2.984e+05
Overflow 0
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10^3
10^2
10
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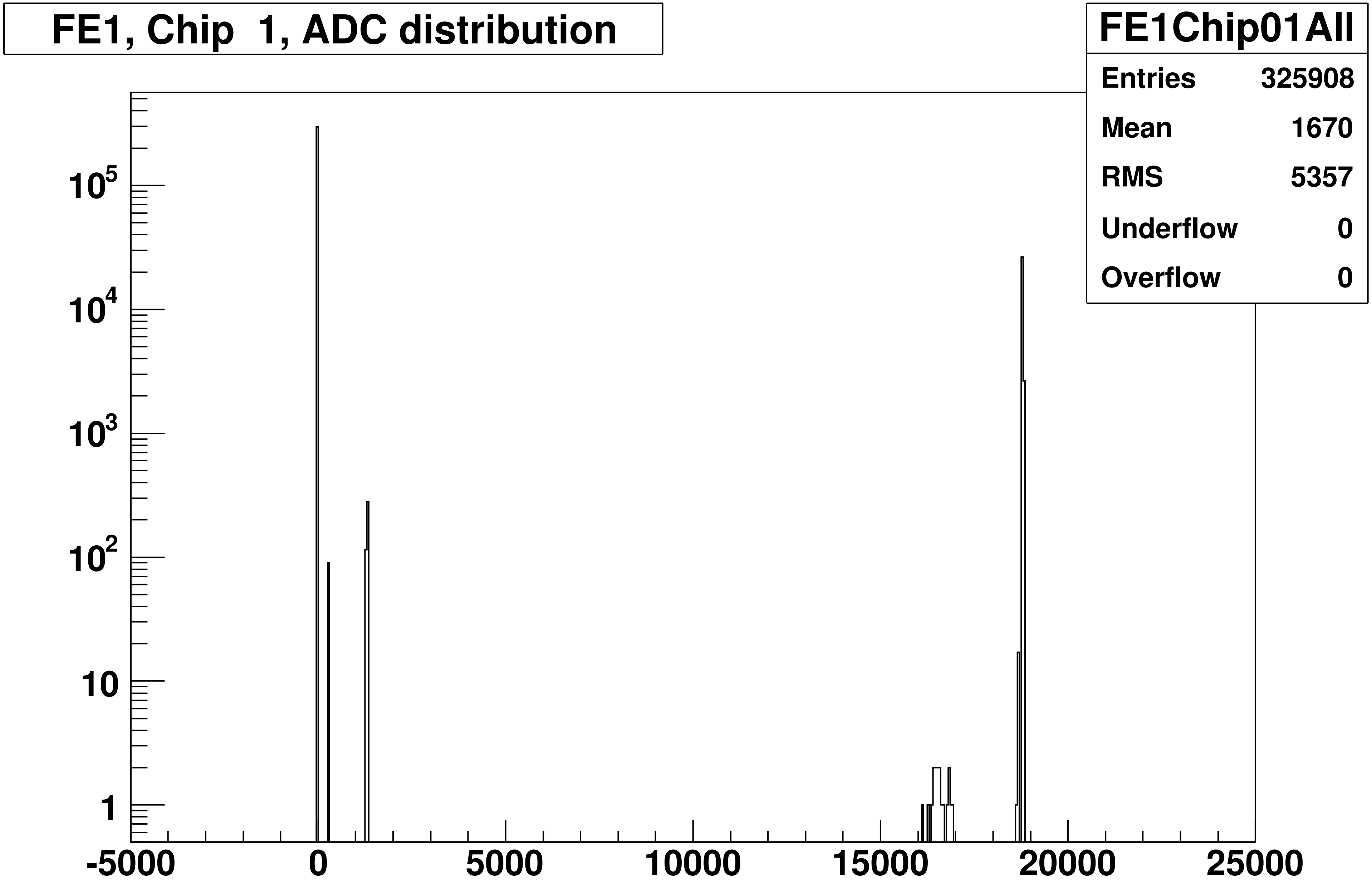
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 325908
Mean 1670
RMS 5357
Underflow 0
Overflow 0
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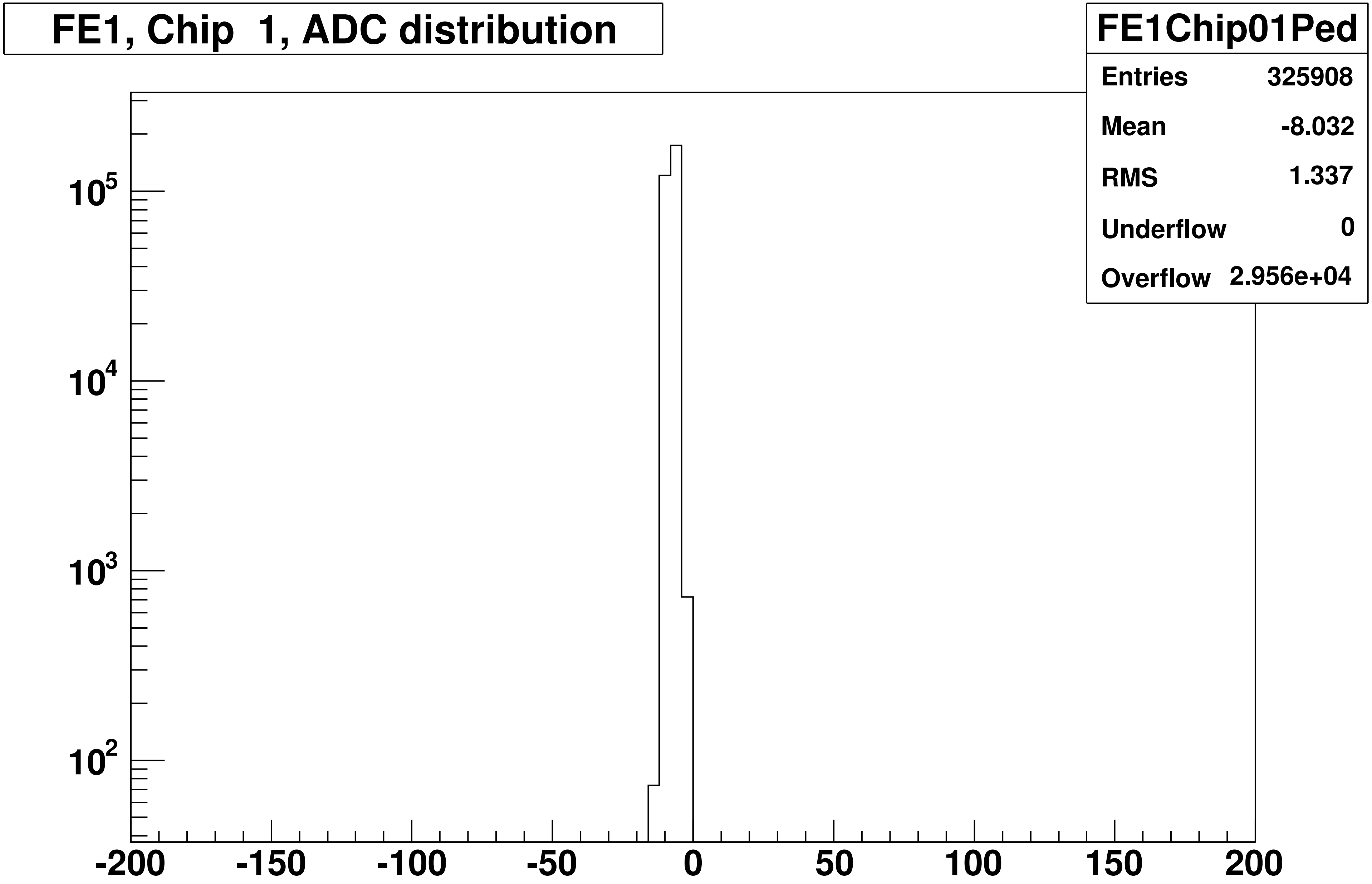
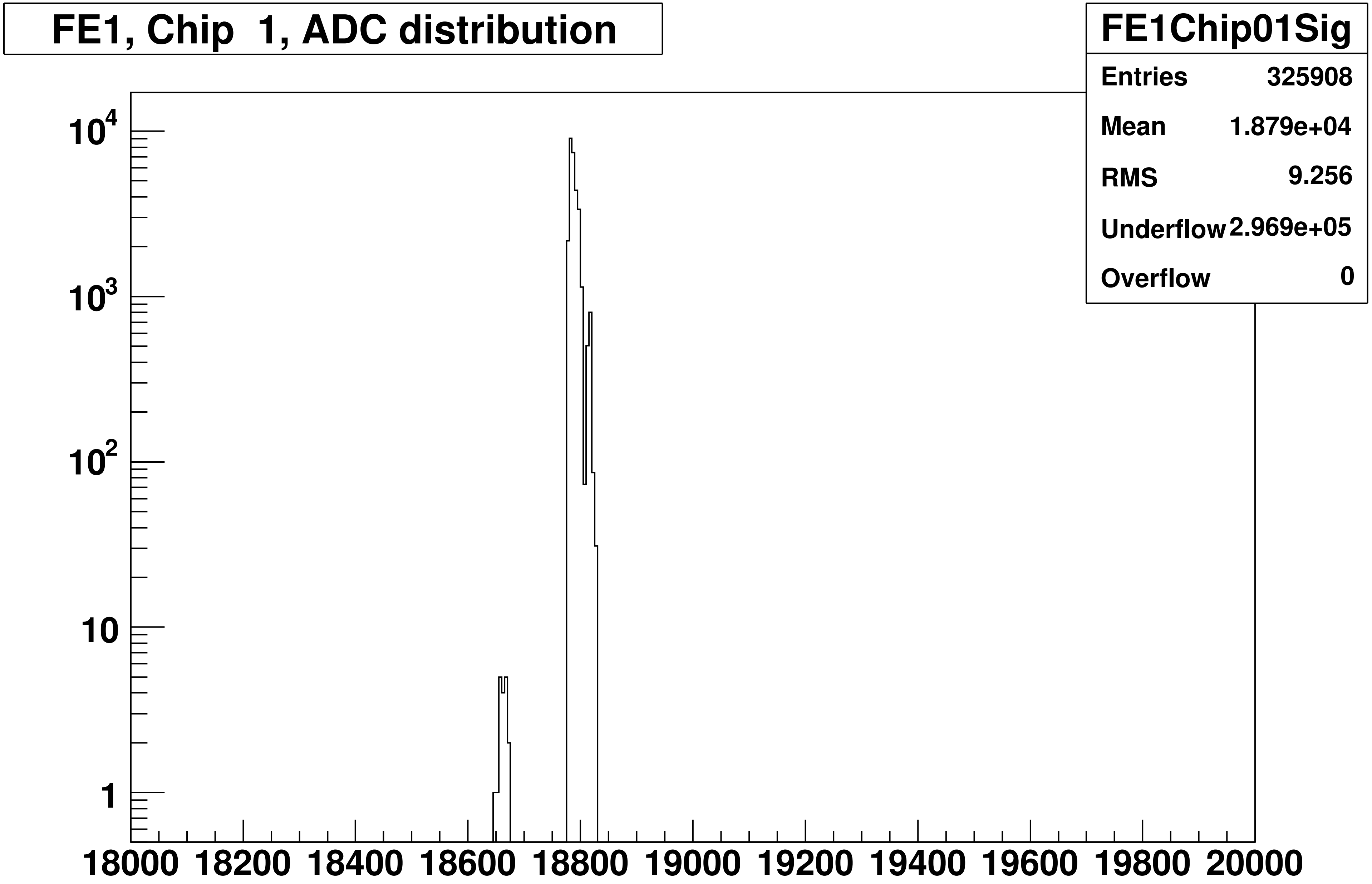
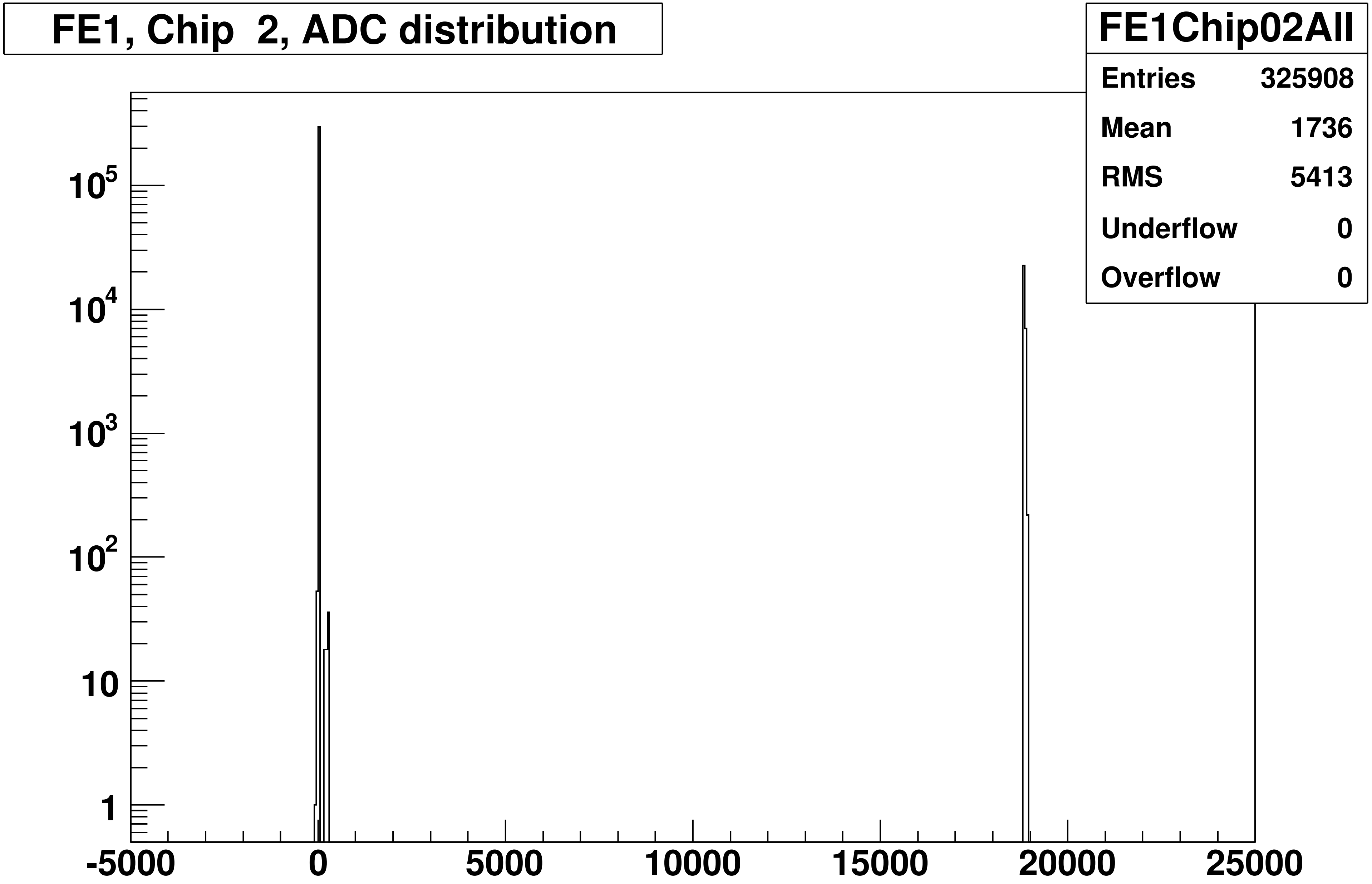
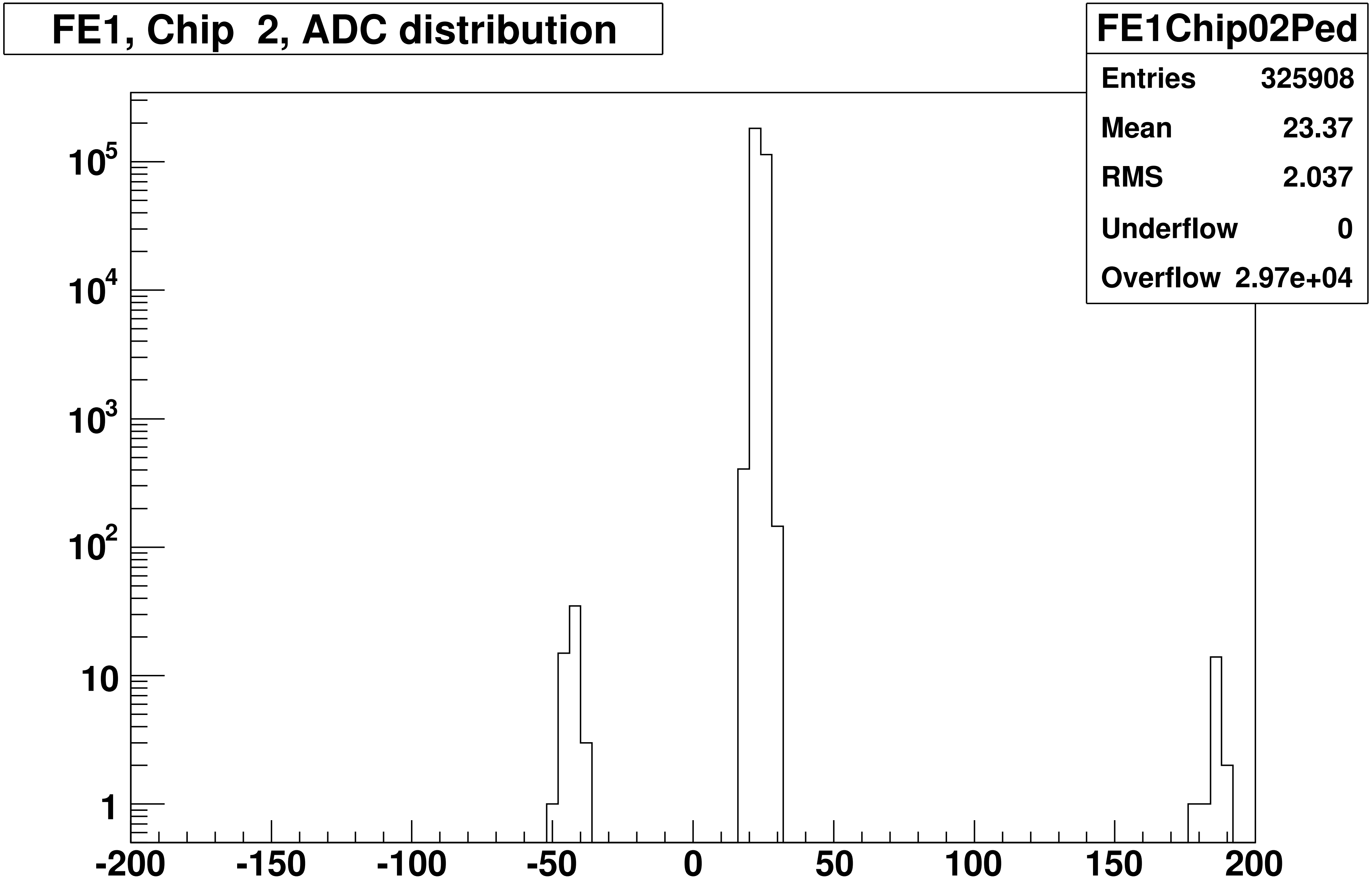
20000
25000



FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 325908
Mean -8.032
RMS 1.337
Underflow 0
Overflow 2.956e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 325908
Mean 1.879e+04
RMS 9.256
Underflow 2.969e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 325908
Mean 1736
RMS 5413
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 325908
Mean 23.37
RMS 2.037
Underflow 0
Overflow 2.97e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
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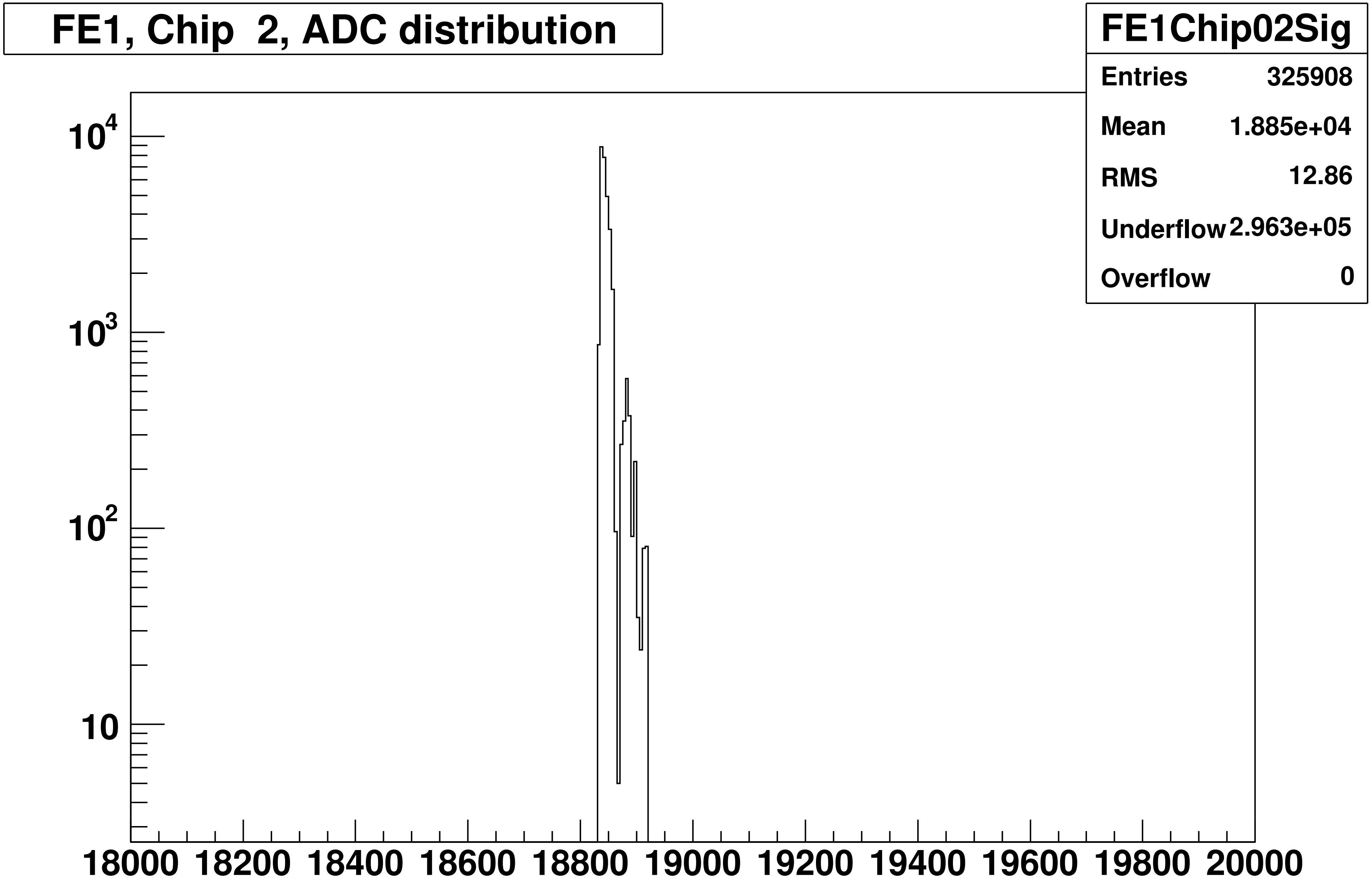
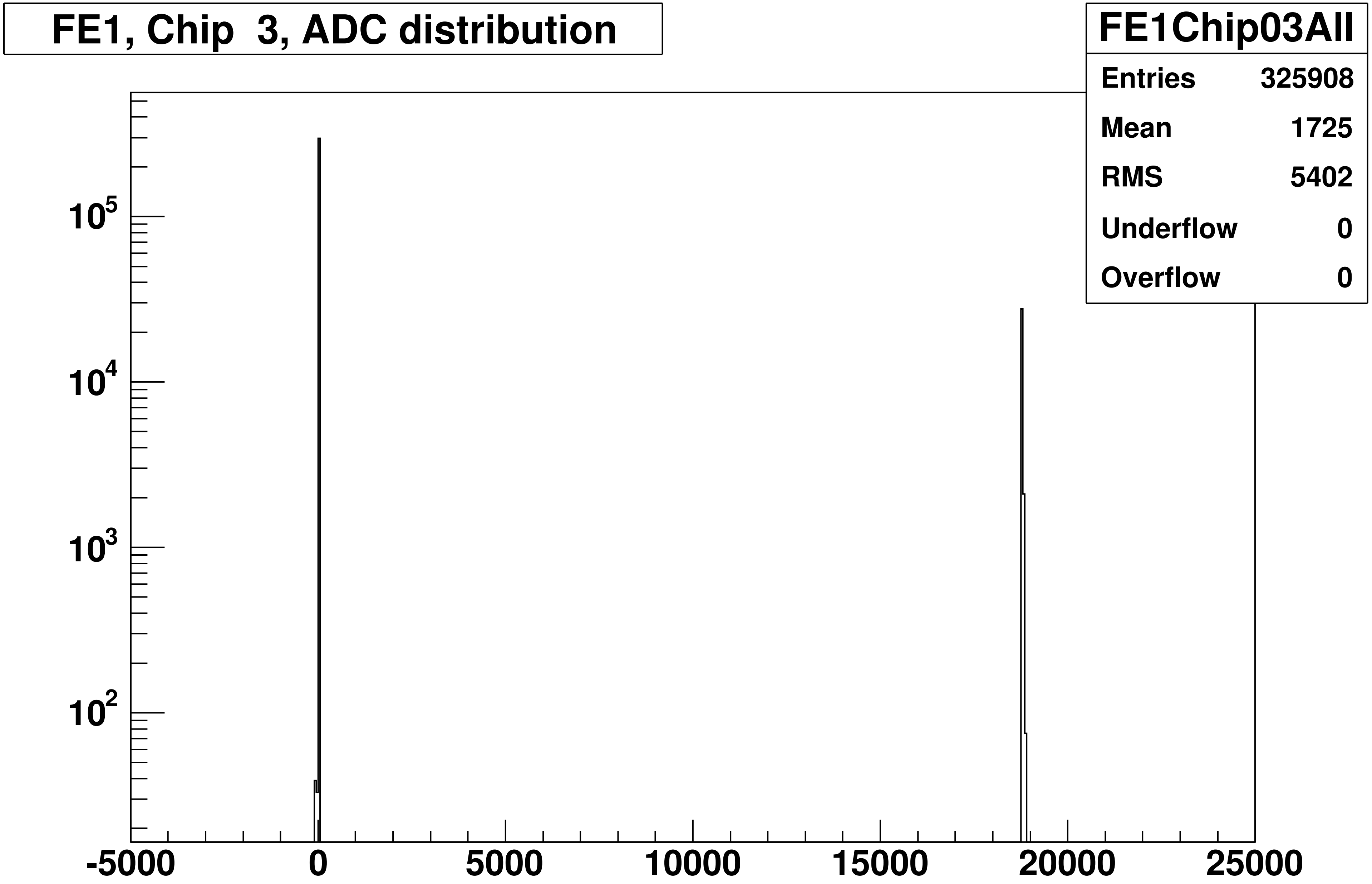
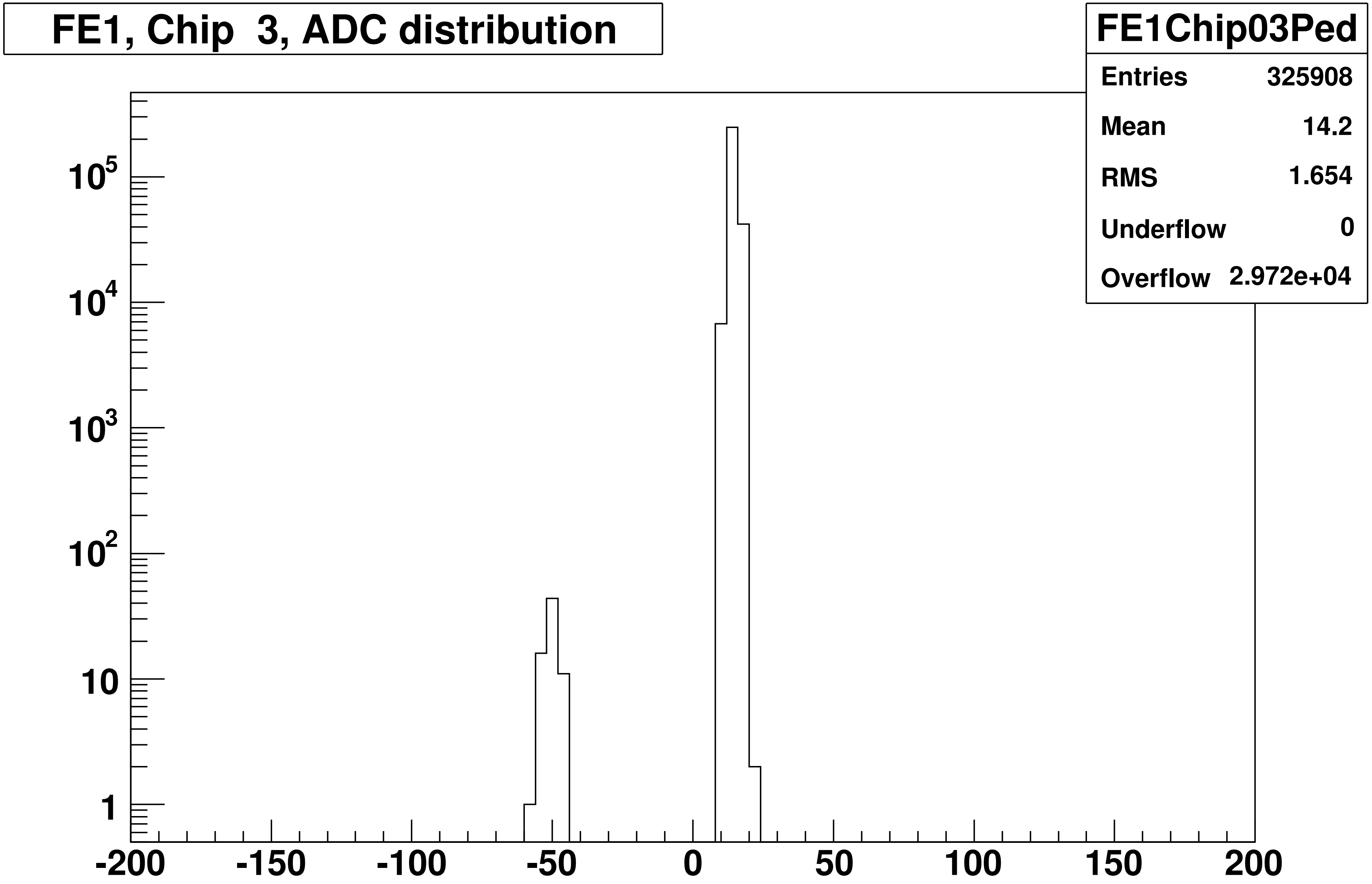
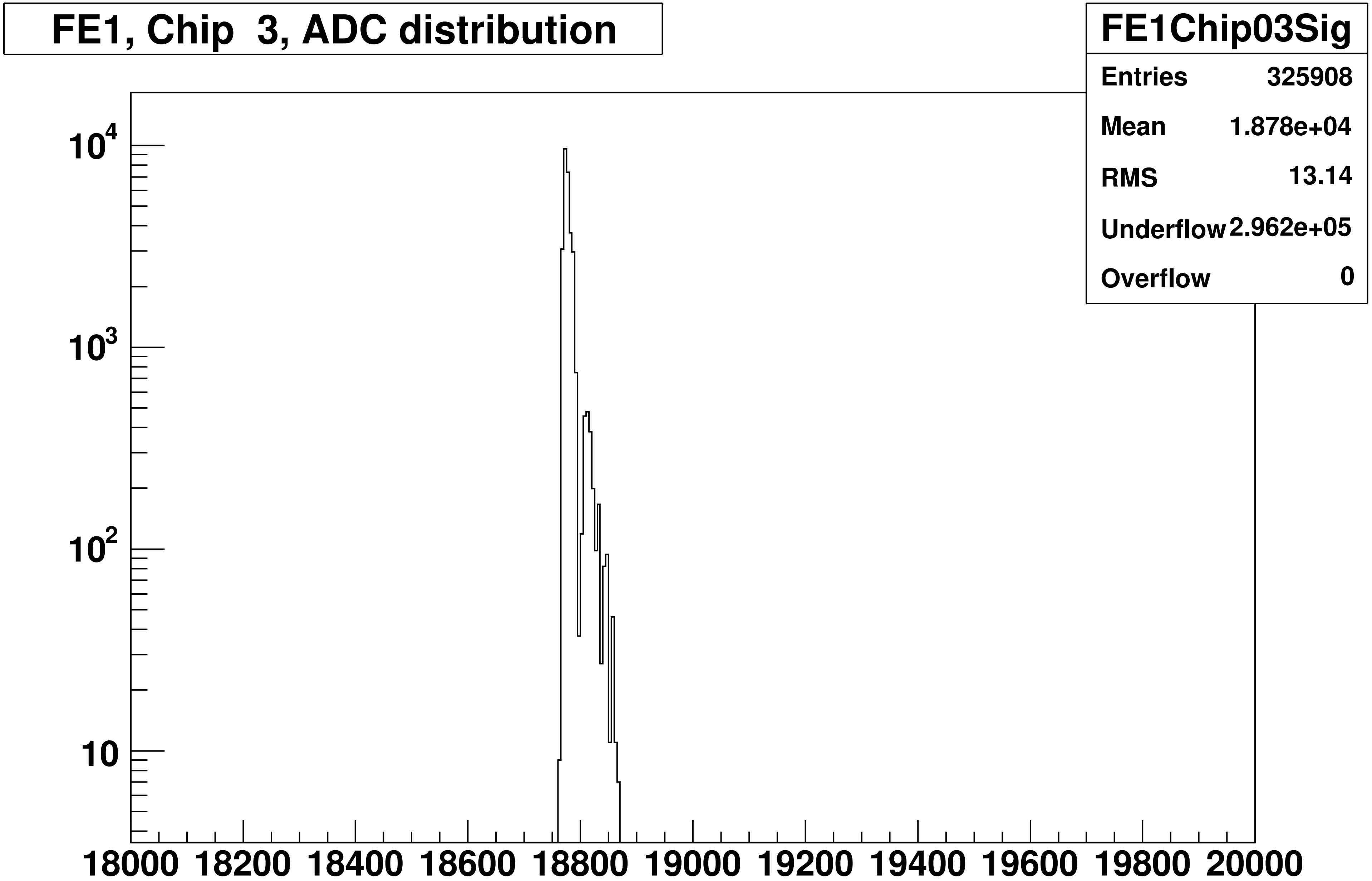
100
150
200

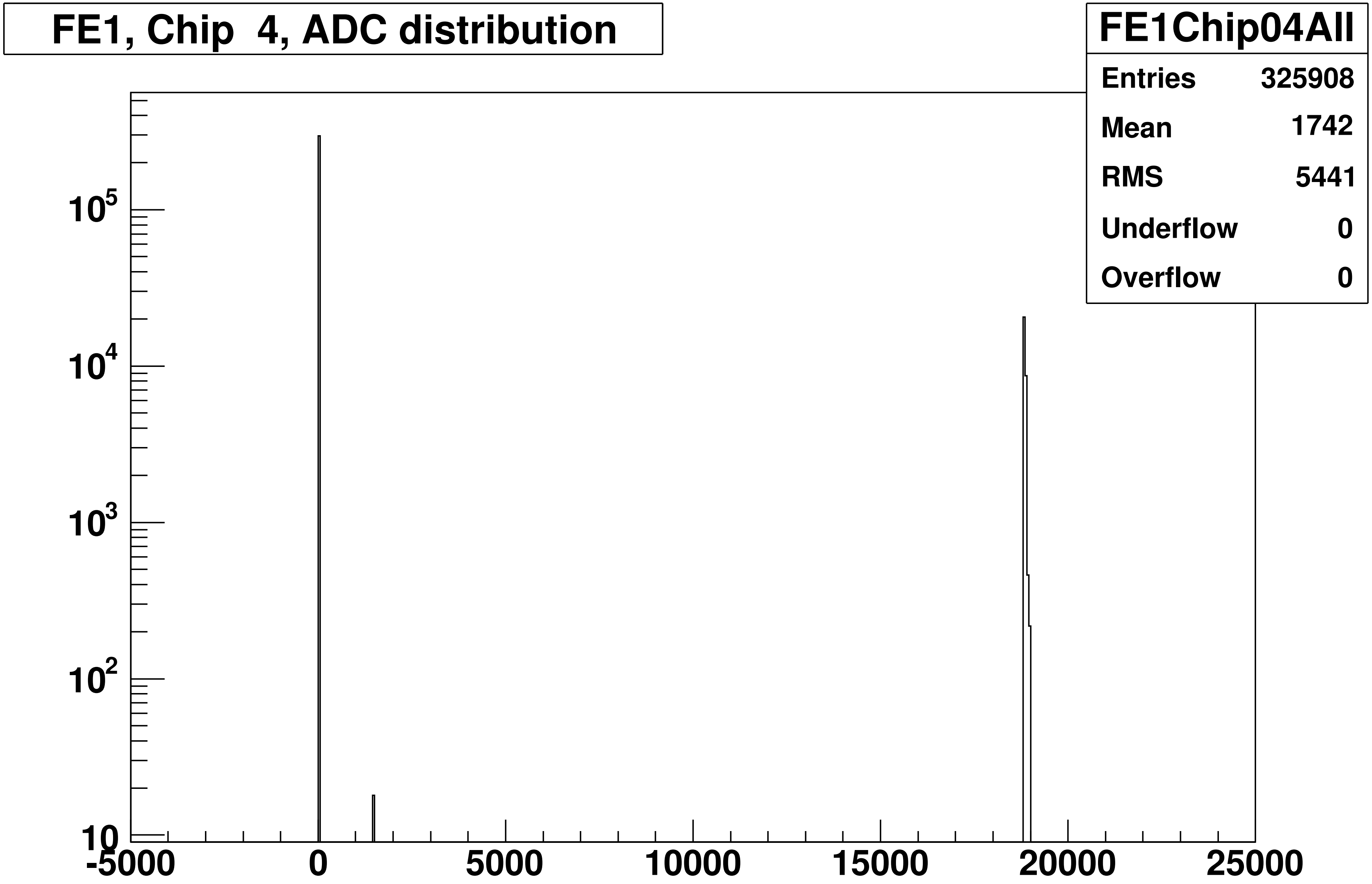

FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 325908
Mean 1.885e+04
RMS 12.86
Underflow 2.963e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



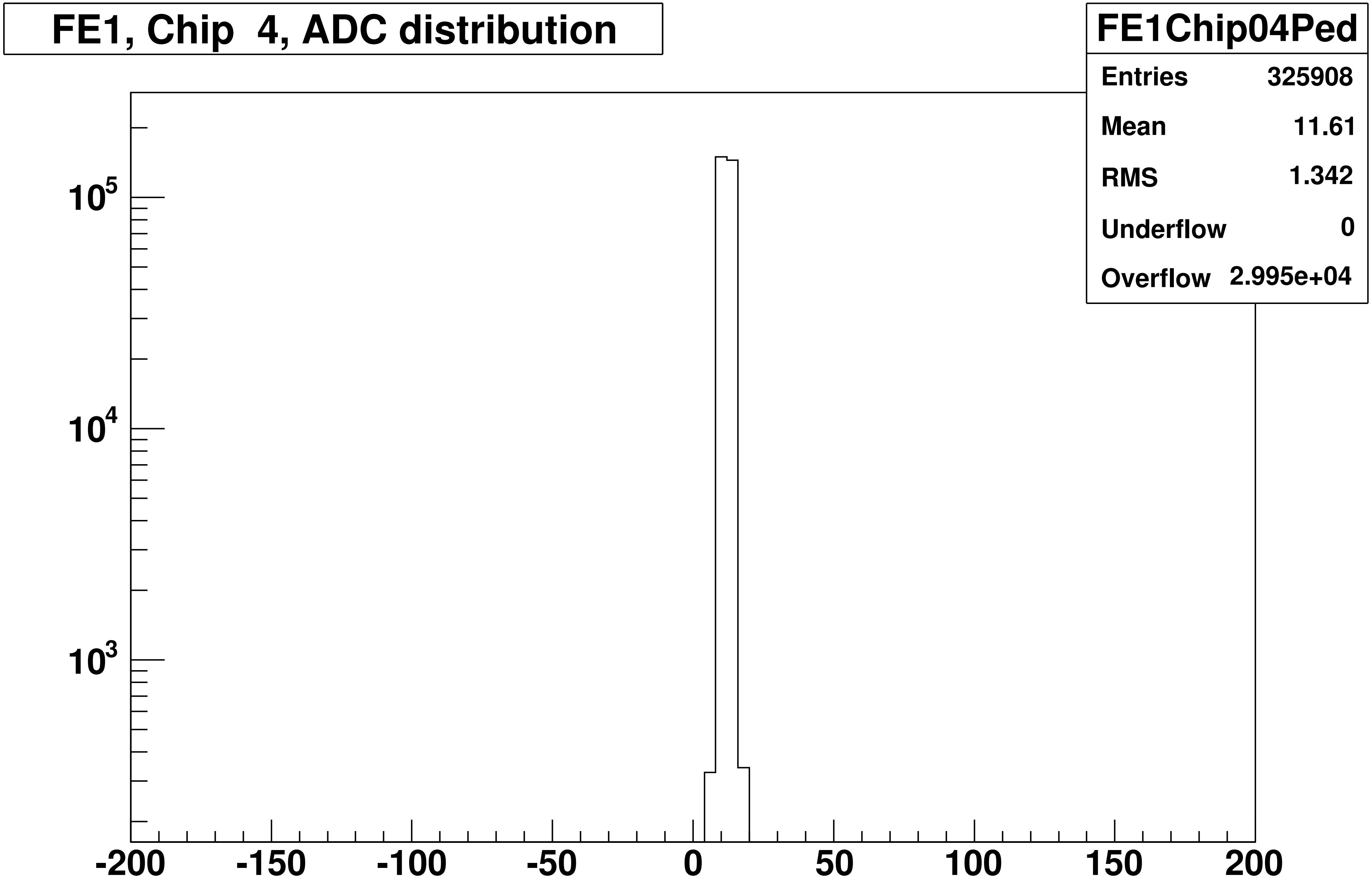
FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 325908
Mean 1725
RMS 5402
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

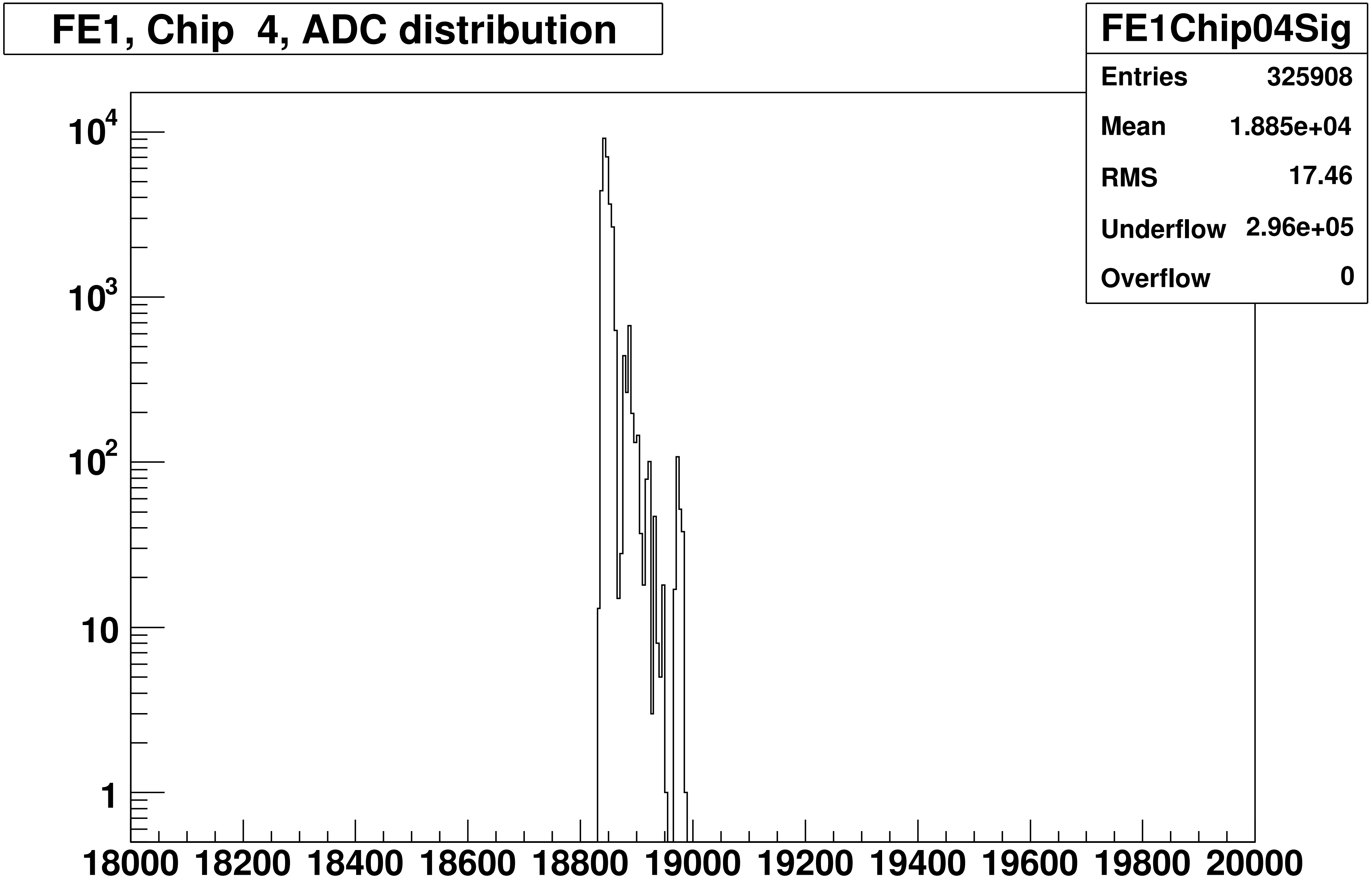

FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 325908
Mean 14.2
RMS 1.654
Underflow 0
Overflow 2.972e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 325908
Mean 1.878e+04
RMS 13.14
Underflow 2.962e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

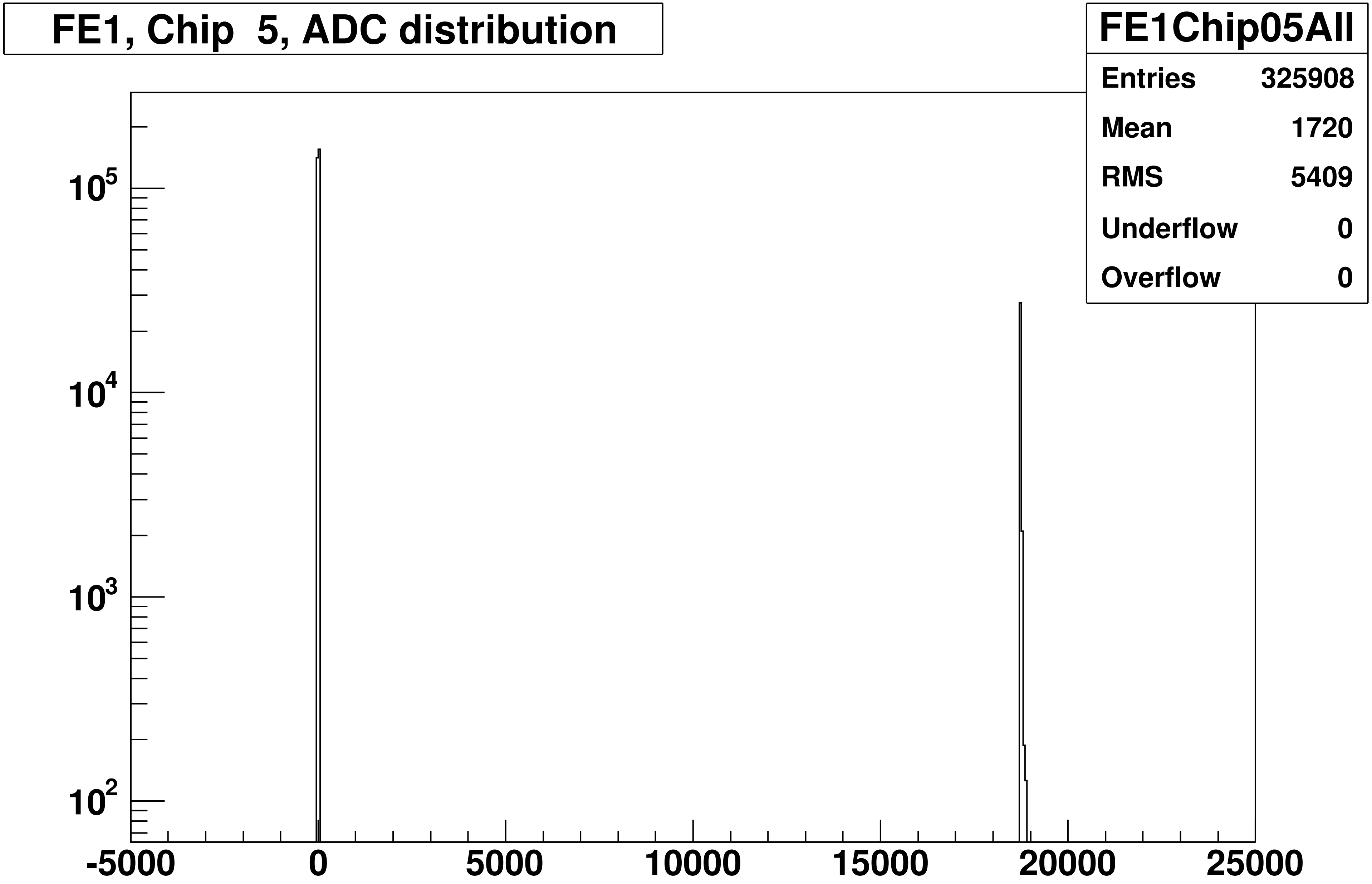


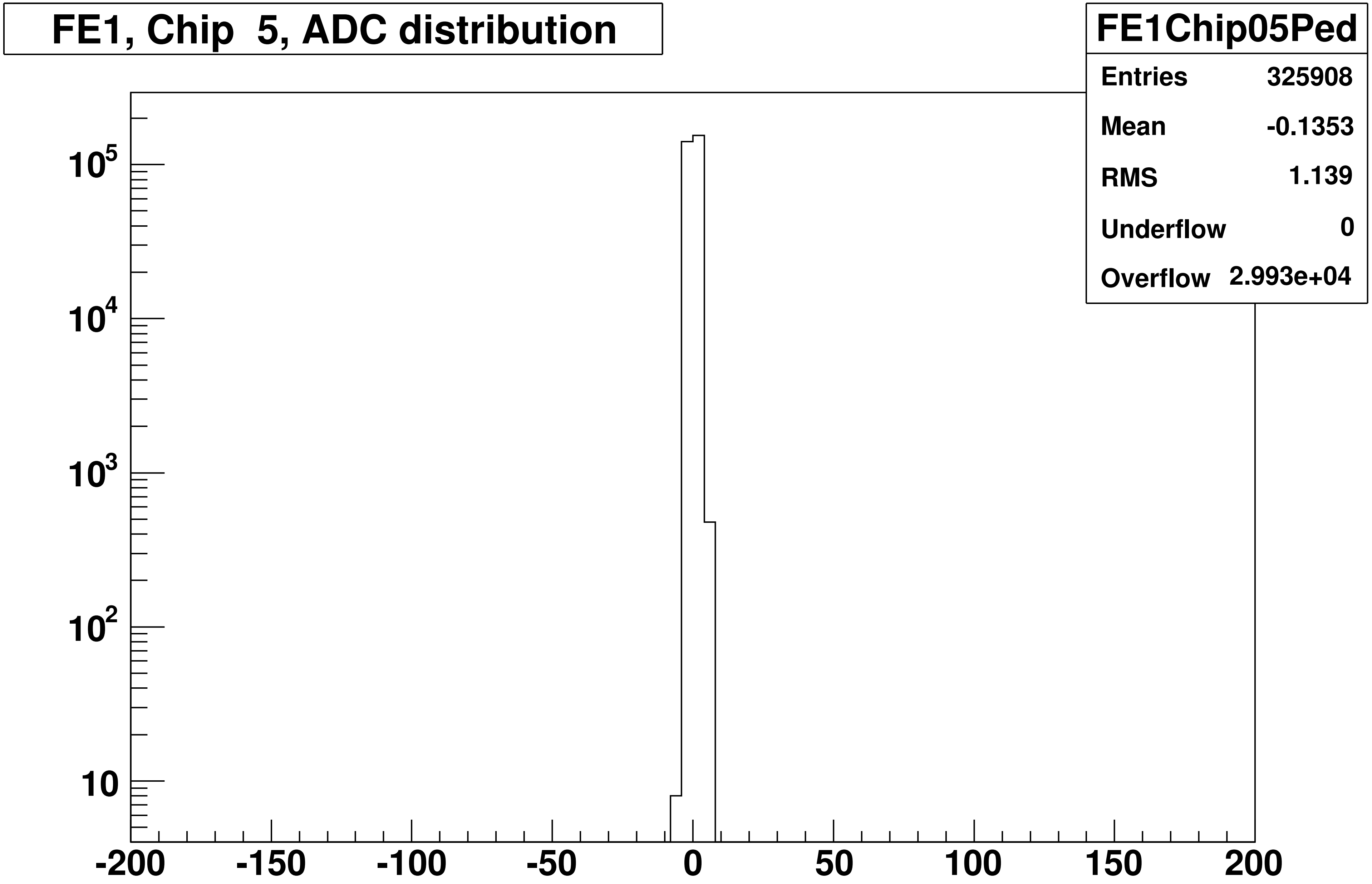
FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 325908
Mean 1742
RMS 5441
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 325908
Mean 11.61
RMS 1.342
Underflow 0
Overflow 2.995e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

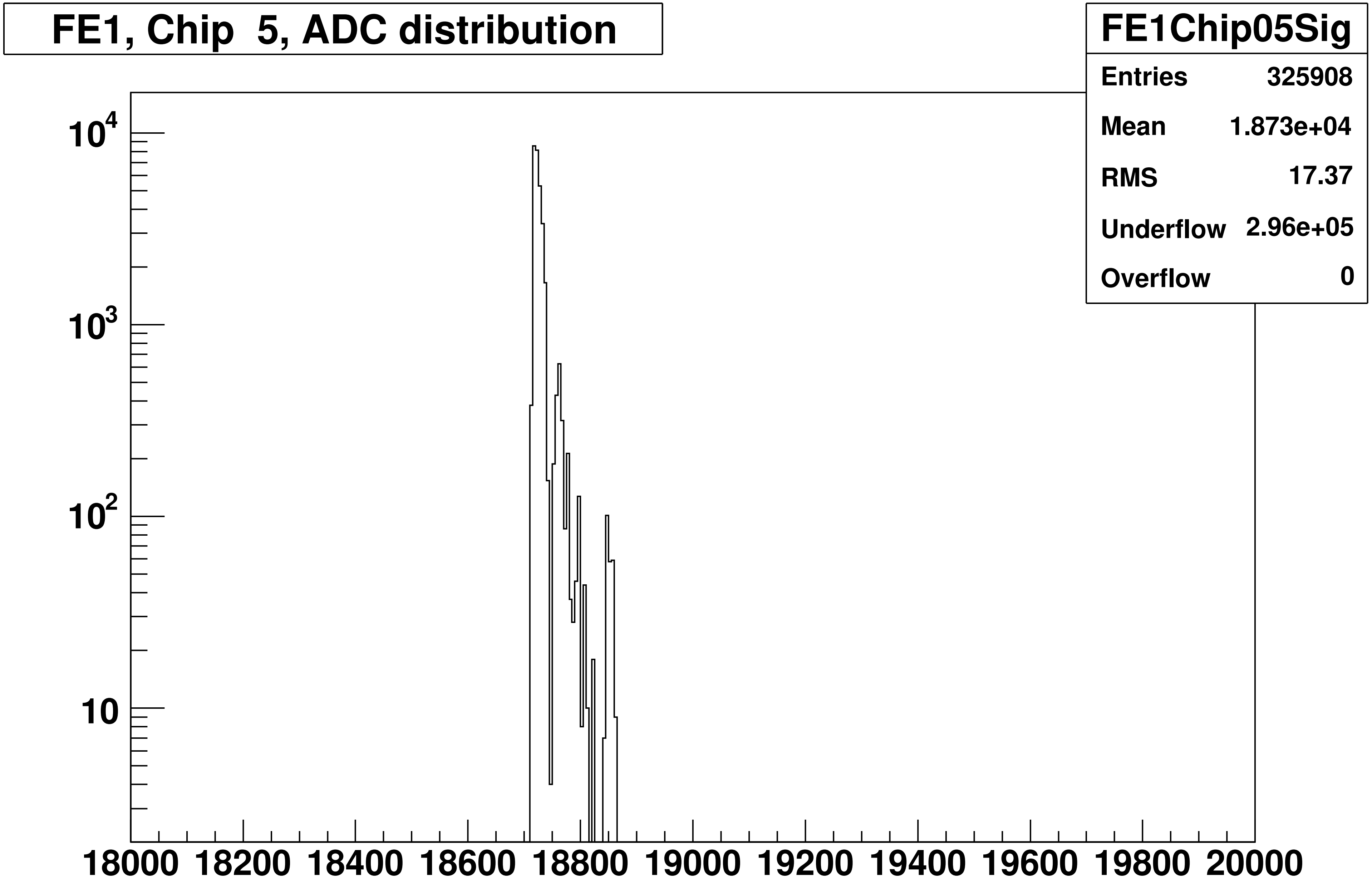


FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 325908
Mean 1.885e+04
RMS 17.46
Underflow 2.96e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

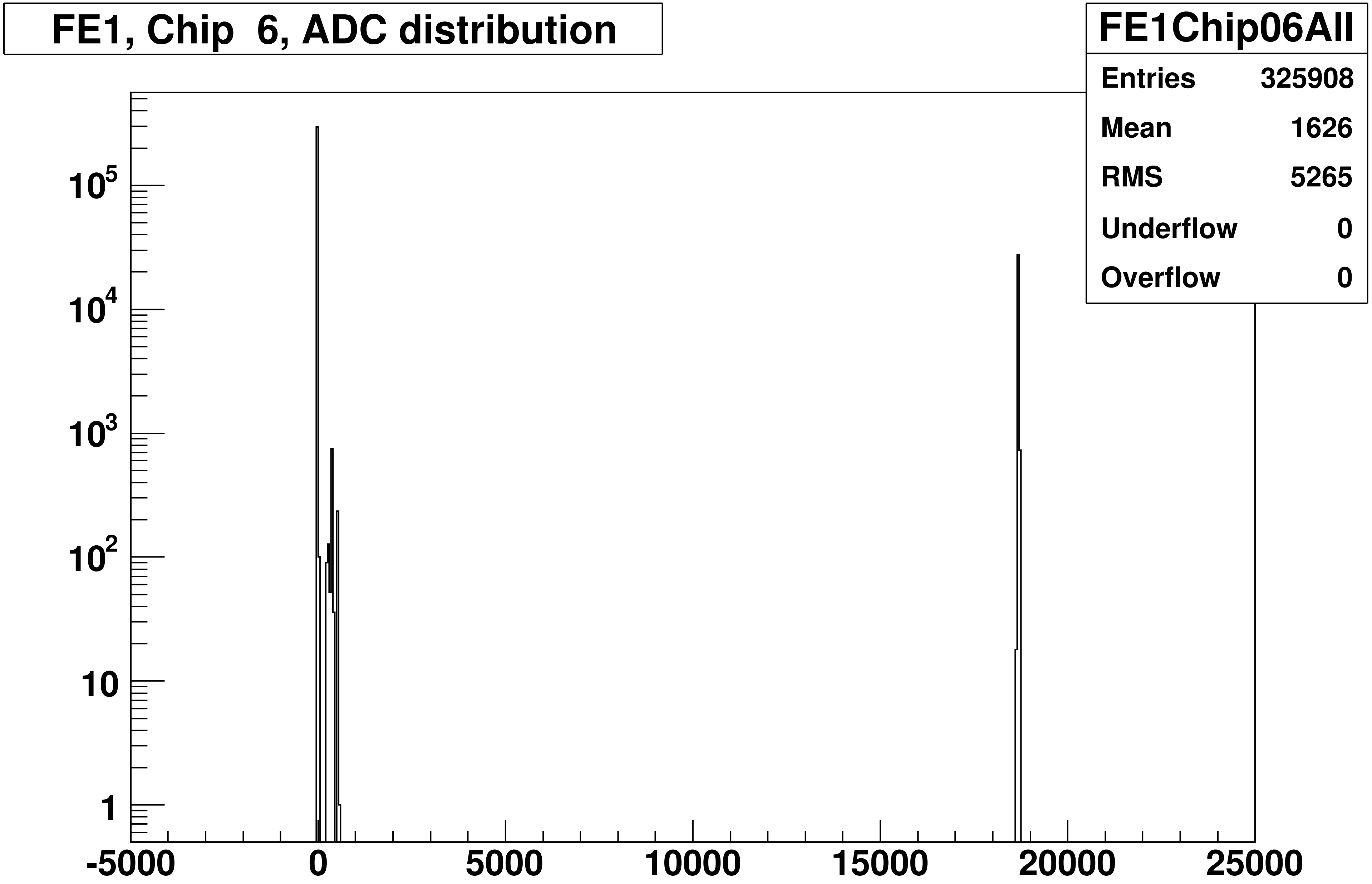

FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 325908
Mean 1720
RMS 5409
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

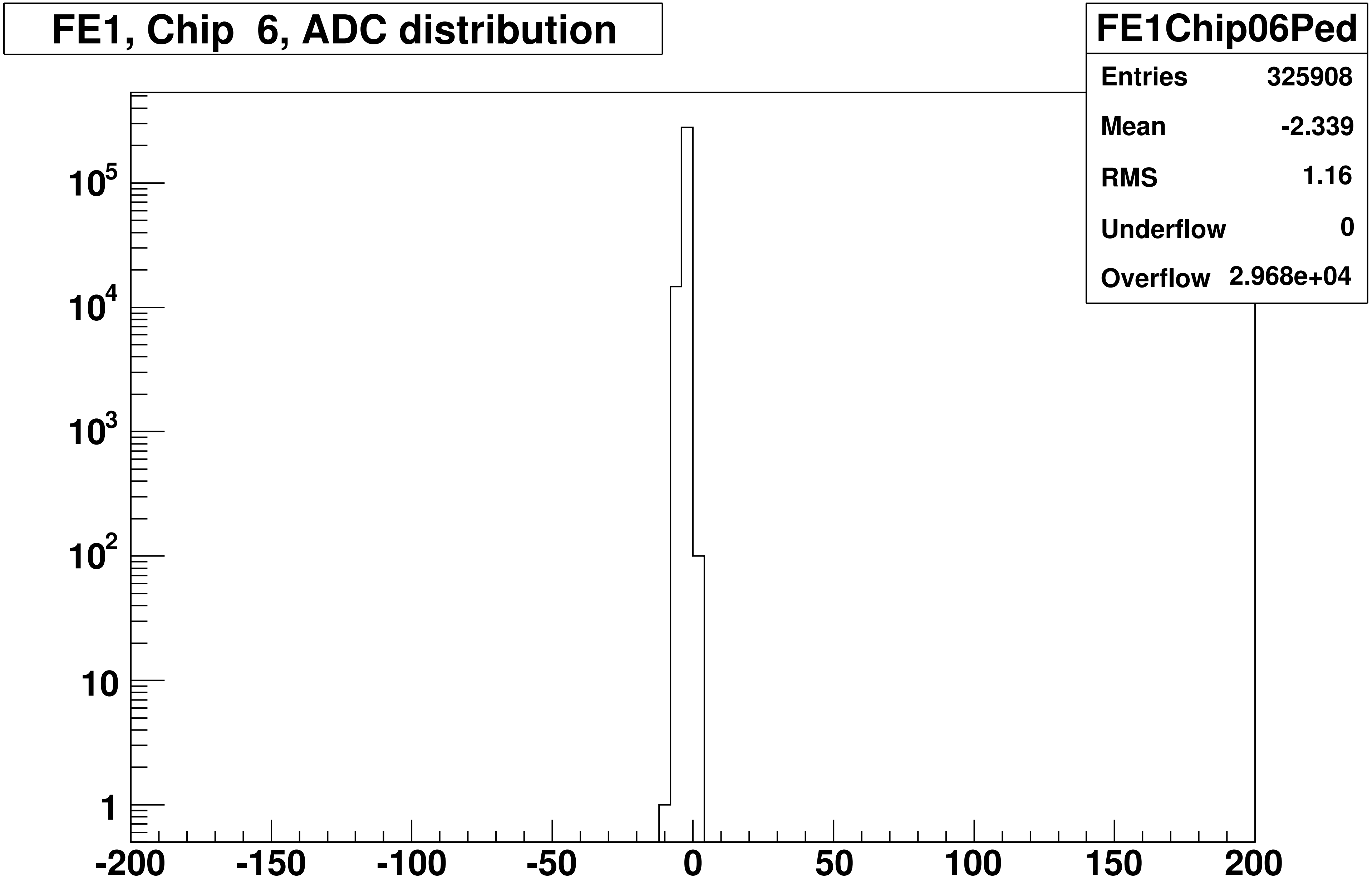


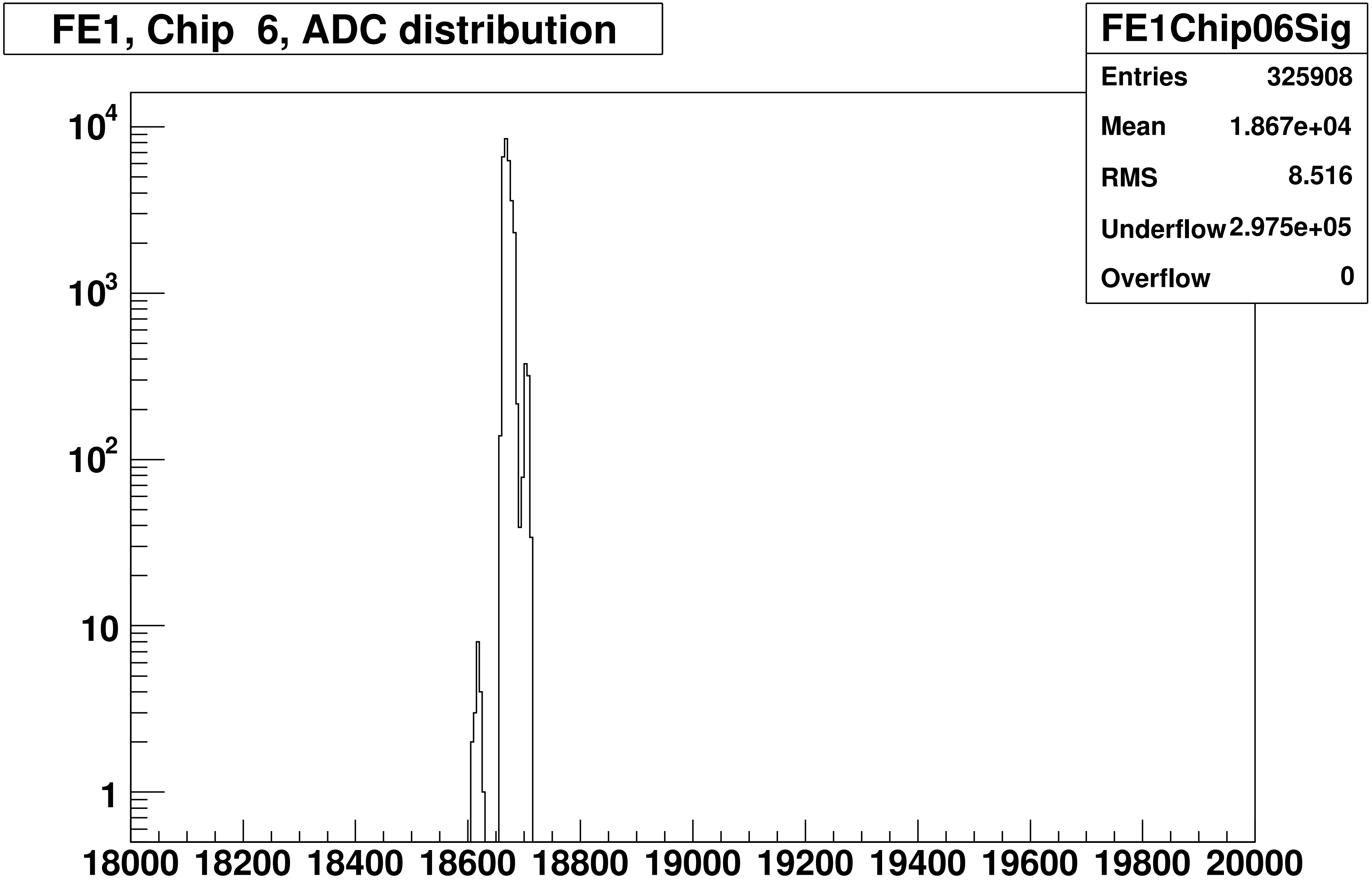
FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 325908
Mean -0.1353
RMS 1.139
Underflow 0
Overflow 2.993e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



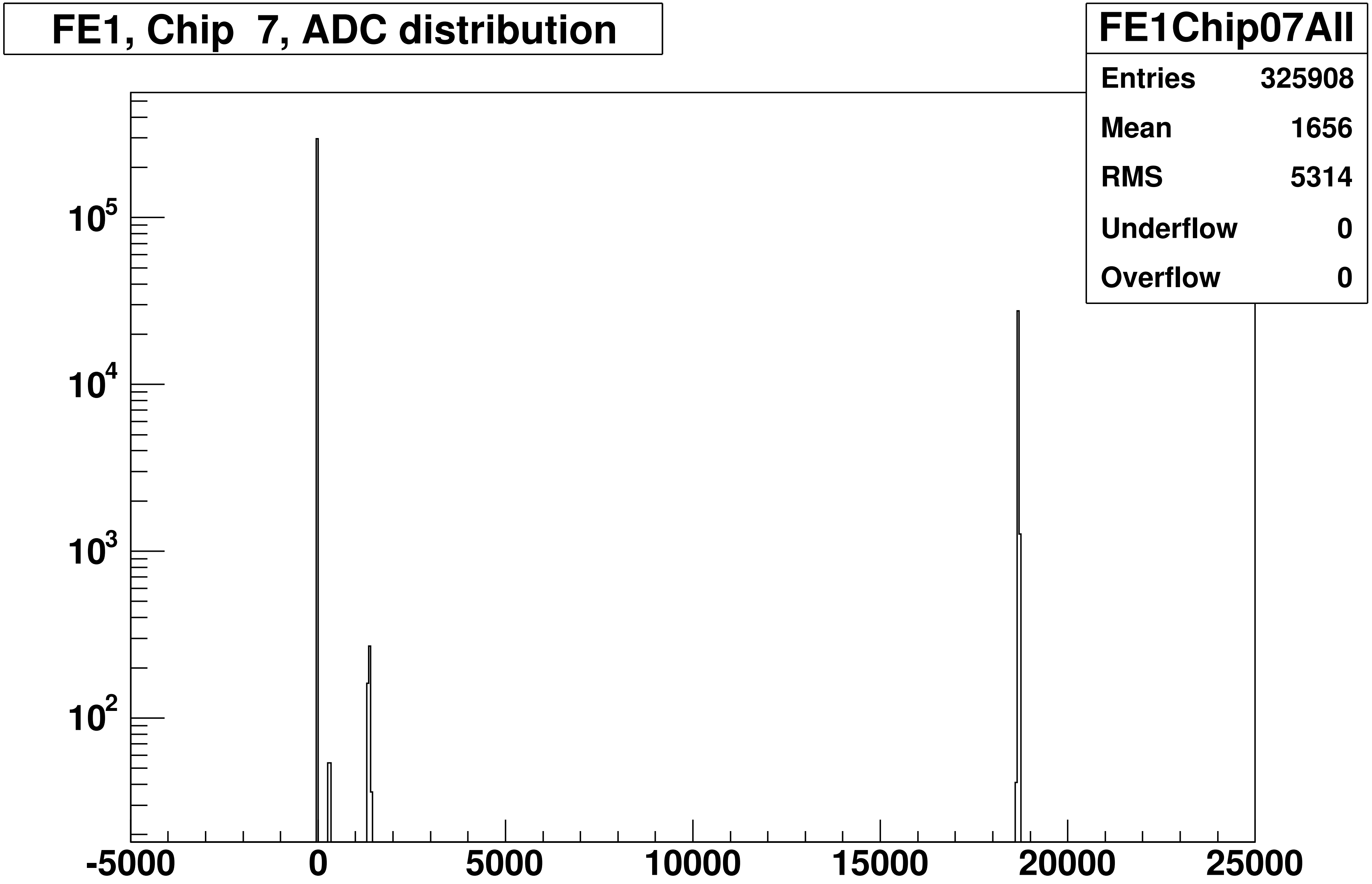
FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 325908
Mean 1.873e+04
RMS 17.37
Underflow 2.96e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

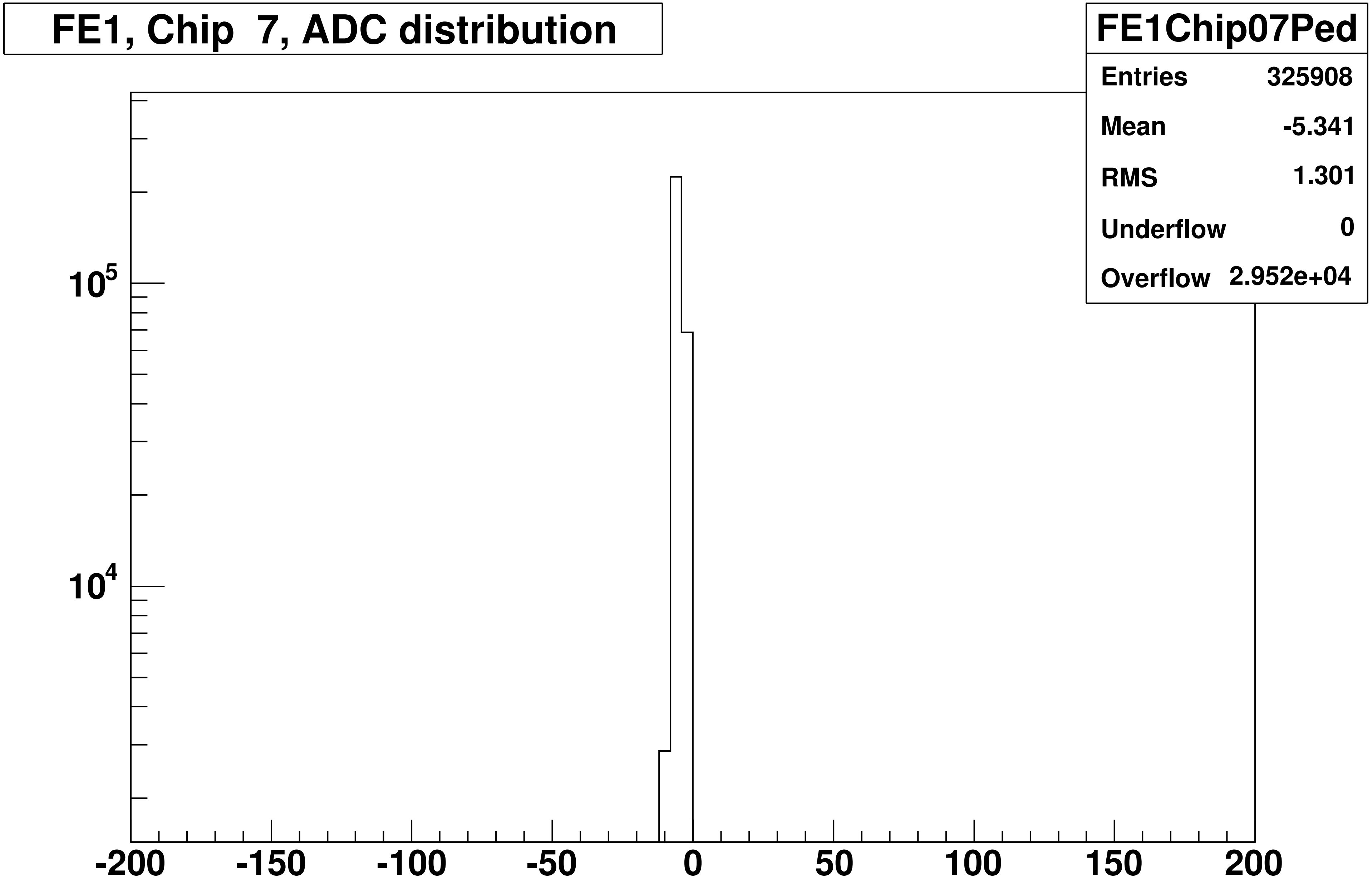

FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 325908
Mean 1626
RMS 5265
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 325908
Mean -2.339
RMS 1.16
Underflow 0
Overflow 2.968e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

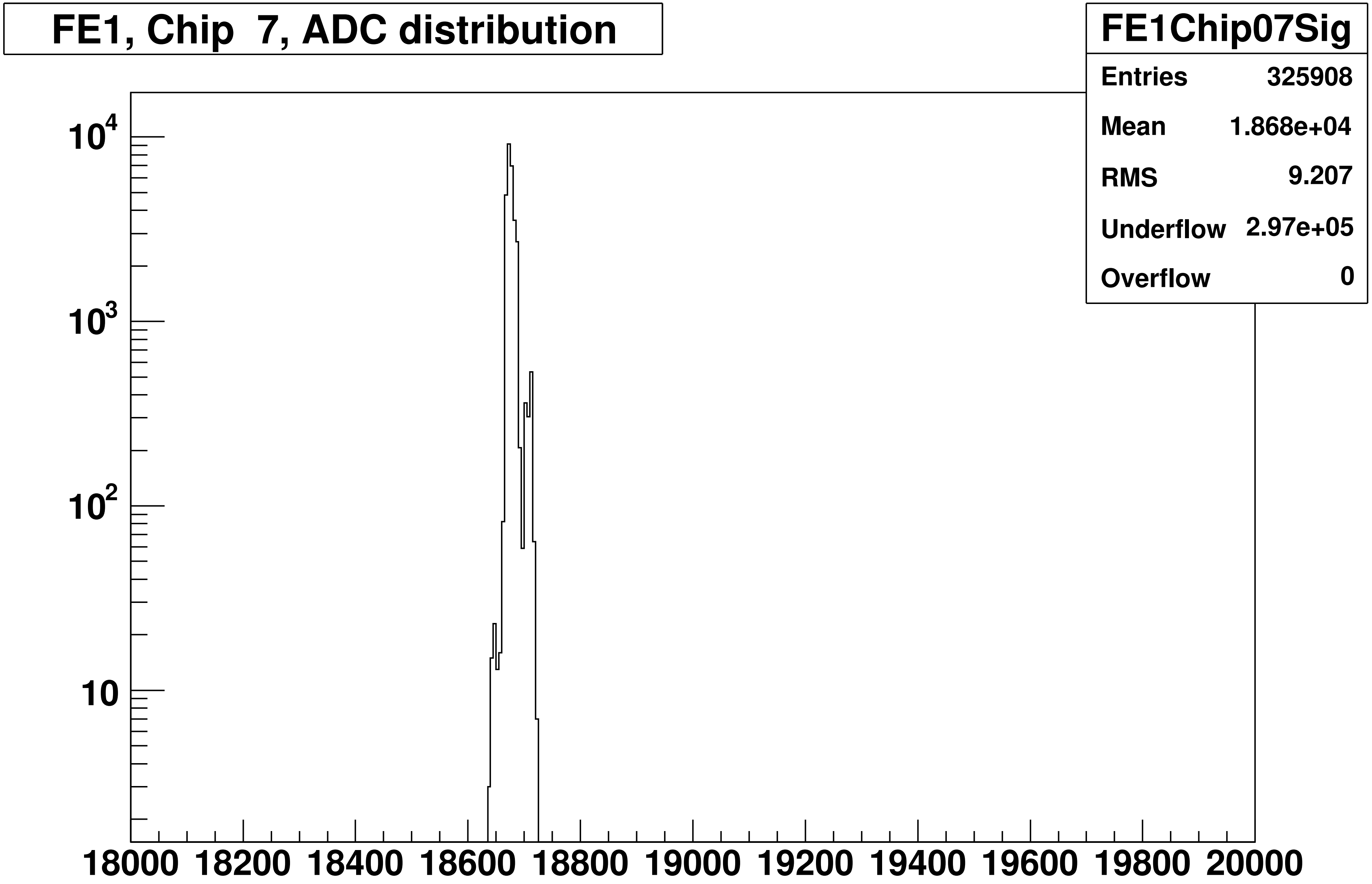


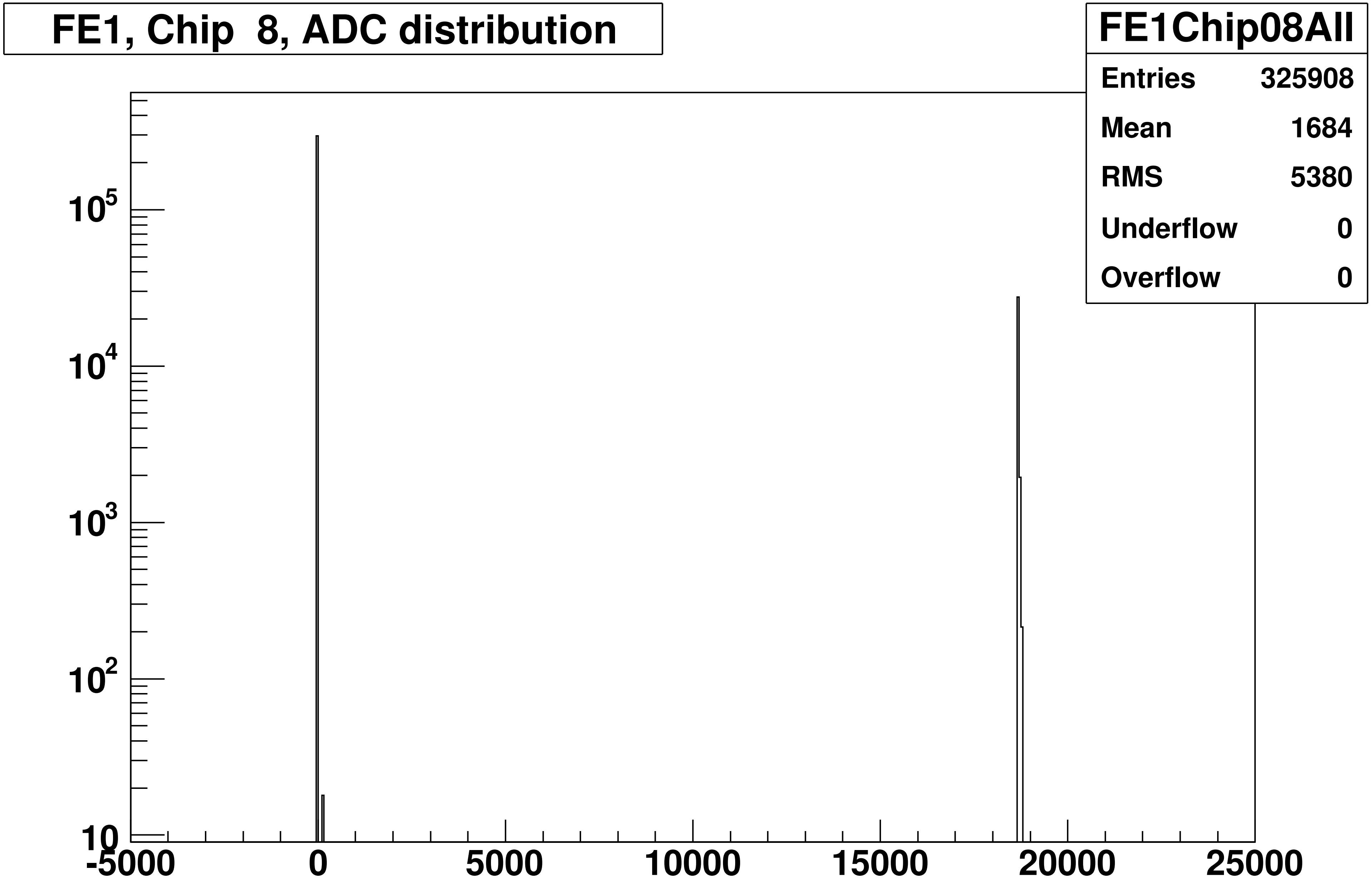
FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 325908
Mean 1.867e+04
RMS 8.516
Underflow 2.975e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 325908
Mean 1656
RMS 5314
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

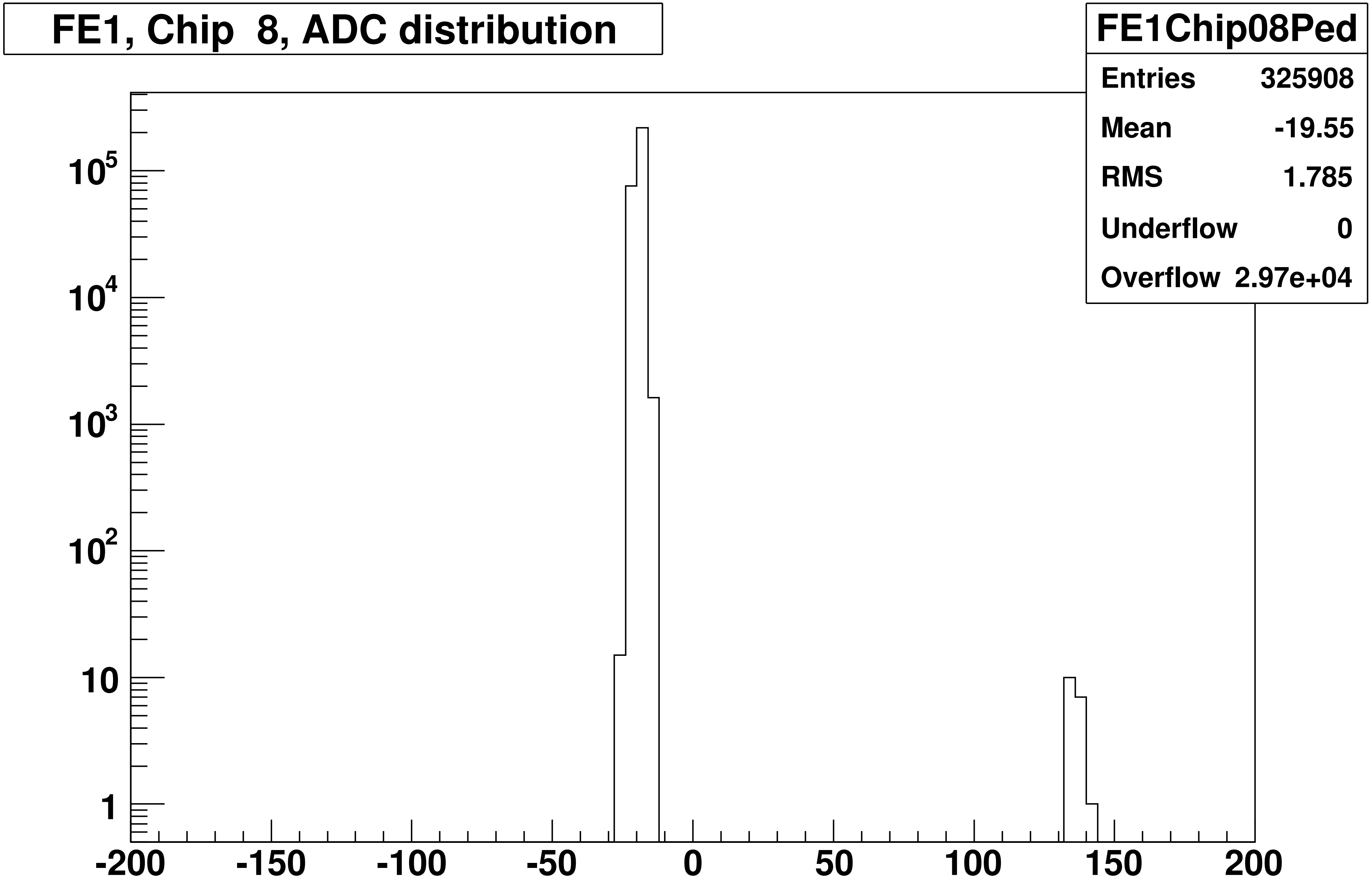


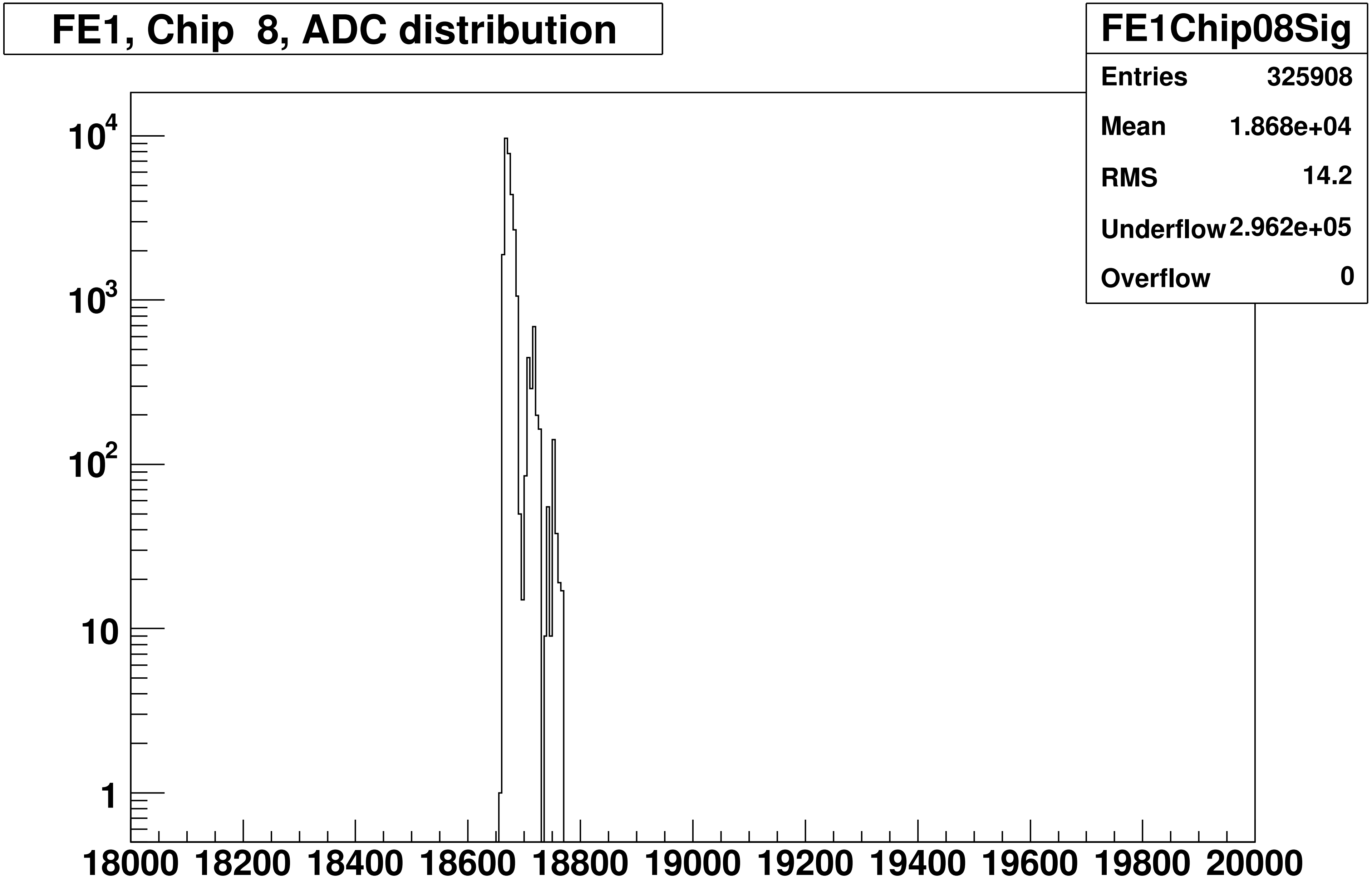
FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 325908
Mean -5.341
RMS 1.301
Underflow 0
Overflow 2.952e+04
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 325908
Mean 1.868e+04
RMS 9.207
Underflow 2.97e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

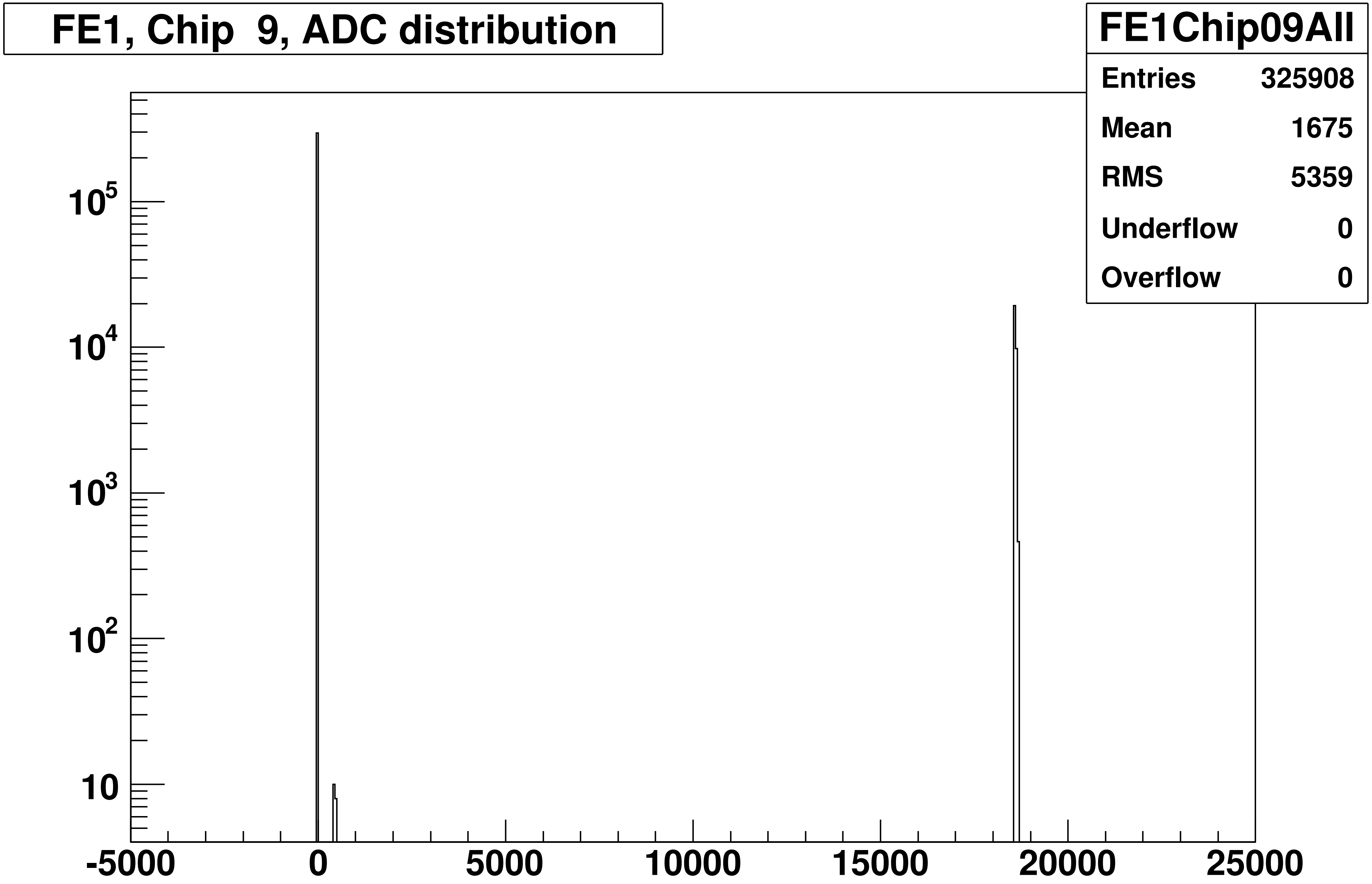


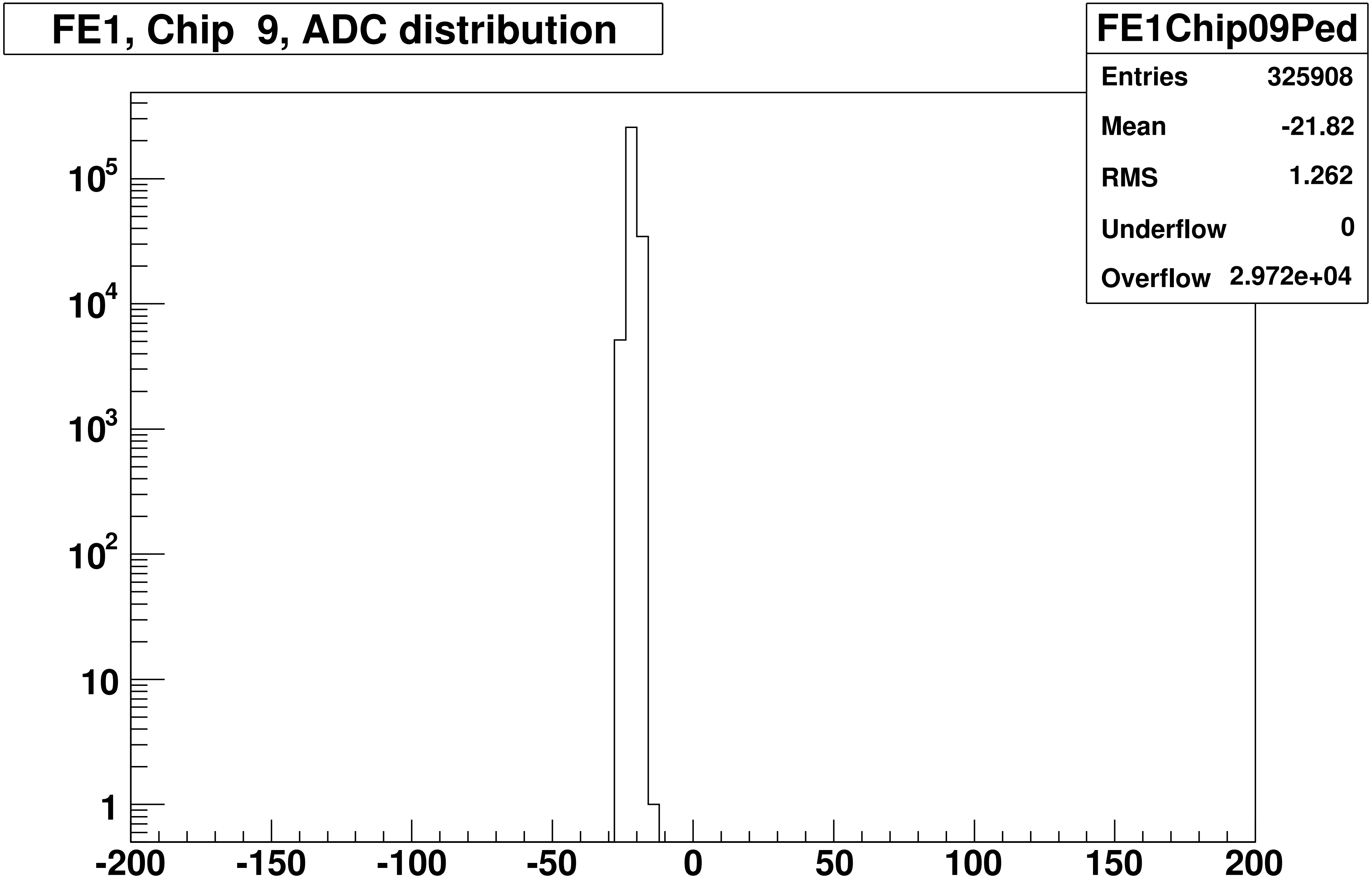
FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 325908
Mean 1684
RMS 5380
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

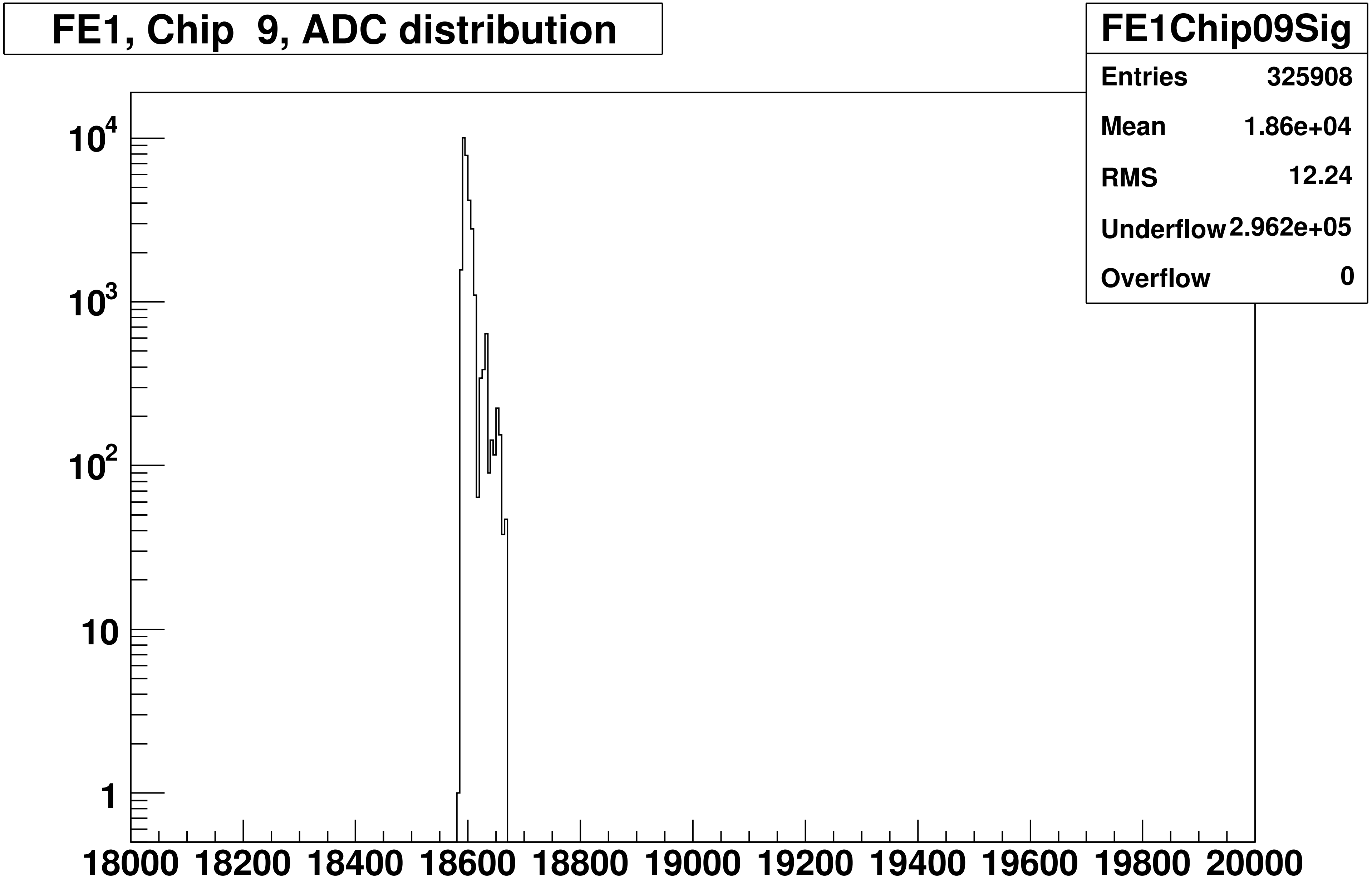

FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 325908
Mean -19.55
RMS 1.785
Underflow 0
Overflow 2.97e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



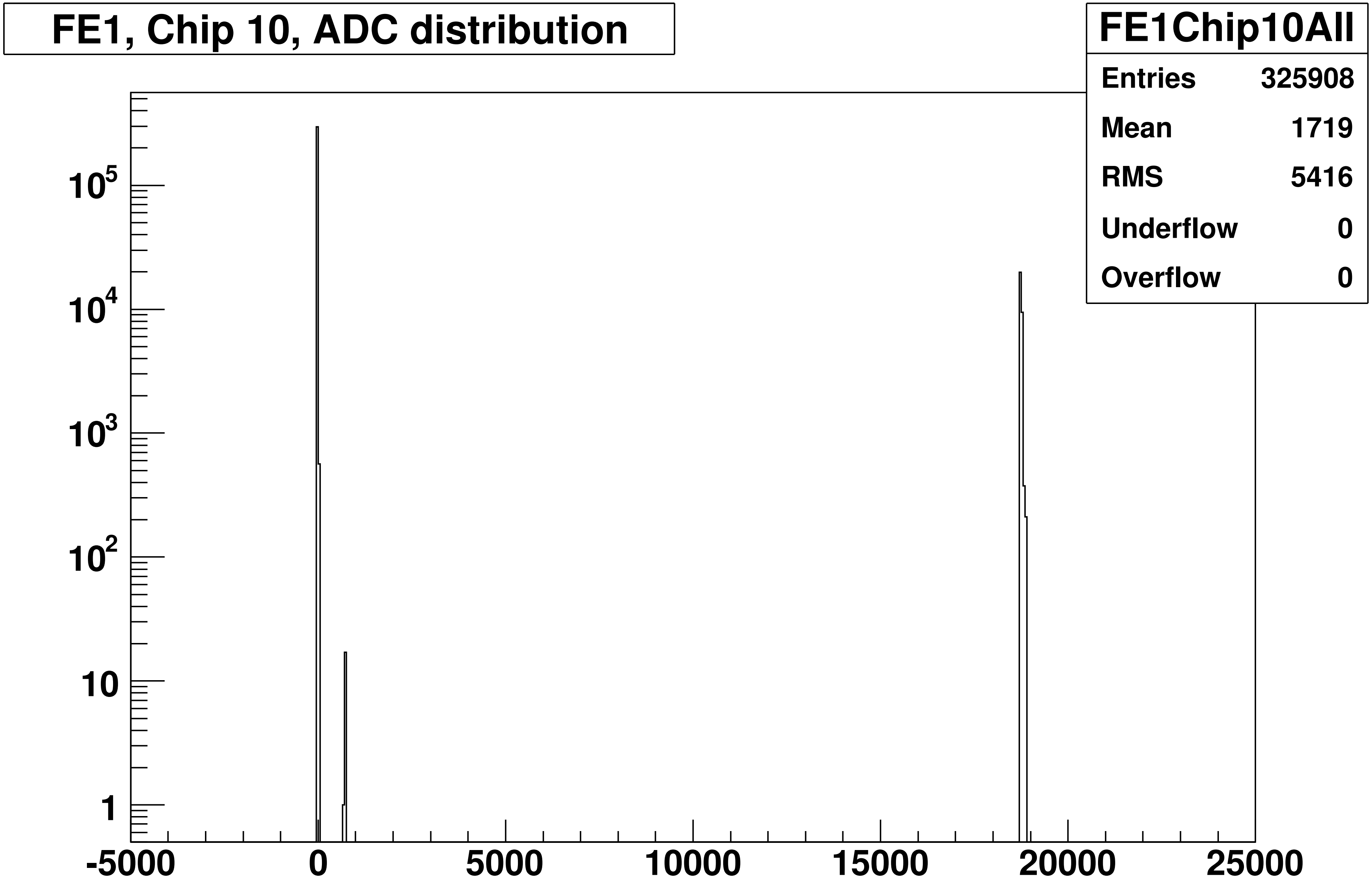
FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 325908
Mean 1.868e+04
RMS 14.2
Underflow 2.962e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 325908
Mean 1675
RMS 5359
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

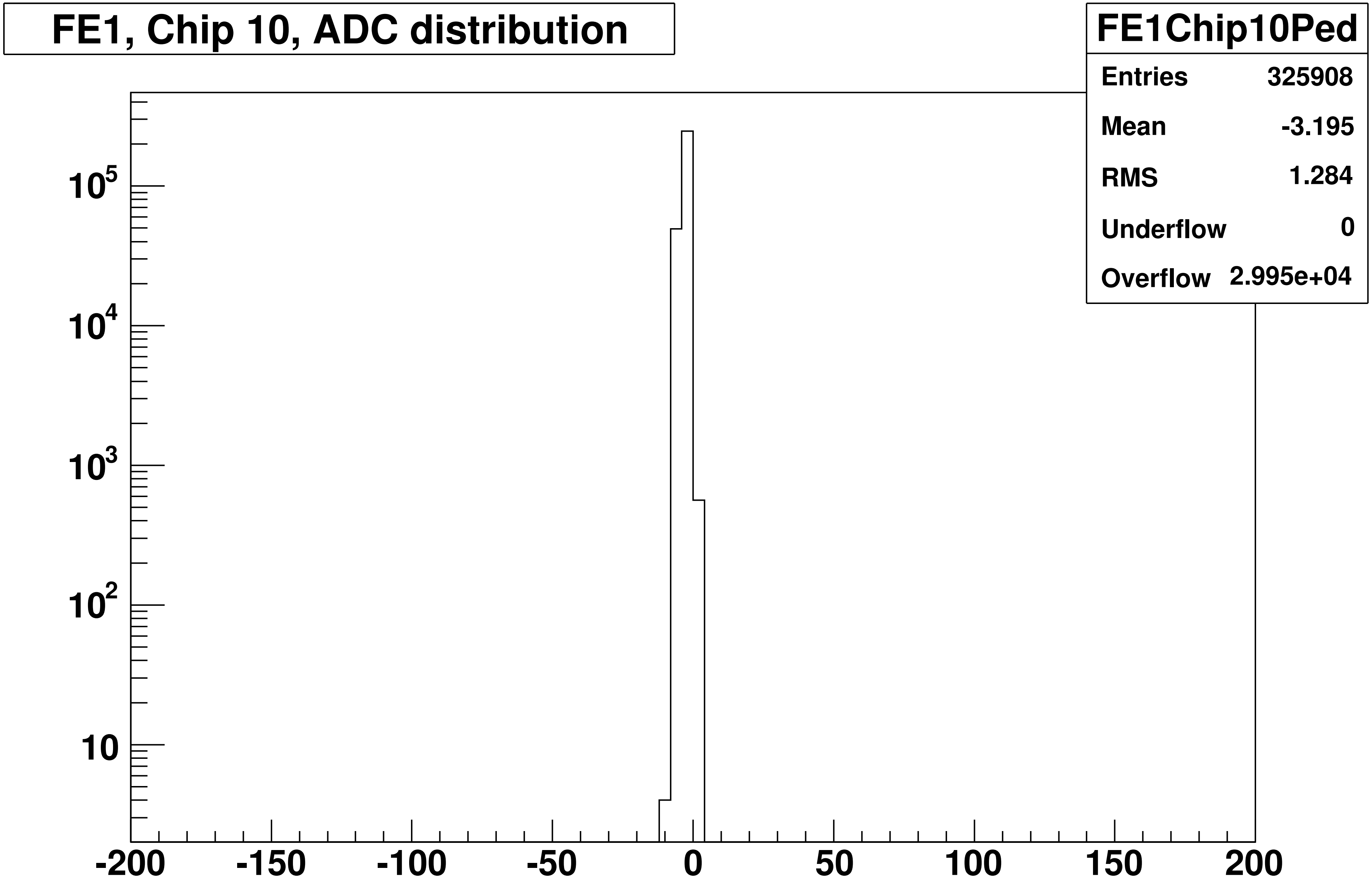

FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 325908
Mean -21.82
RMS 1.262
Underflow 0
Overflow 2.972e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

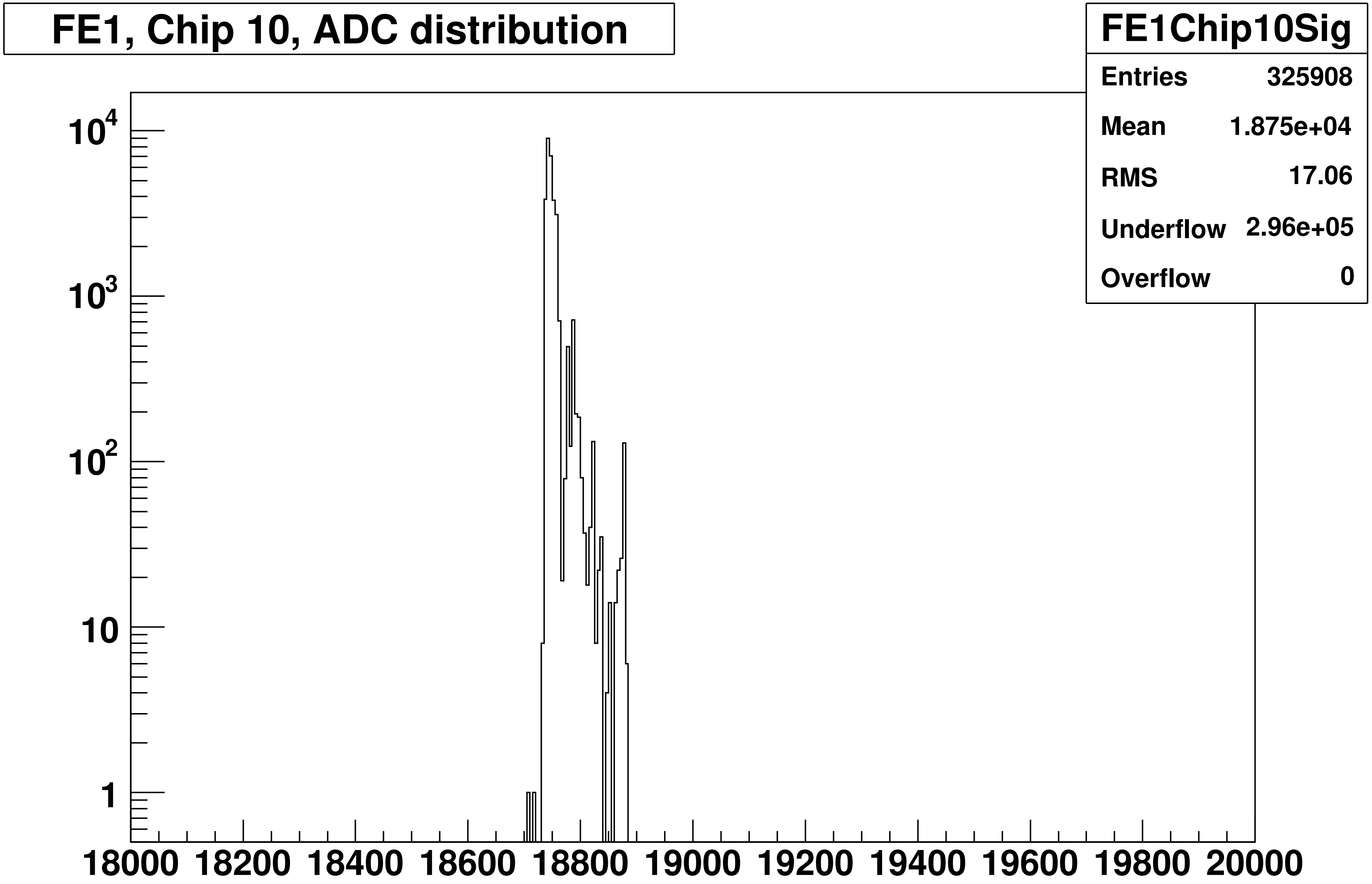


FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 325908
Mean 1.86e+04
RMS 12.24
Underflow 2.962e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

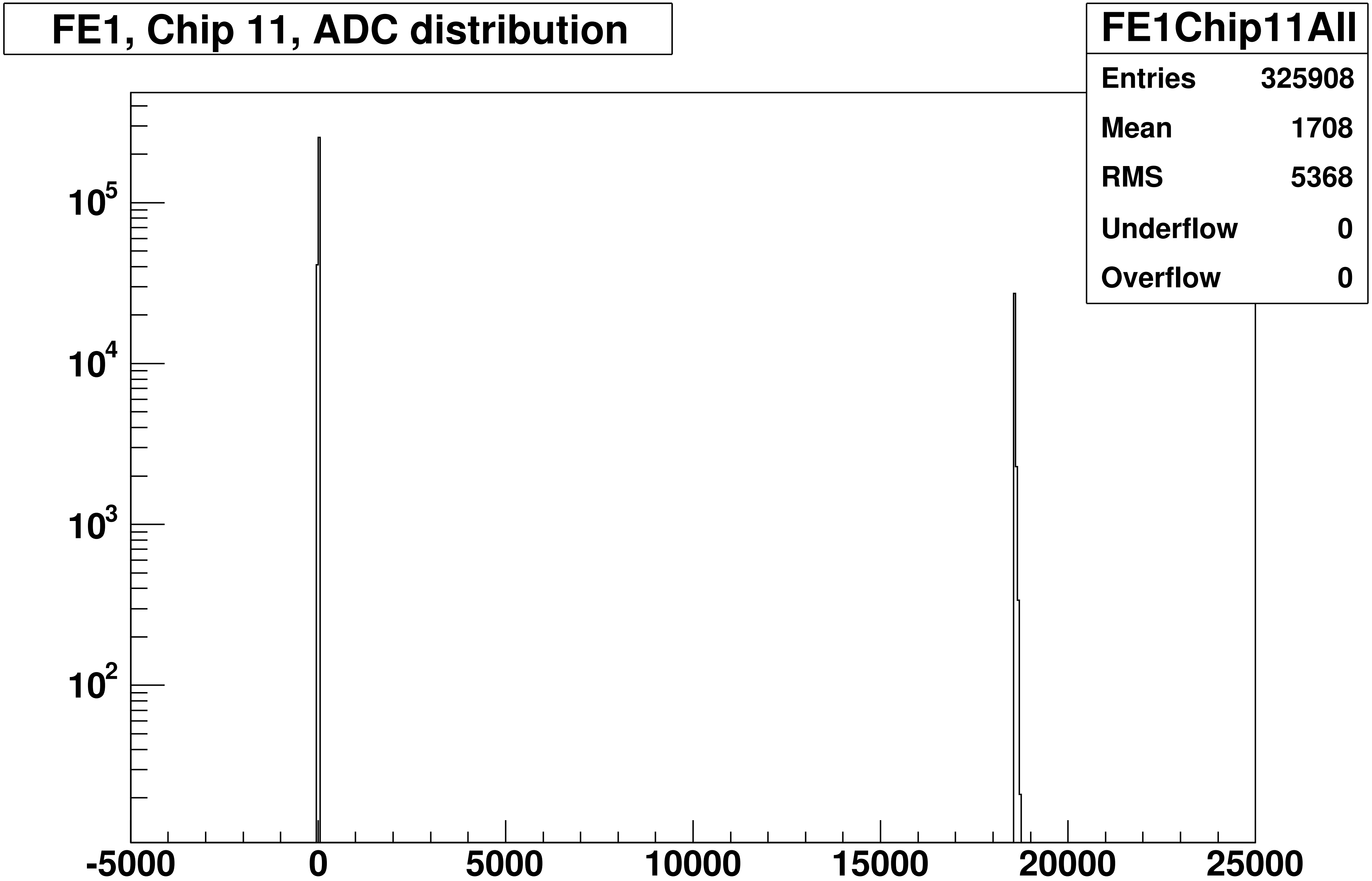


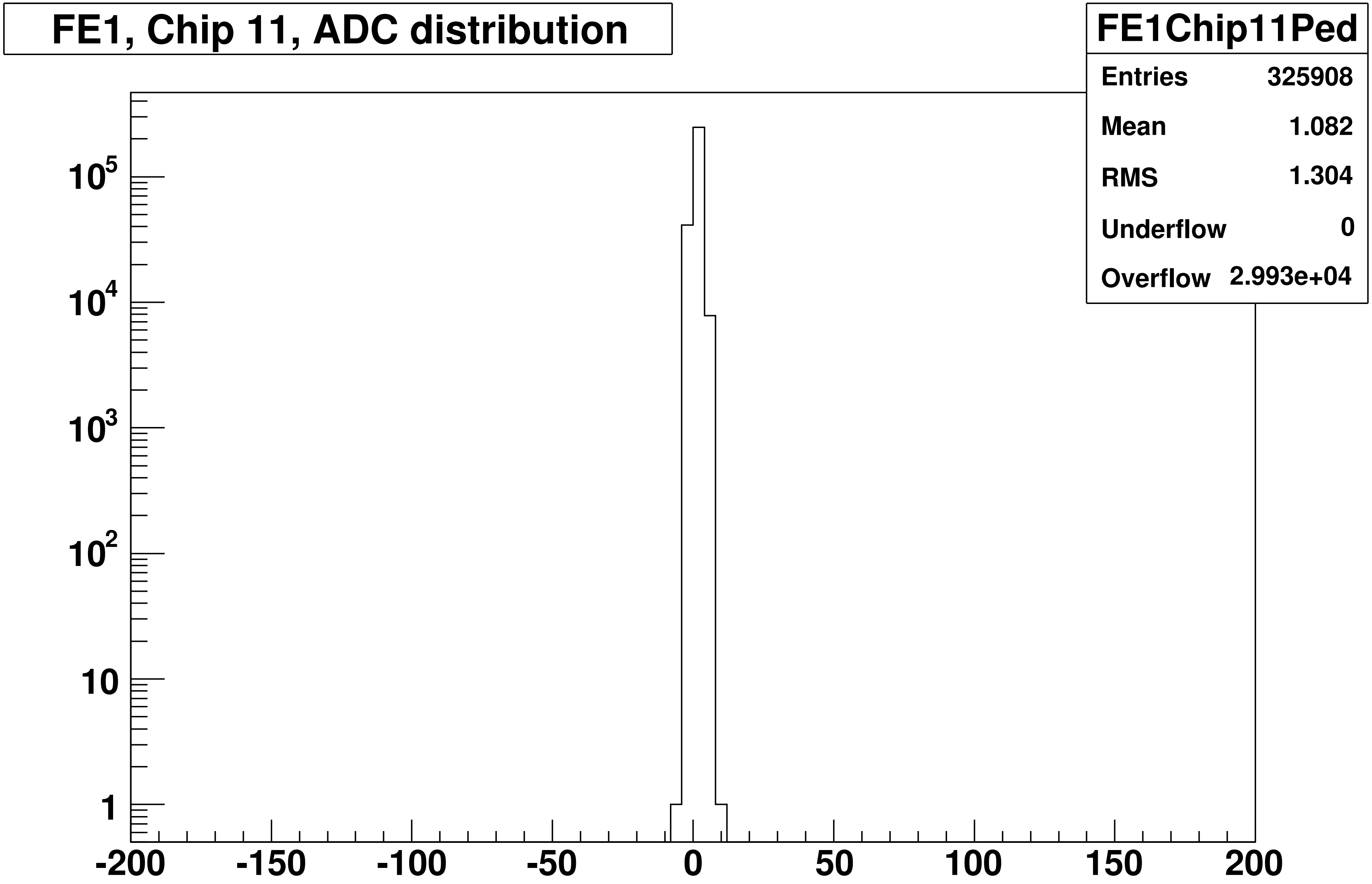
FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 325908
Mean 1719
RMS 5416
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 325908
Mean -3.195
RMS 1.284
Underflow 0
Overflow 2.995e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 325908
Mean 1.875e+04
RMS 17.06
Underflow 2.96e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 325908
Mean 1708
RMS 5368
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 325908
Mean 1.082
RMS 1.304
Underflow 0
Overflow 2.993e+04
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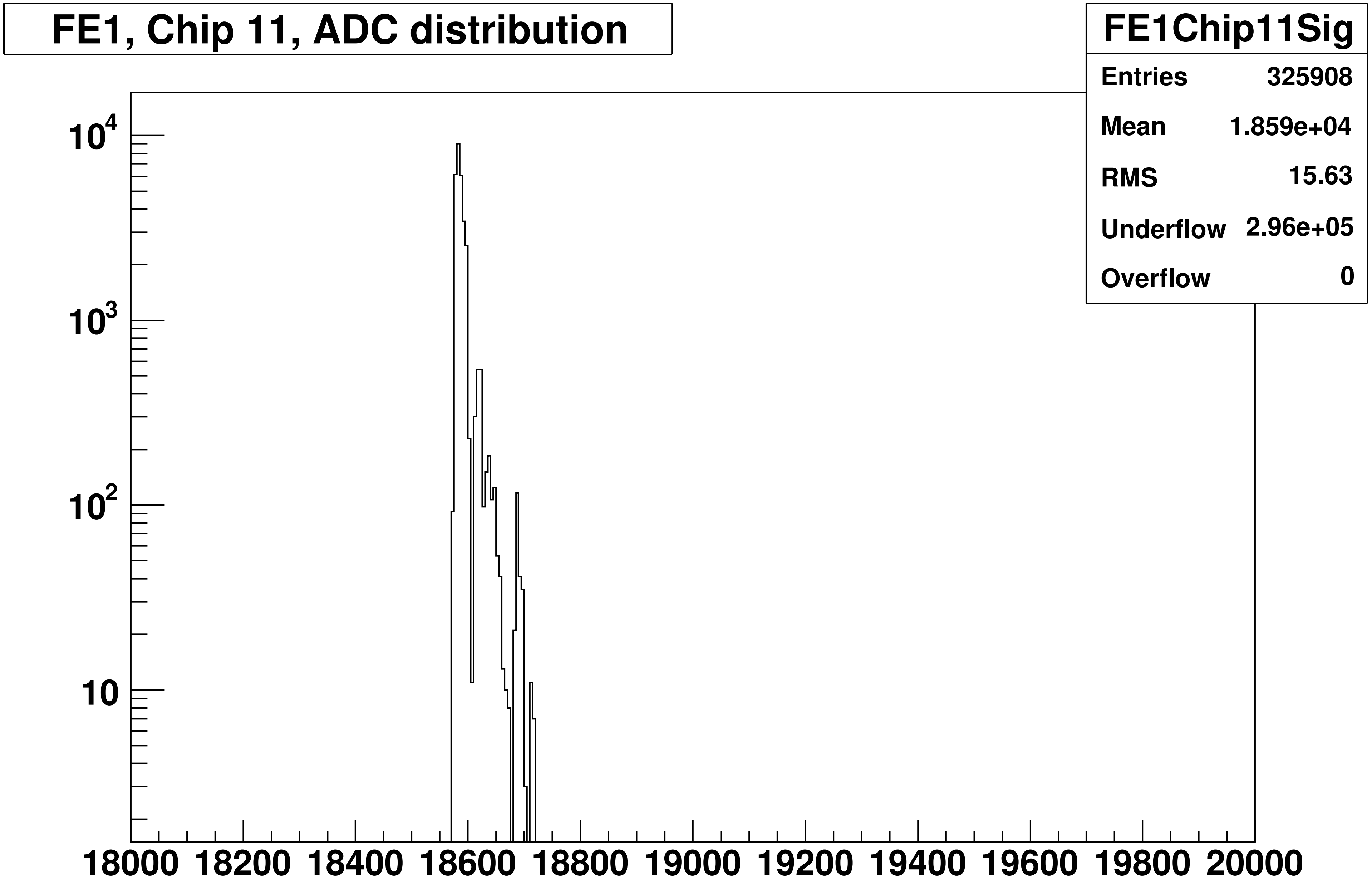
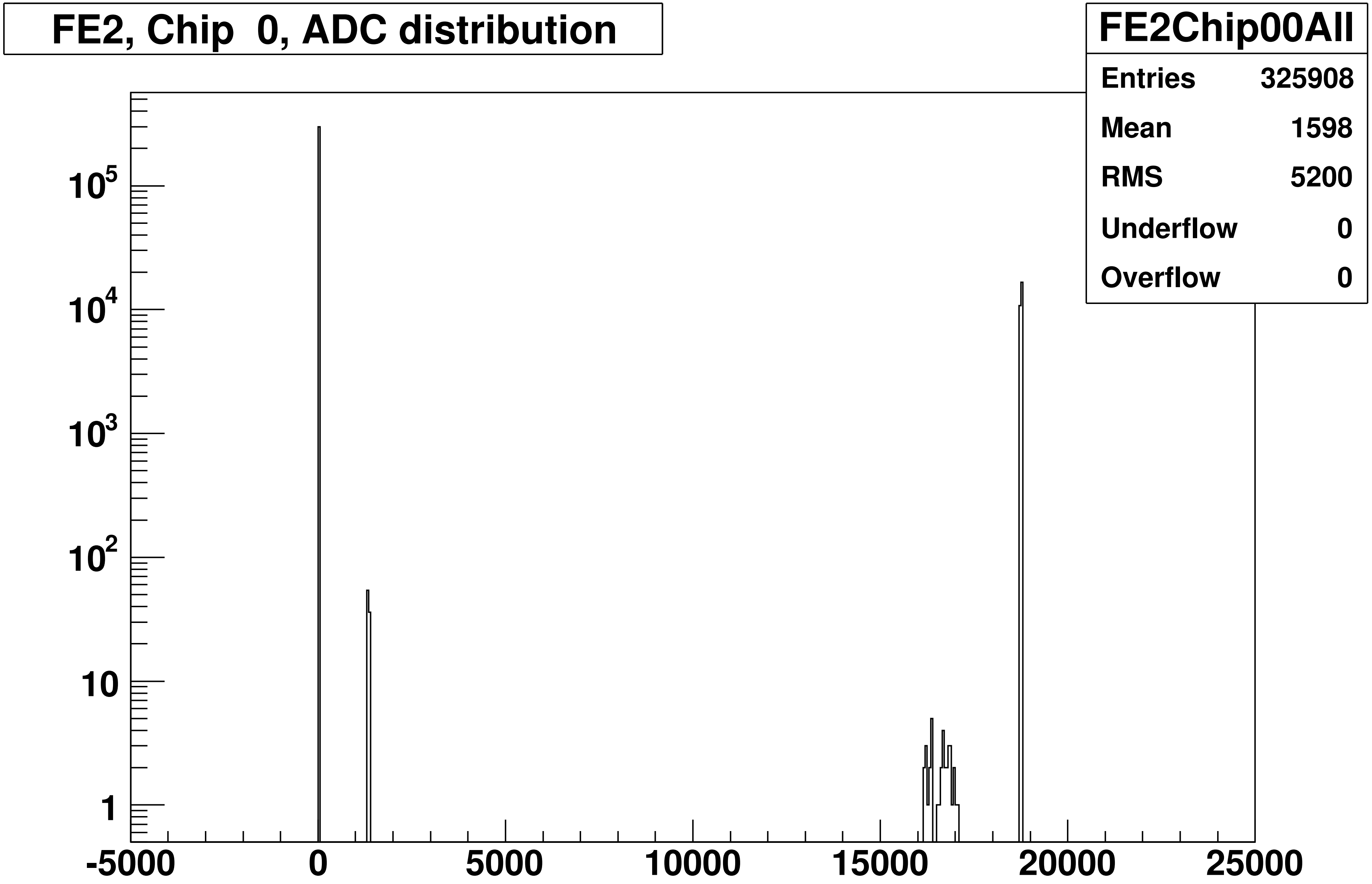
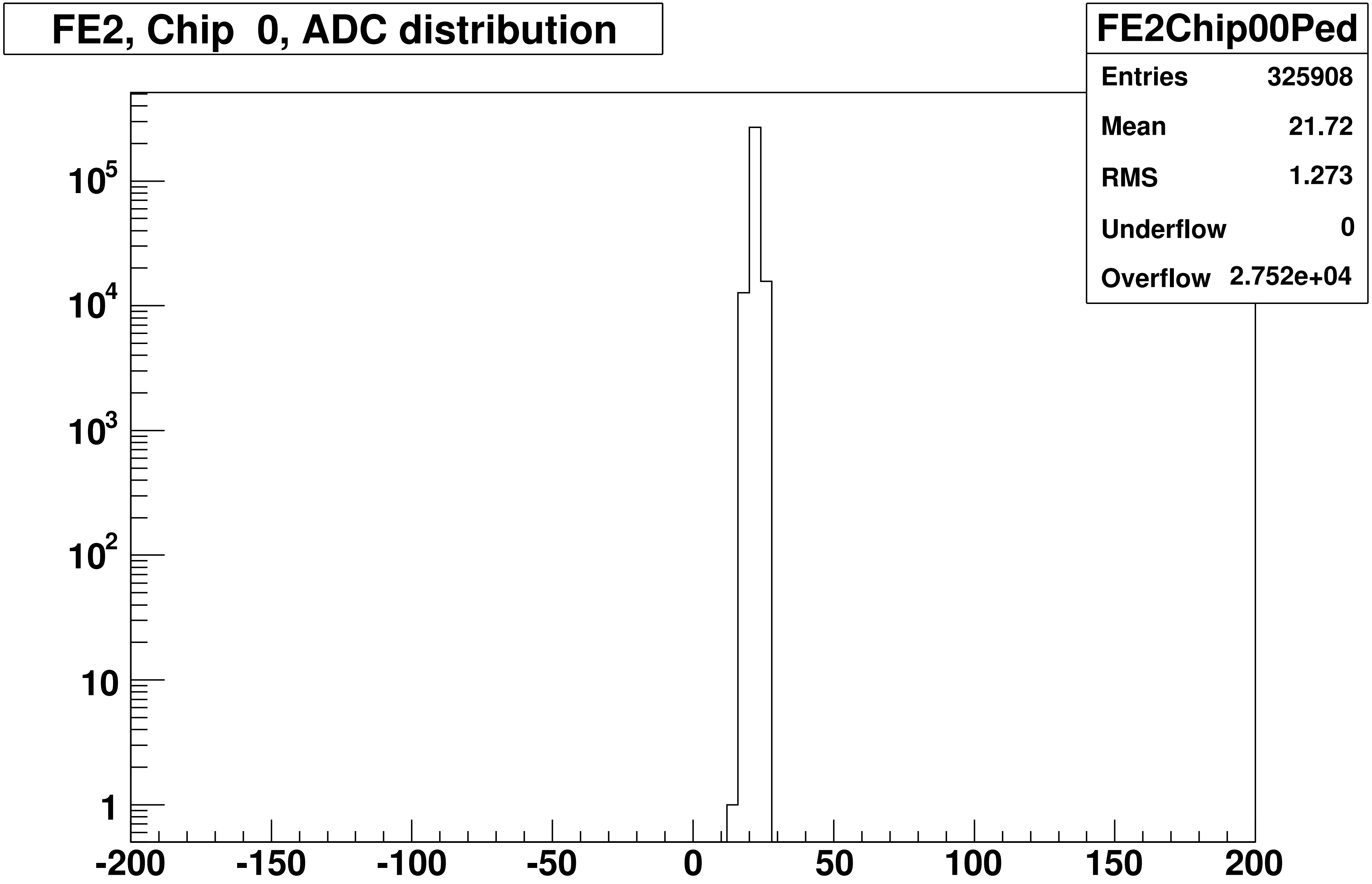
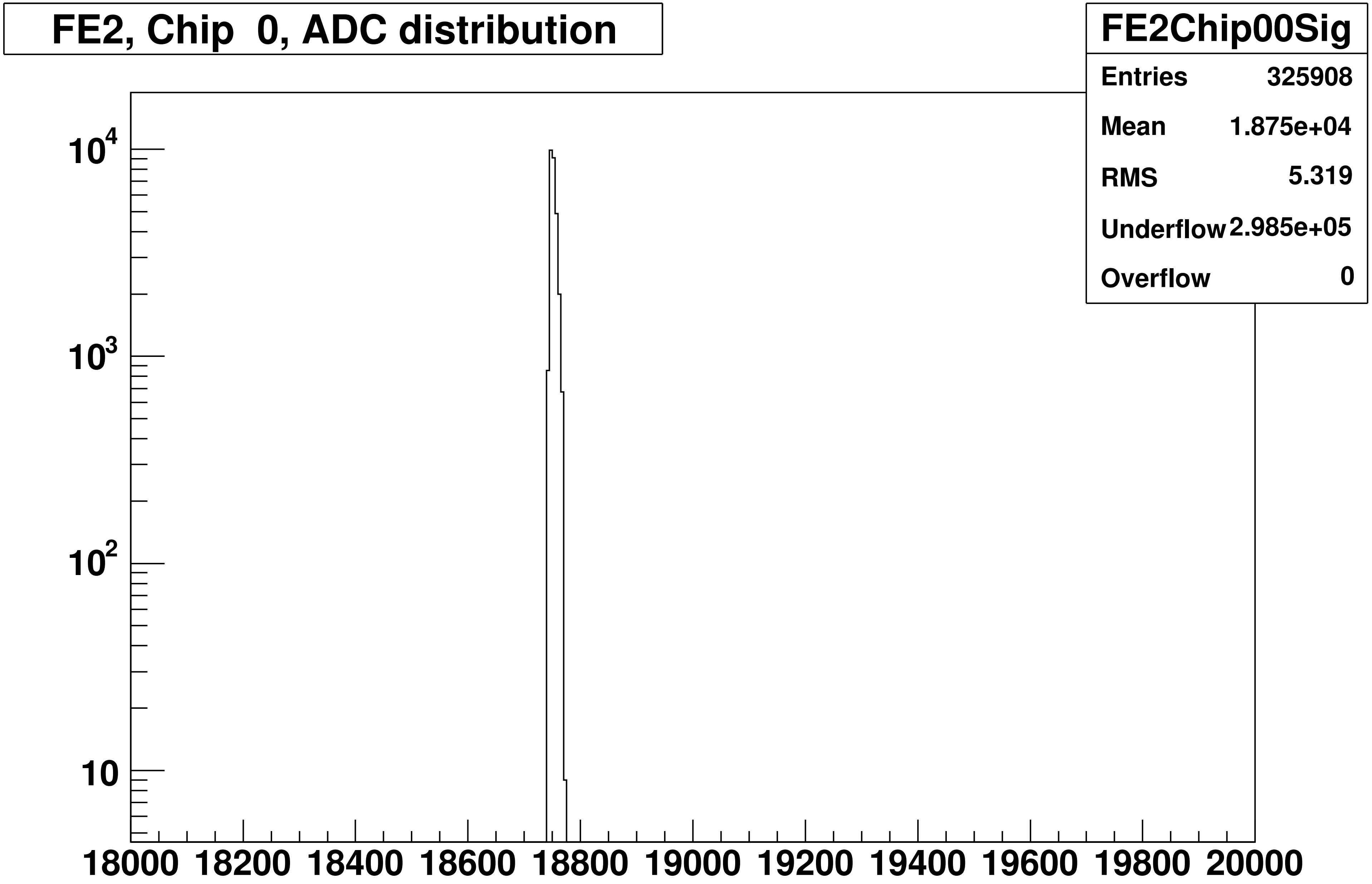
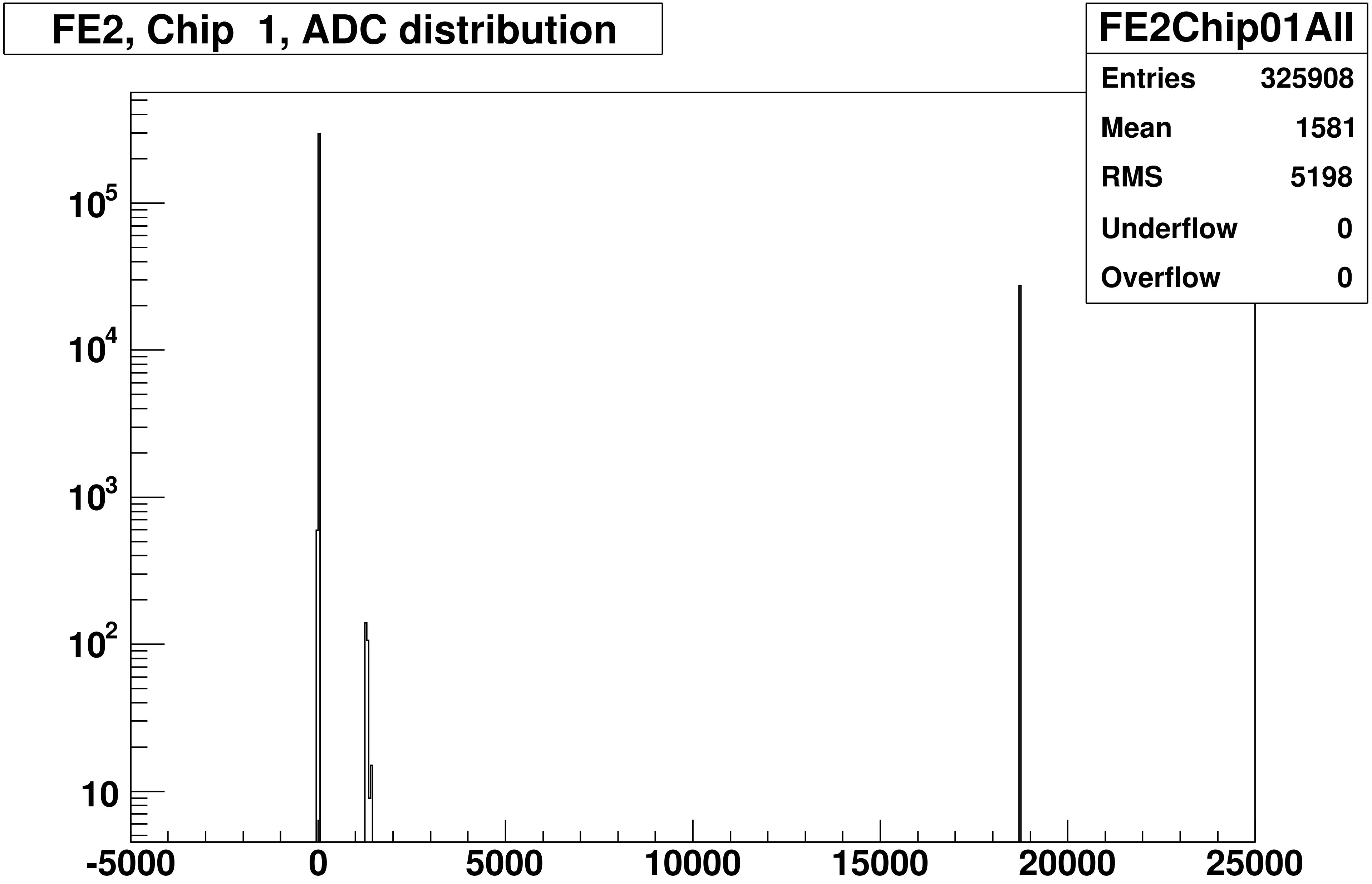
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

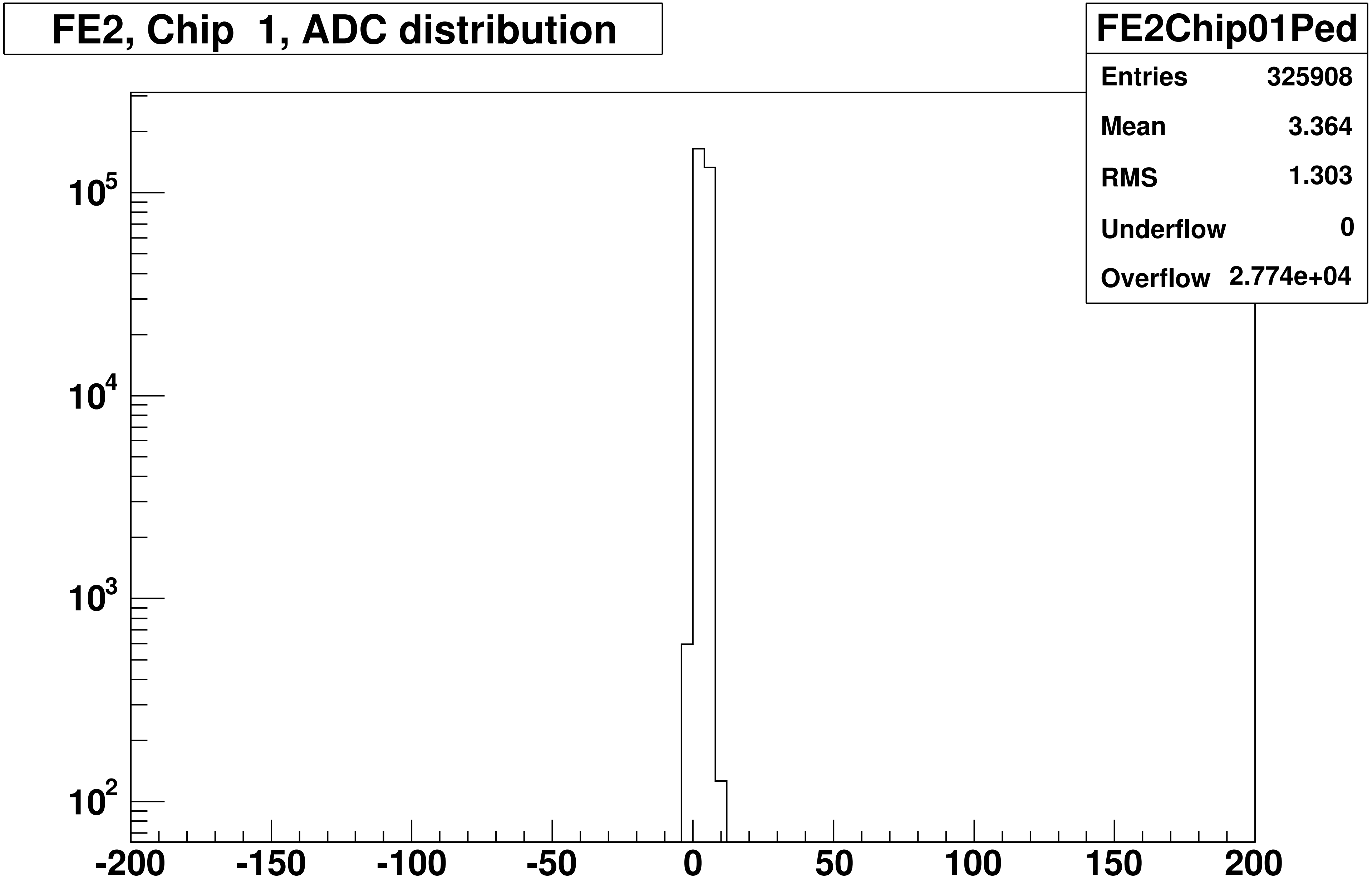

FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 325908
Mean 1.859e+04
RMS 15.63
Underflow 2.96e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

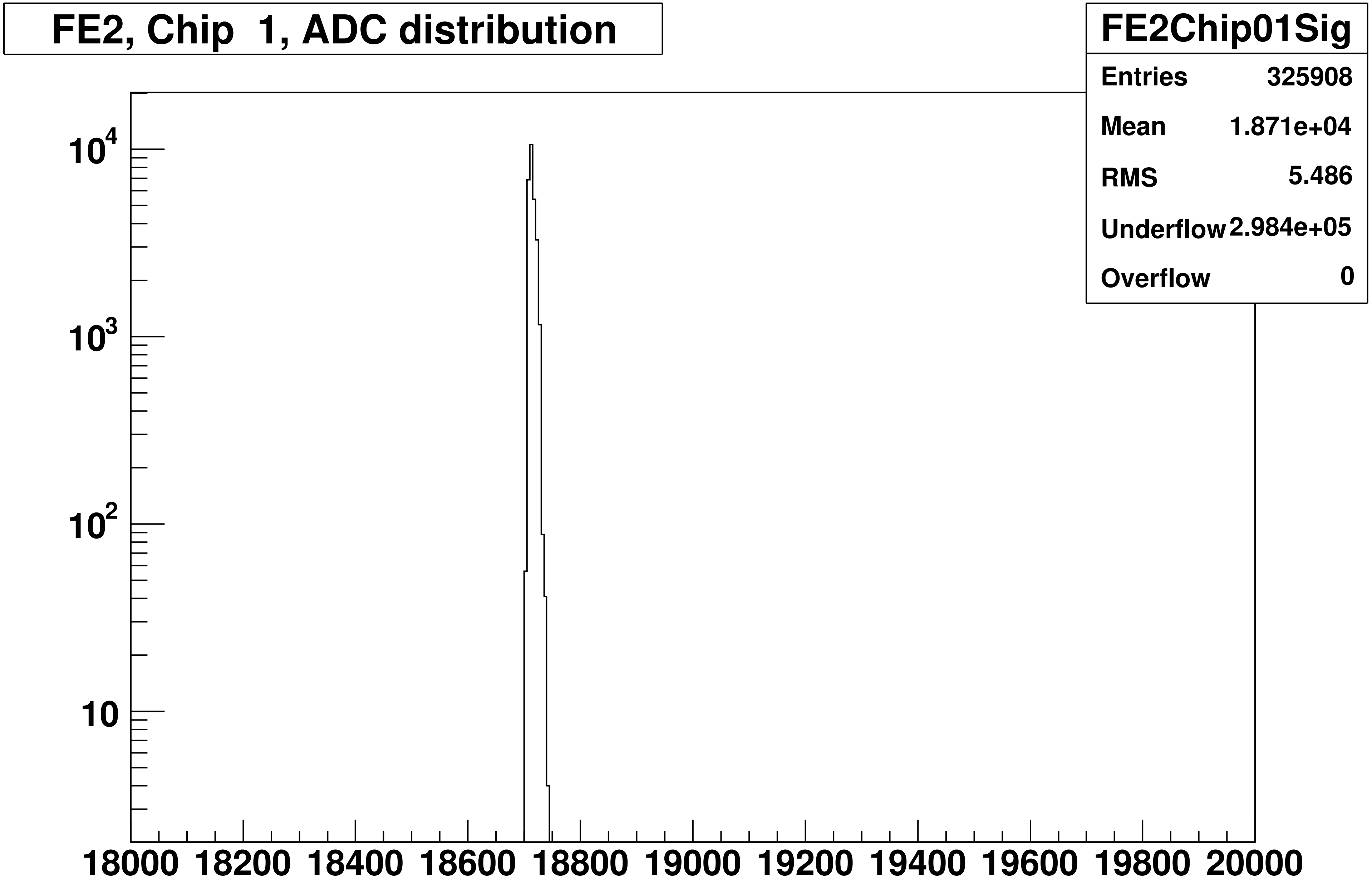

FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 325908
Mean 1598
RMS 5200
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

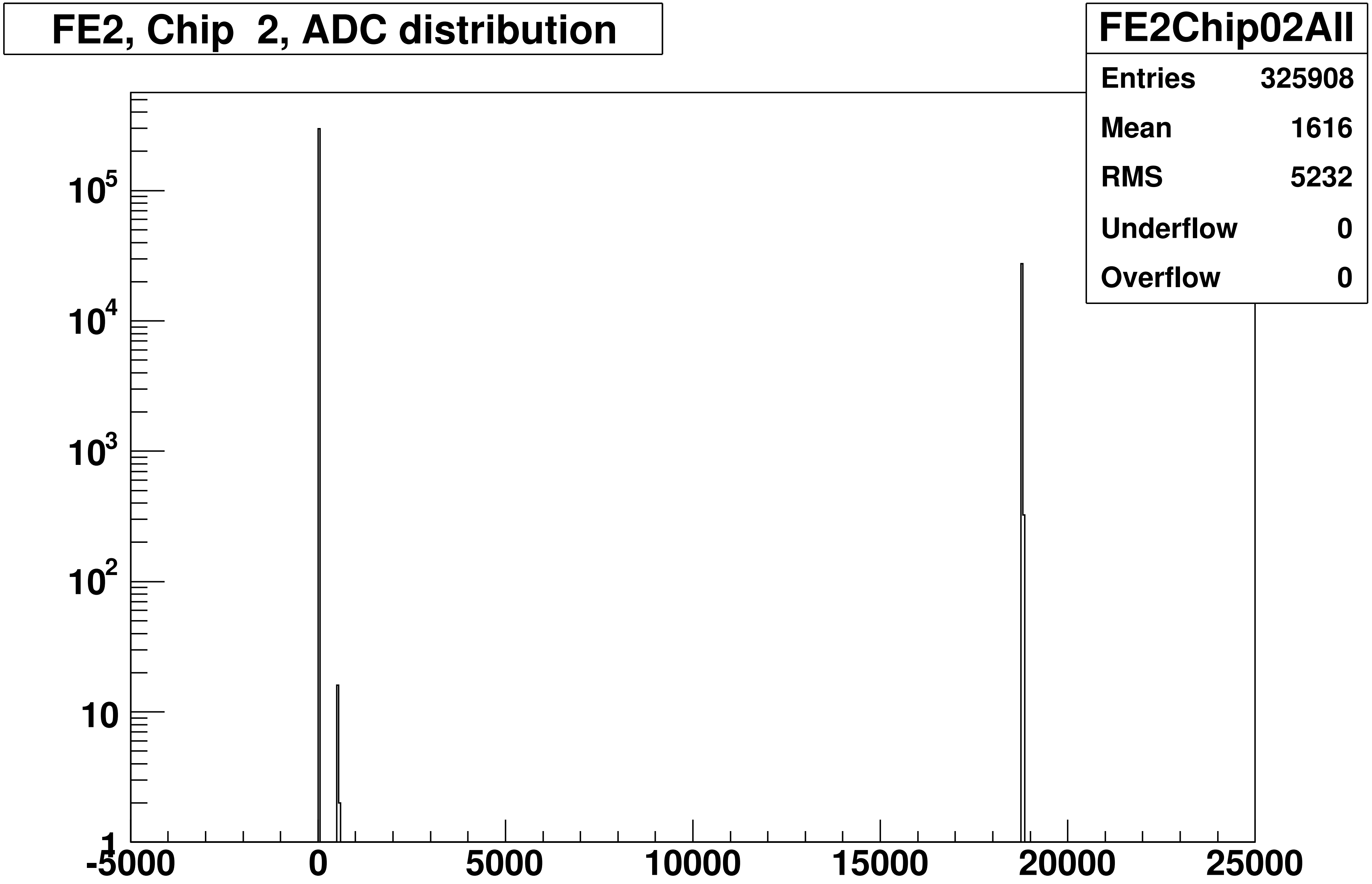

FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 325908
Mean 21.72
RMS 1.273
Underflow 0
Overflow 2.752e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

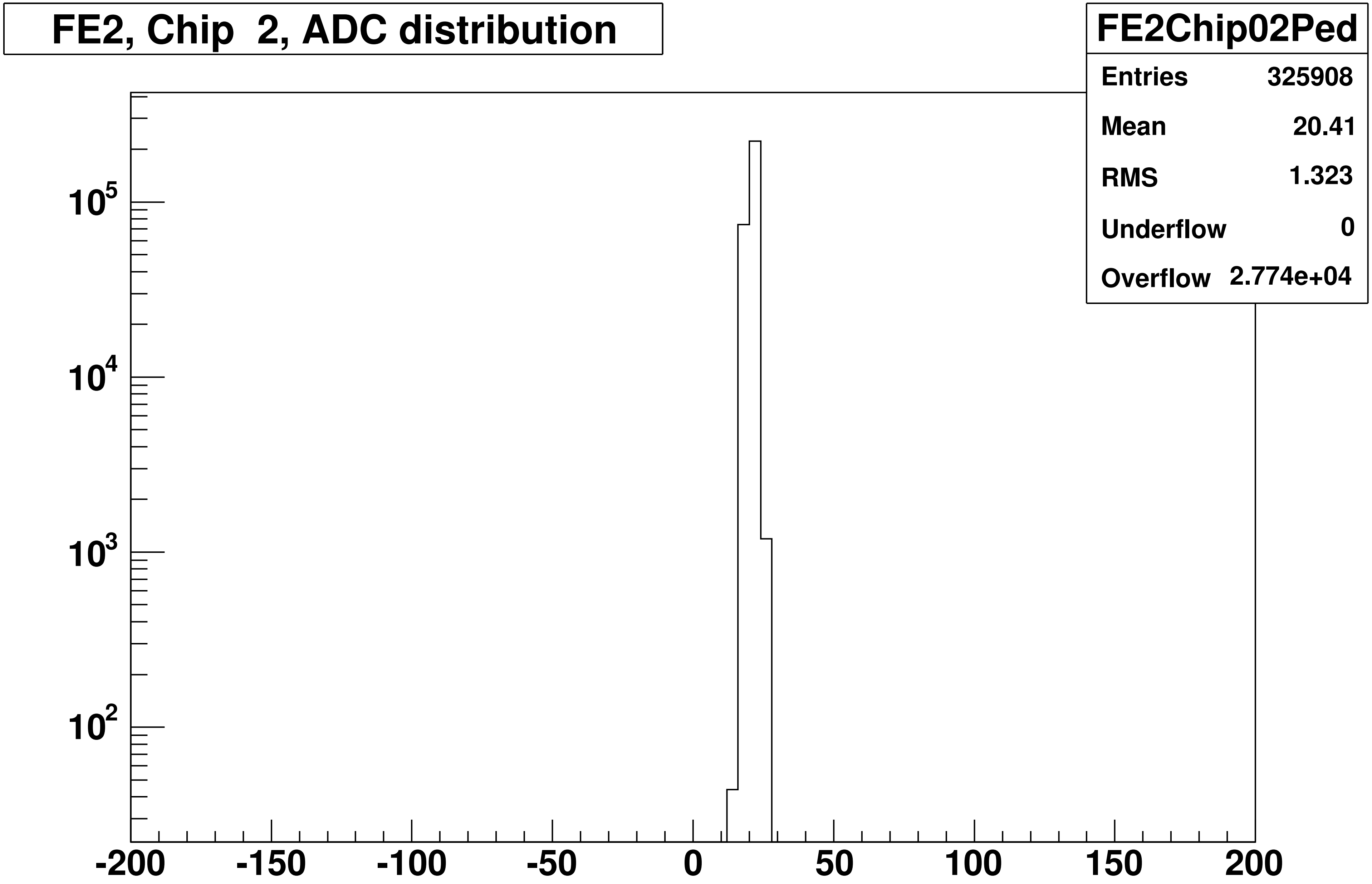

FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 325908
Mean 1.875e+04
RMS 5.319
Underflow 2.985e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 325908
Mean 1581
RMS 5198
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

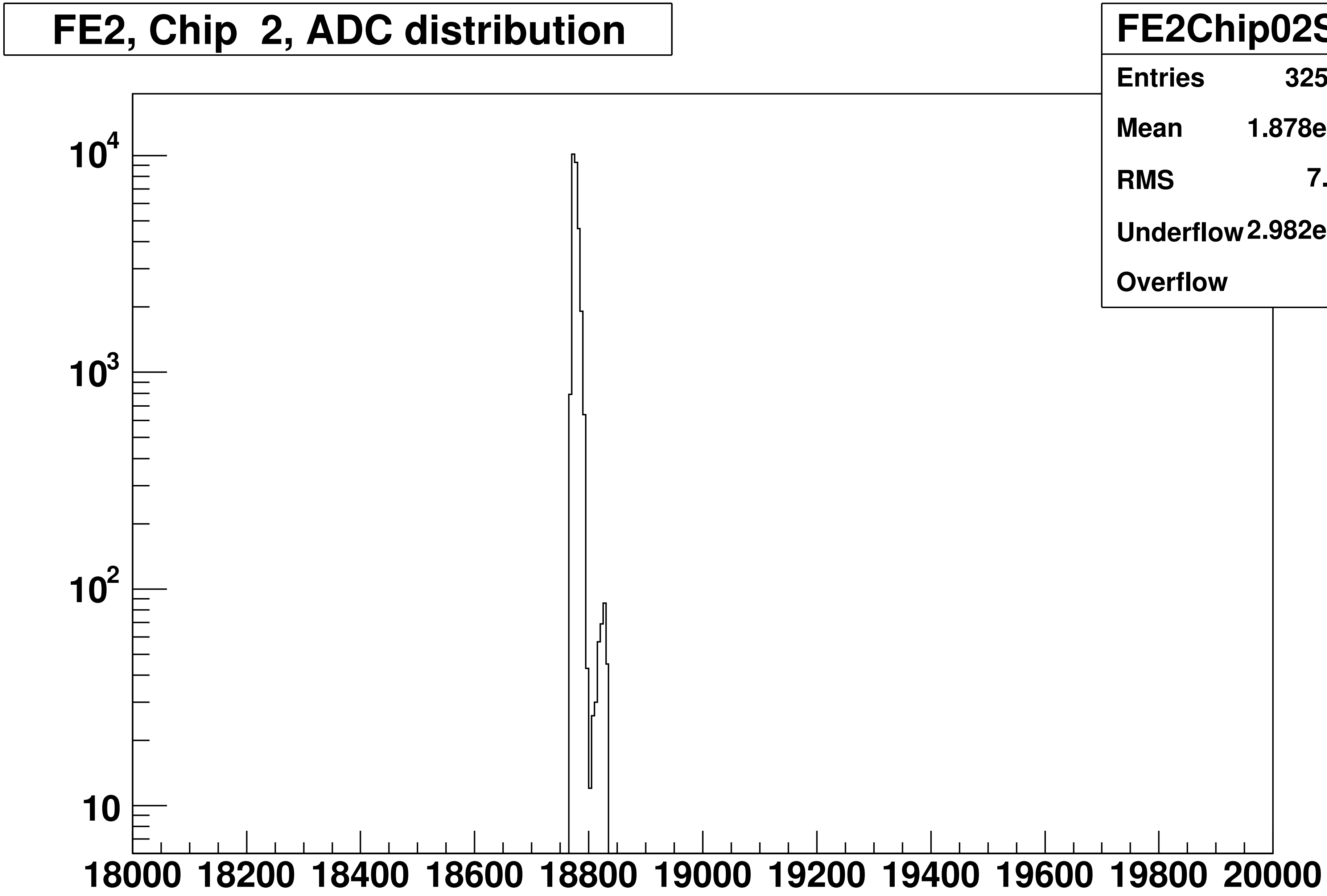

FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 325908
Mean 3.364
RMS 1.303
Underflow 0
Overflow 2.774e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 325908
Mean 1.871e+04
RMS 5.486
Underflow 2.984e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

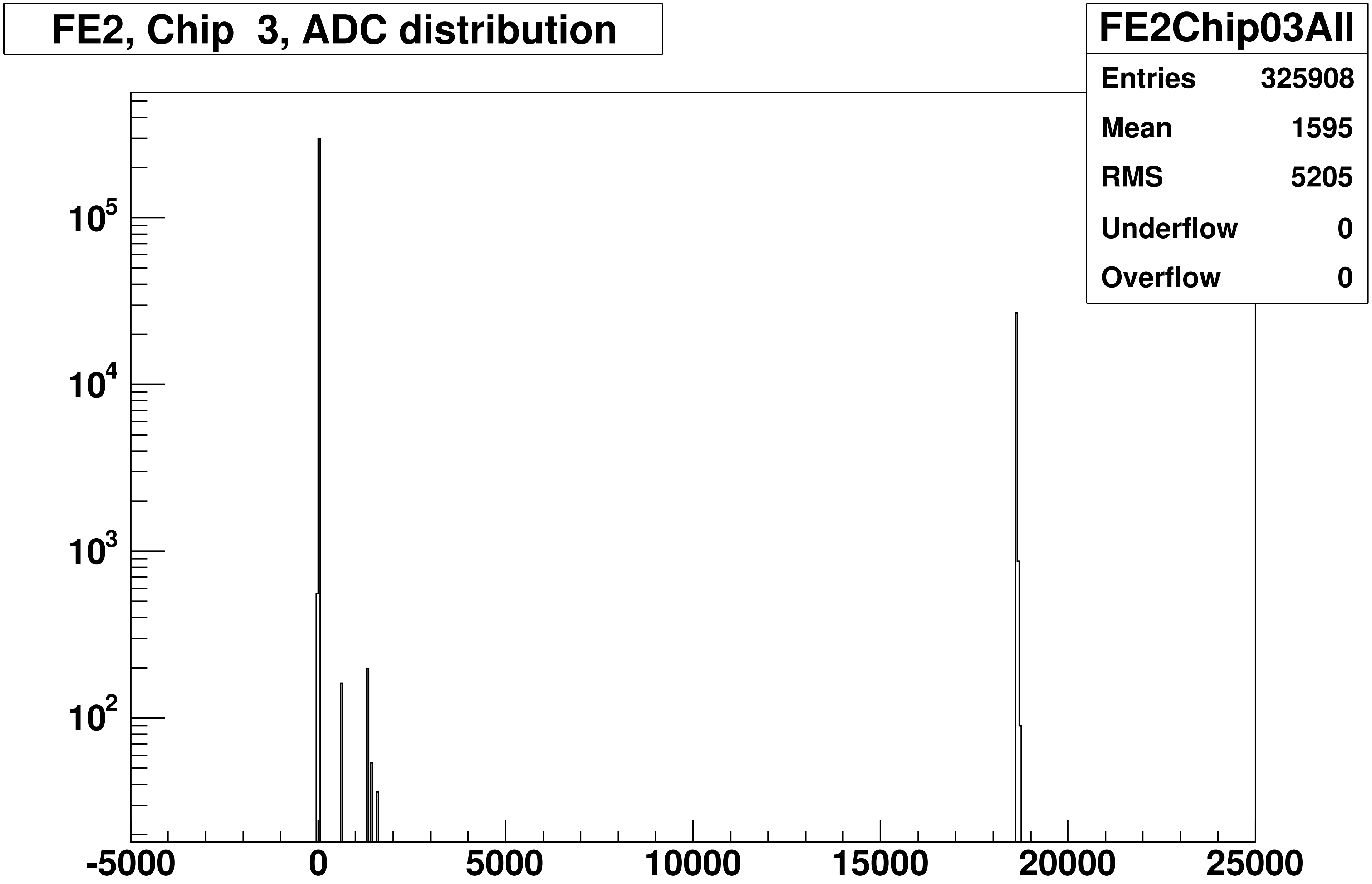

FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 325908
Mean 1616
RMS 5232
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 325908
Mean 20.41
RMS 1.323
Underflow 0
Overflow 2.774e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 325908
Mean 1.878e+04
RMS 7.173
Underflow 2.982e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 325908
Mean 1595
RMS 5205
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
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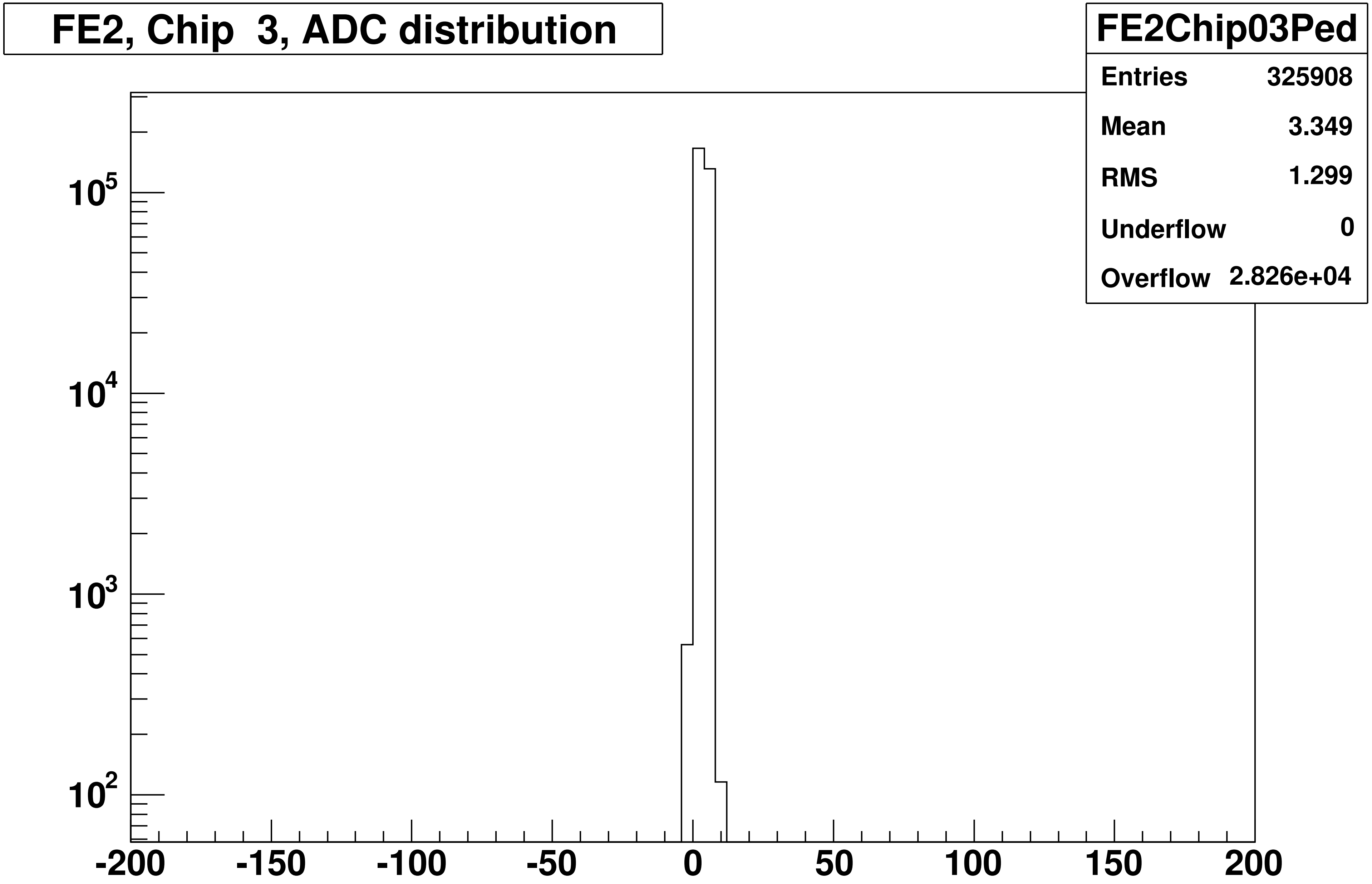
10000
15000
20000
25000


FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 325908
Mean 3.349
RMS 1.299
Underflow 0
Overflow 2.826e+04
10^5
10^4
10^3
10^2
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-150
-100
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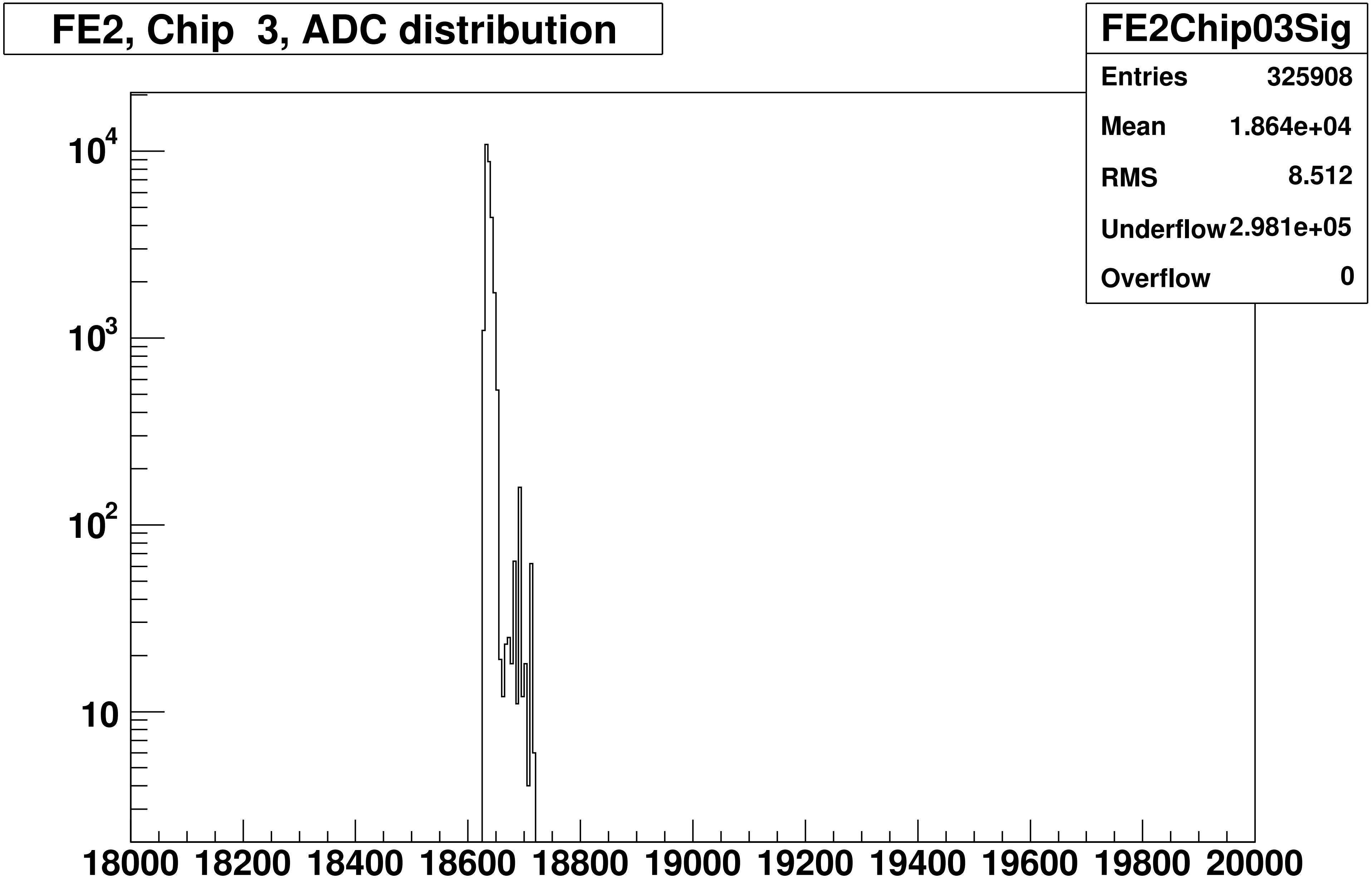
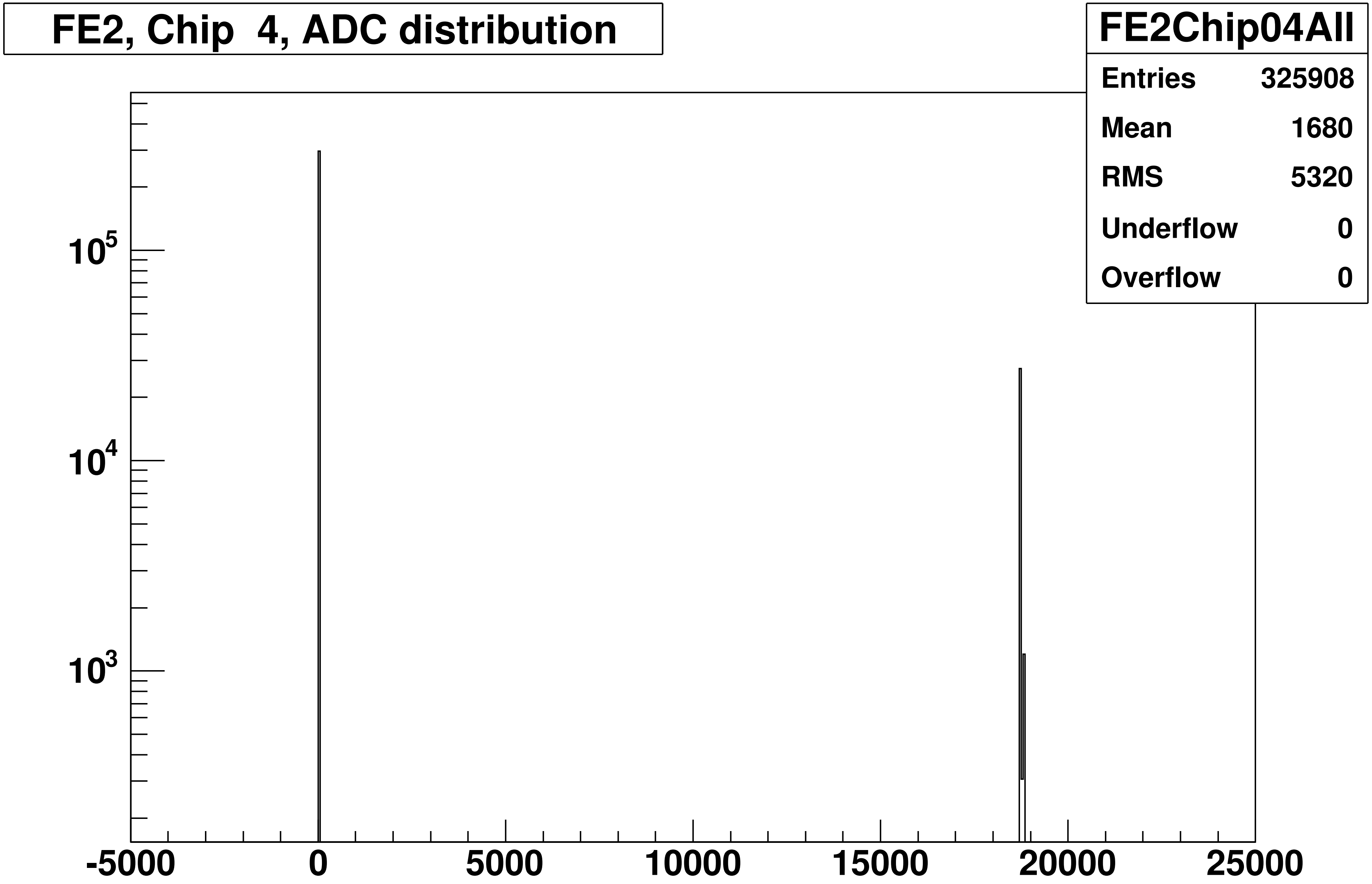
0
50
100
150
200


FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 325908
Mean 1.864e+04
RMS 8.512
Underflow 2.981e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 325908
Mean 1680
RMS 5320
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
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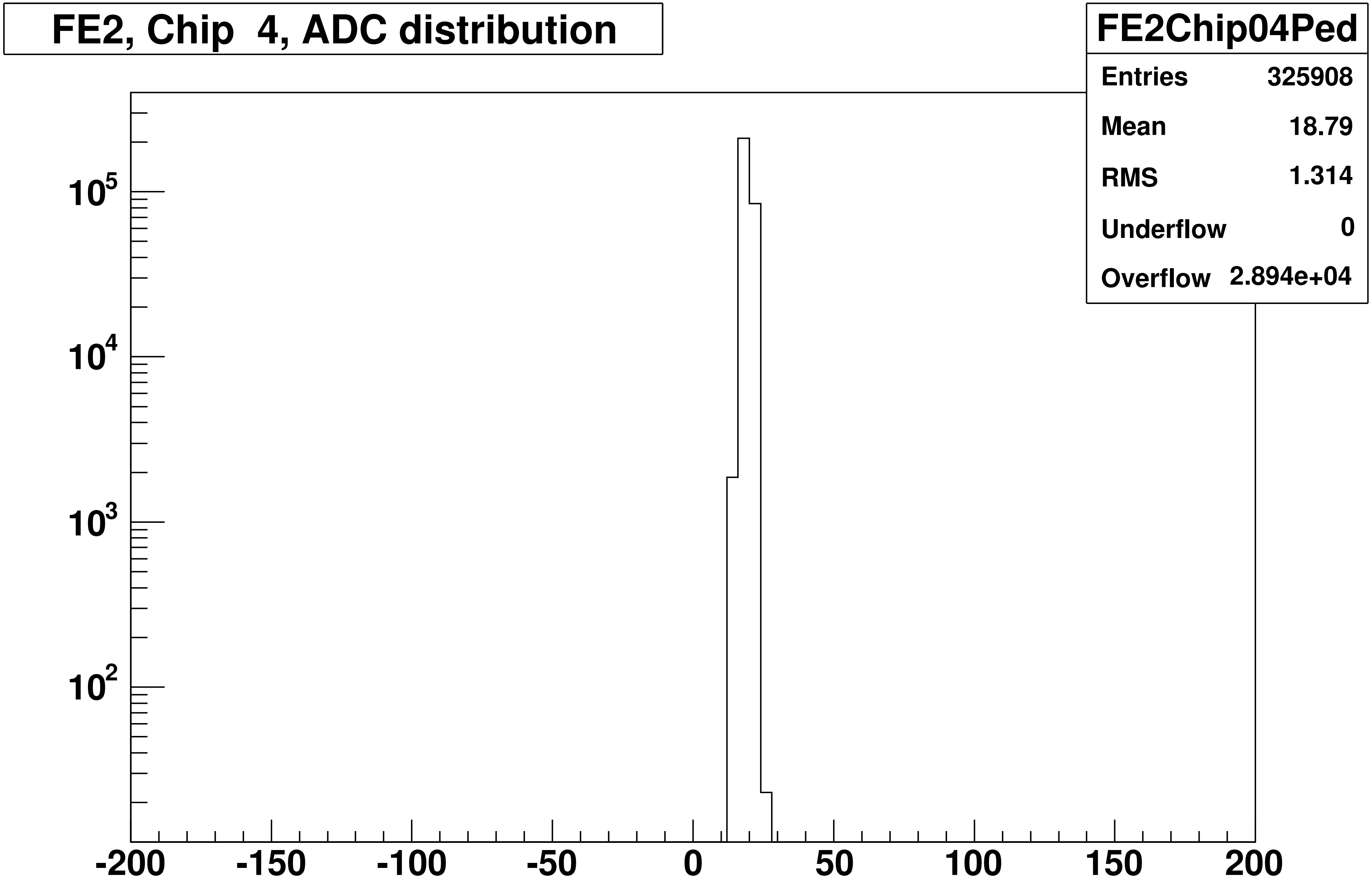
10000
15000
20000
25000



FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 325908
Mean 18.79
RMS 1.314
Underflow 0
Overflow 2.894e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
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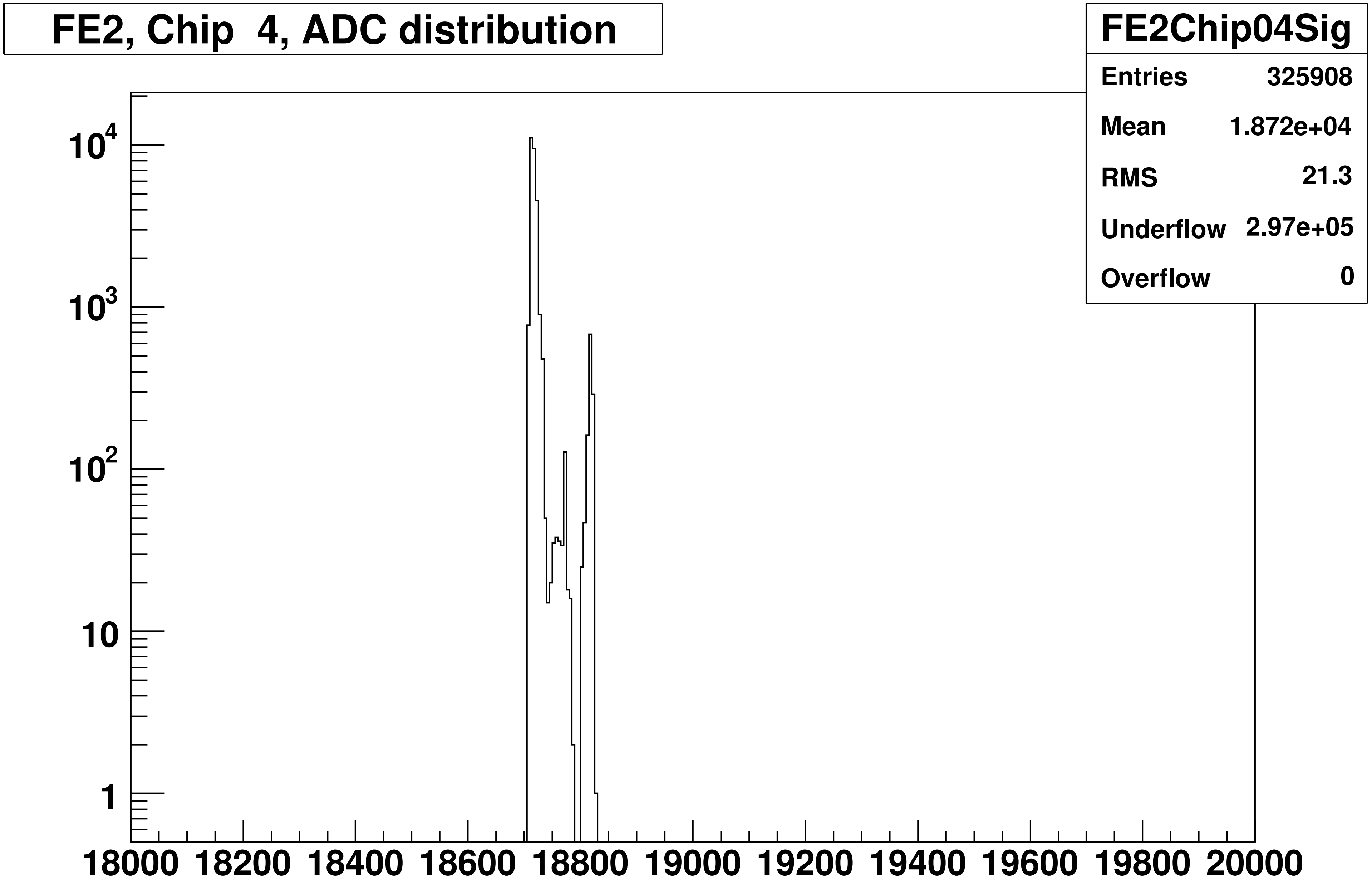
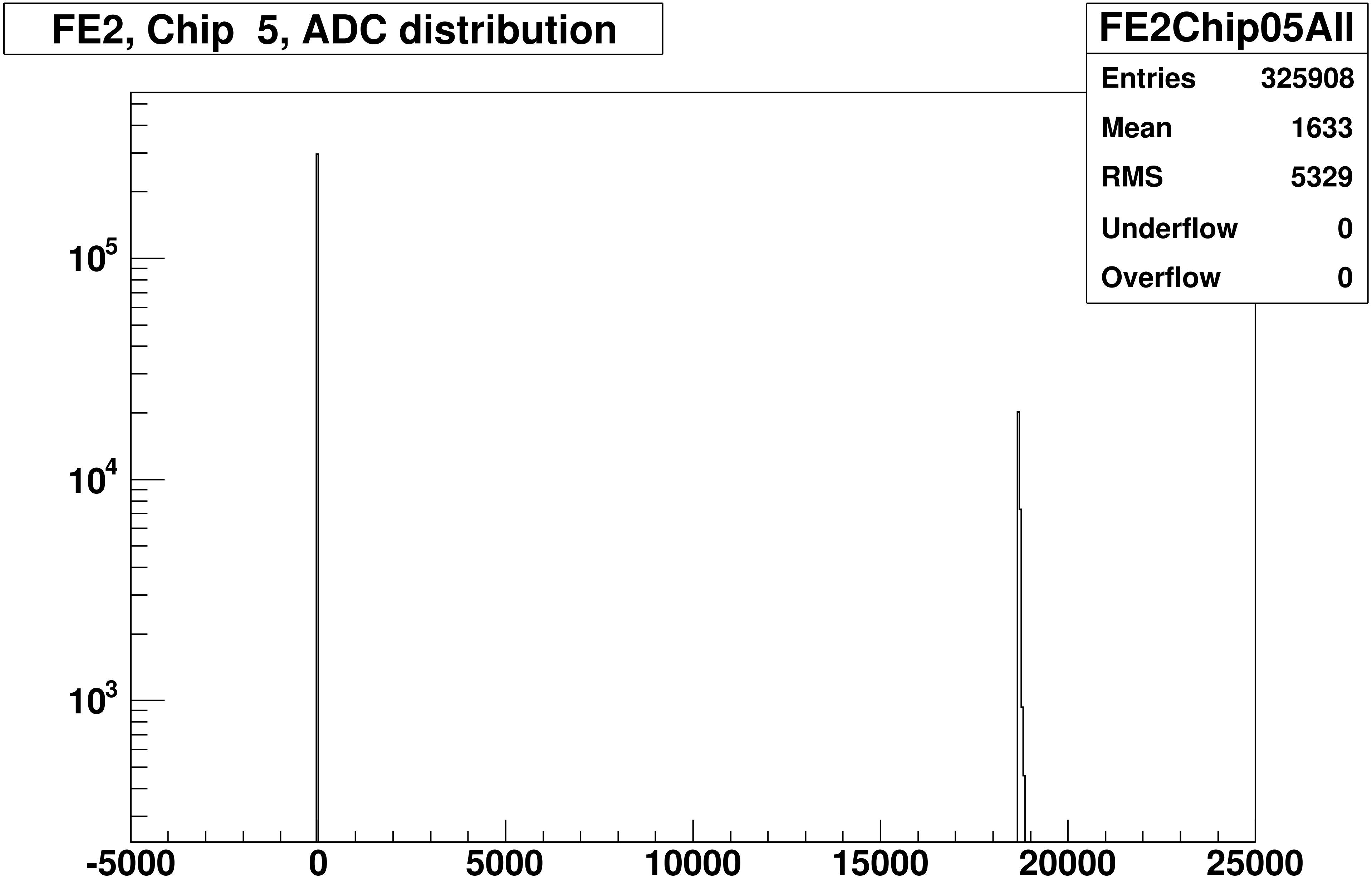
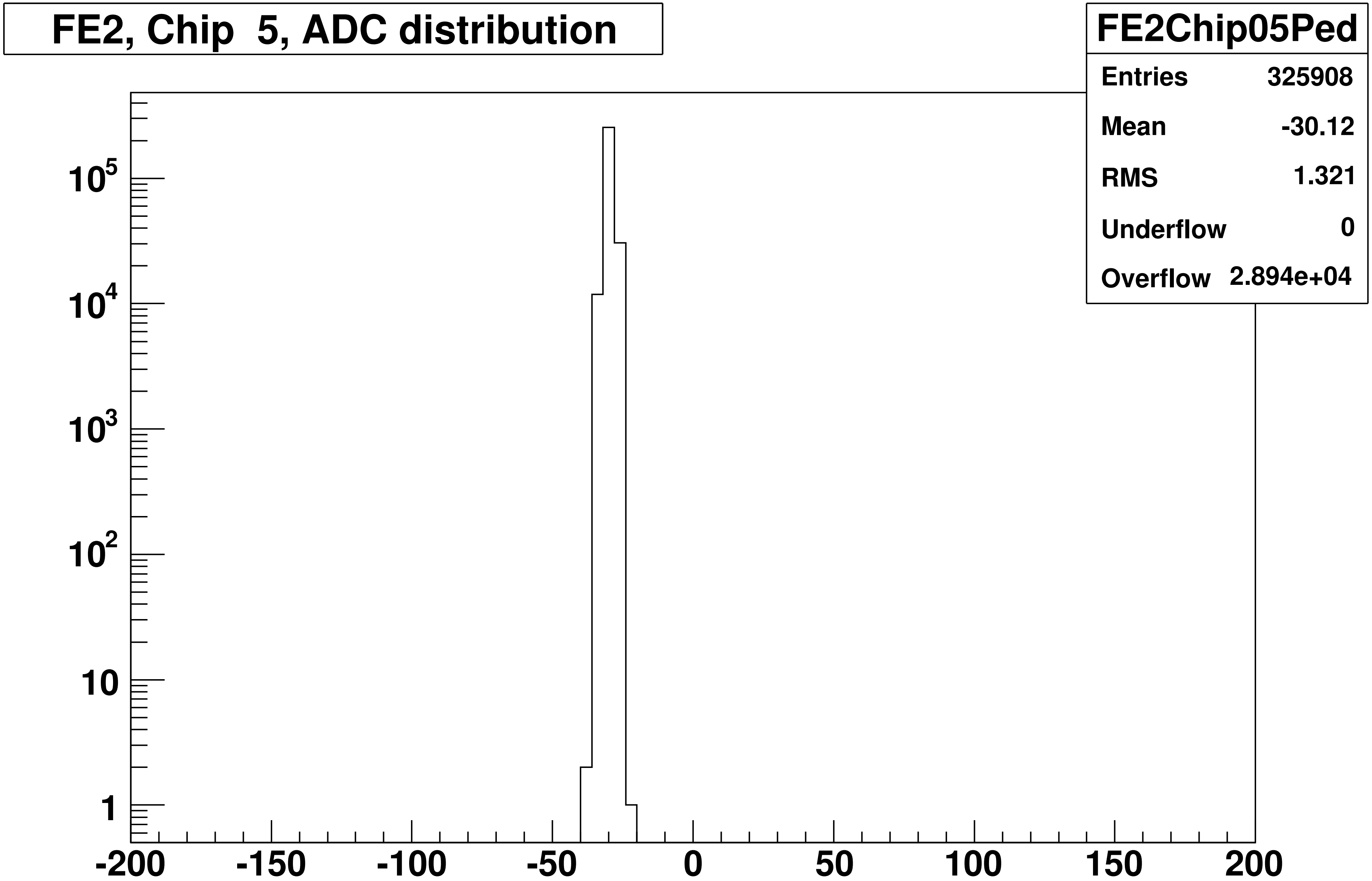
100
150
200


FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 325908
Mean 1.872e+04
RMS 21.3
Underflow 2.97e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 325908
Mean 1633
RMS 5329
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 325908
Mean -30.12
RMS 1.321
Underflow 0
Overflow 2.894e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
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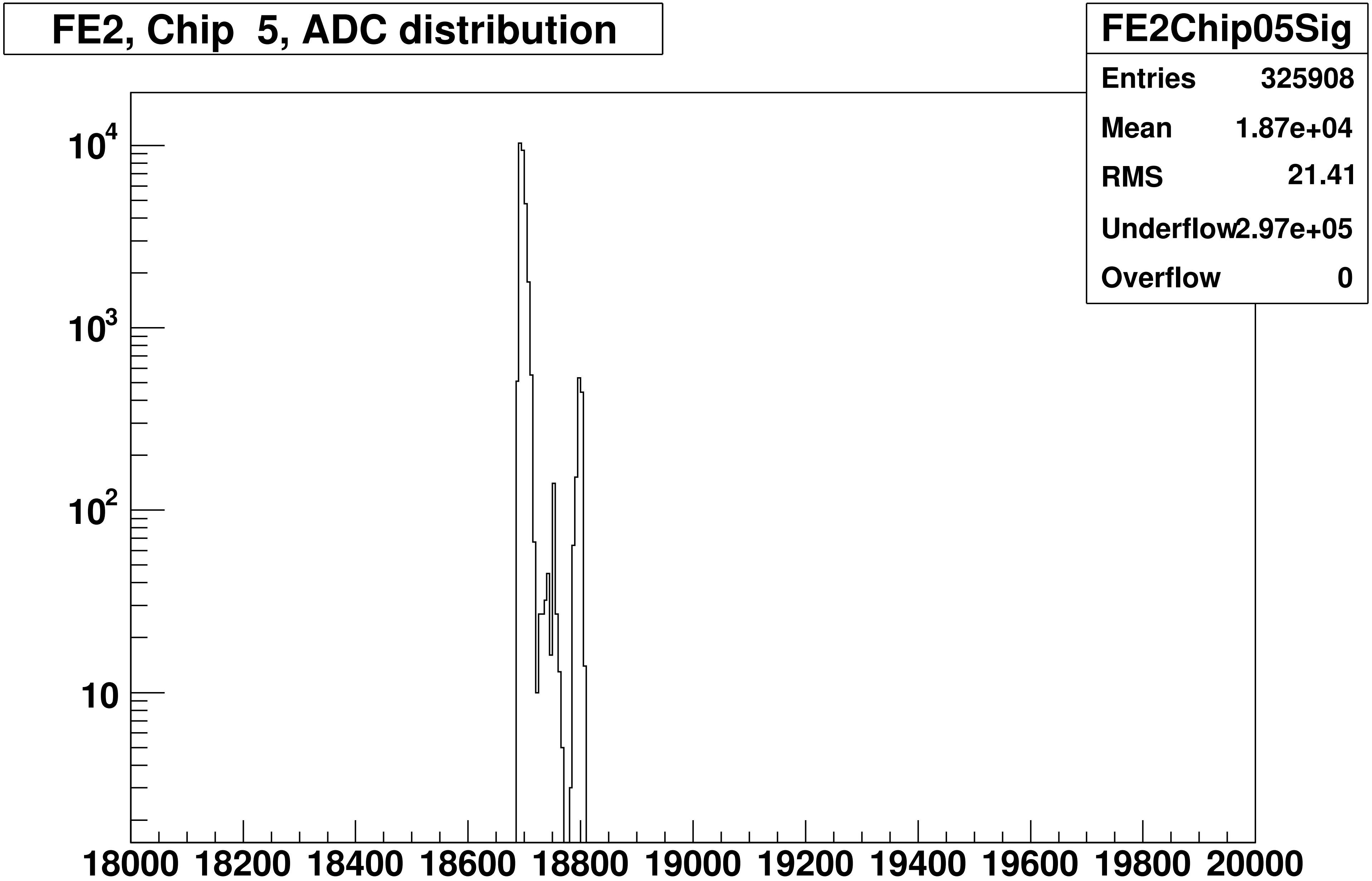
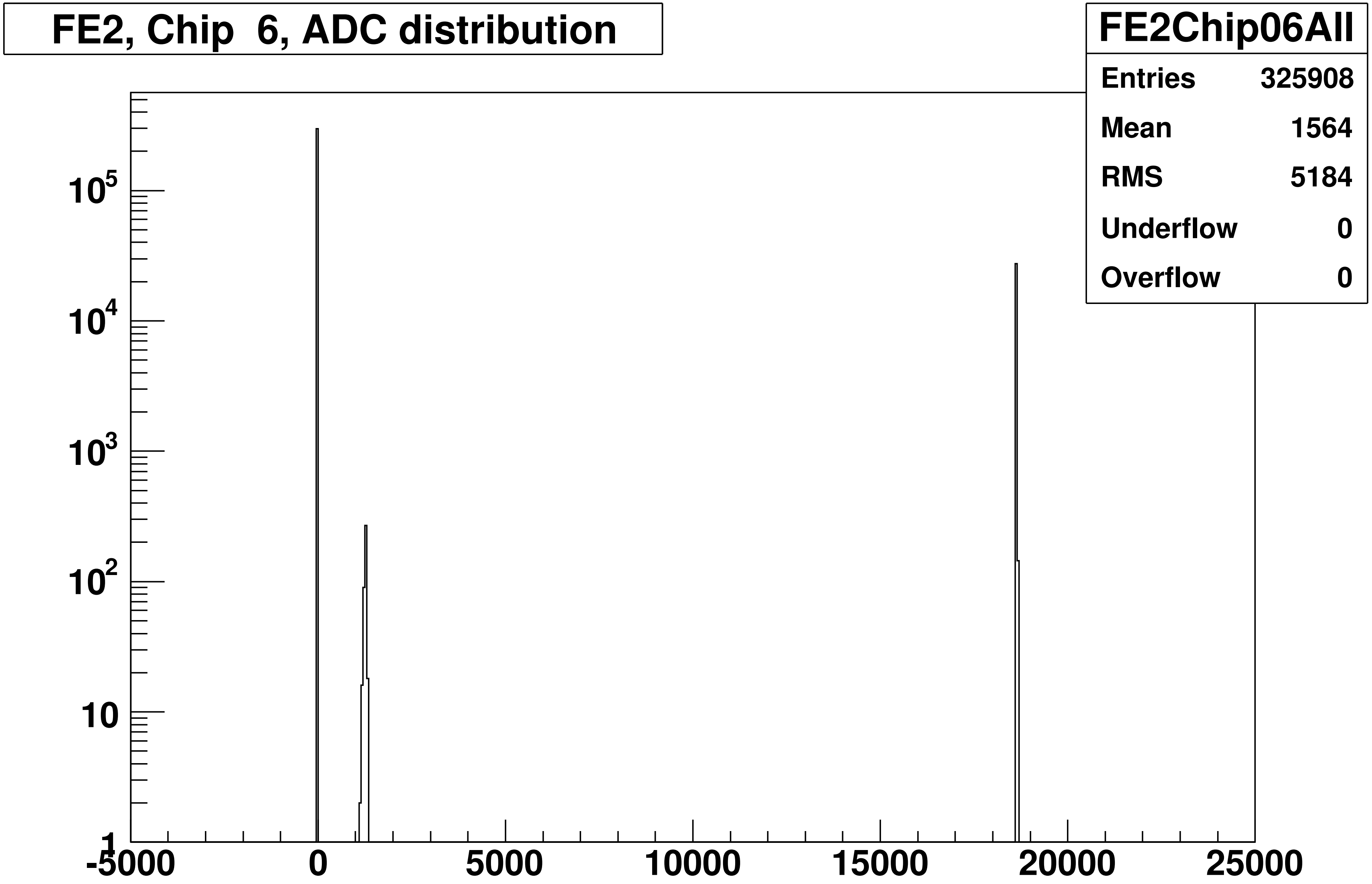
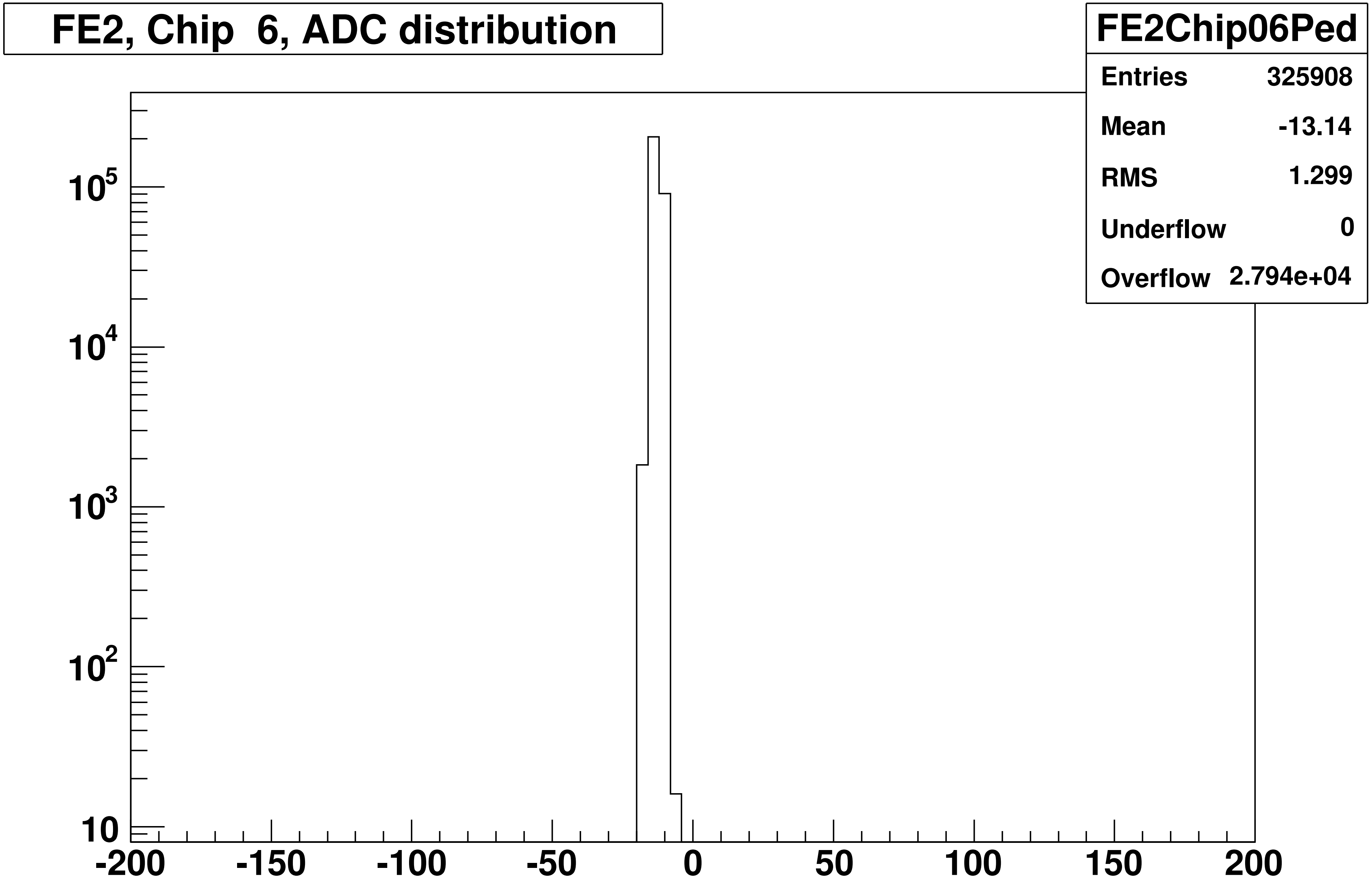
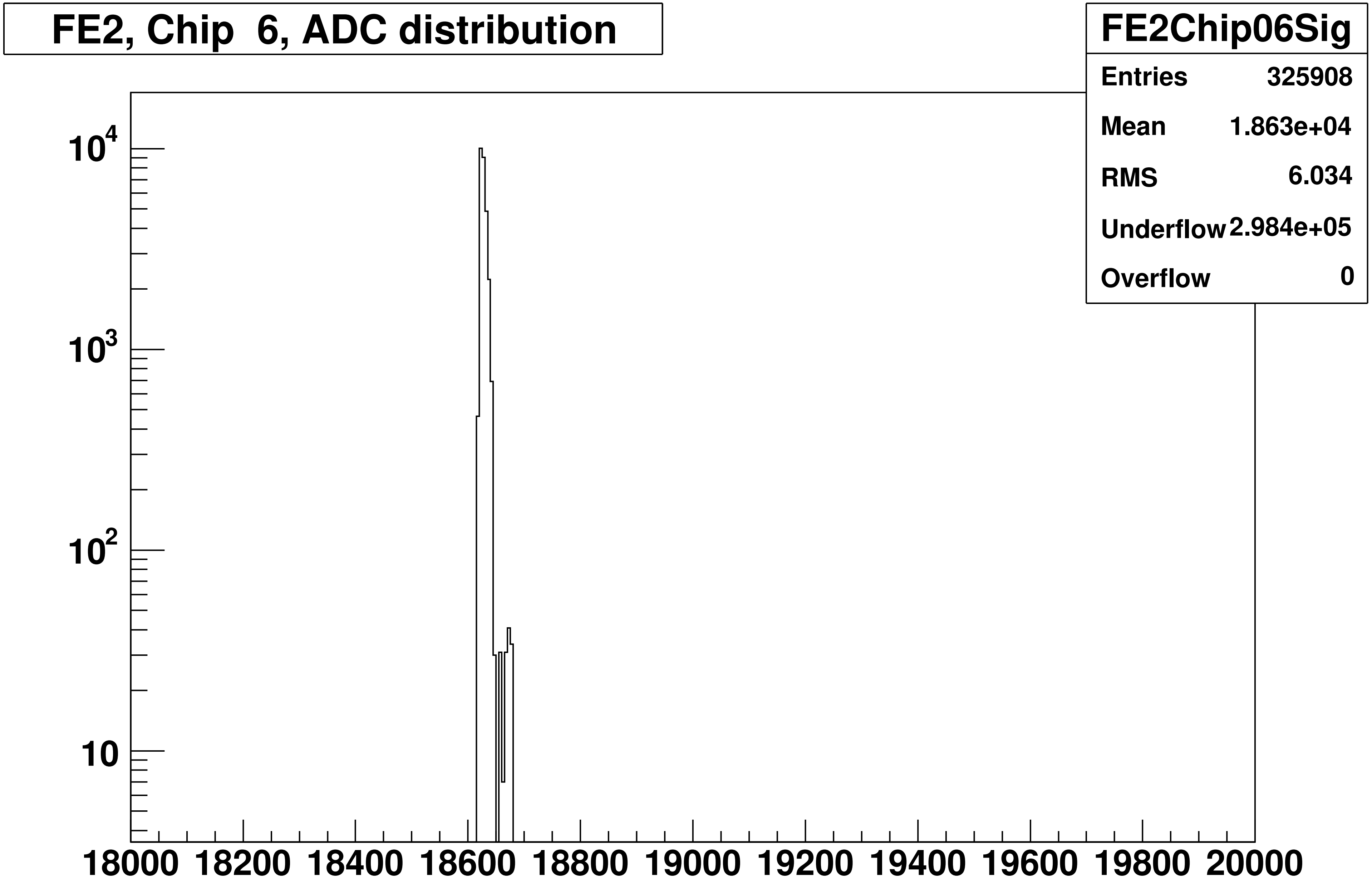
0
50
100
150
200

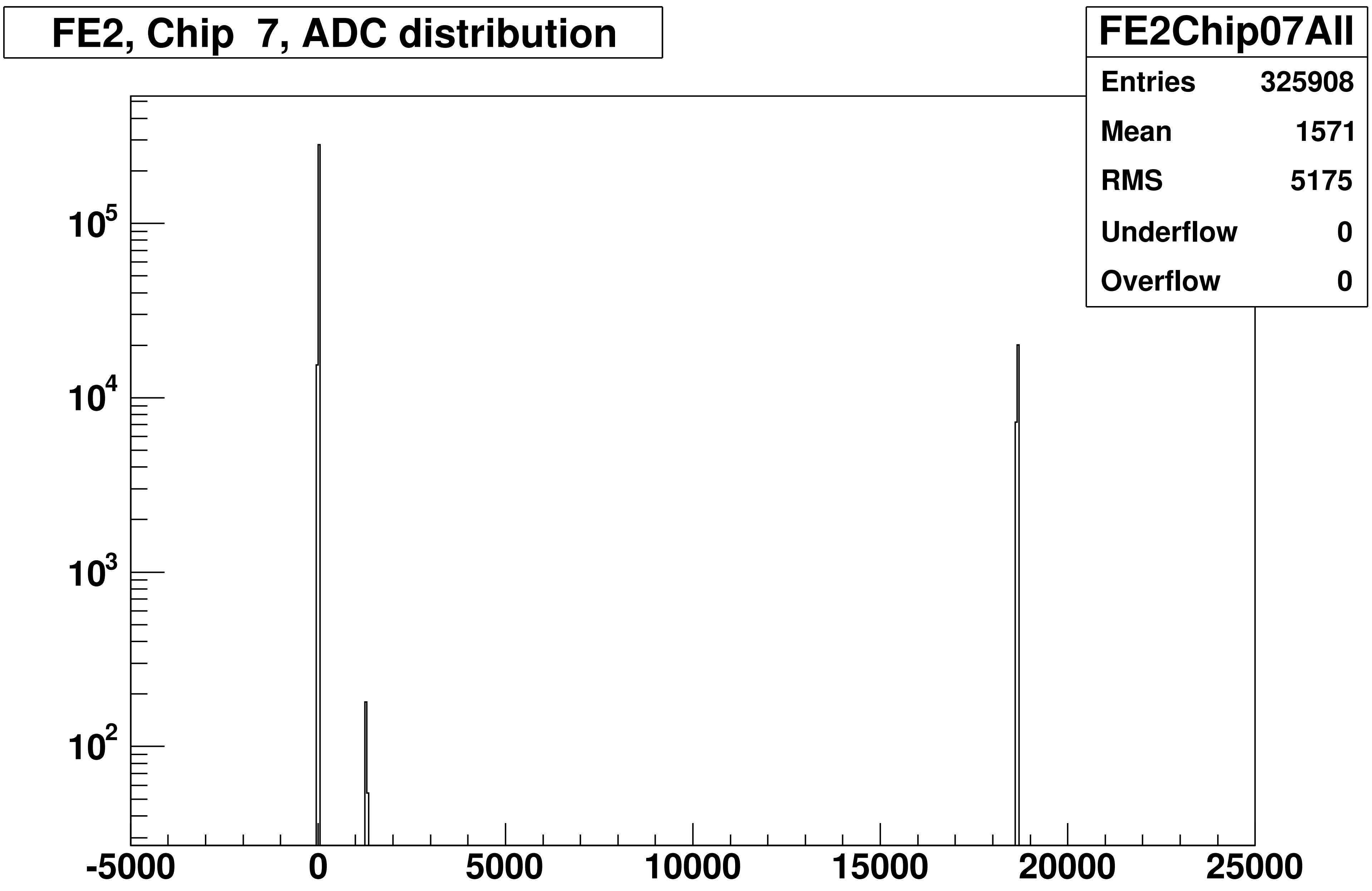

FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 325908
Mean 1.87e+04
RMS 21.41
Underflow 2.97e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

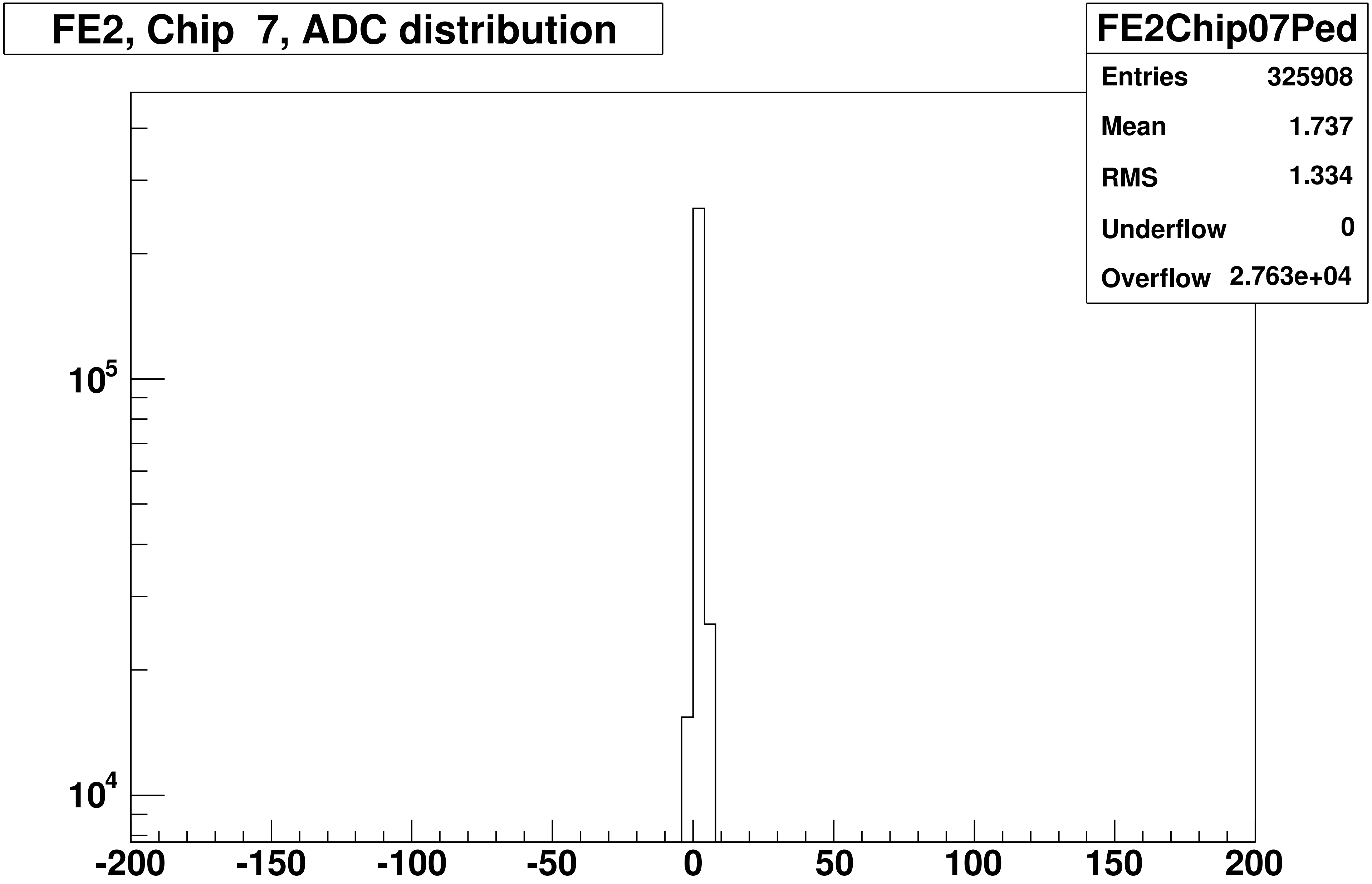


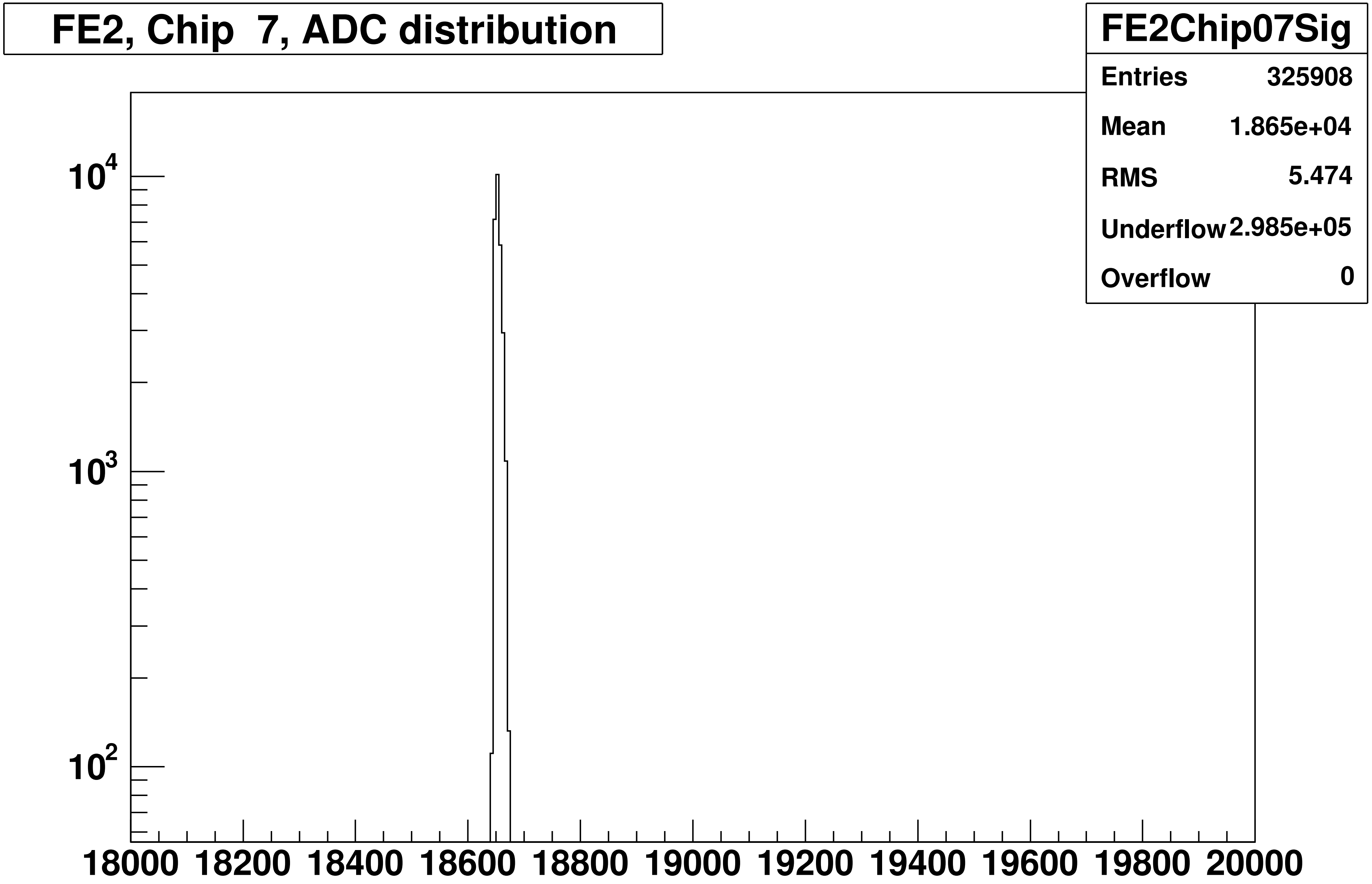
FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 325908
Mean 1564
RMS 5184
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 325908
Mean -13.14
RMS 1.299
Underflow 0
Overflow 2.794e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

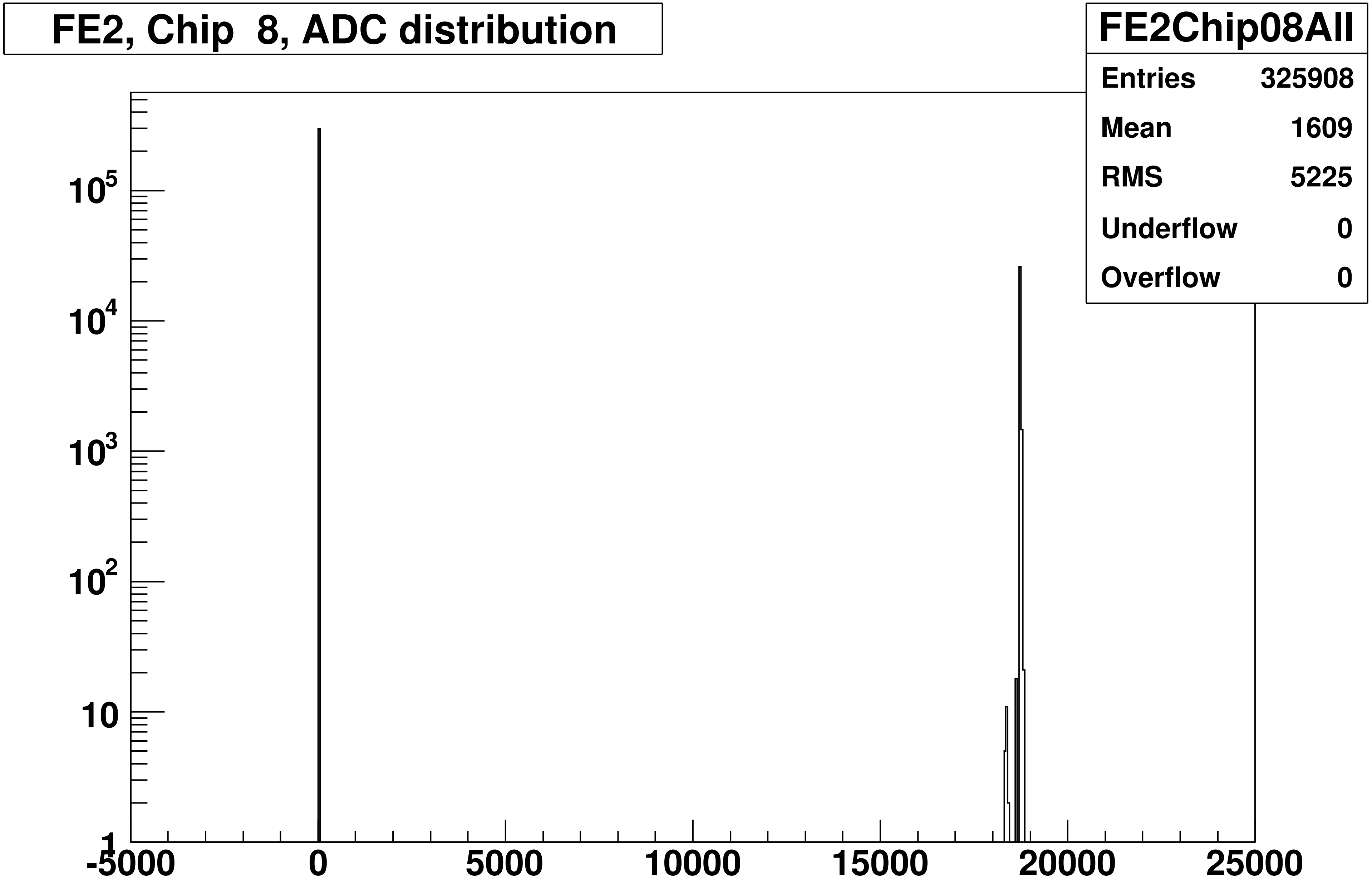

FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 325908
Mean 1.863e+04
RMS 6.034
Underflow 2.984e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

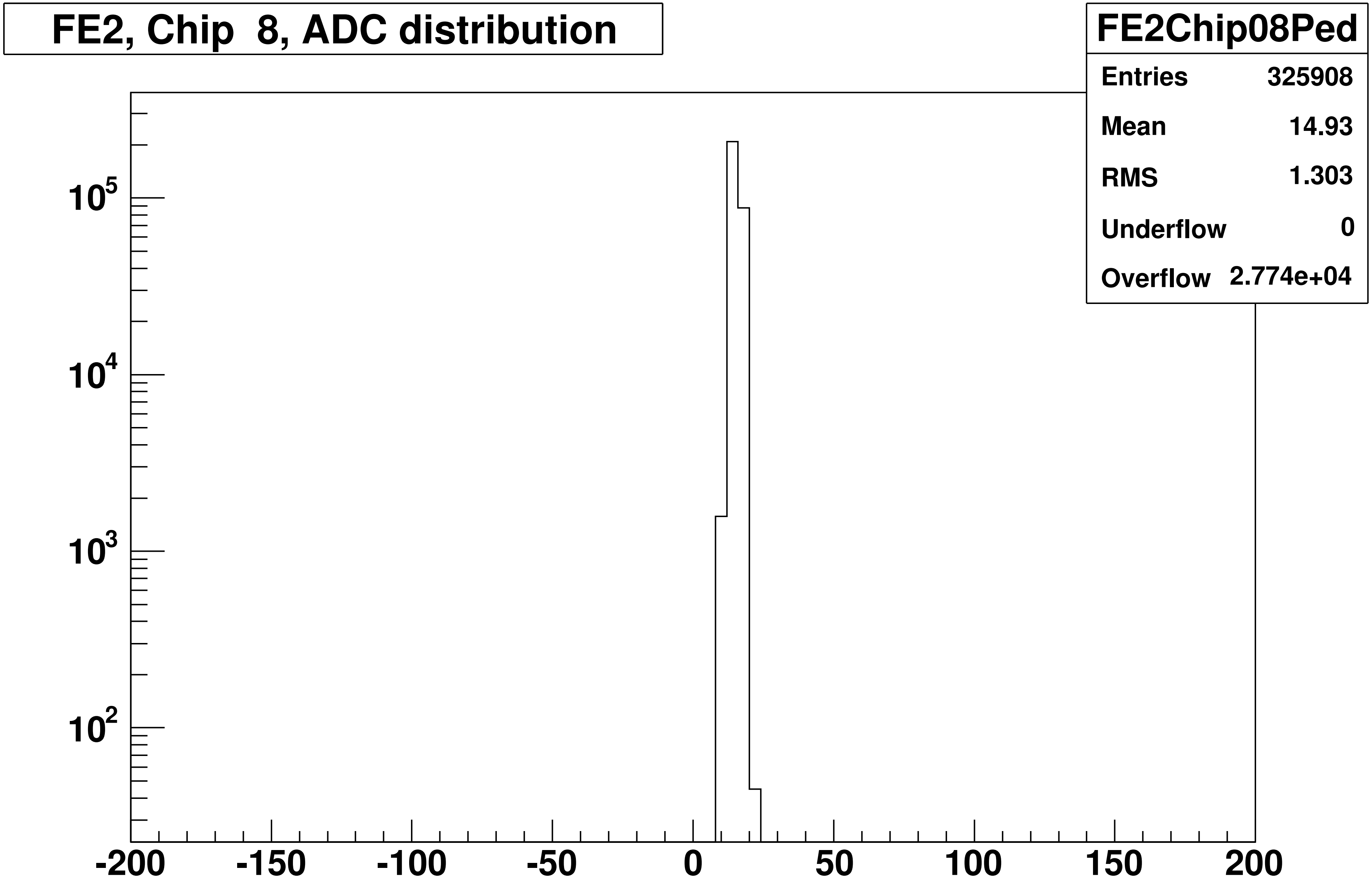

FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 325908
Mean 1571
RMS 5175
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

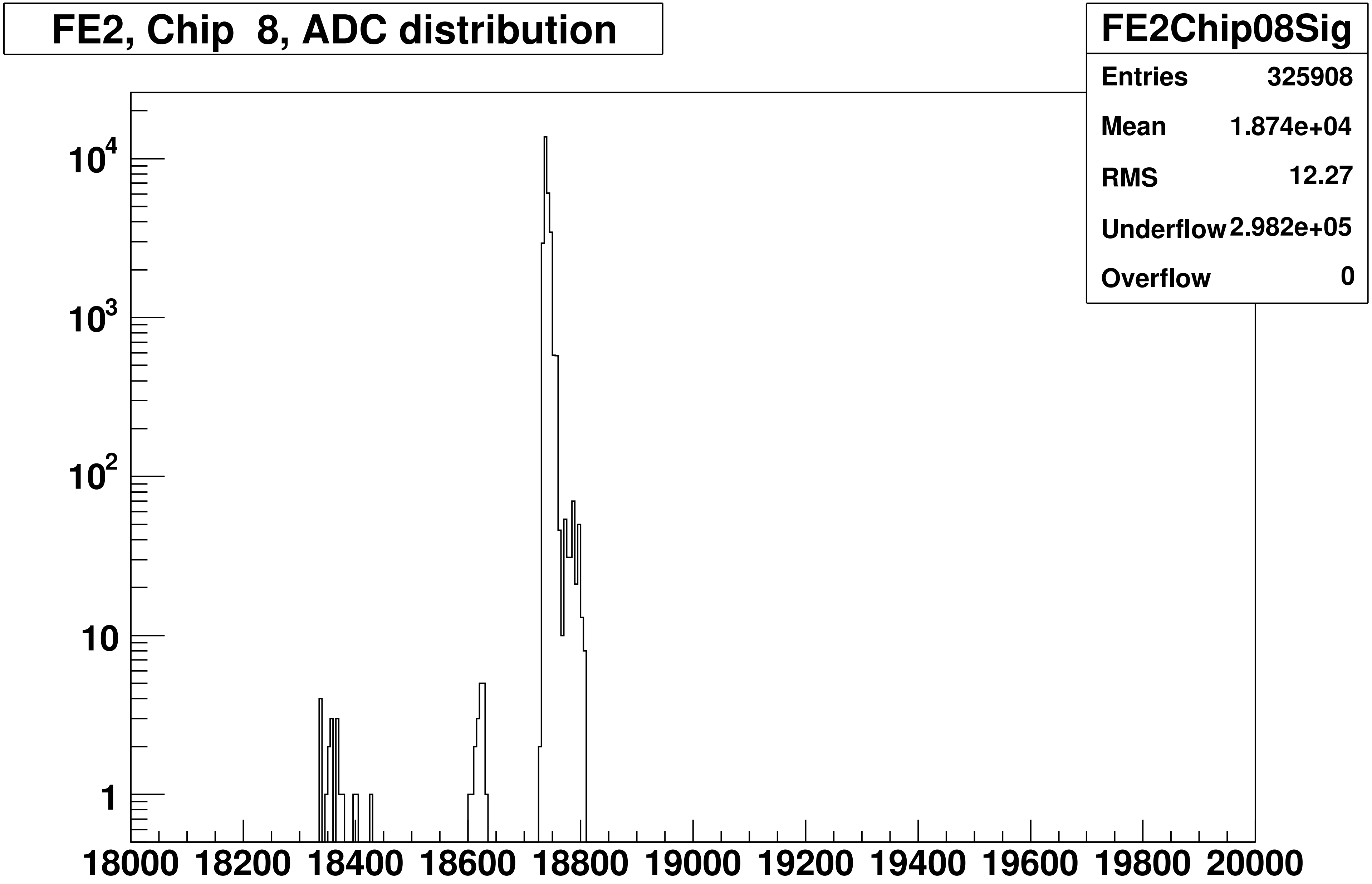

FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 325908
Mean 1.737
RMS 1.334
Underflow 0
Overflow 2.763e+04
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200



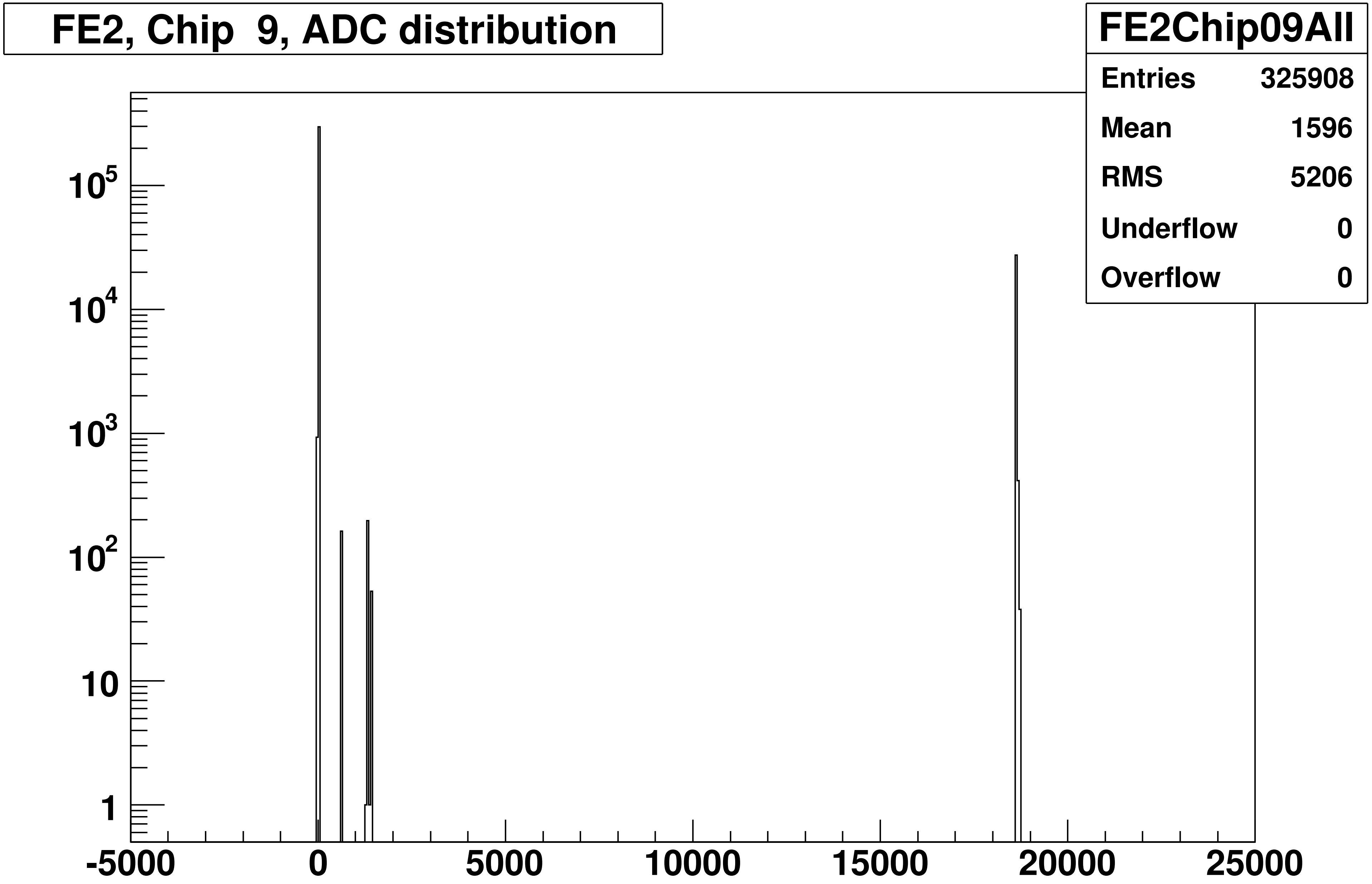
FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 325908
Mean 1.865e+04
RMS 5.474
Underflow 2.985e+05
Overflow 0
10^4
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 325908
Mean 1609
RMS 5225
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

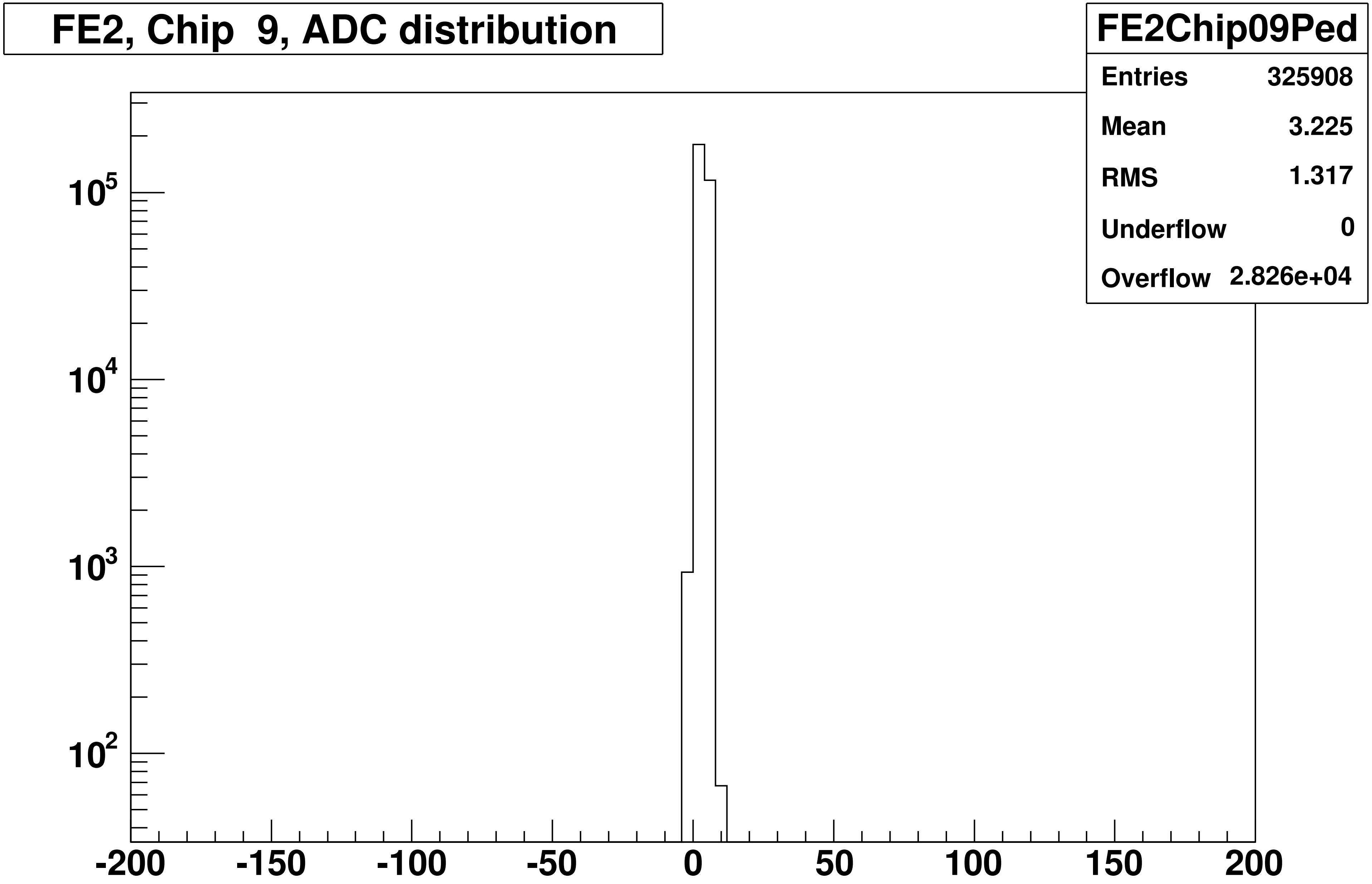

FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 325908
Mean 14.93
RMS 1.303
Underflow 0
Overflow 2.774e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



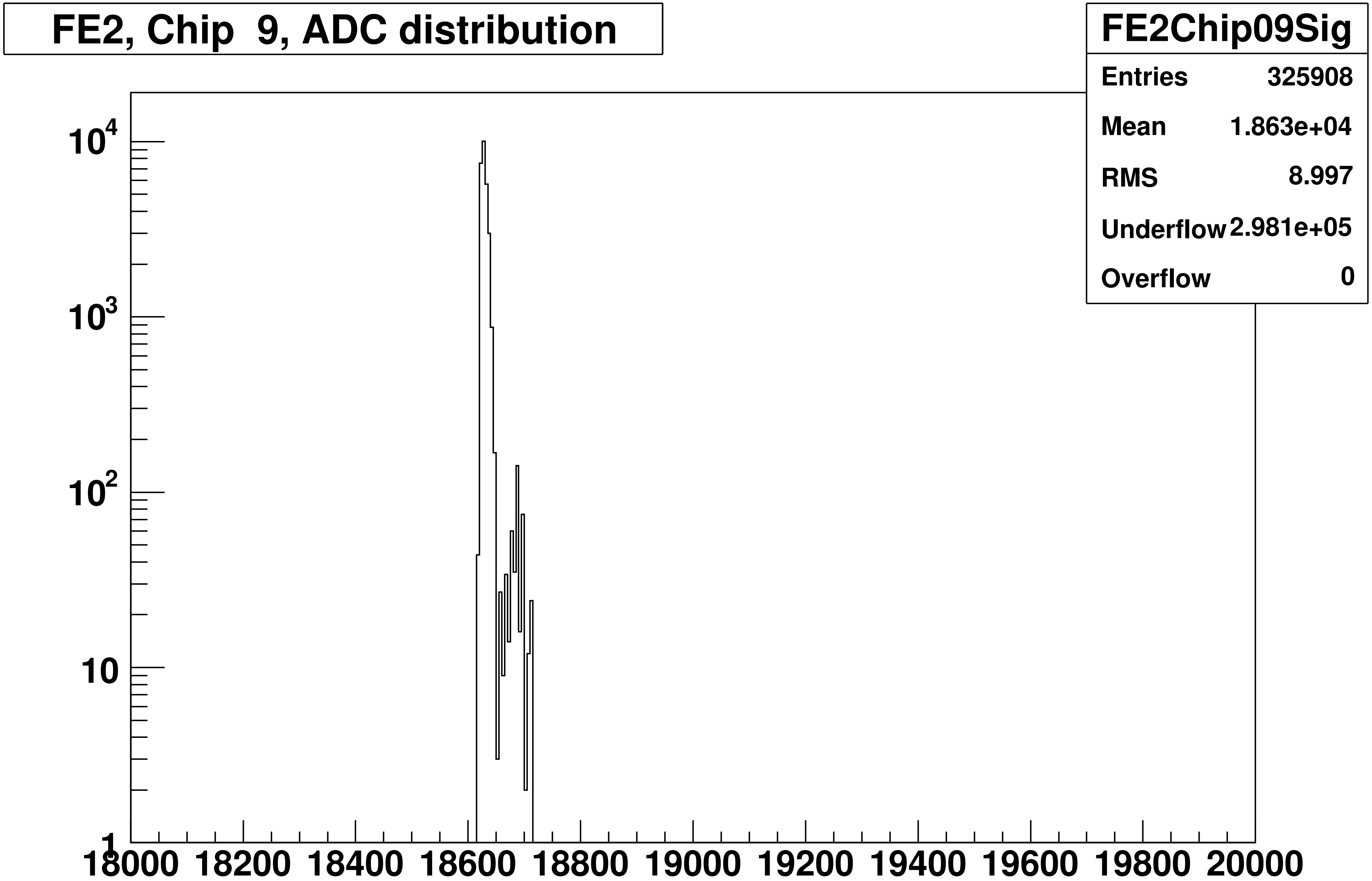
FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 325908
Mean 1.874e+04
RMS 12.27
Underflow 2.982e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

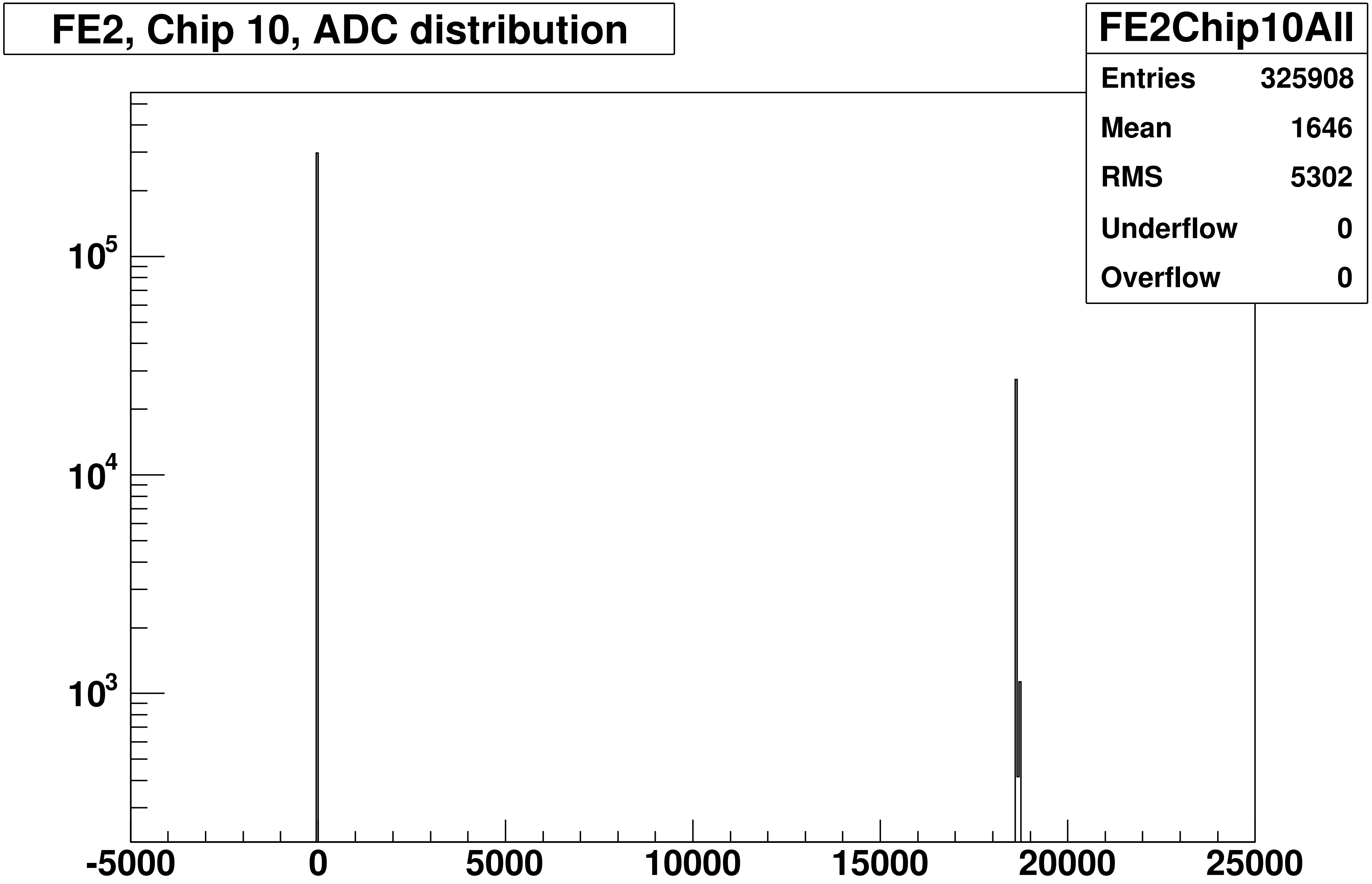


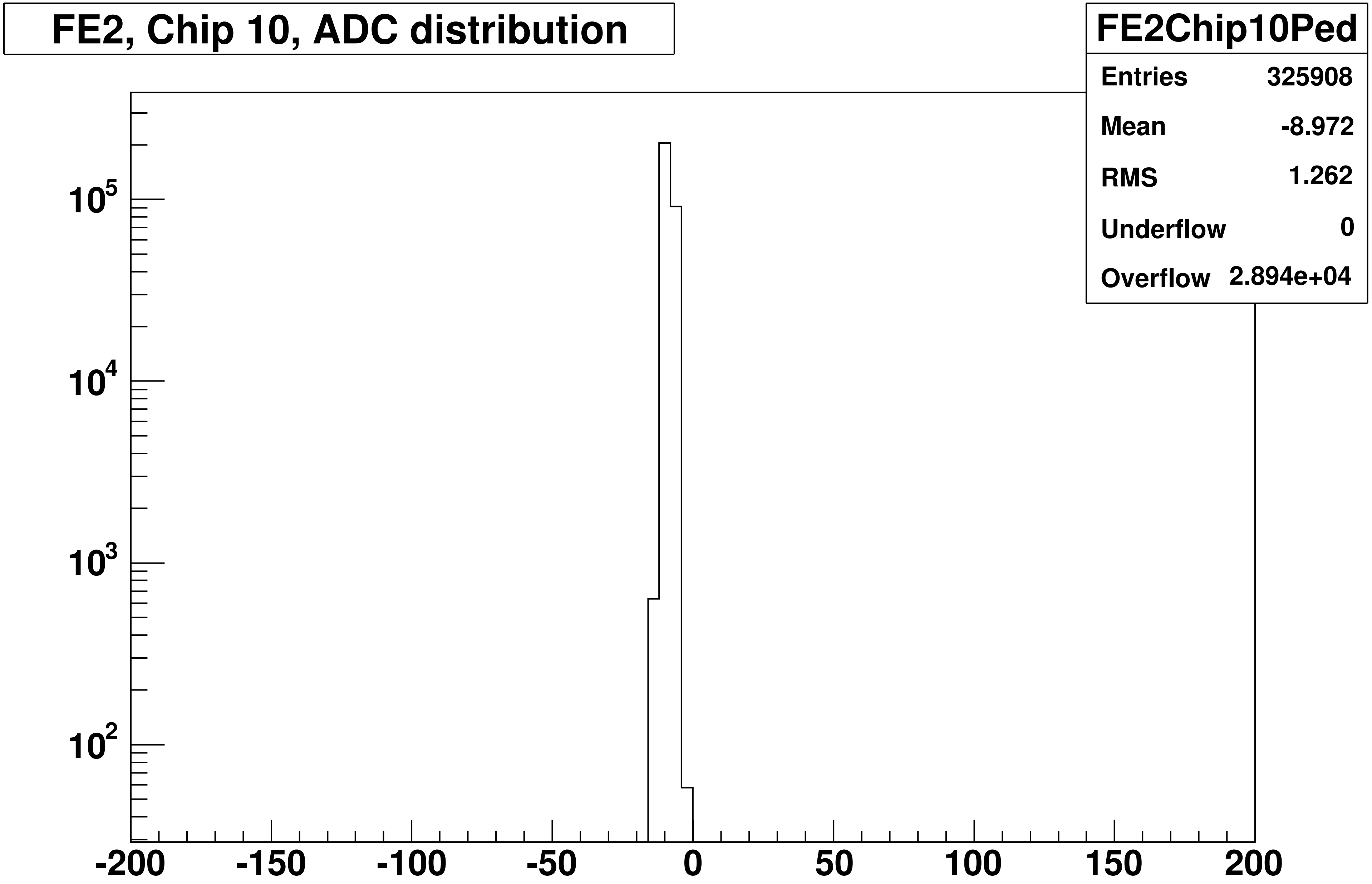
FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 325908
Mean 1596
RMS 5206
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

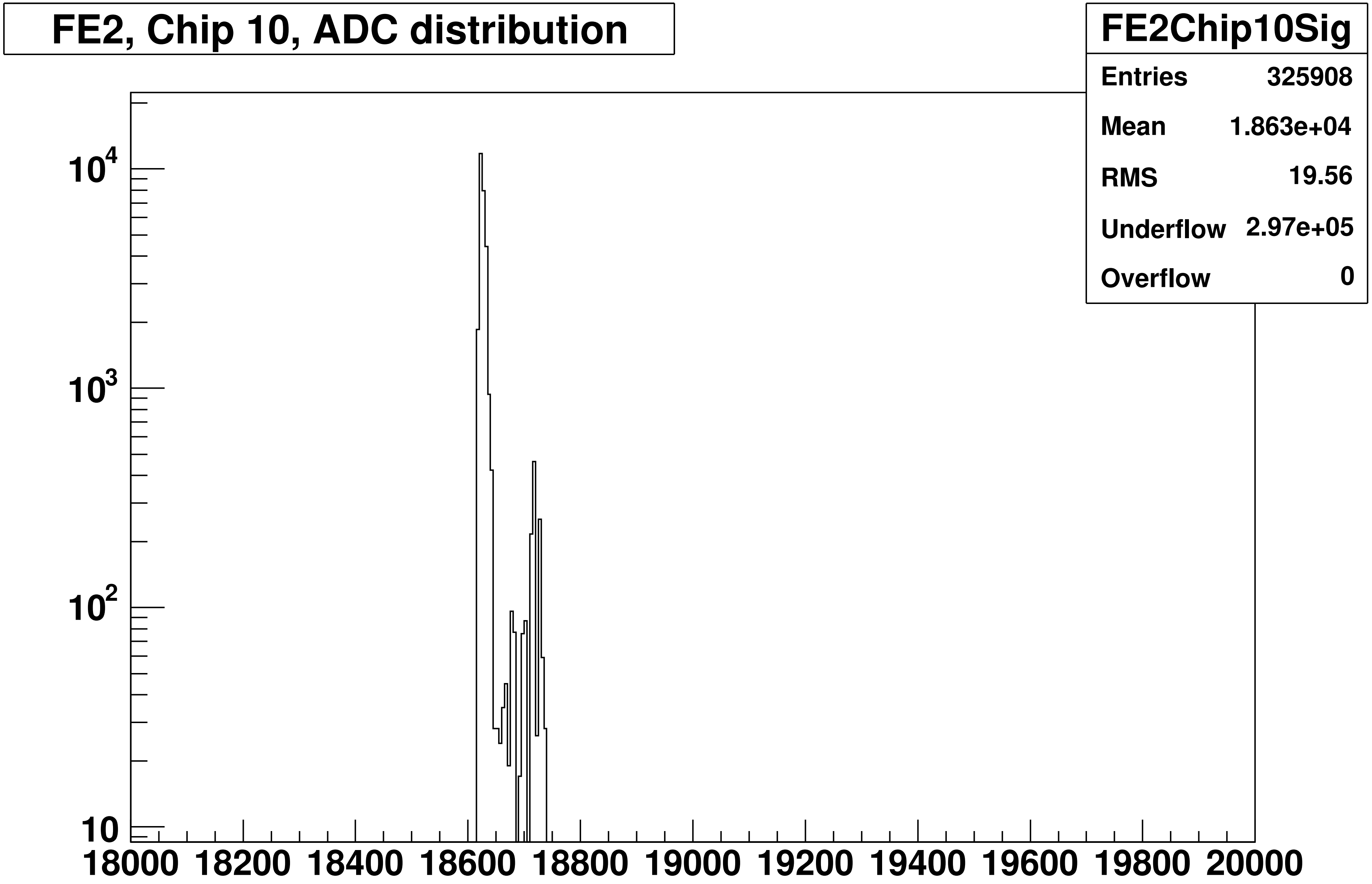


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 325908
Mean 3.225
RMS 1.317
Underflow 0
Overflow 2.826e+04
10^5
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200

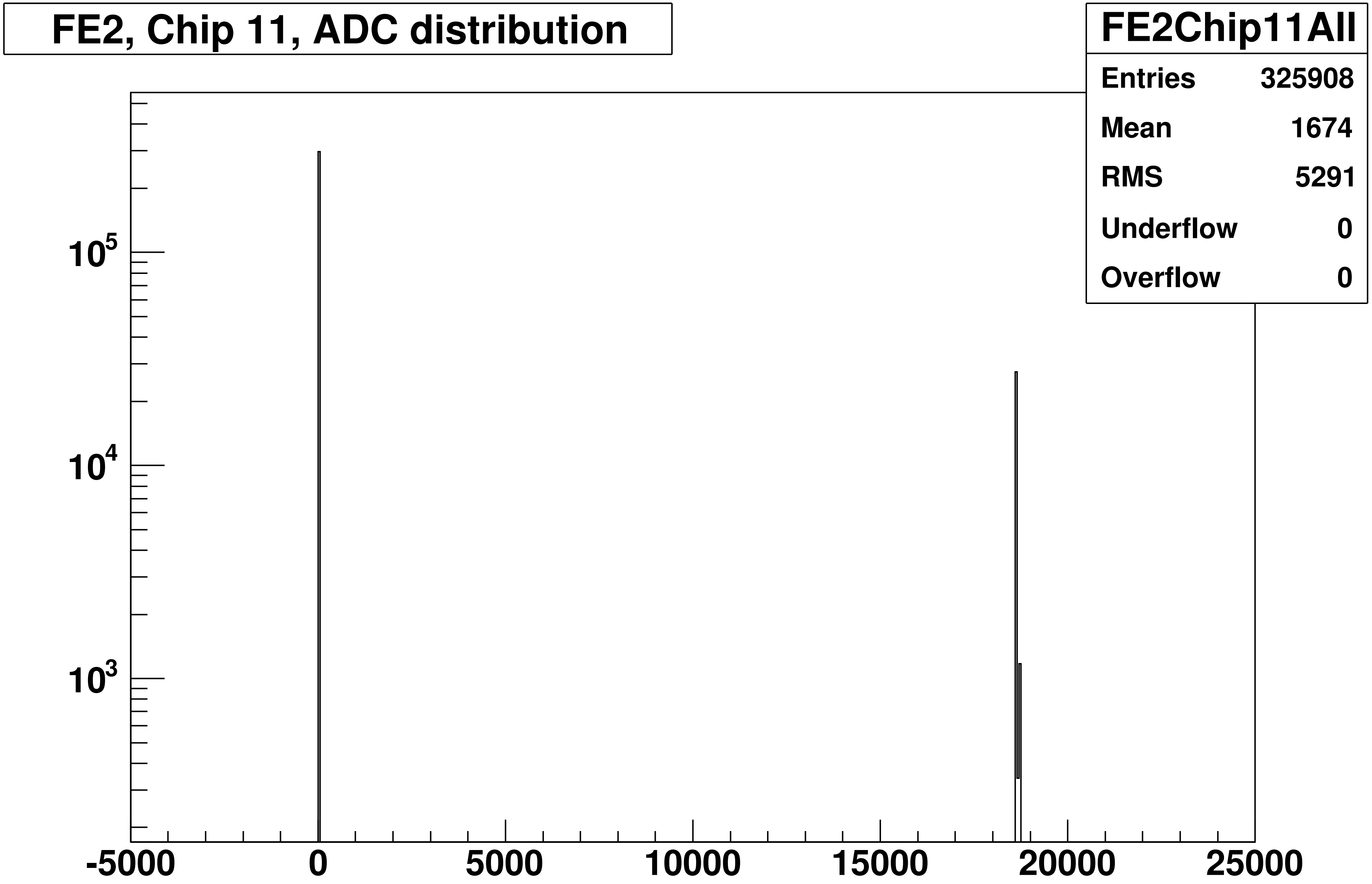

FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 325908
Mean 1.863e+04
RMS 8.997
Underflow 2.981e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

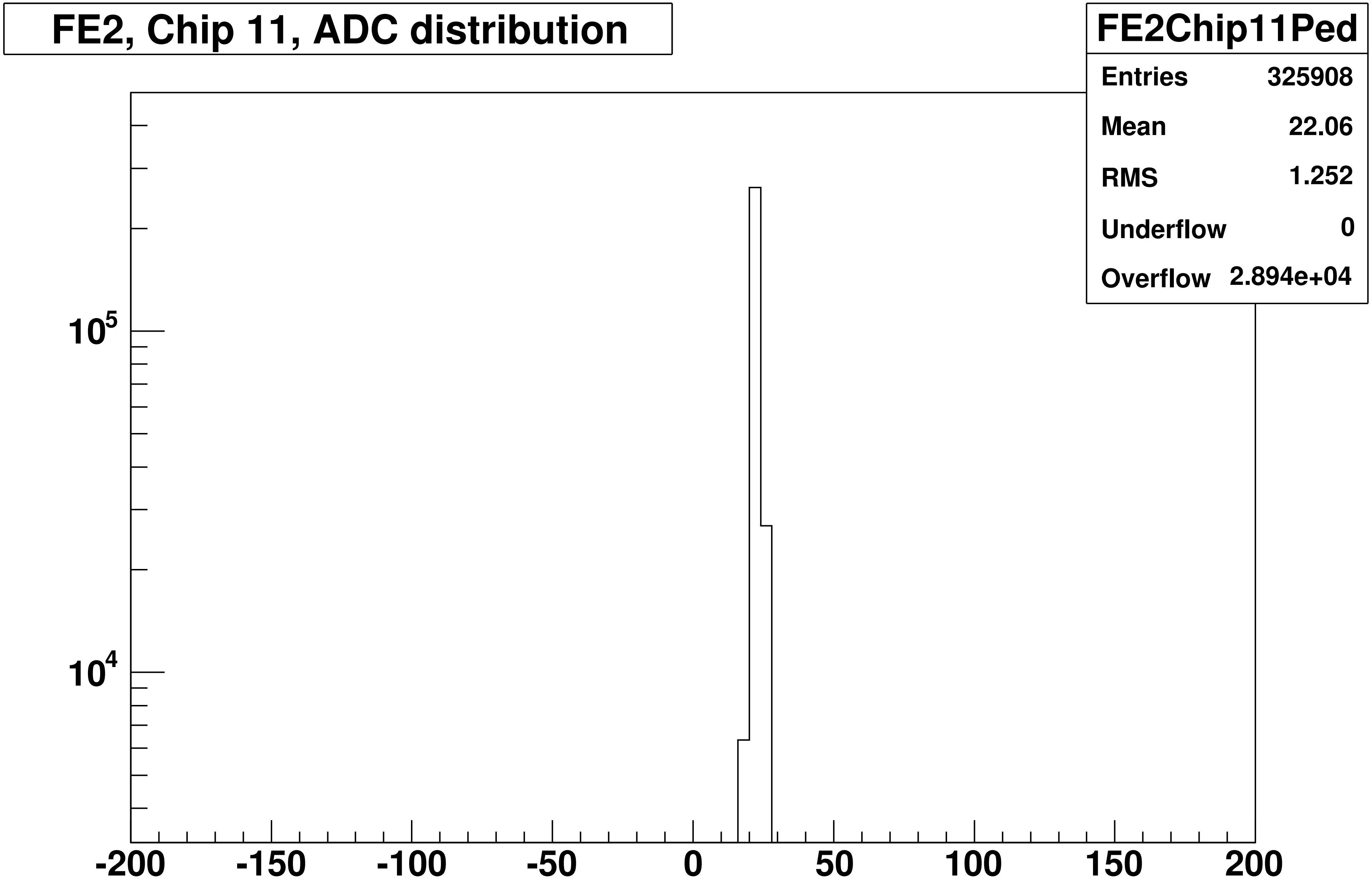

FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 325908
Mean 1646
RMS 5302
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 325908
Mean -8.972
RMS 1.262
Underflow 0
Overflow 2.894e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

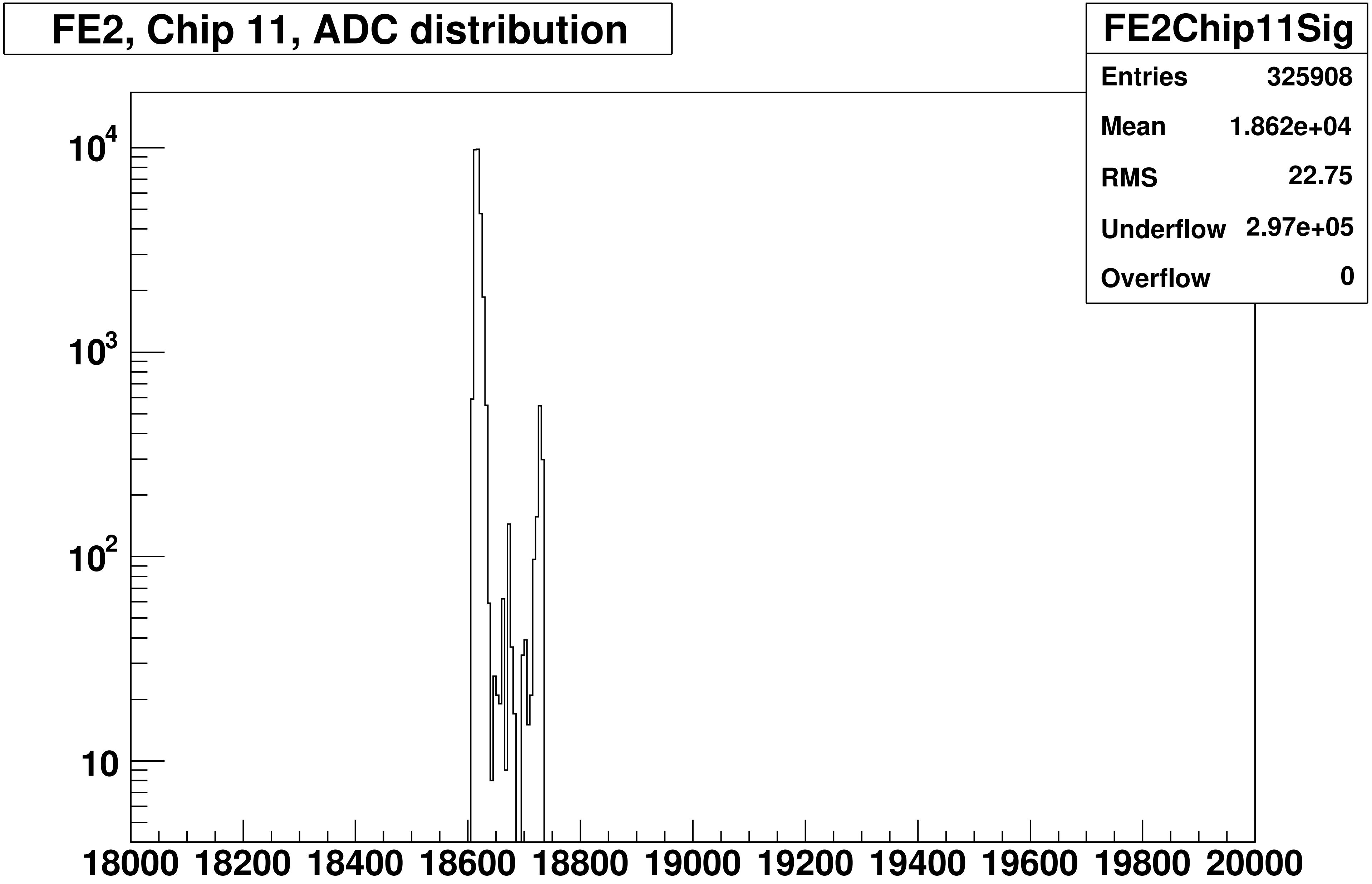


FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 325908
Mean 1.863e+04
RMS 19.56
Underflow 2.97e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

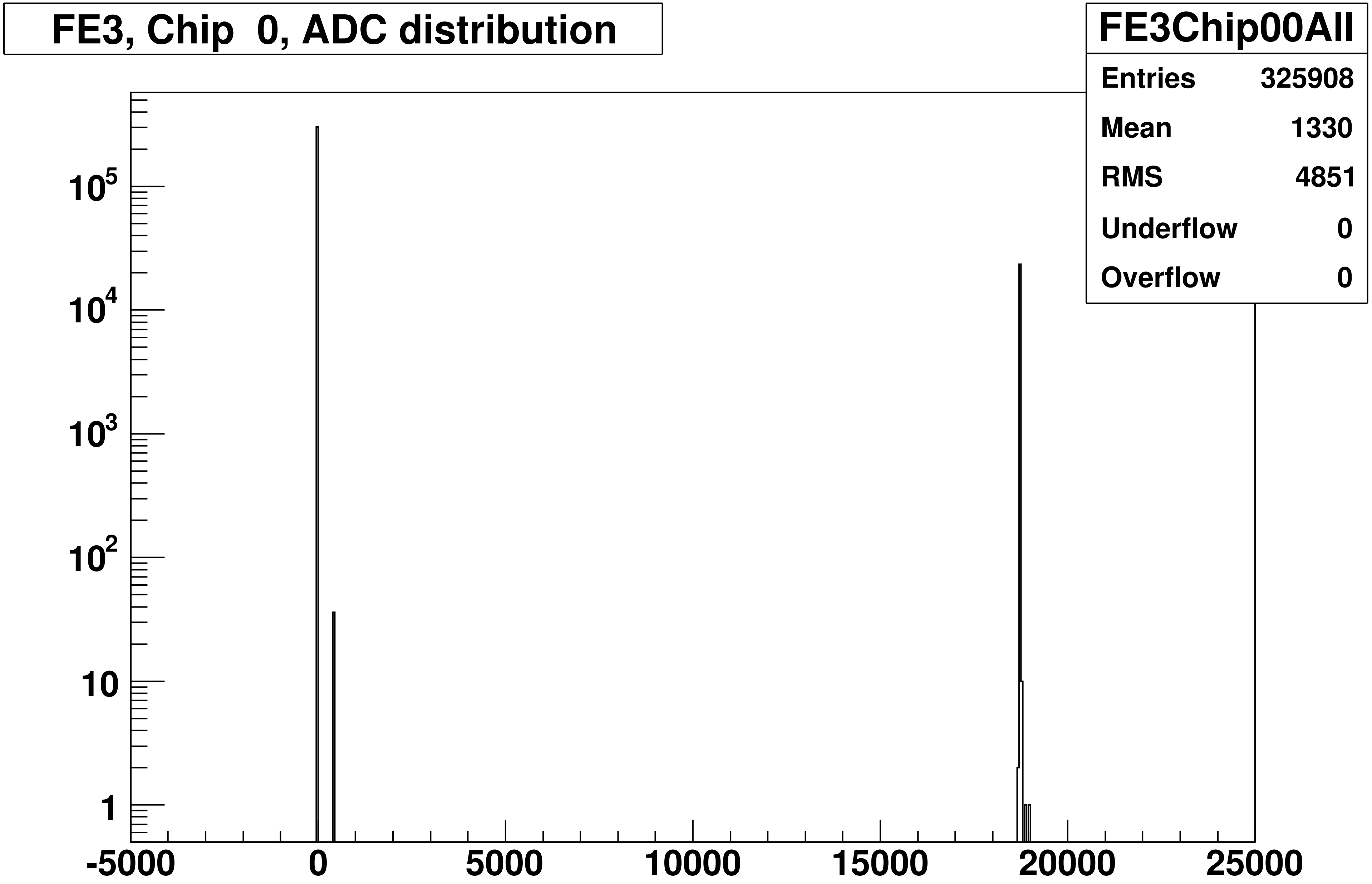

FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 325908
Mean 1674
RMS 5291
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



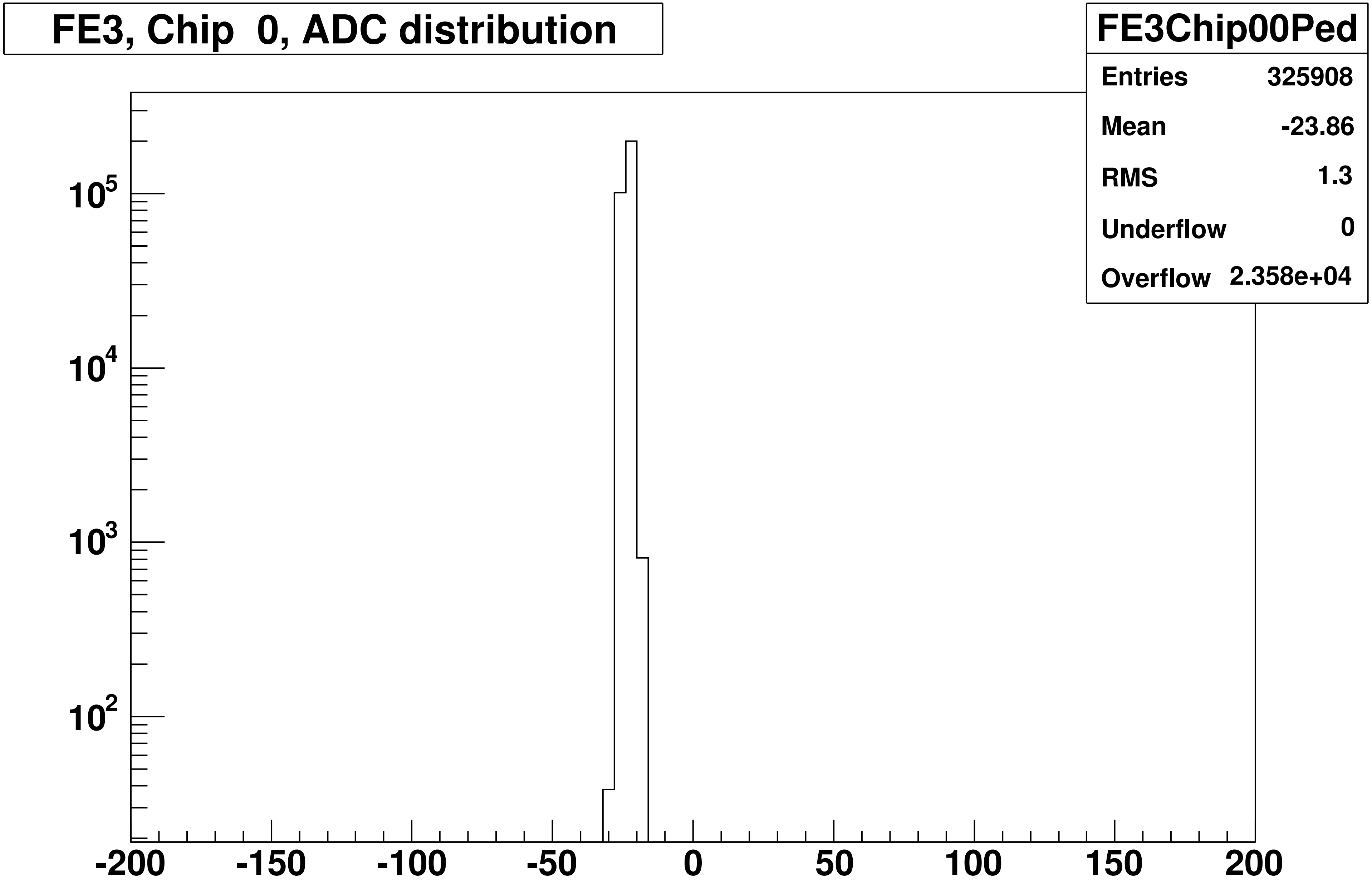
FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 325908
Mean 22.06
RMS 1.252
Underflow 0
Overflow 2.894e+04
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200



FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 325908
Mean 1.862e+04
RMS 22.75
Underflow 2.97e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 325908
Mean 1330
RMS 4851
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 325908
Mean -23.86
RMS 1.3
Underflow 0
Overflow 2.358e+04
10^5
10^4
10^3
10^2
-200
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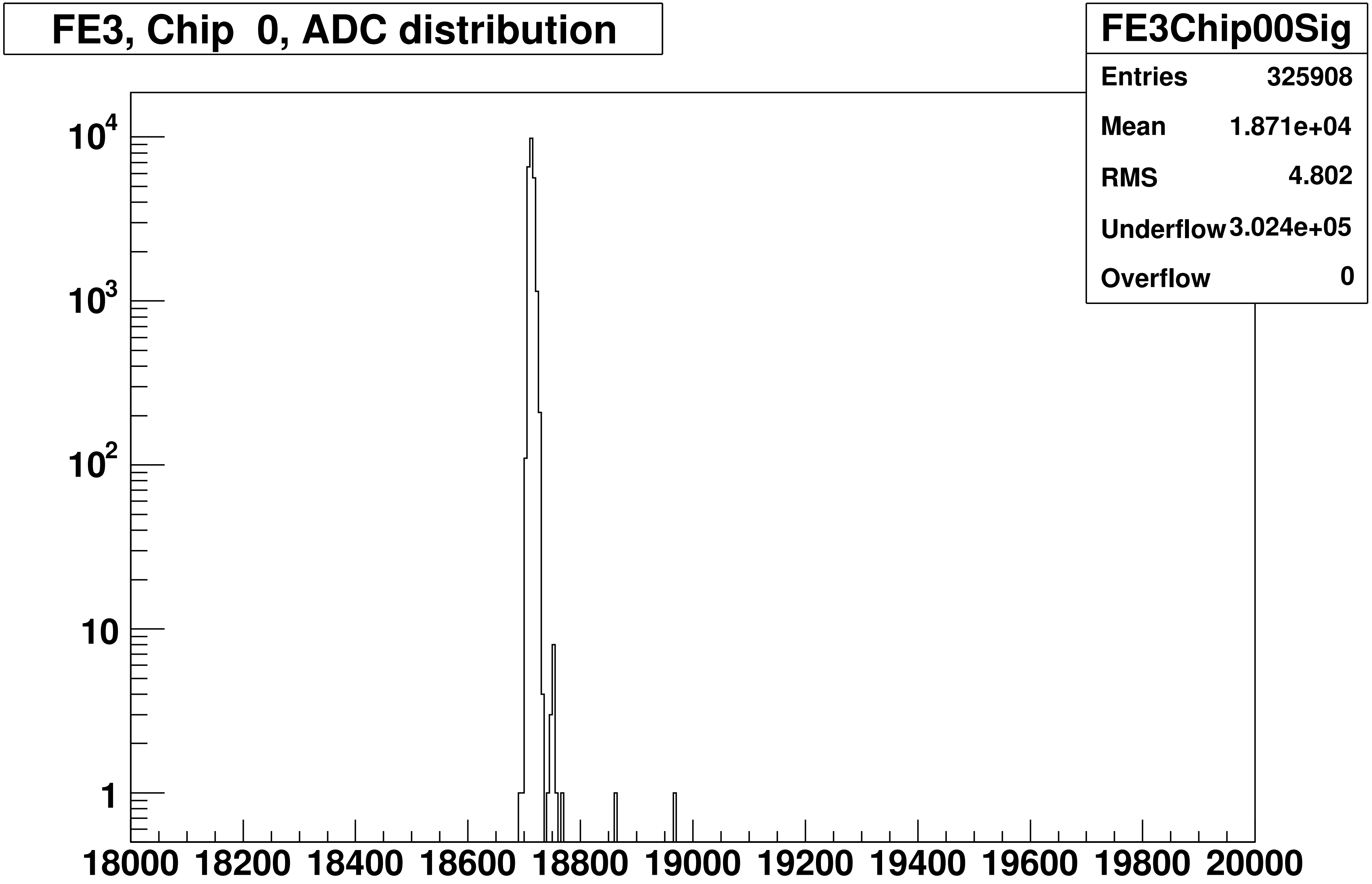
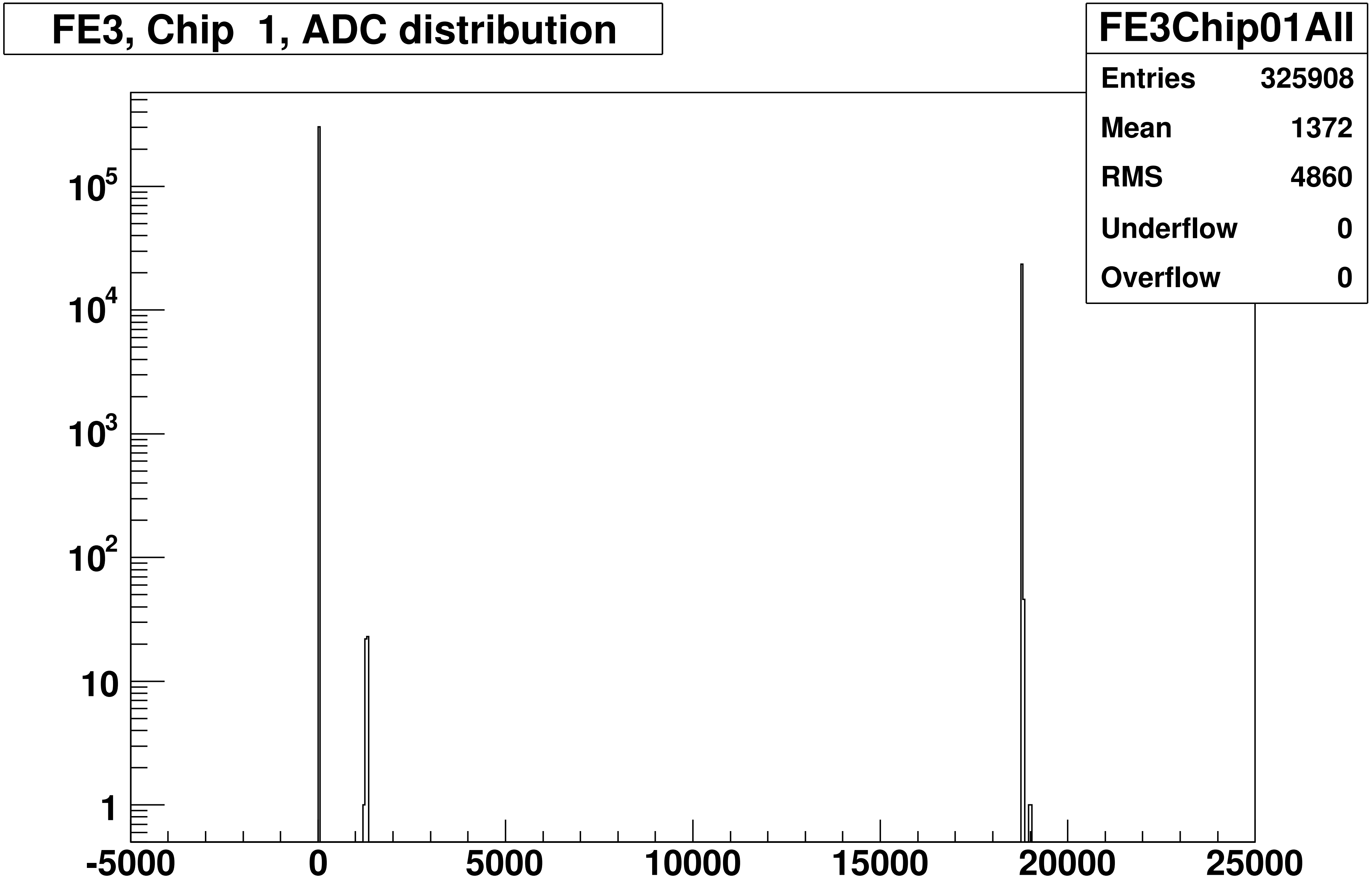
-100
-50
0
50
100
150
200


FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 325908
Mean 1.871e+04
RMS 4.802
Underflow 3.024e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 325908
Mean 1372
RMS 4860
Underflow 0
Overflow 0
10
1
-5000
0
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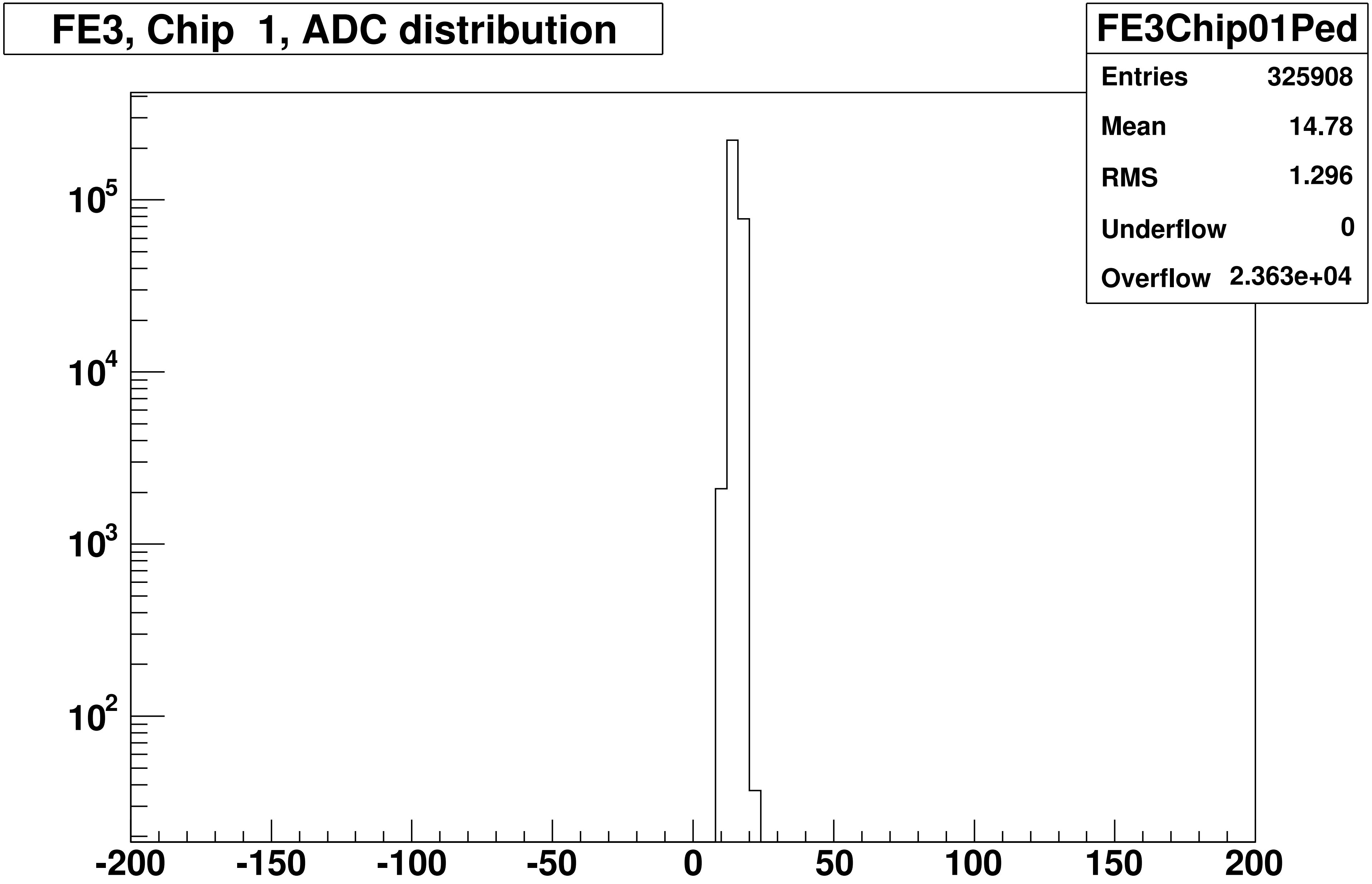
15000
20000
25000



FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 325908
Mean 14.78
RMS 1.296
Underflow 0
Overflow 2.363e+04
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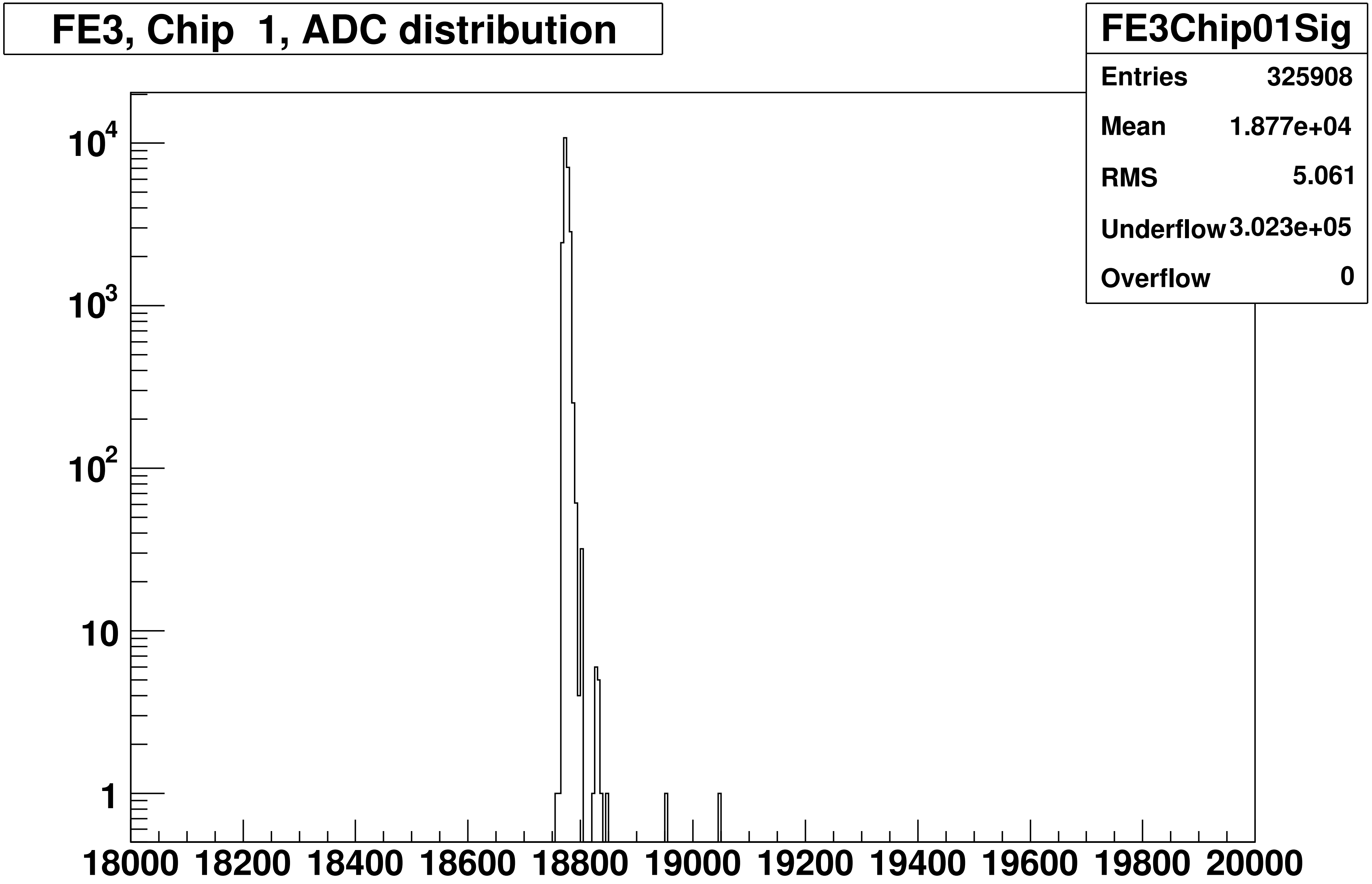
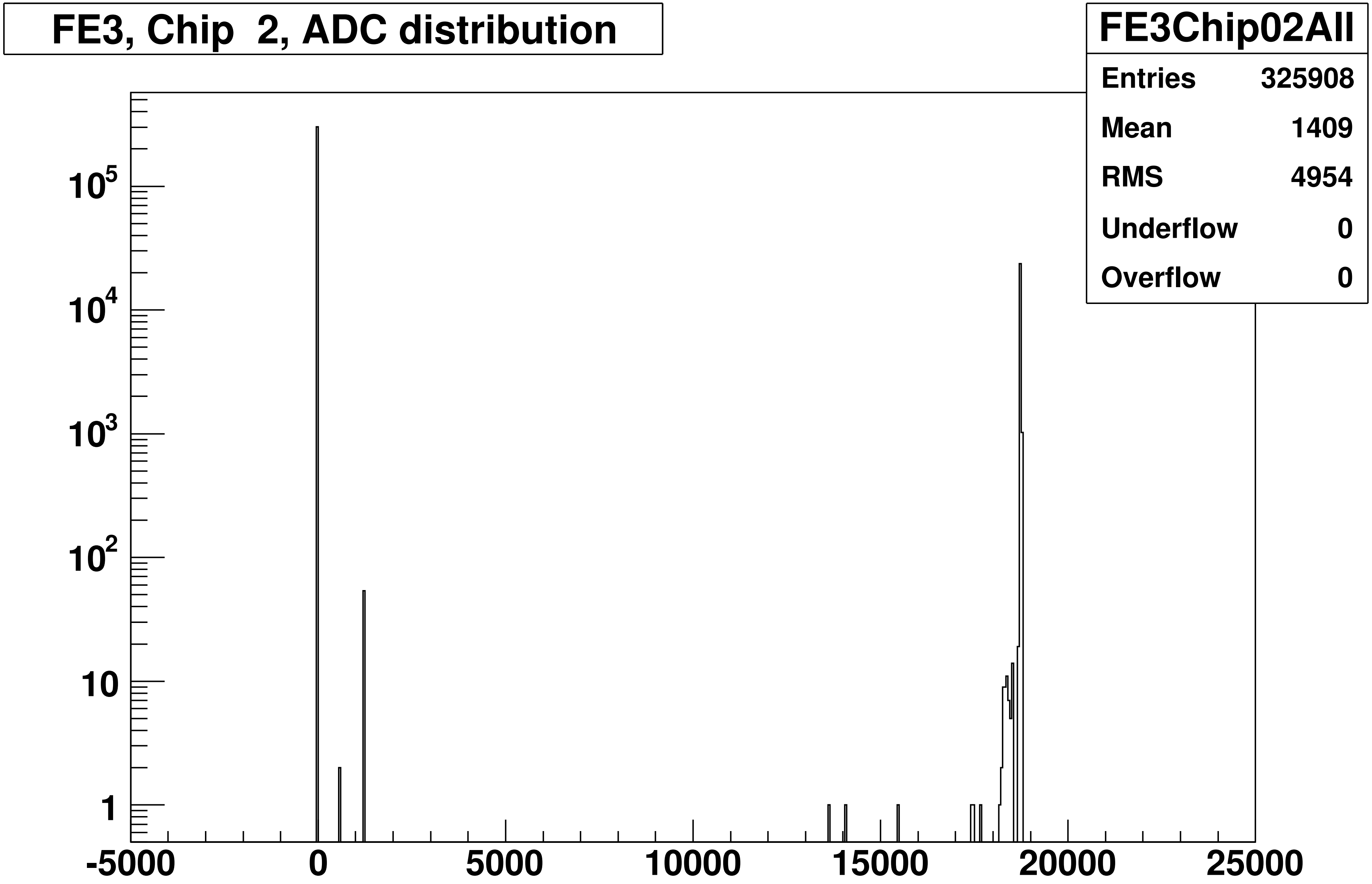
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 325908
Mean 1.877e+04
RMS 5.061
Underflow 3.023e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 325908
Mean 1409
RMS 4954
Underflow 0
Overflow 0
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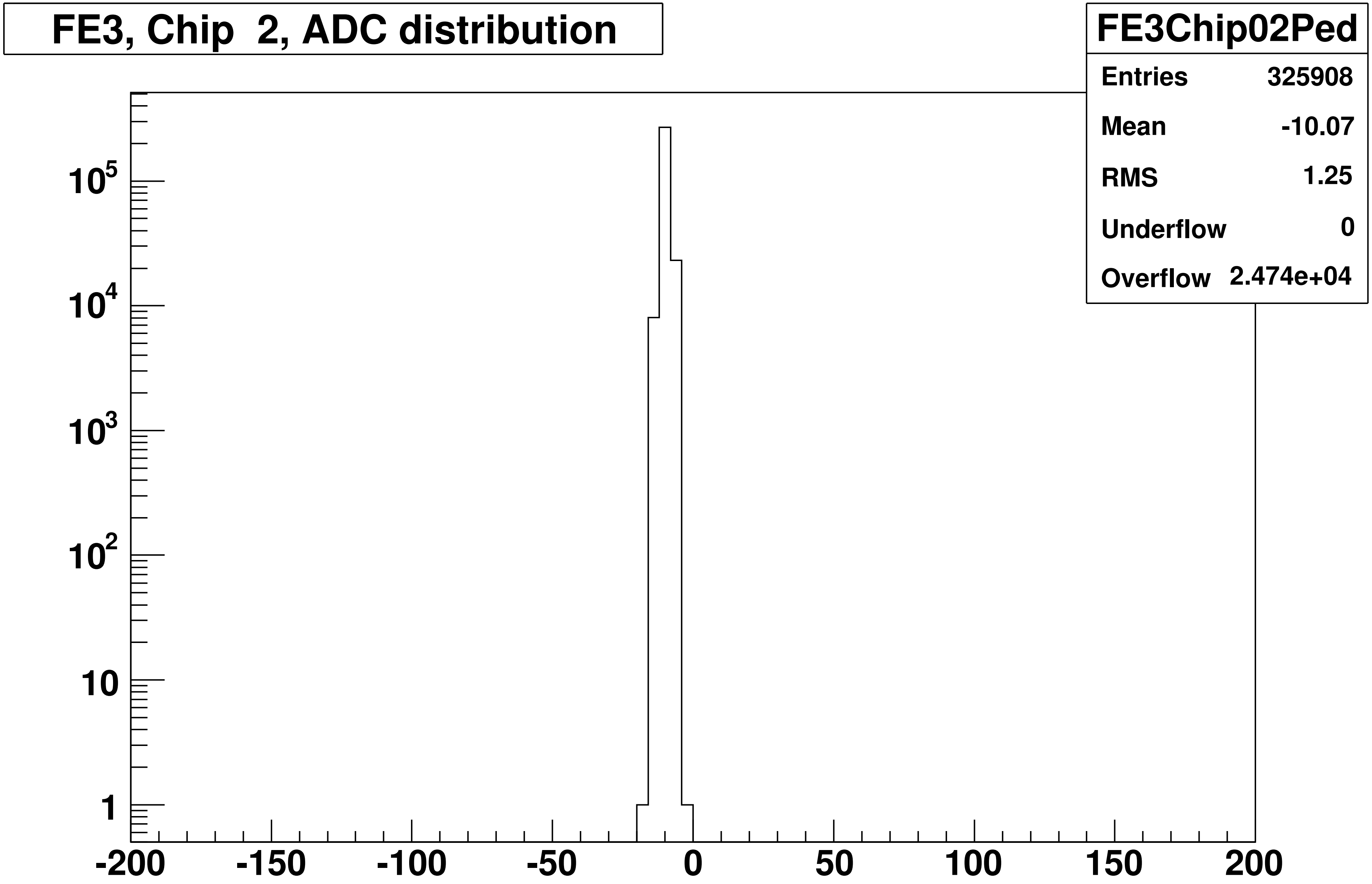
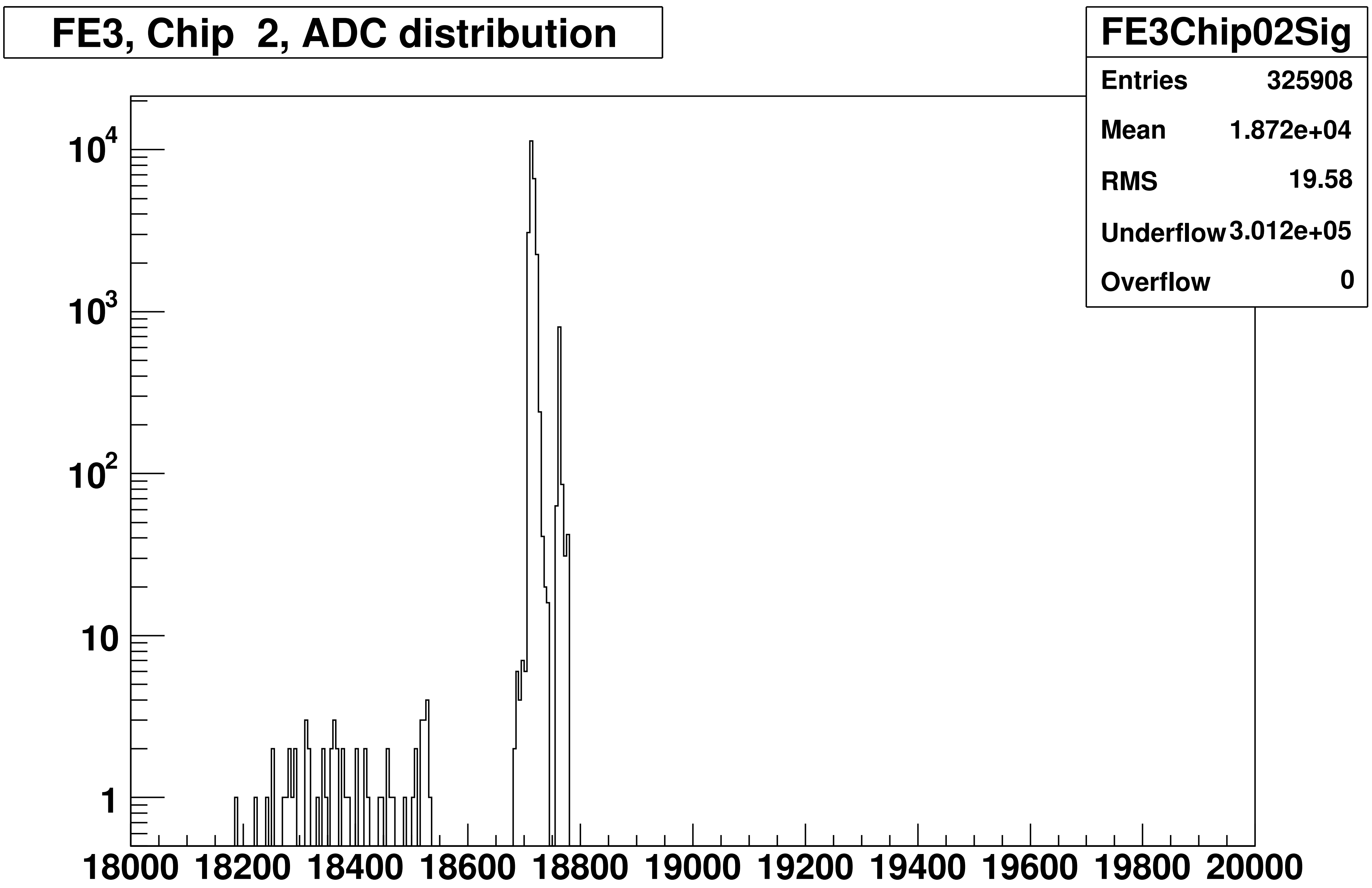
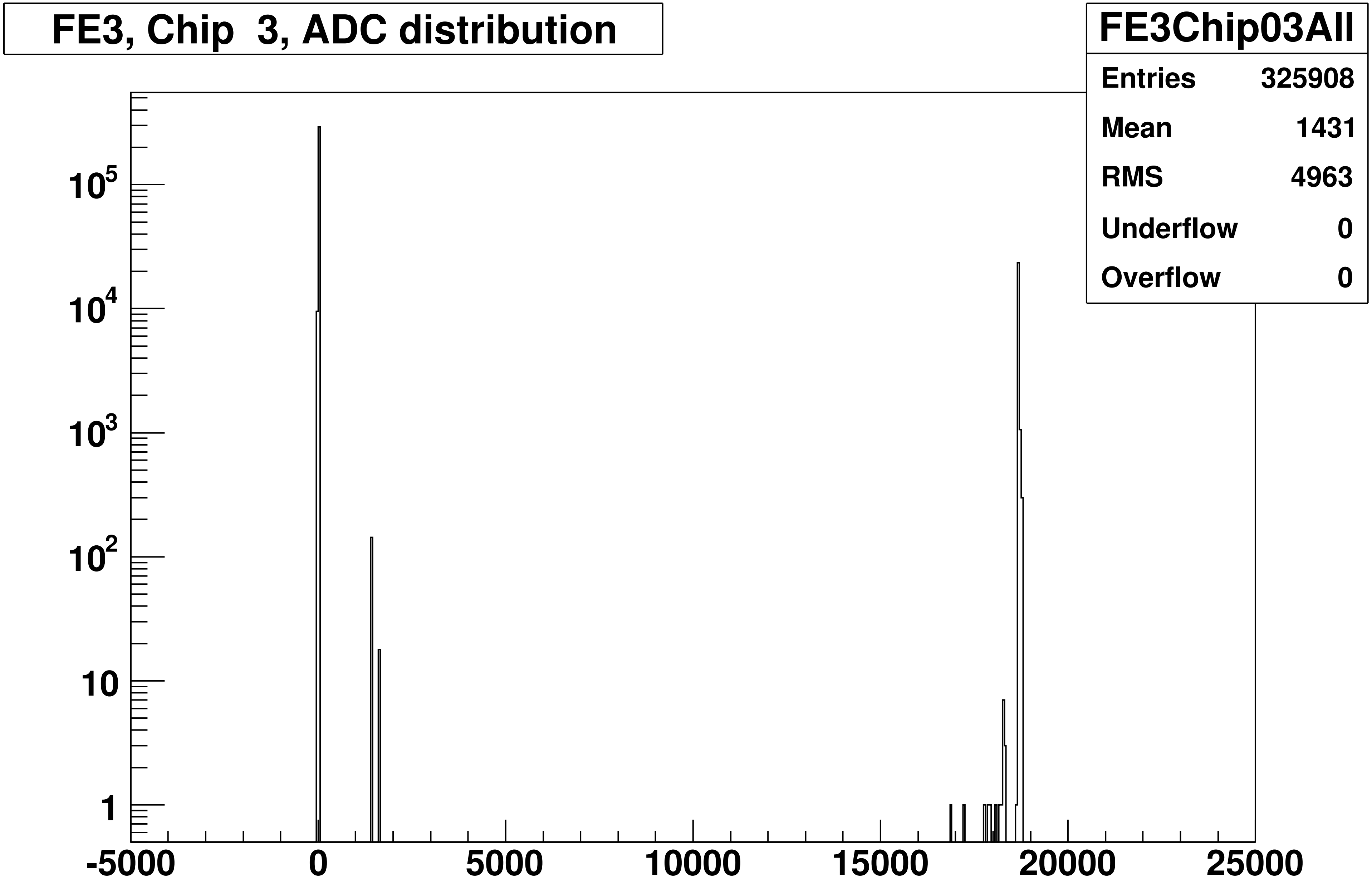
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 325908
Mean -10.07
RMS 1.25
Underflow 0
Overflow 2.474e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 325908
Mean 1.872e+04
RMS 19.58
Underflow 3.012e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 325908
Mean 1431
RMS 4963
Underflow 0
Overflow 0
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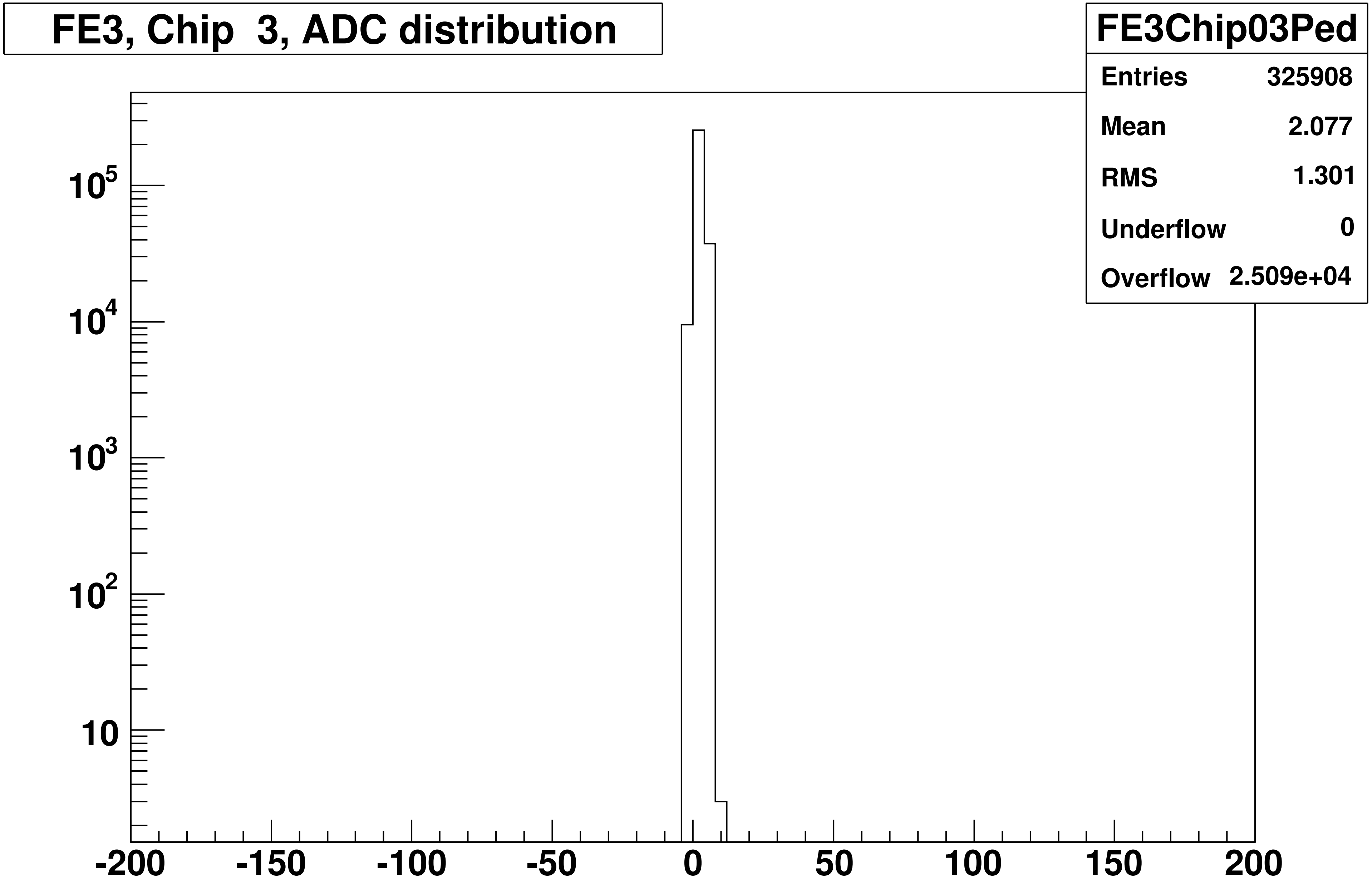
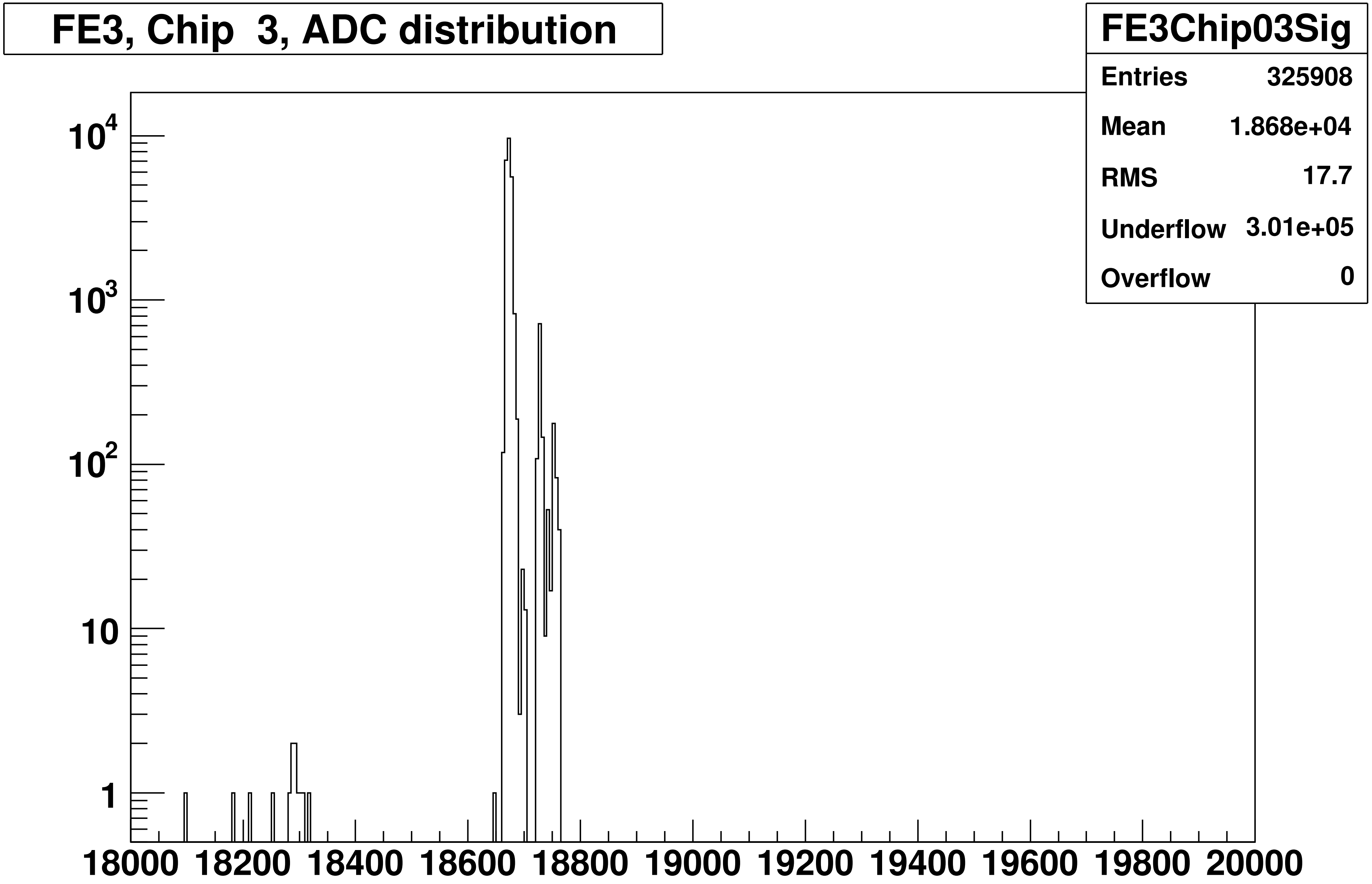
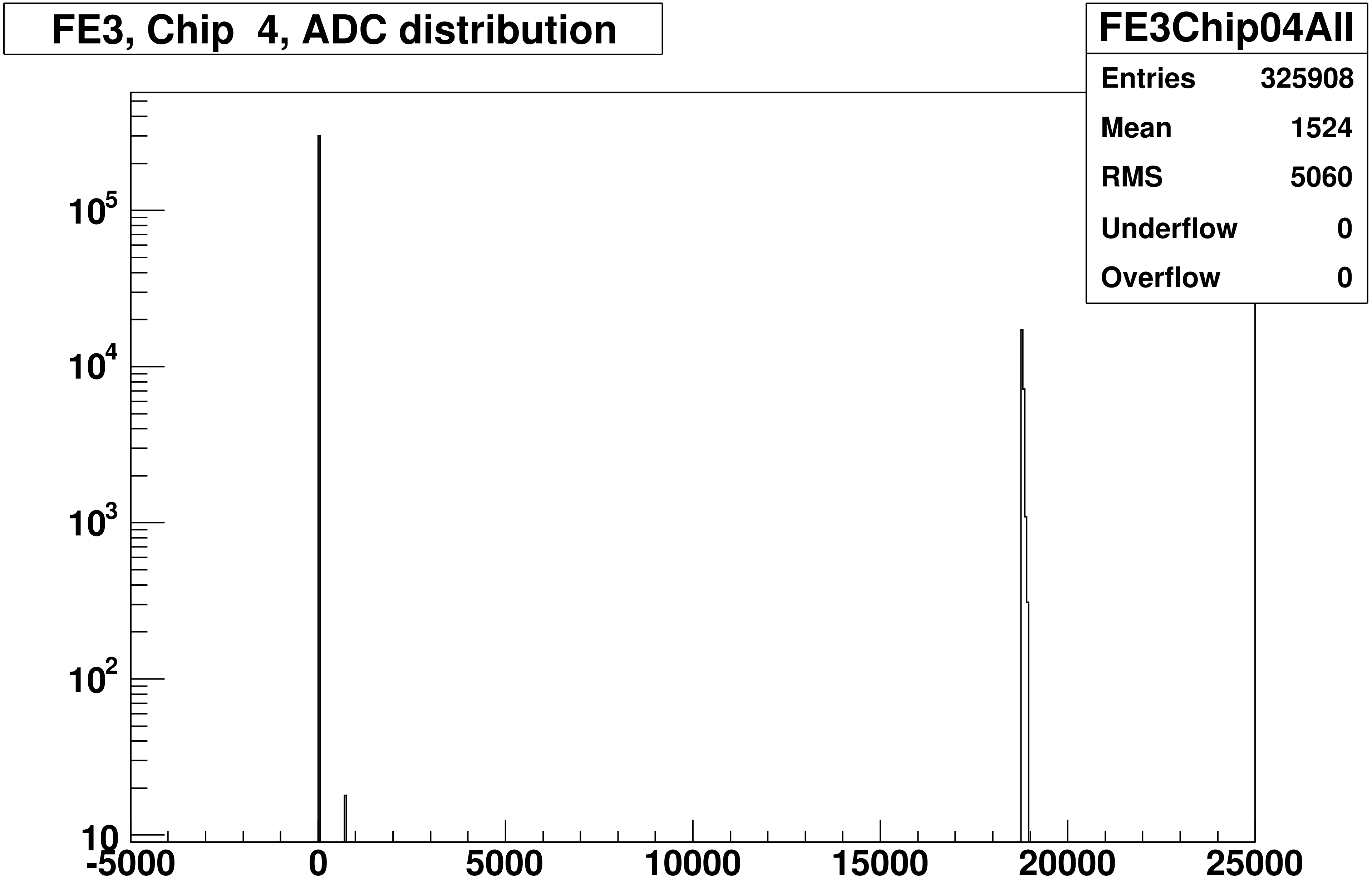
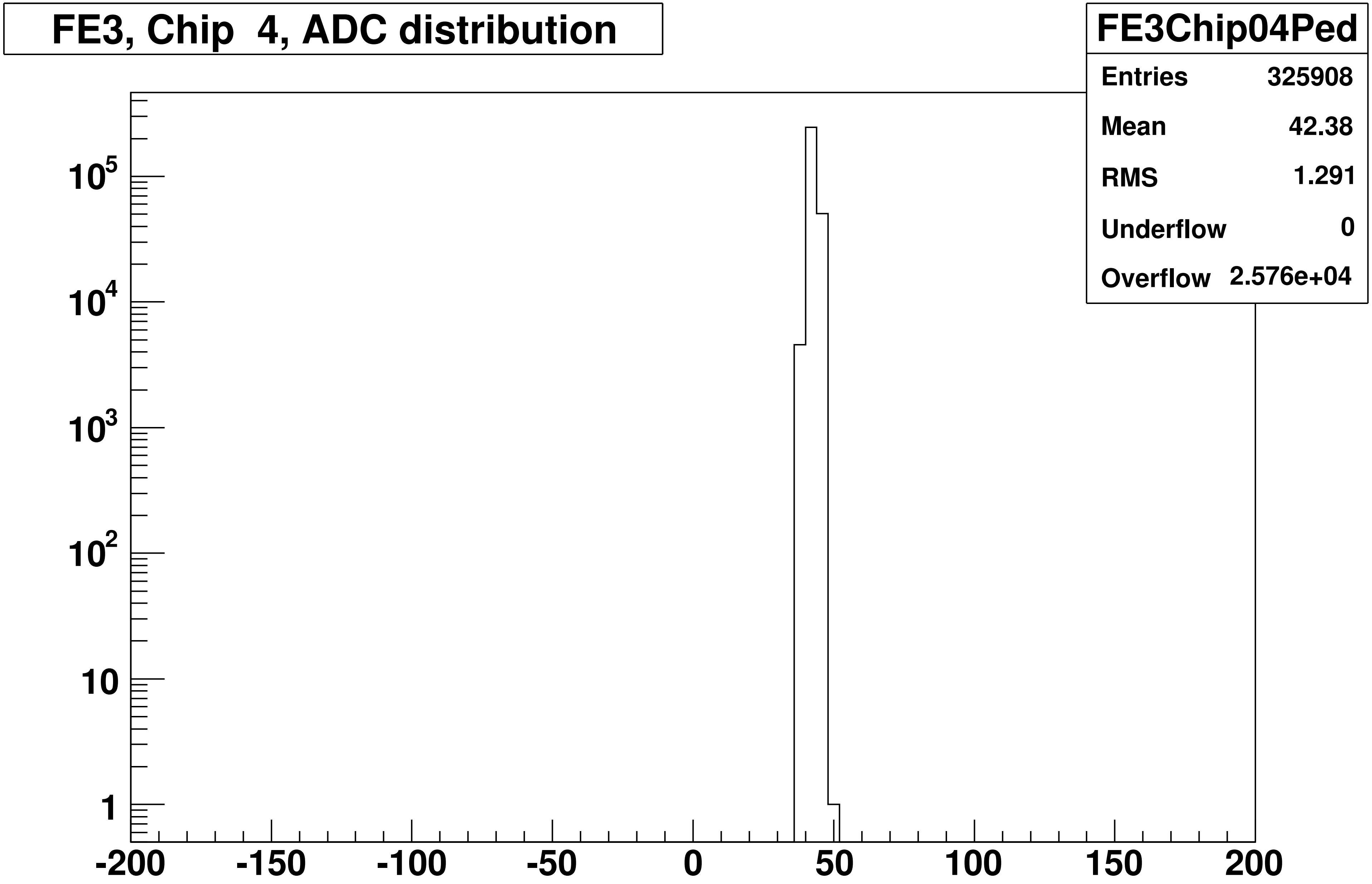
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

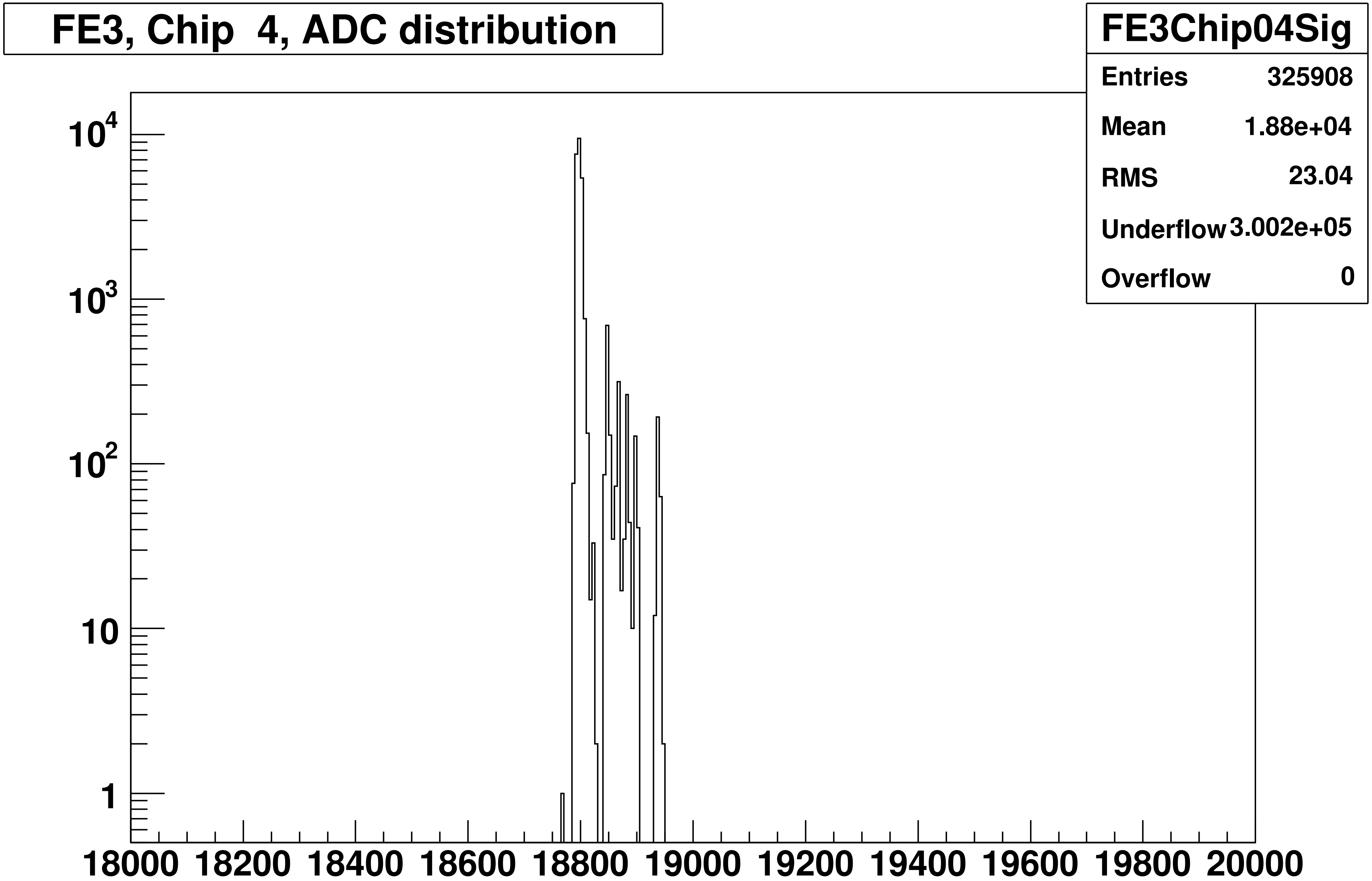

FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 325908
Mean 2.077
RMS 1.301
Underflow 0
Overflow 2.509e+04
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200



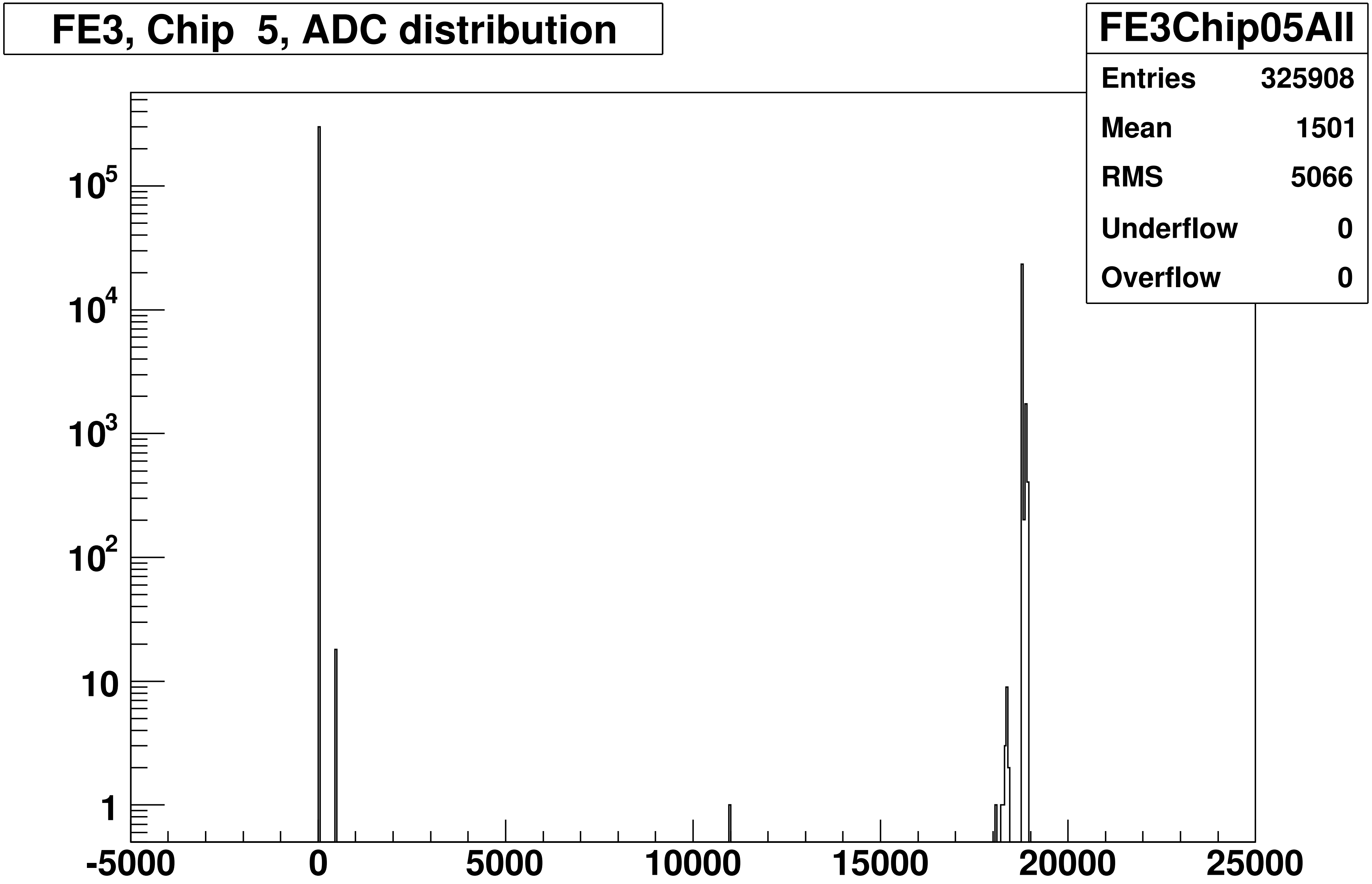
FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 325908
Mean 1.868e+04
RMS 17.7
Underflow 3.01e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 325908
Mean 1524
RMS 5060
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 325908
Mean 42.38
RMS 1.291
Underflow 0
Overflow 2.576e+04
10^5
10^4
10^3
10^2
10
1
-200 -150 -100 -50 0 50 100 150 200



FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 325908
Mean 1.88e+04
RMS 23.04
Underflow 3.002e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 325908
Mean 1501
RMS 5066
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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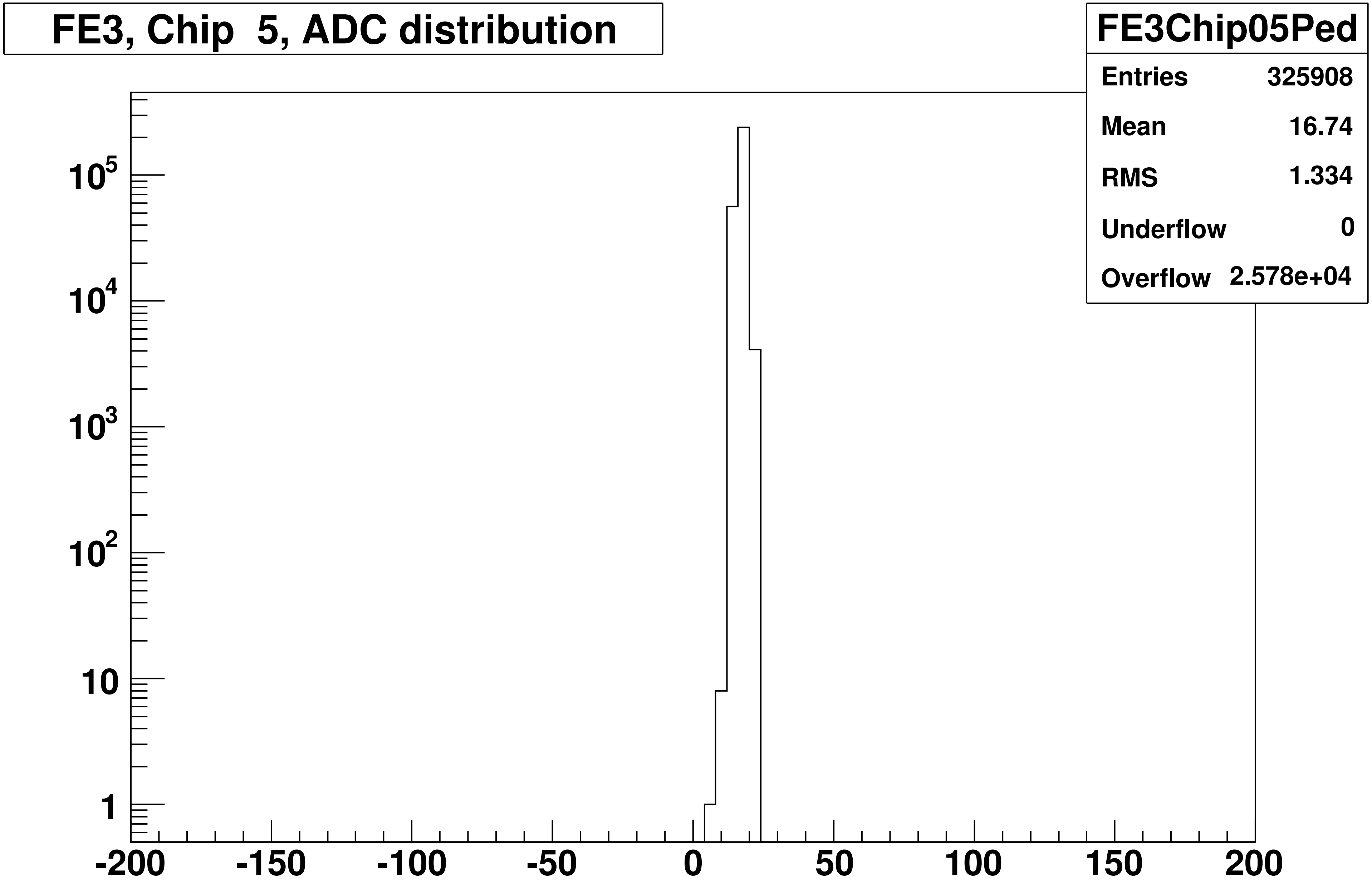
10000
15000
20000
25000



FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 325908
Mean 16.74
RMS 1.334
Underflow 0
Overflow 2.578e+04
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10^3
10^2
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-100
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150
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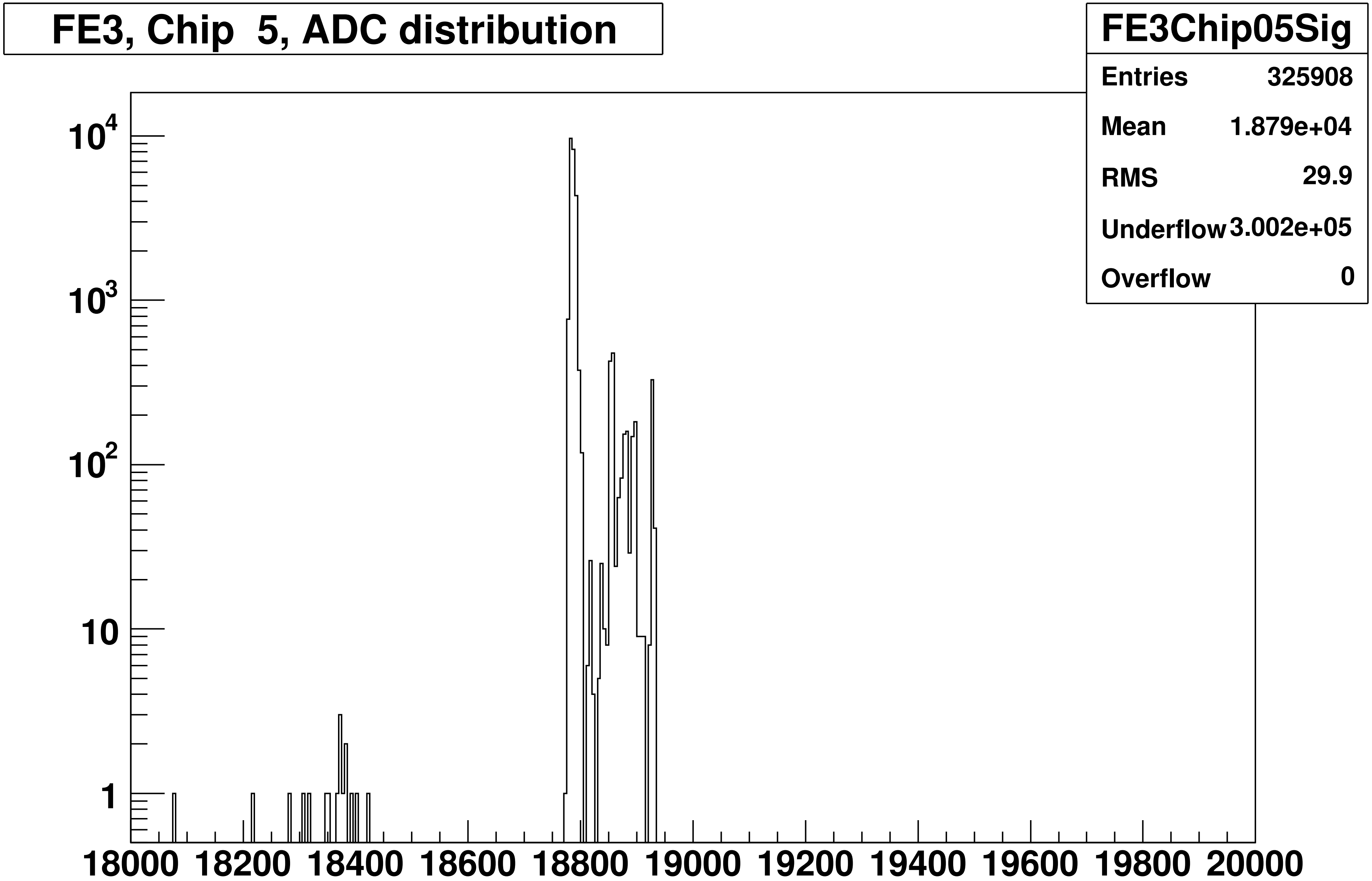
FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 325908
Mean 1.879e+04
RMS 29.9
Underflow 3.002e+05
Overflow 0
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10^2
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1
18000
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18600
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19000
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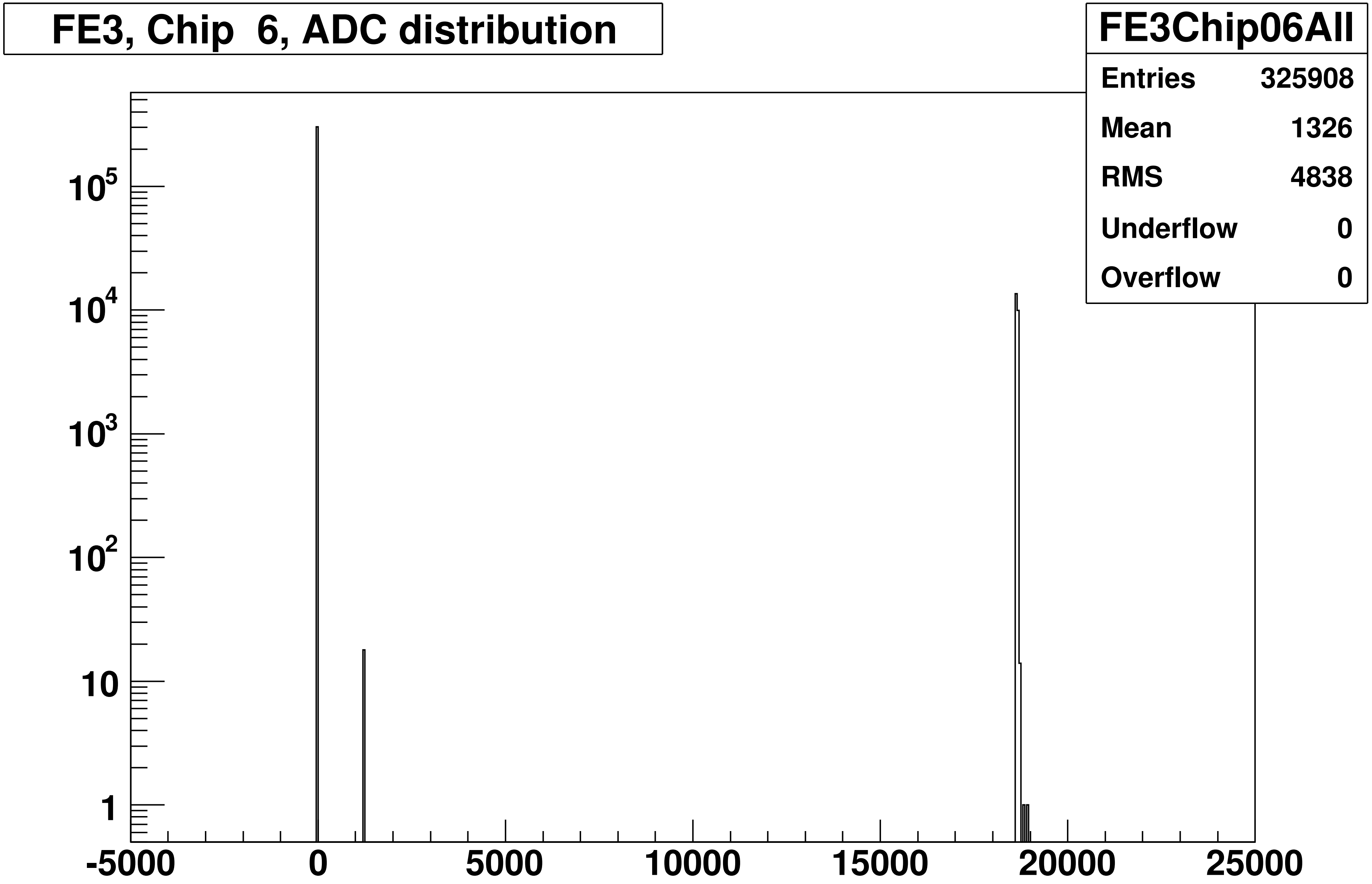
19400
19600
19800
20000


FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 325908
Mean 1326
RMS 4838
Underflow 0
Overflow 0
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10^4
10^3
10^2
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0
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10000
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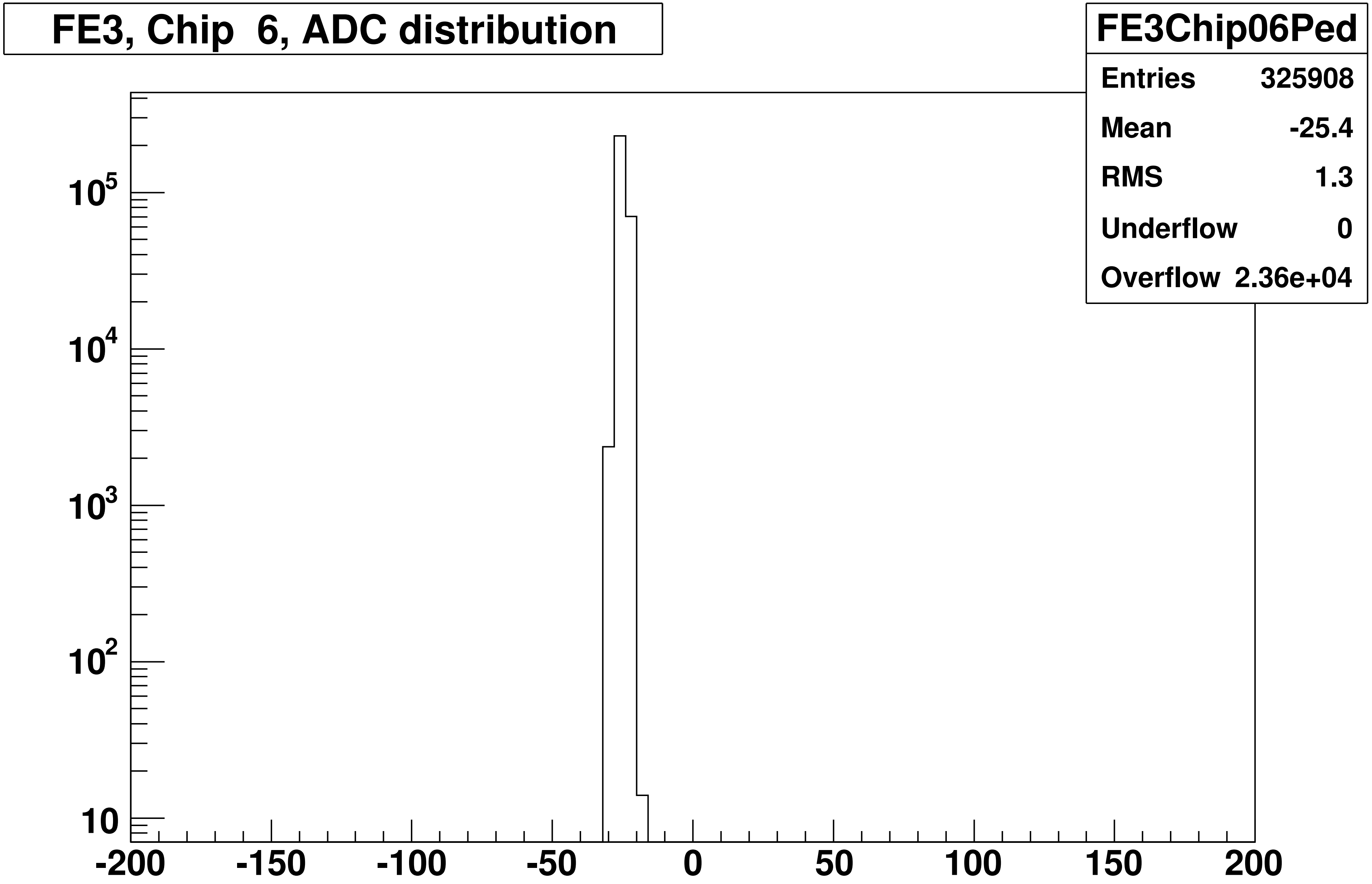

FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 325908
Mean -25.4
RMS 1.3
Underflow 0
Overflow 2.36e+04
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10^3
10^4
10^5
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150
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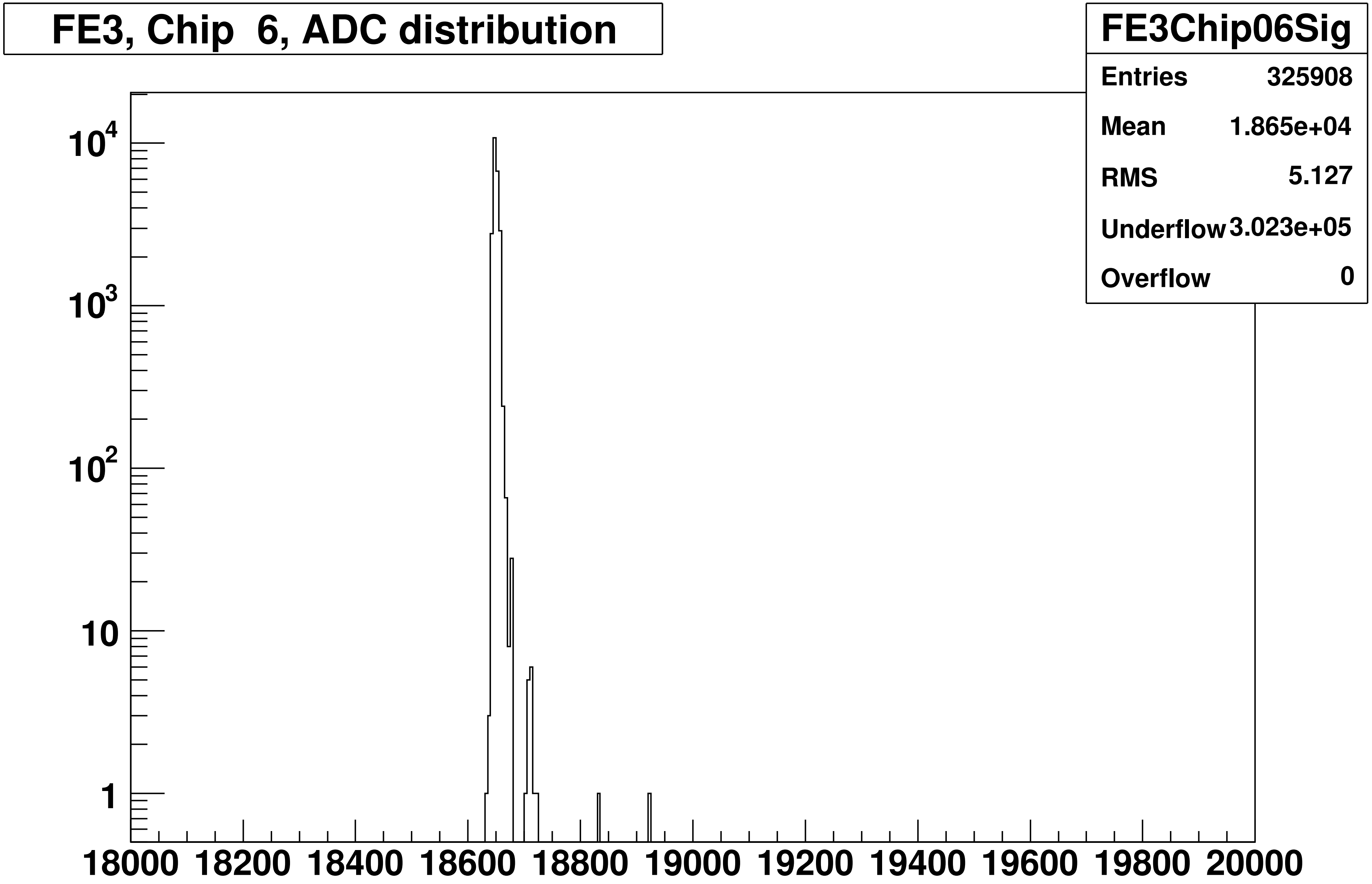

FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 325908
Mean 1.865e+04
RMS 5.127
Underflow 3.023e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
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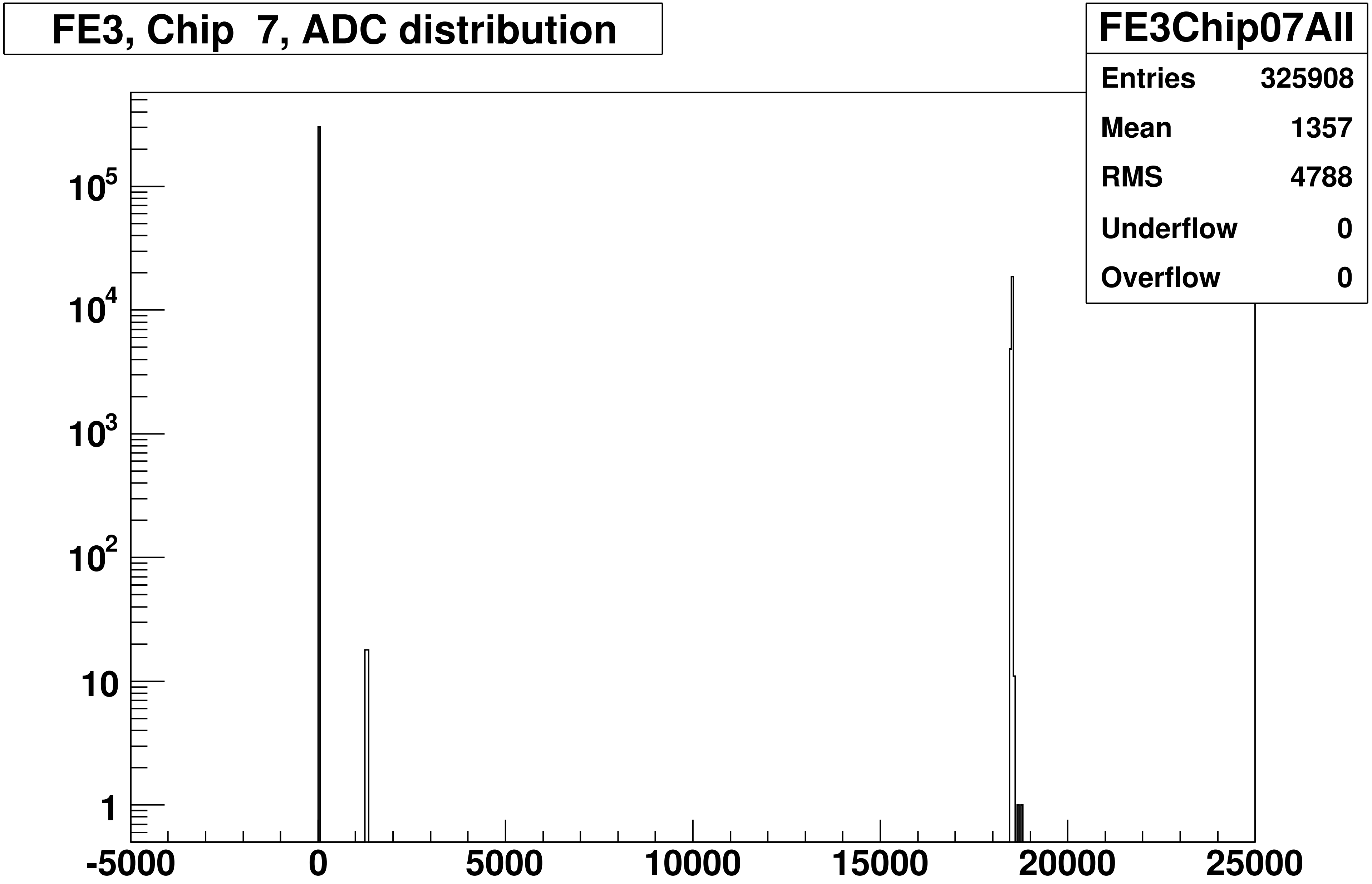
19600
19800
20000



FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 325908
Mean 1357
RMS 4788
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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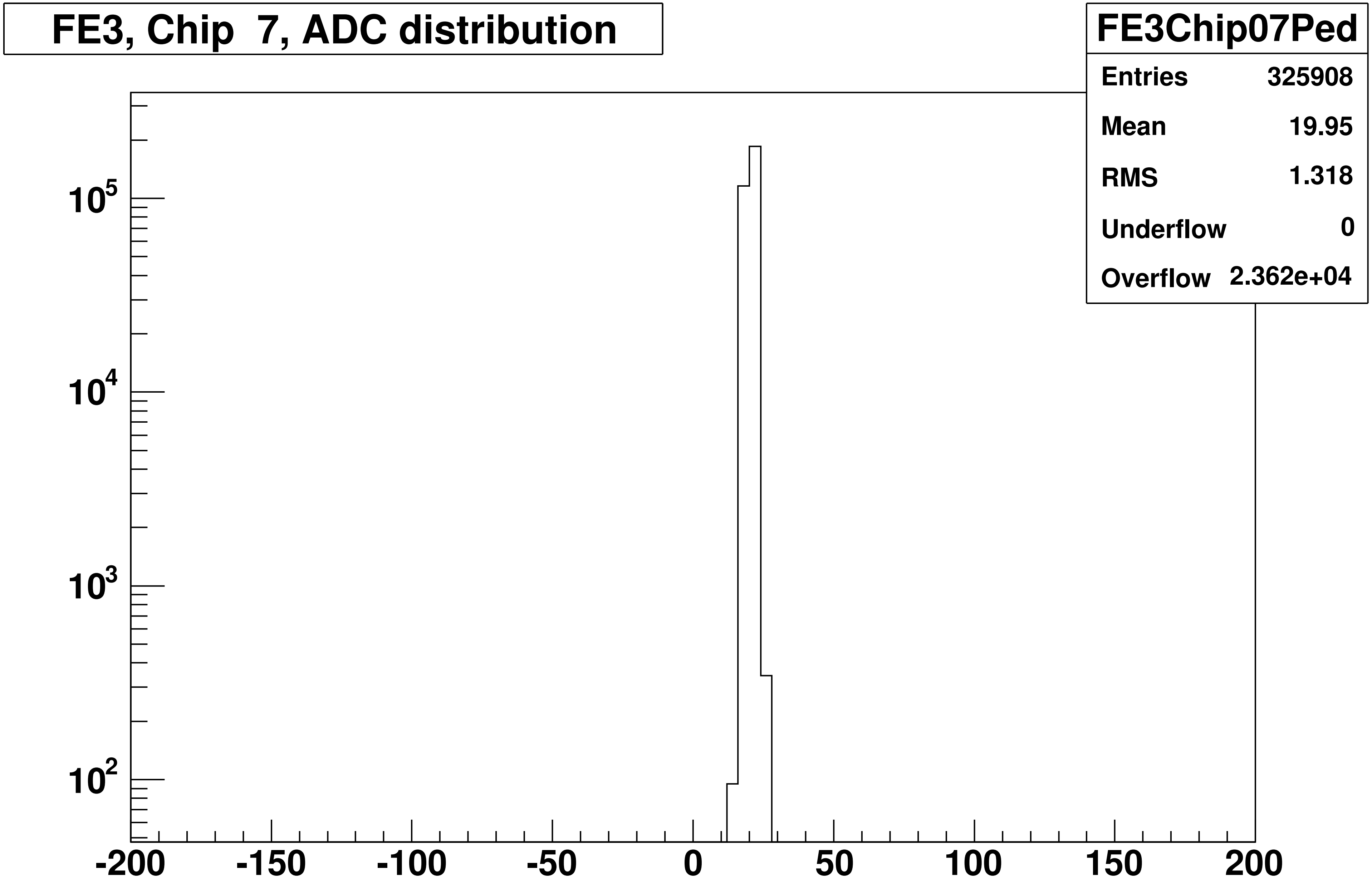
10000
15000
20000
25000


FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 325908
Mean 19.95
RMS 1.318
Underflow 0
Overflow 2.362e+04
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10^4
10^3
10^2
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100
150
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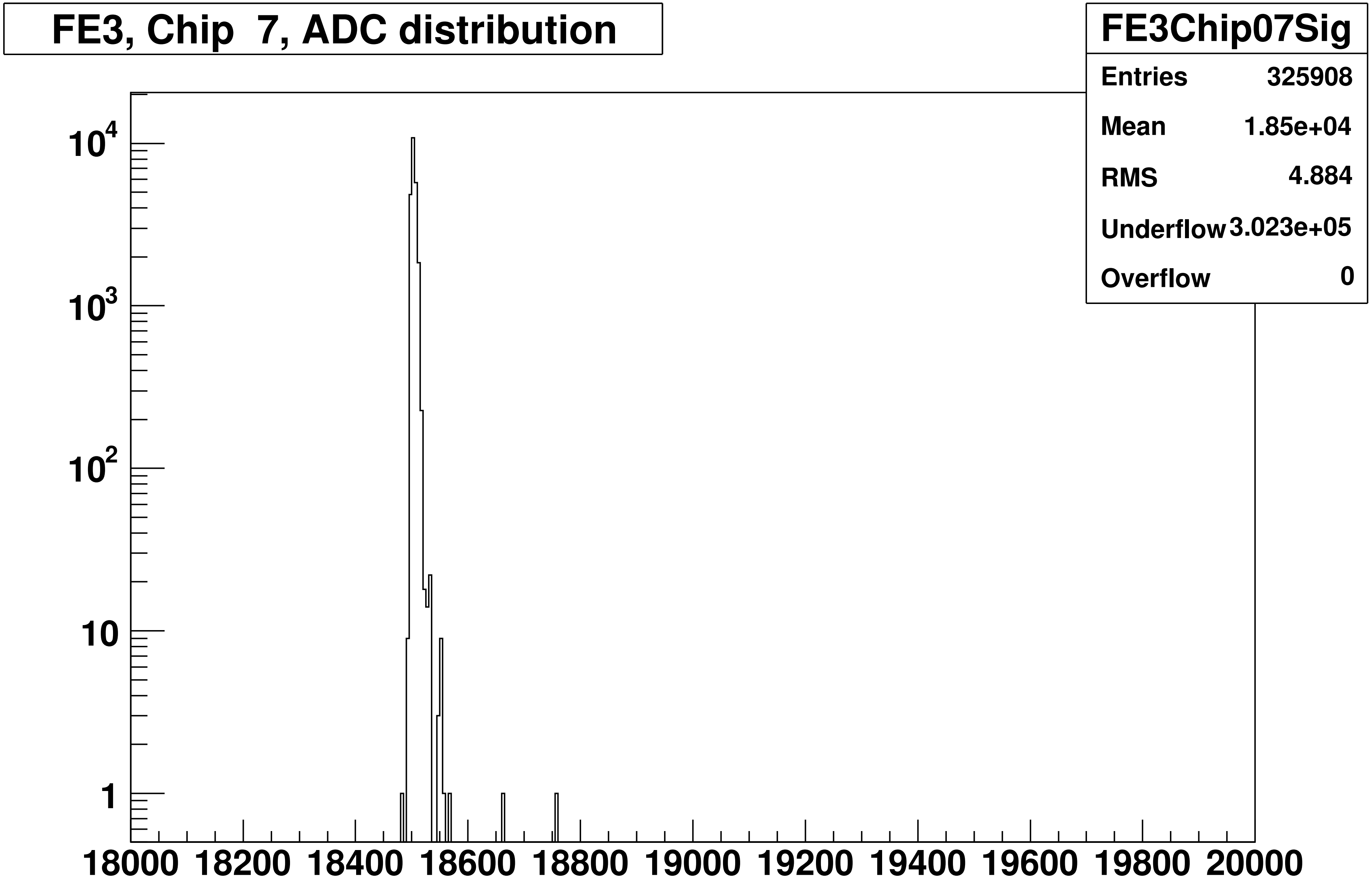
FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 325908
Mean 1.85e+04
RMS 4.884
Underflow 3.023e+05
Overflow 0
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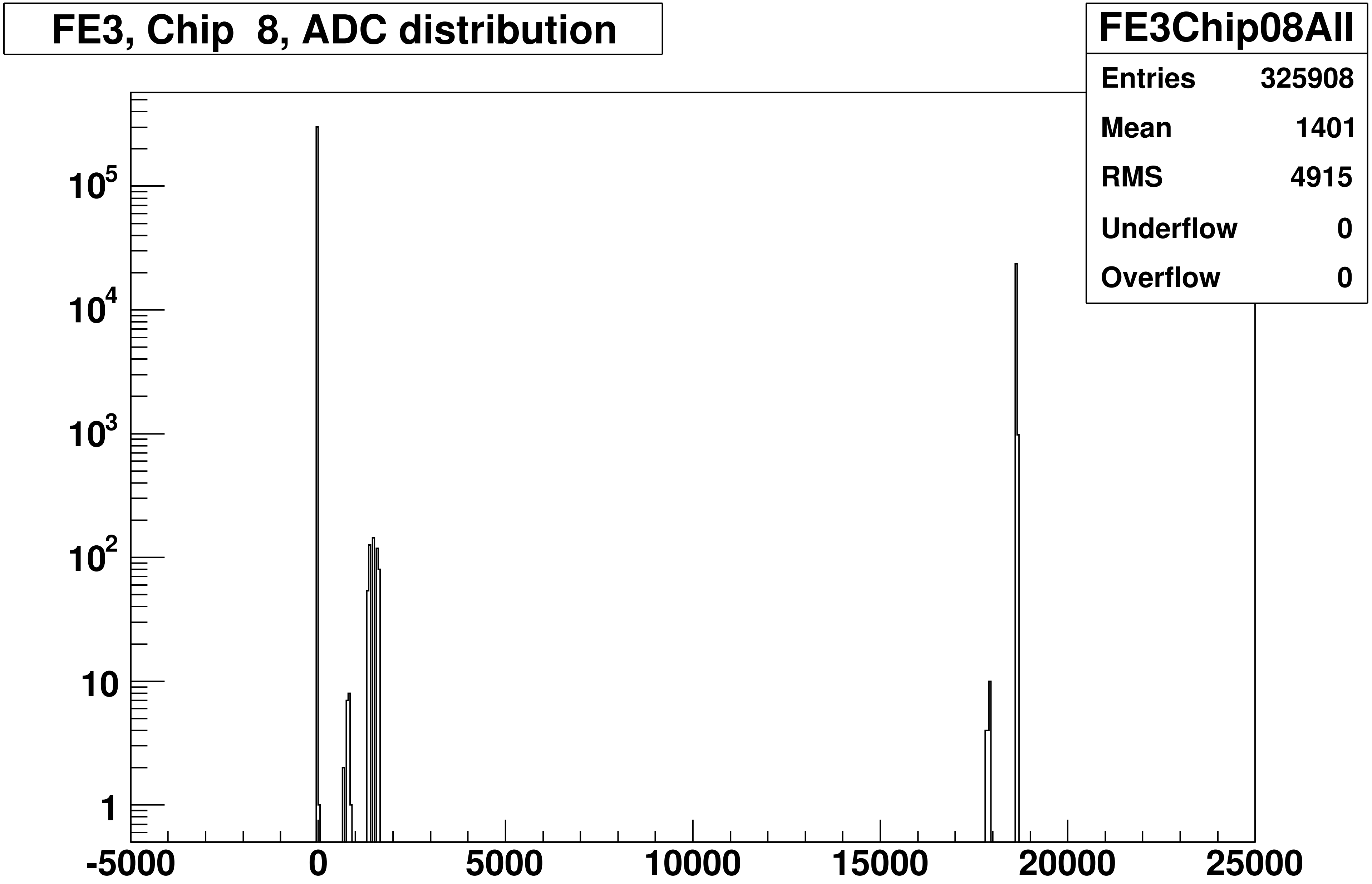
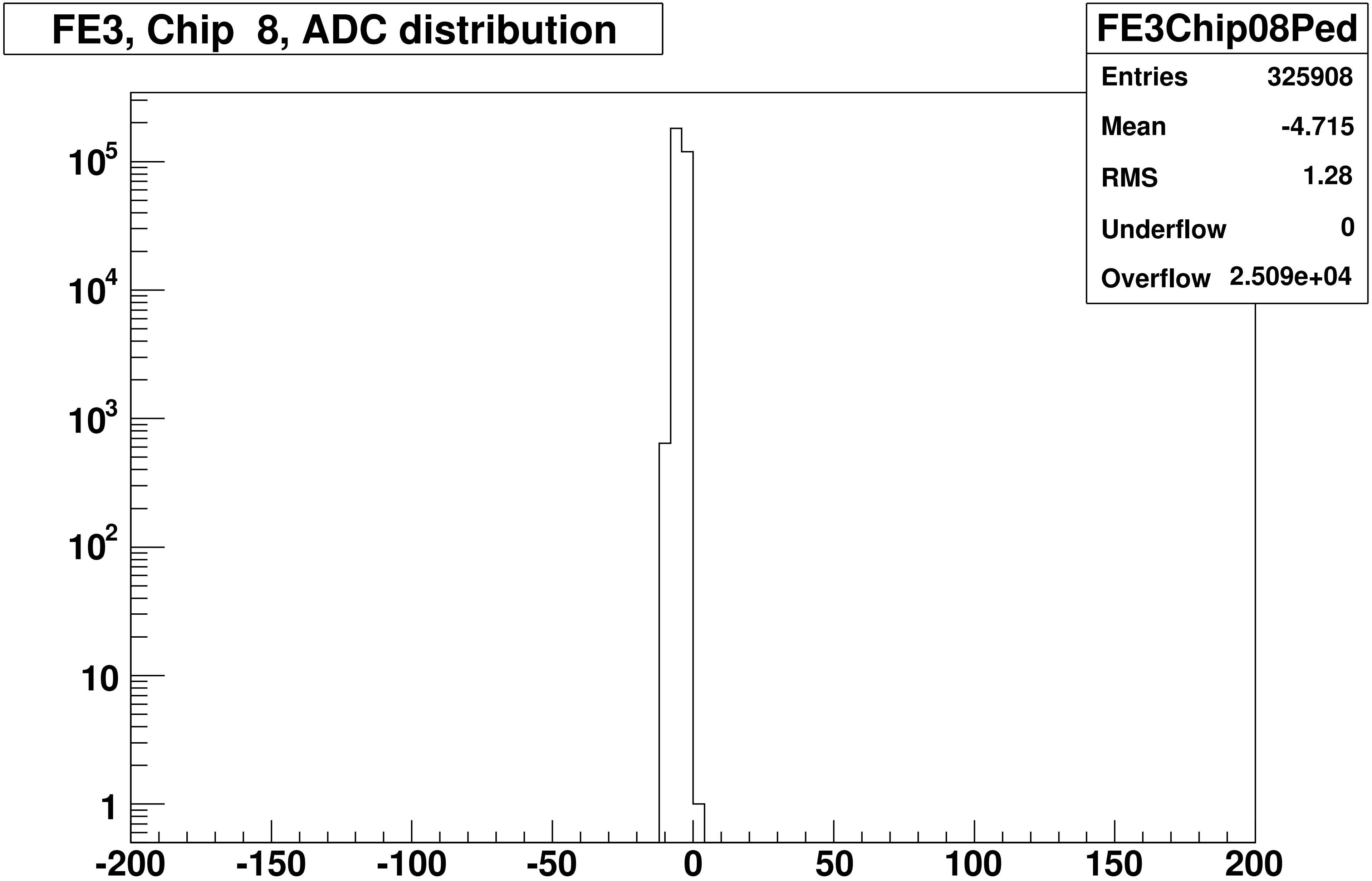
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 325908
Mean 1401
RMS 4915
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 325908
Mean -4.715
RMS 1.28
Underflow 0
Overflow 2.509e+04
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10^4
10^3
10^2
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-100
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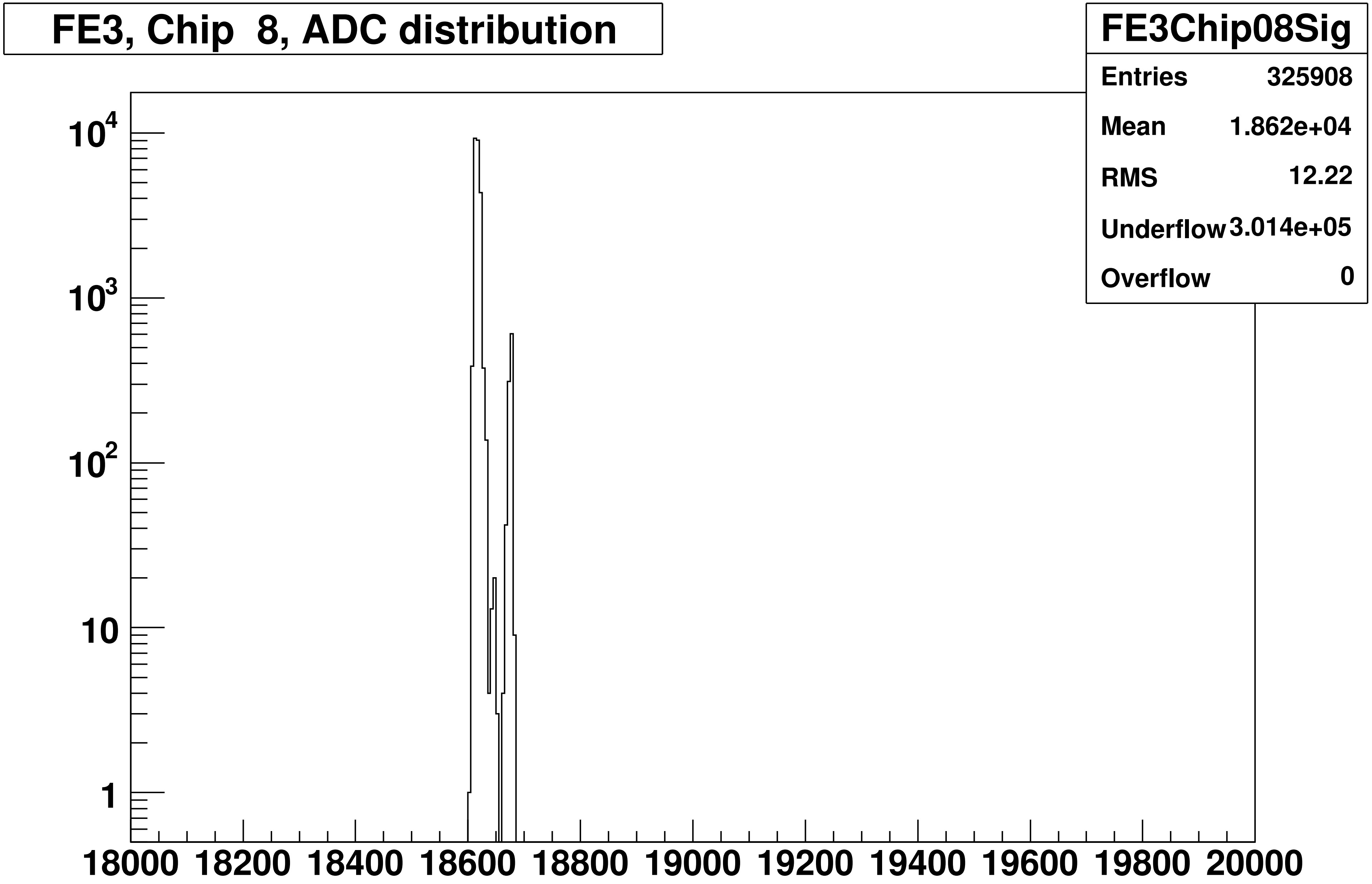
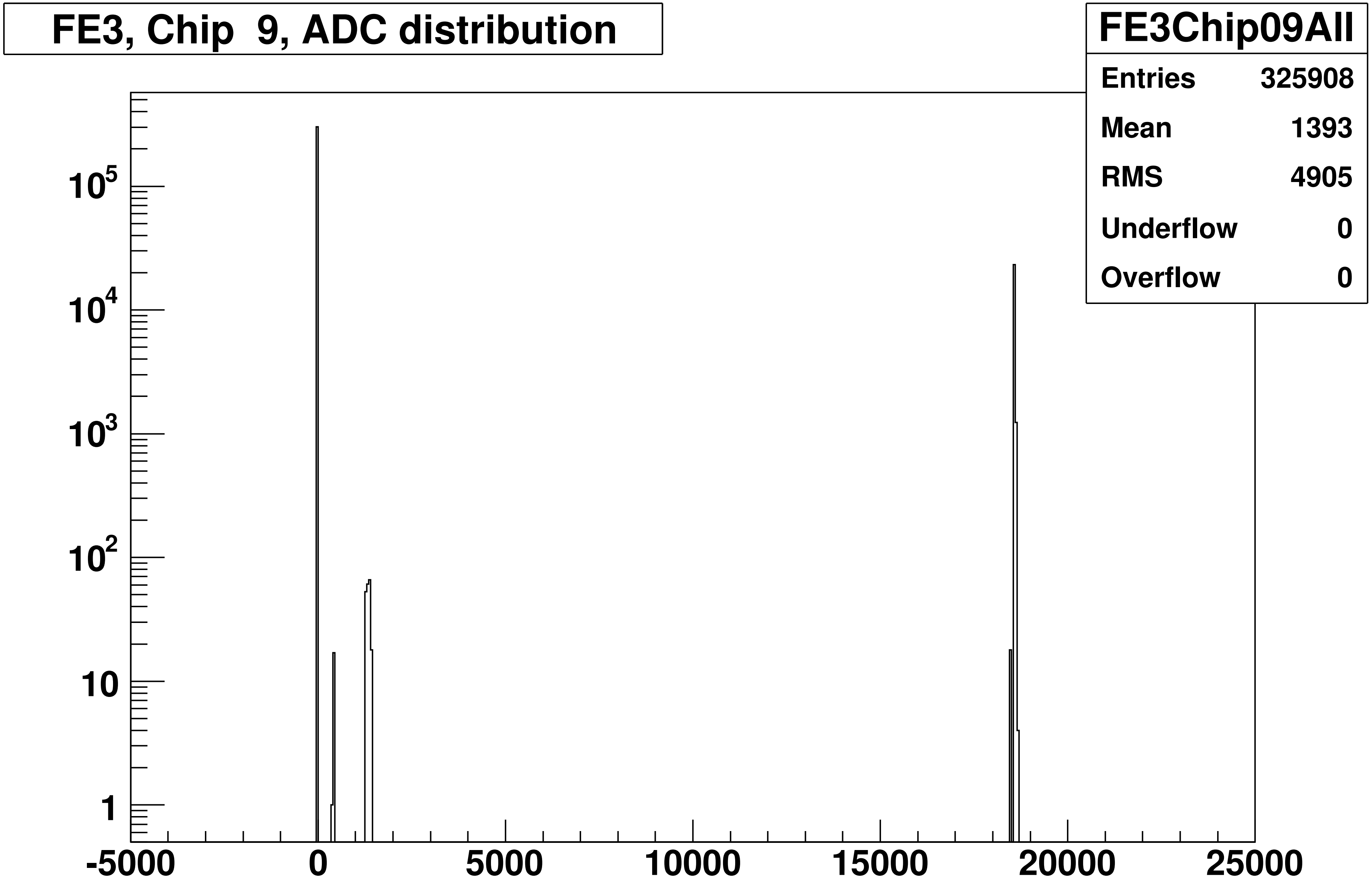
0
50
100
150
200


FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 325908
Mean 1.862e+04
RMS 12.22
Underflow 3.014e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 325908
Mean 1393
RMS 4905
Underflow 0
Overflow 0
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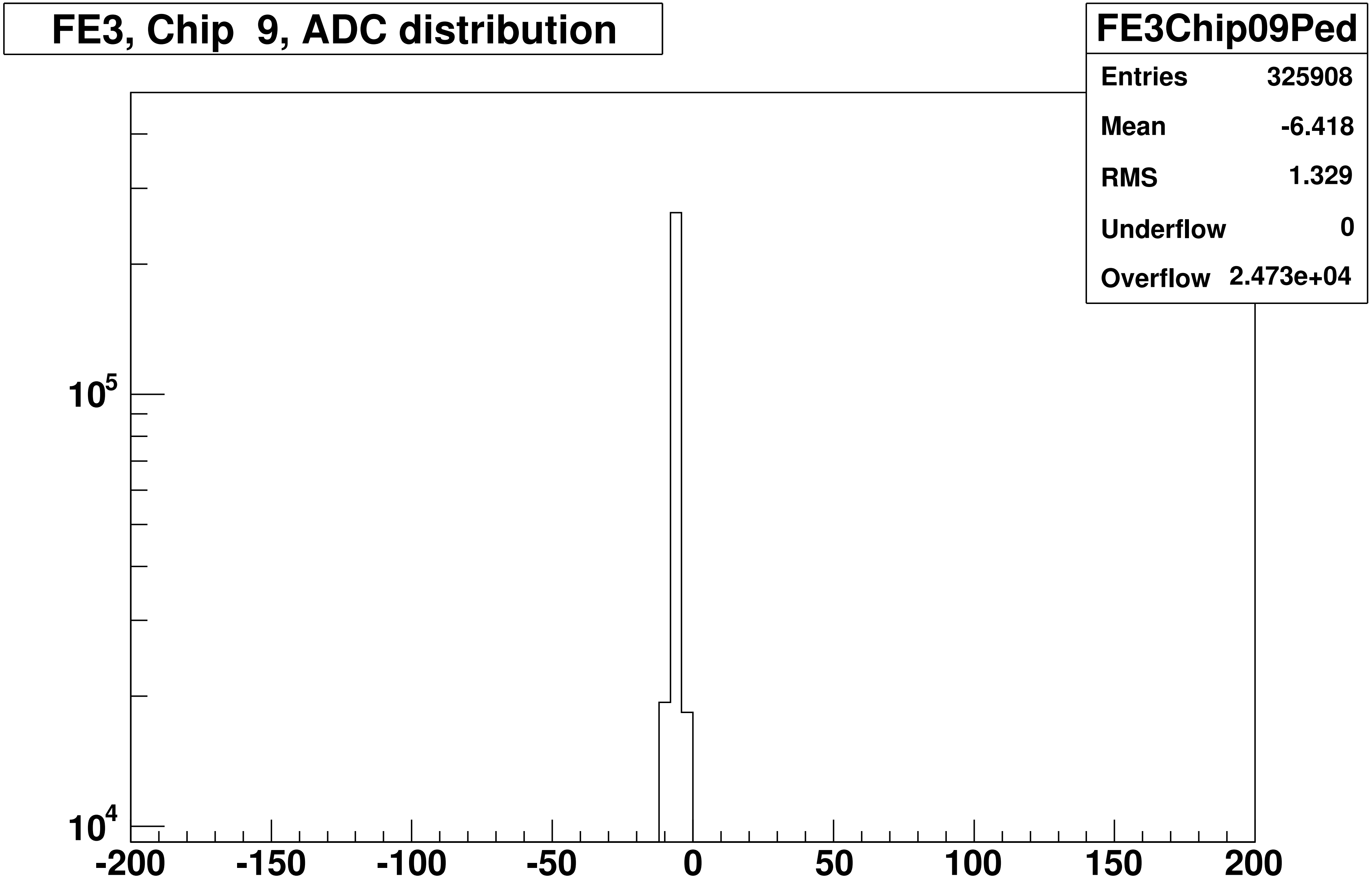
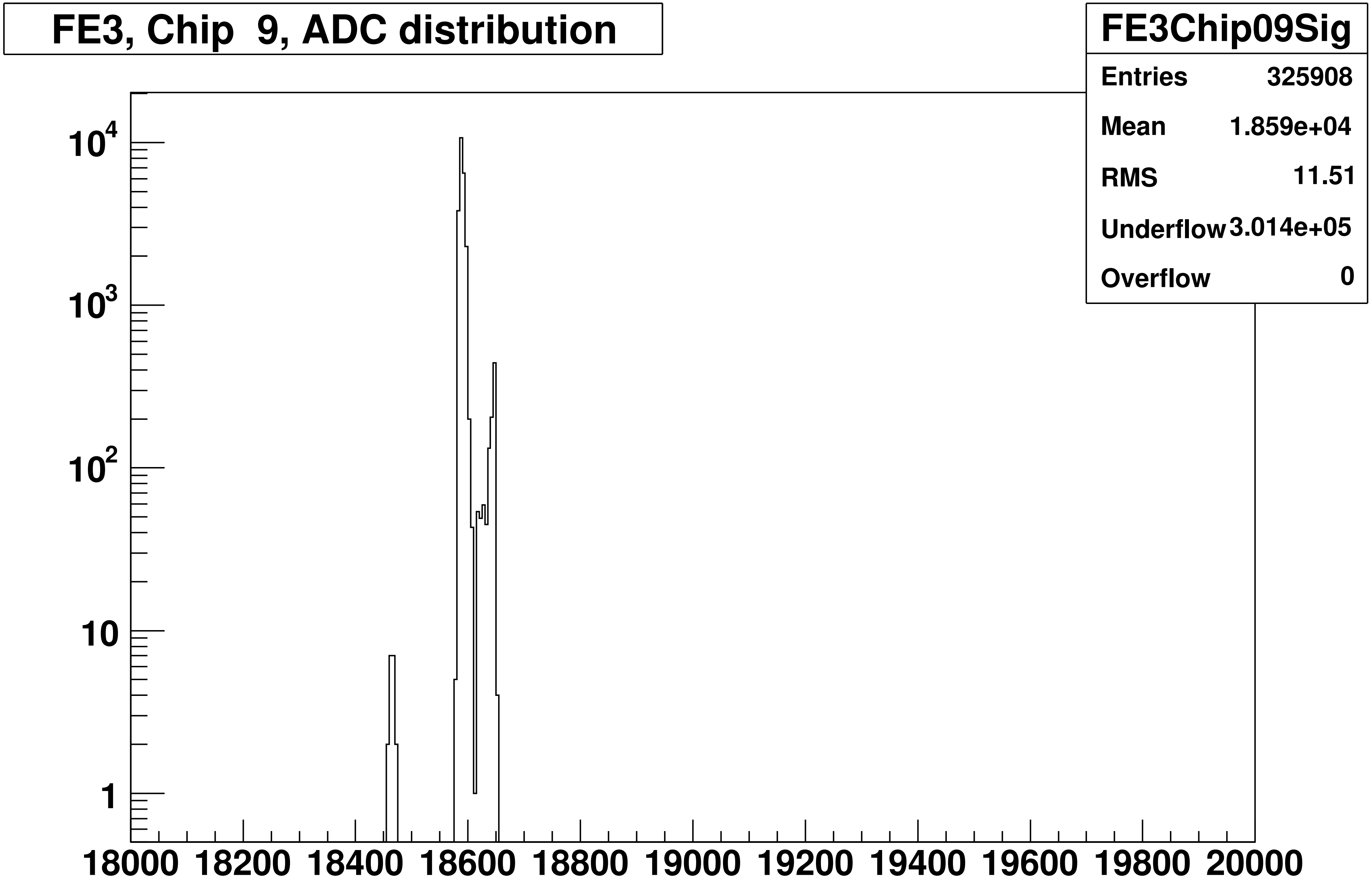
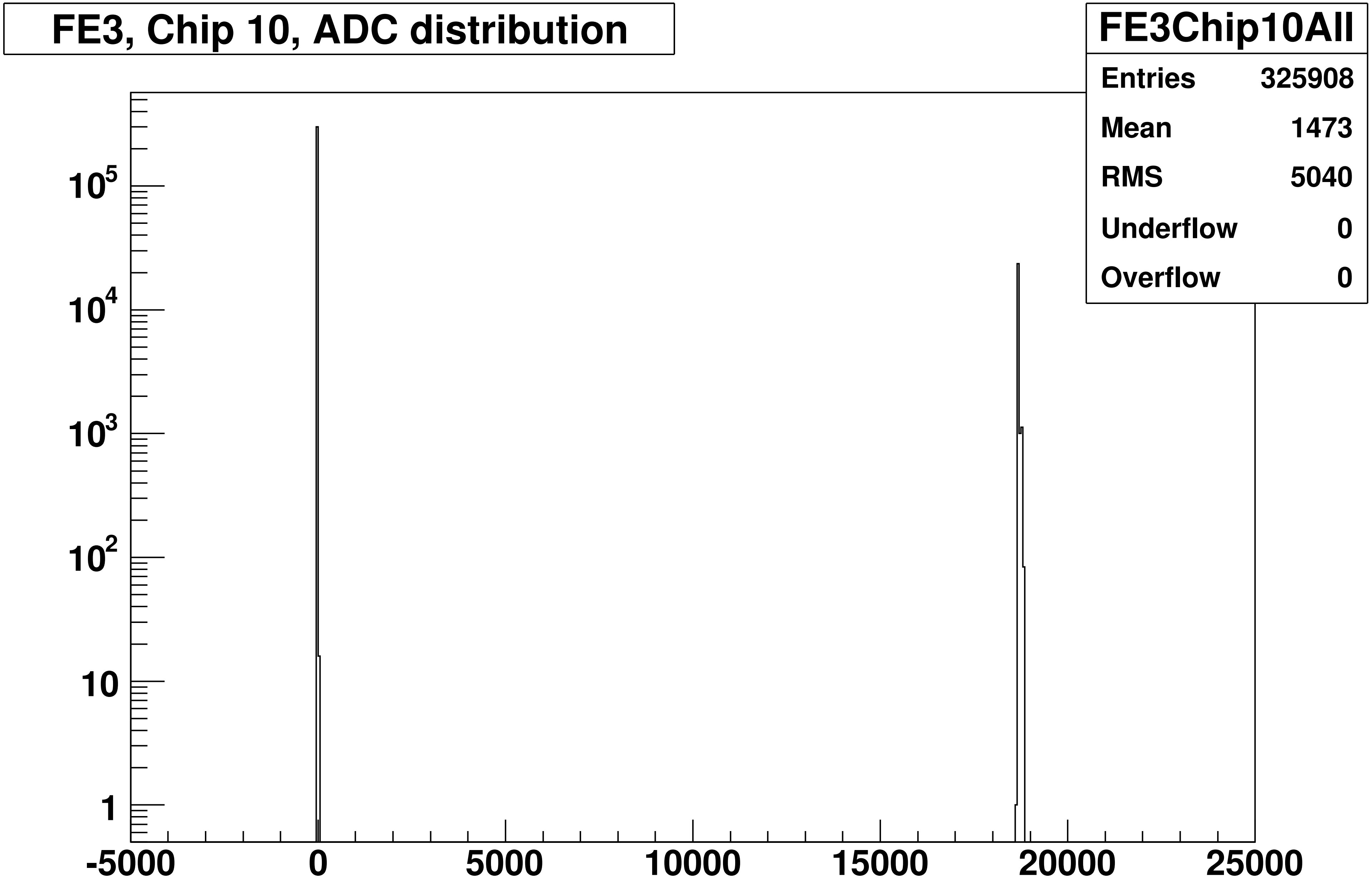
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 325908
Mean -6.418
RMS 1.329
Underflow 0
Overflow 2.473e+04
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 325908
Mean 1.859e+04
RMS 11.51
Underflow 3.014e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 325908
Mean 1473
RMS 5040
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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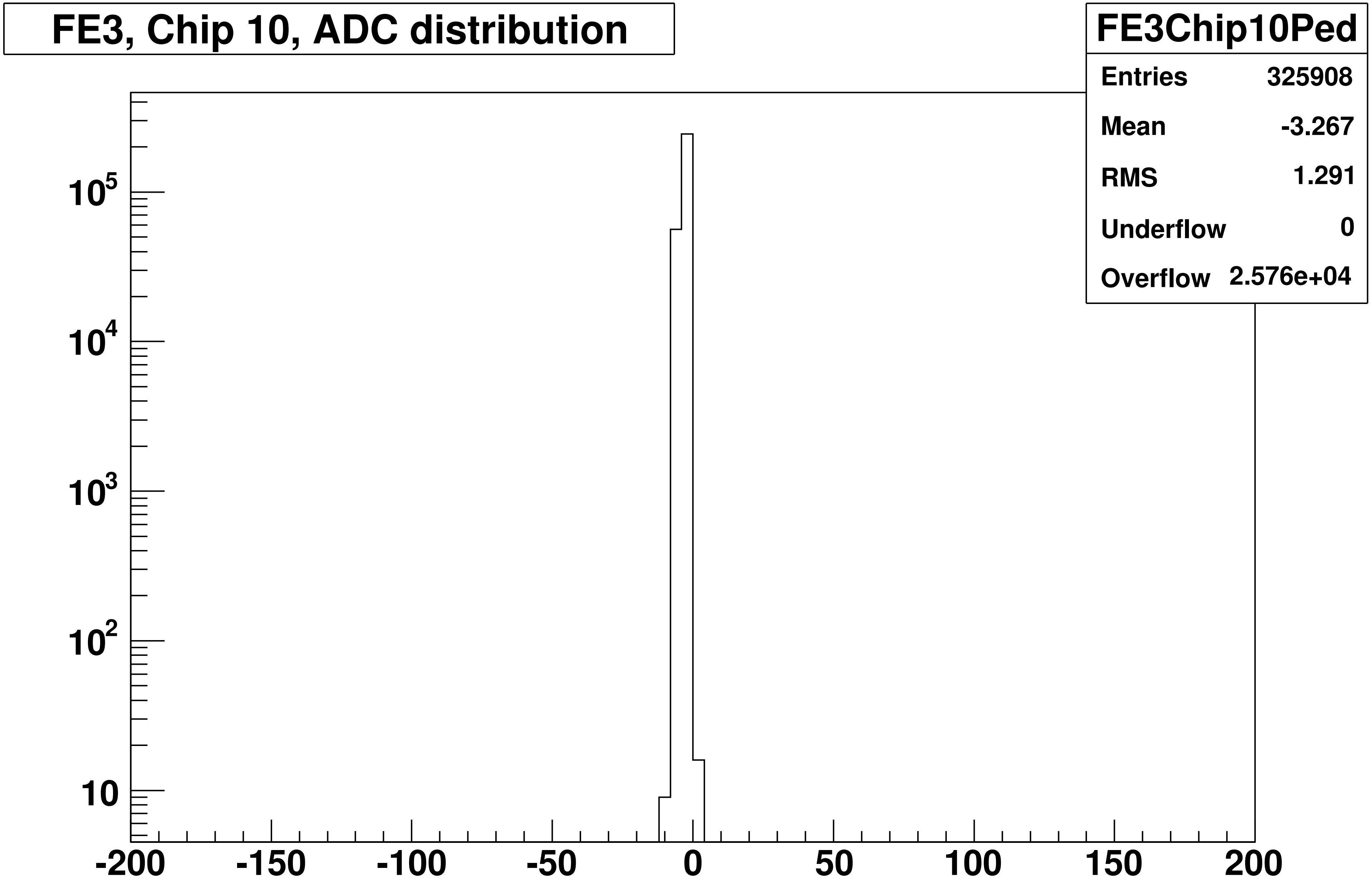
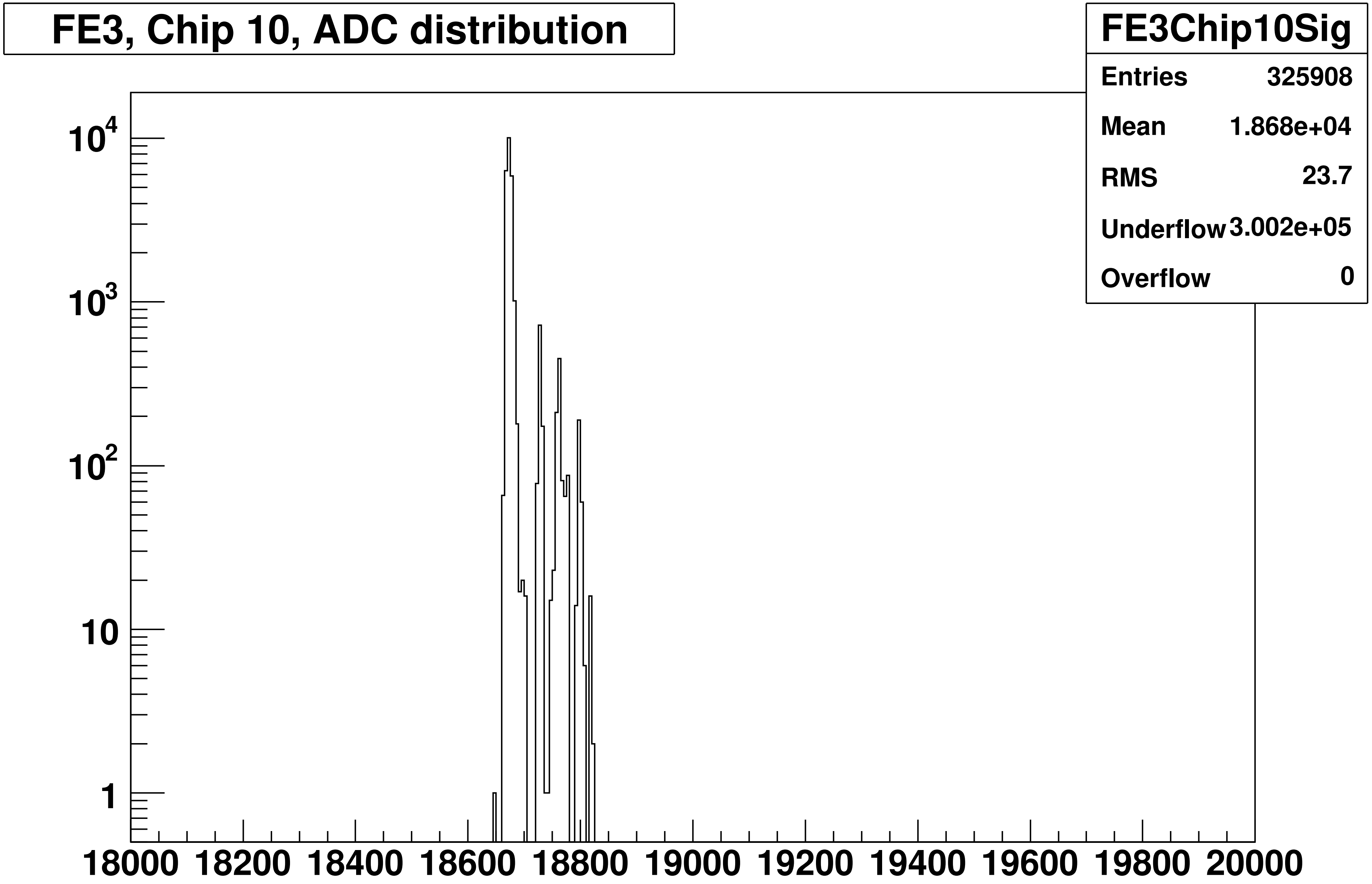
10000
15000
20000
25000


FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 325908
Mean -3.267
RMS 1.291
Underflow 0
Overflow 2.576e+04
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 325908
Mean 1.868e+04
RMS 23.7
Underflow 3.002e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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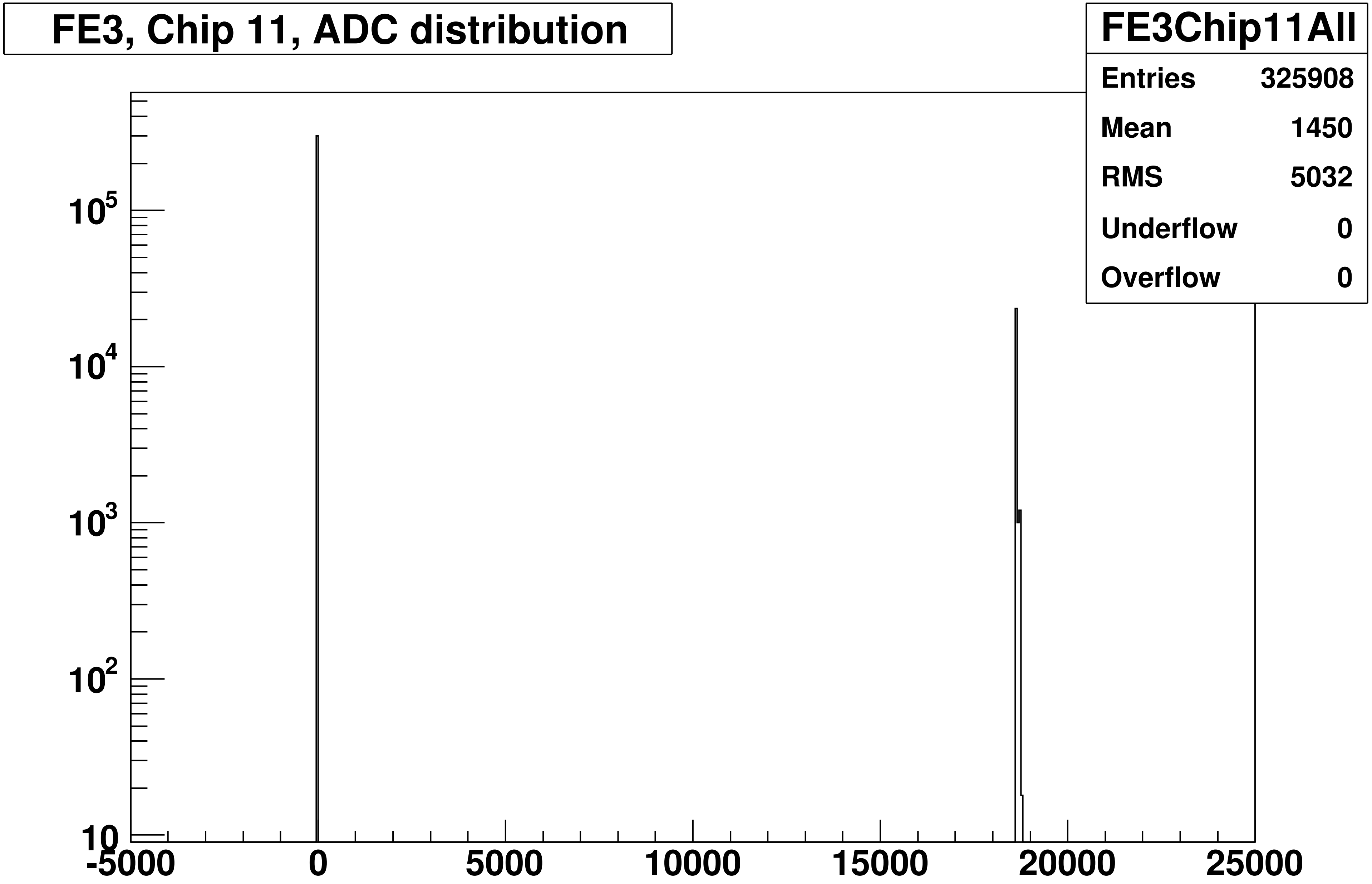
19000
19200
19400
19600
19800
20000


FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 325908
Mean 1450
RMS 5032
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
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0
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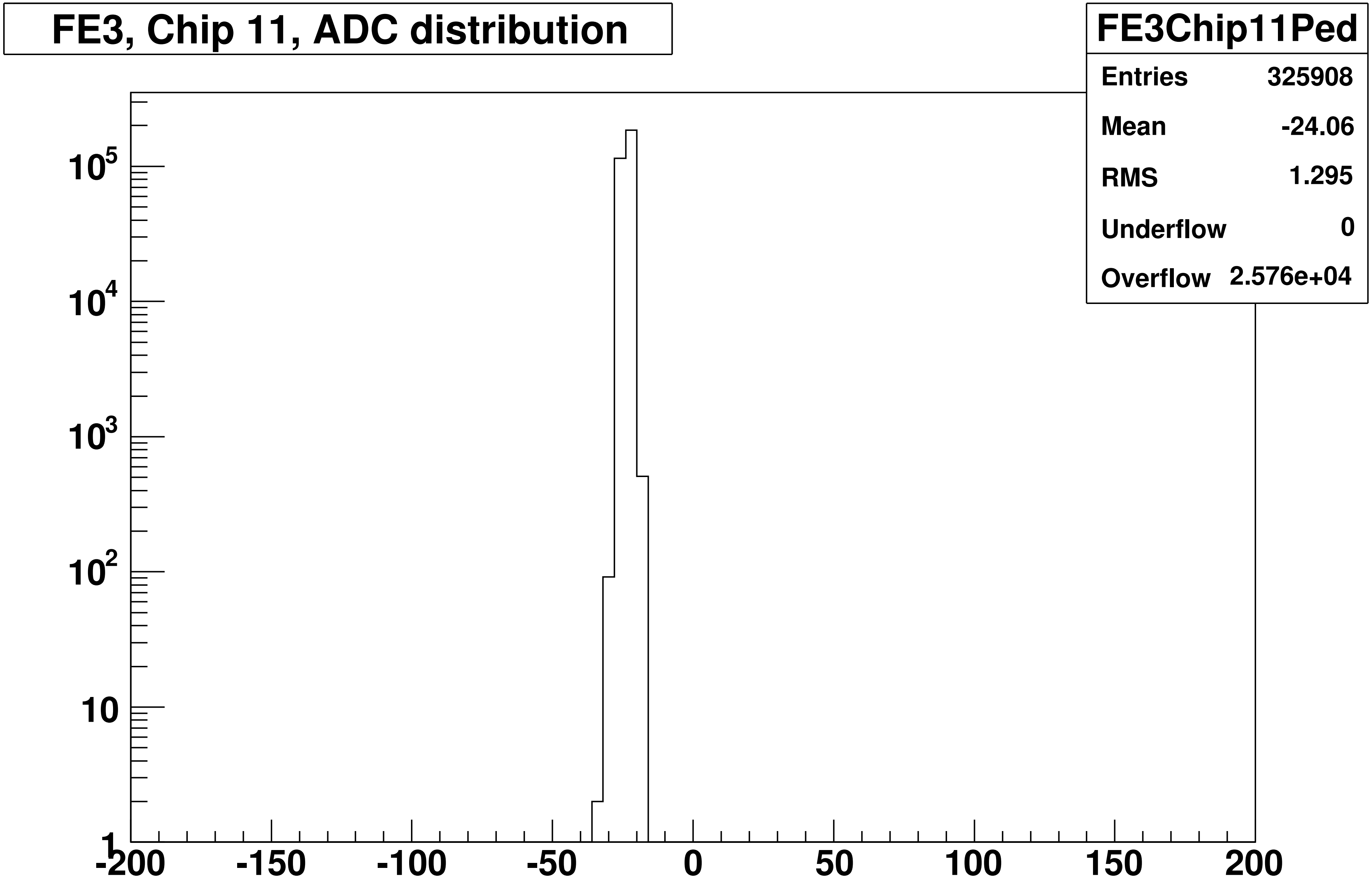
10000
15000
20000
25000


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 325908
Mean -24.06
RMS 1.295
Underflow 0
Overflow 2.576e+04
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10^4
10^3
10^2
10
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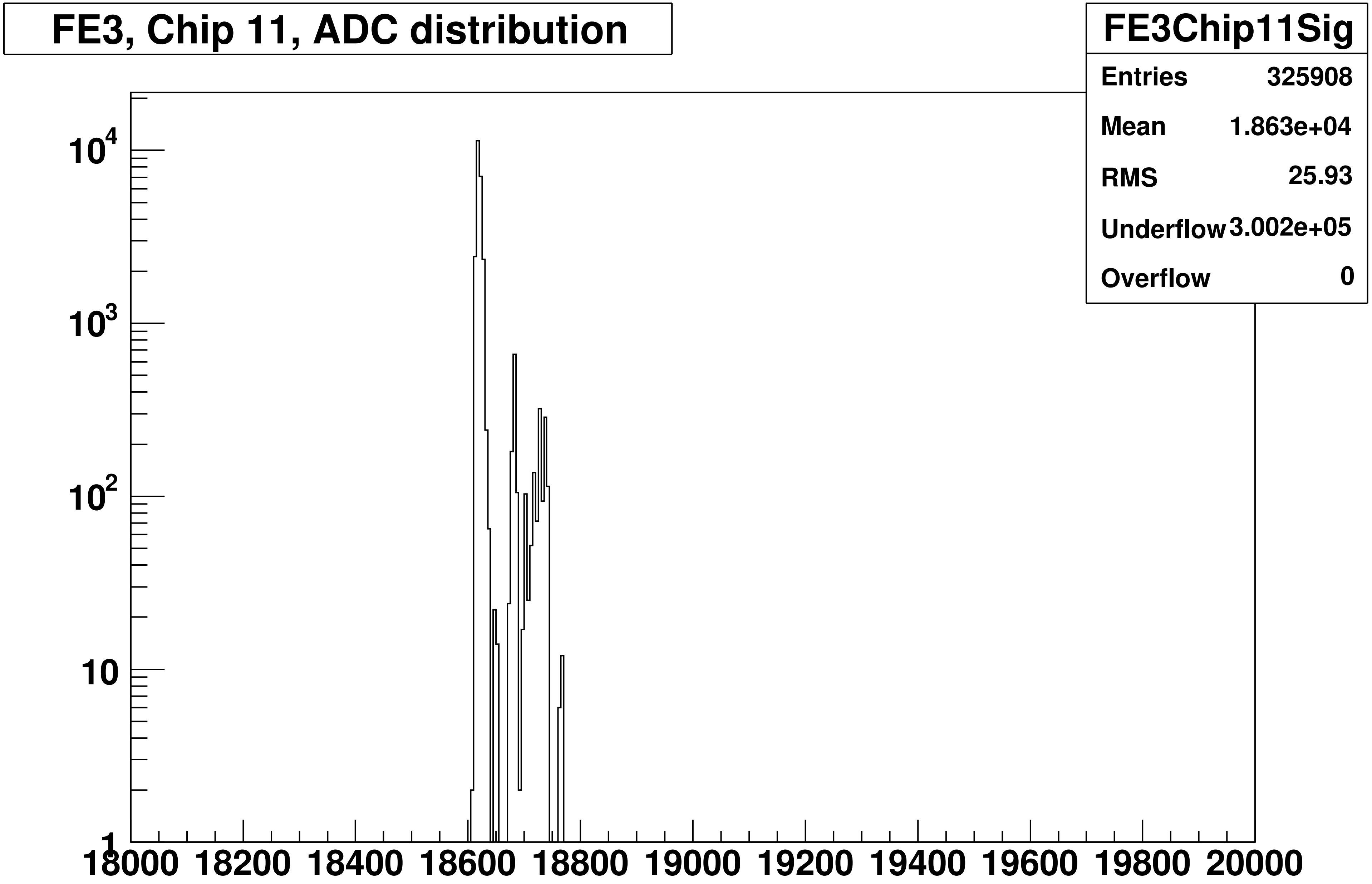
100
150
200


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 325908
Mean 1.863e+04
RMS 25.93
Underflow 3.002e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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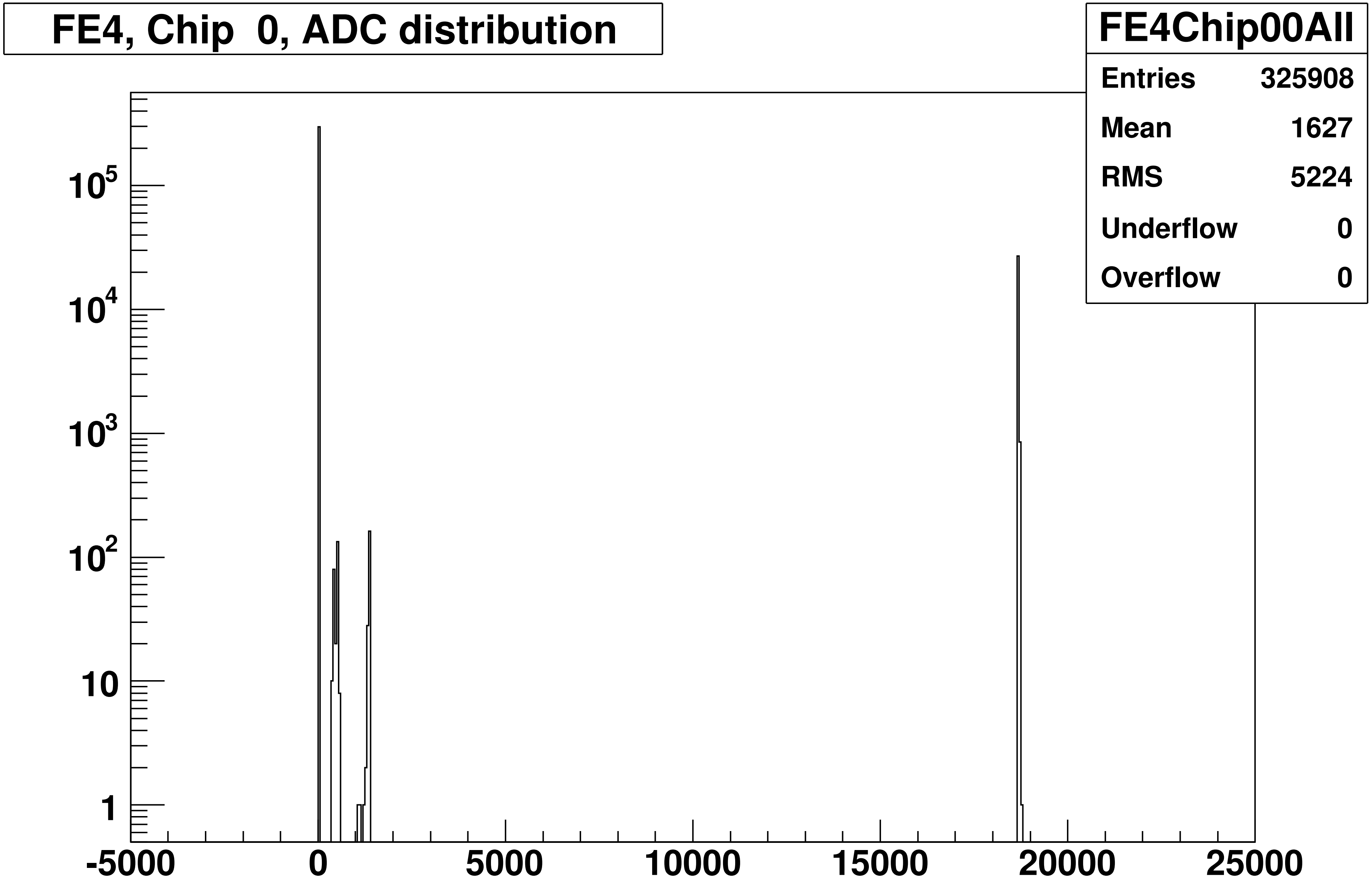
19000
19200
19400
19600
19800
20000



FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 325908
Mean 1627
RMS 5224
Underflow 0
Overflow 0
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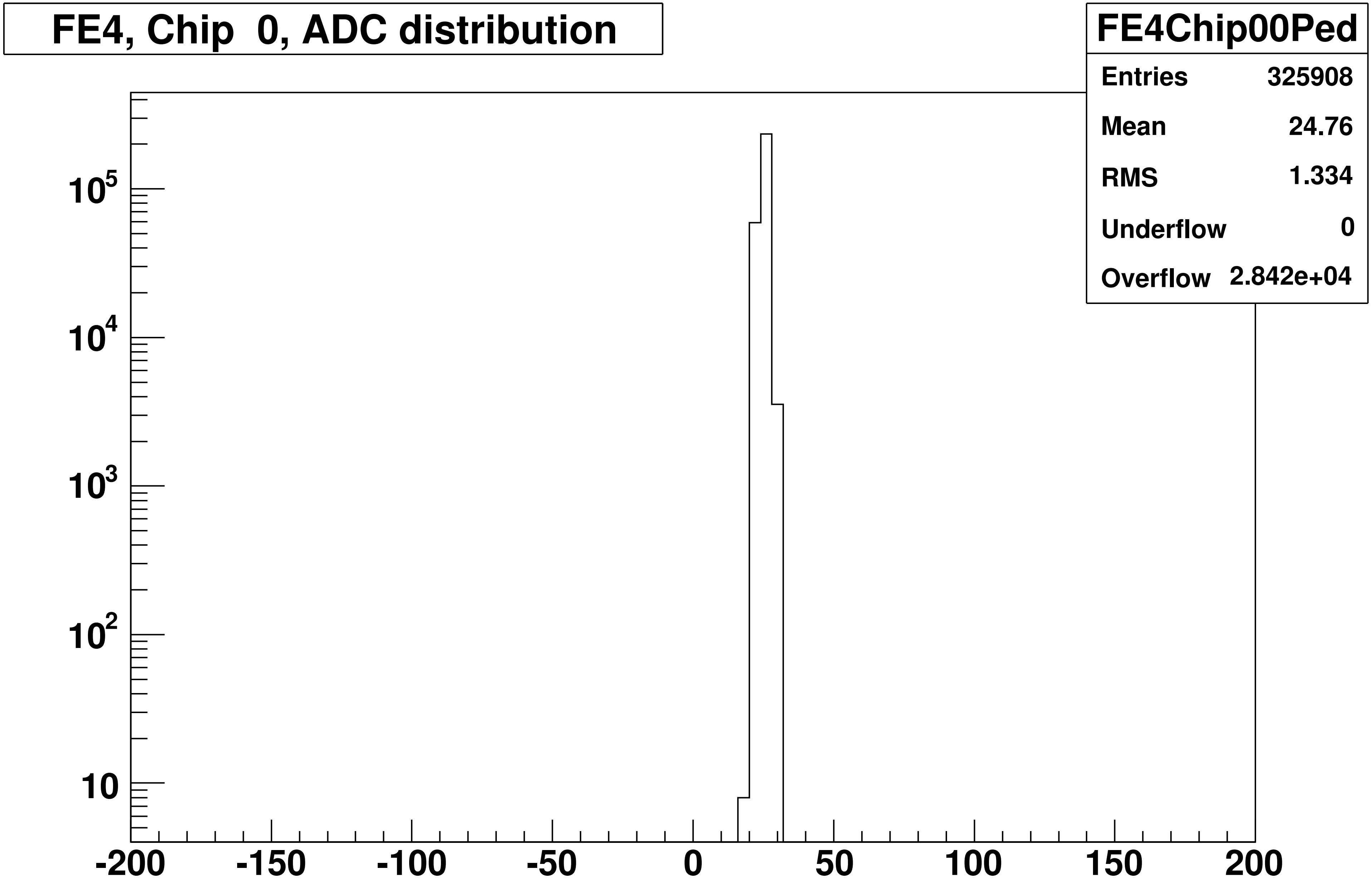
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 325908
Mean 24.76
RMS 1.334
Underflow 0
Overflow 2.842e+04
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10^4
10^3
10^2
10
-200
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-100
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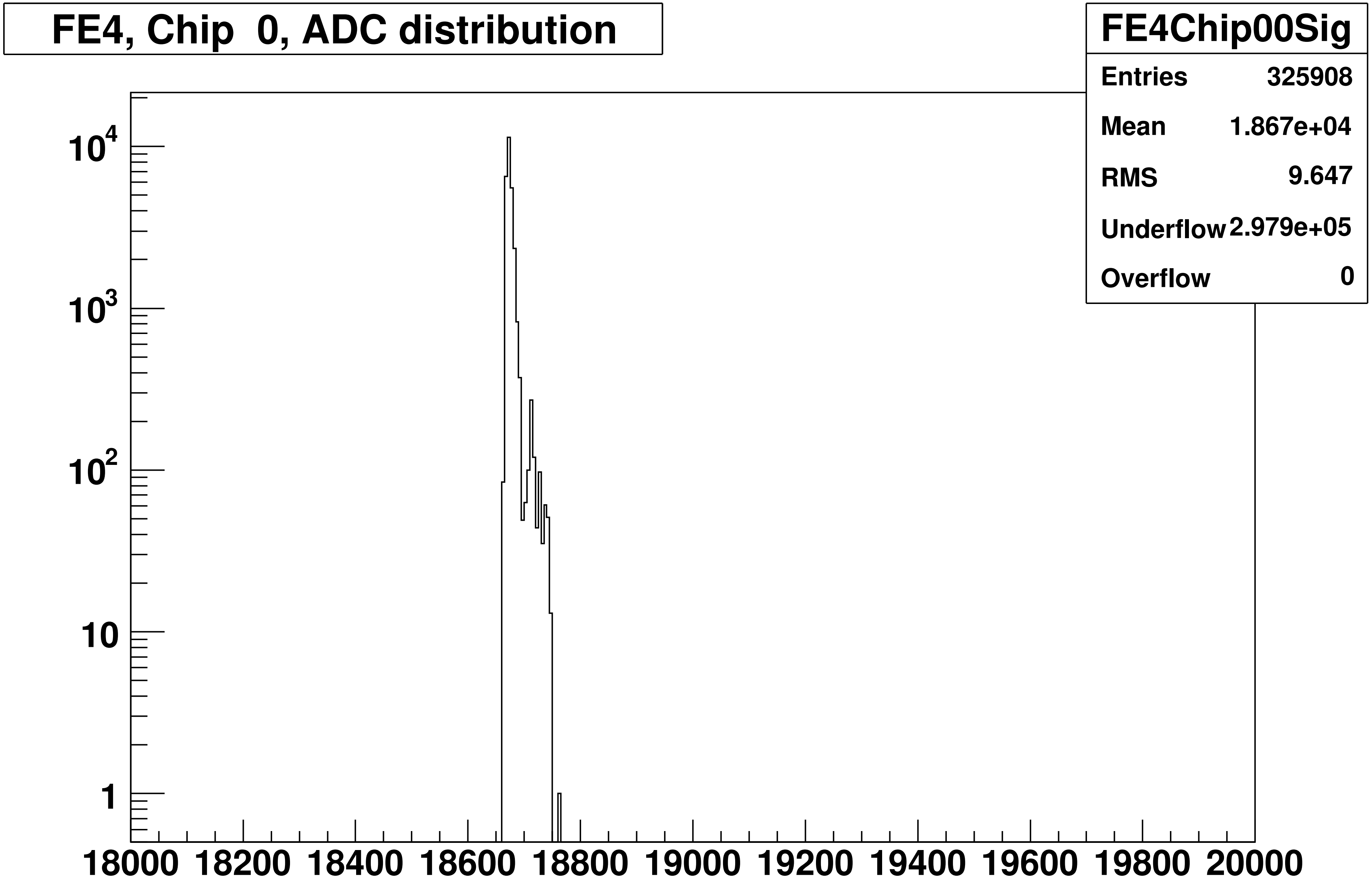
150
200



FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 325908
Mean 1.867e+04
RMS 9.647
Underflow 2.979e+05
Overflow 0
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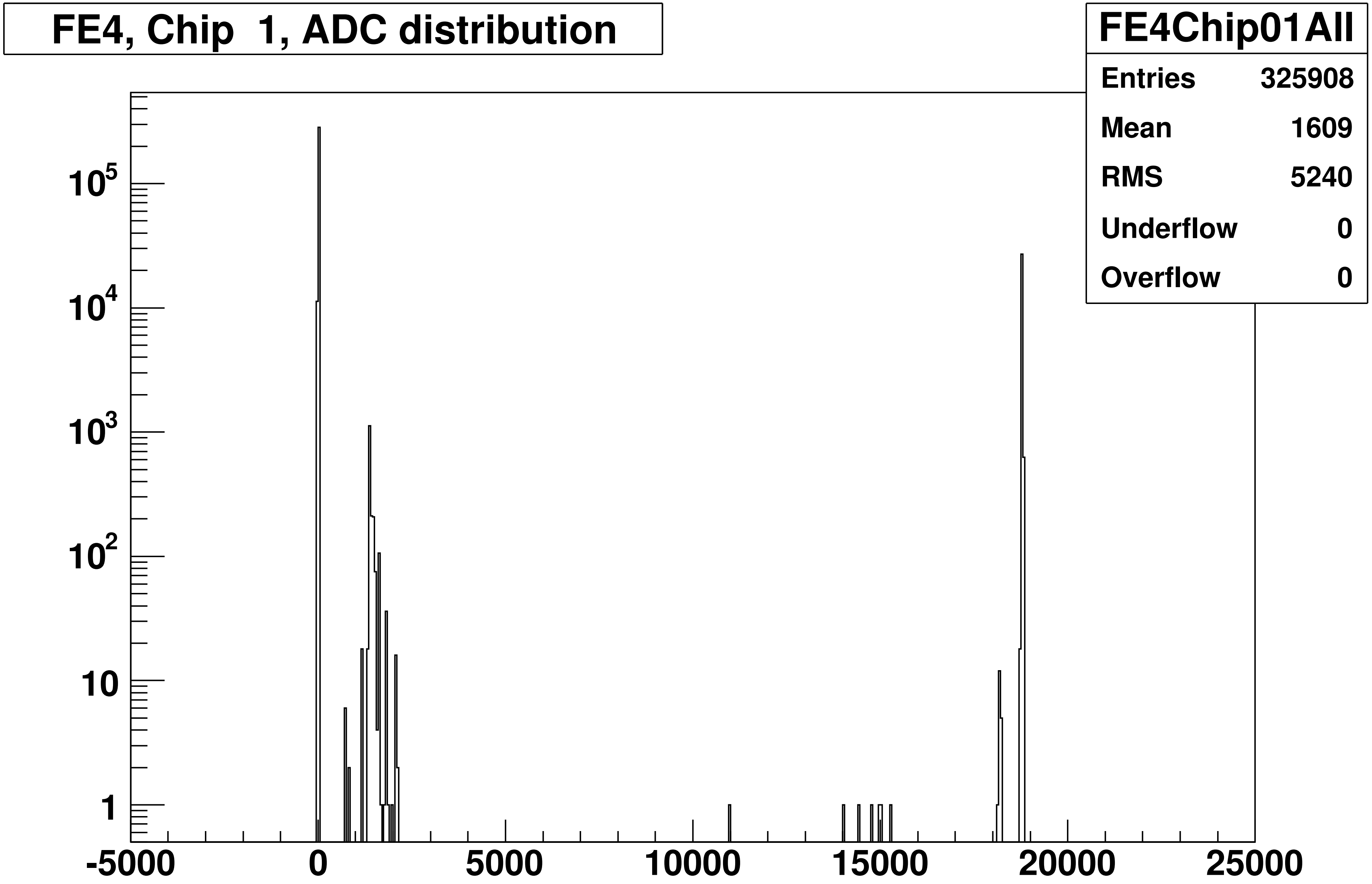
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 325908
Mean 1609
RMS 5240
Underflow 0
Overflow 0
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10^3
10^2
10
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0
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10000
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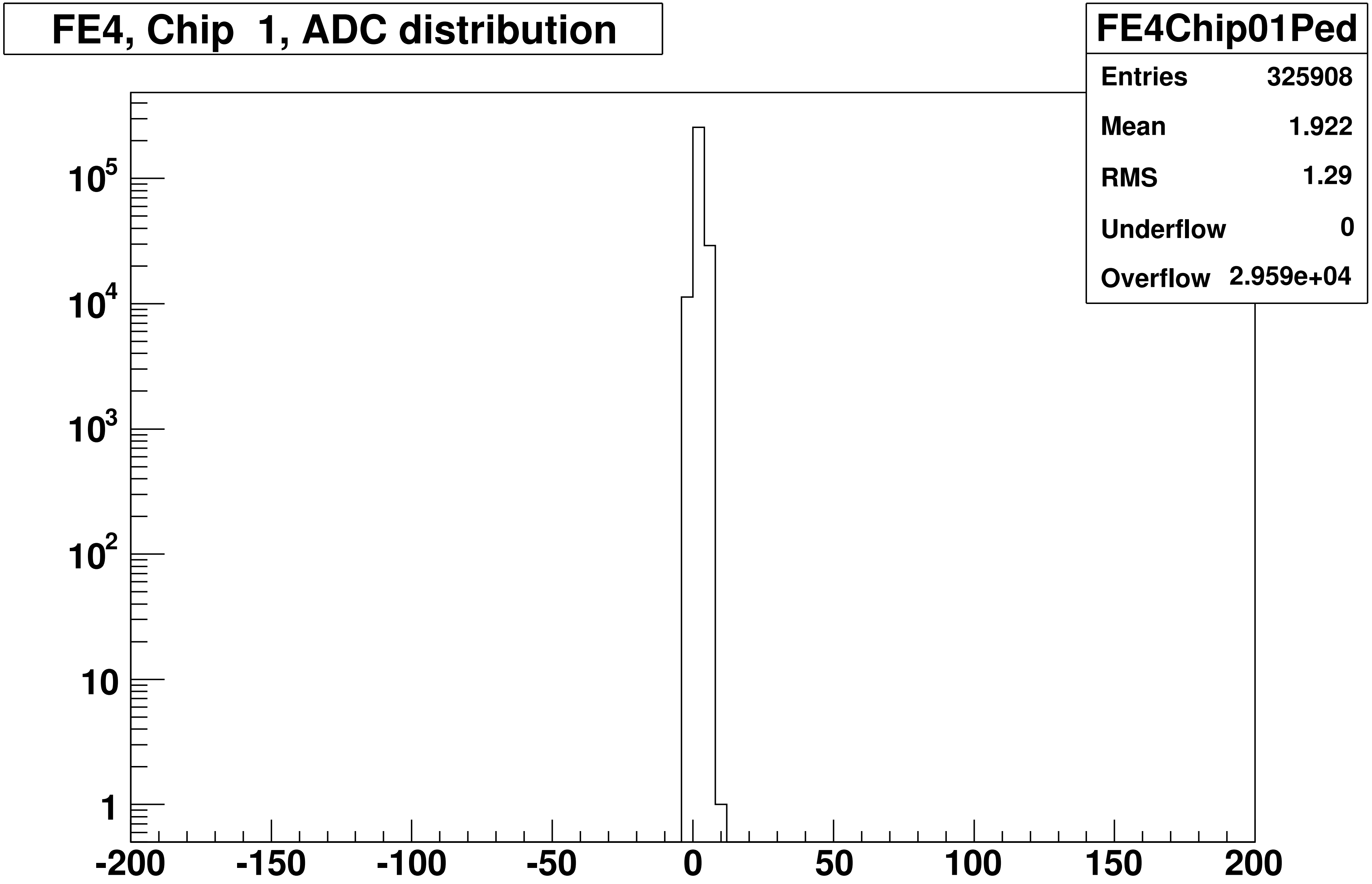
25000



FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 325908
Mean 1.922
RMS 1.29
Underflow 0
Overflow 2.959e+04
10^5
10^4
10^3
10^2
10
1
-200
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-100
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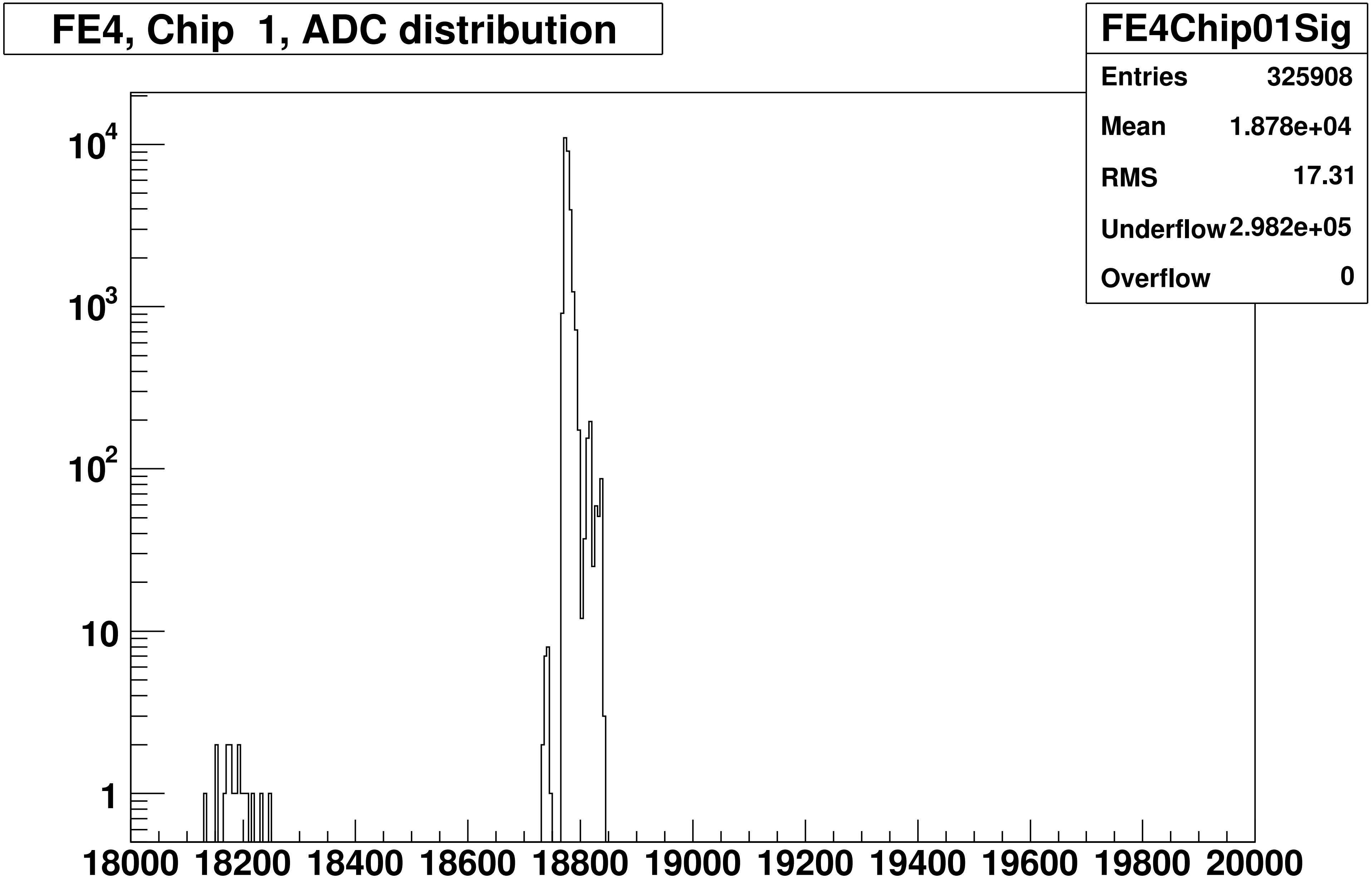
50
100
150
200


FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 325908
Mean 1.878e+04
RMS 17.31
Underflow 2.982e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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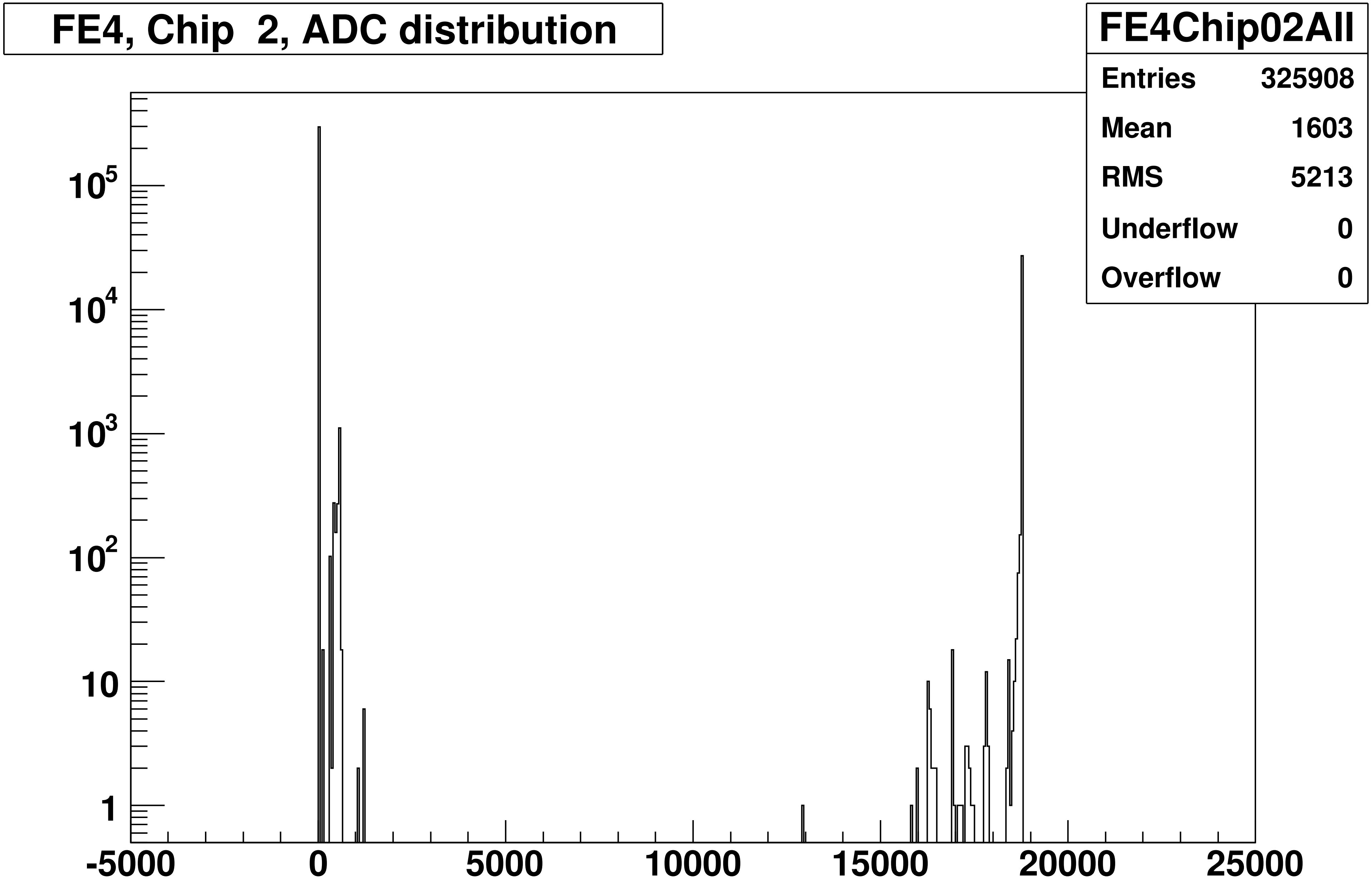
19600
19800
20000



FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 325908
Mean 1603
RMS 5213
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
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0
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10000
15000
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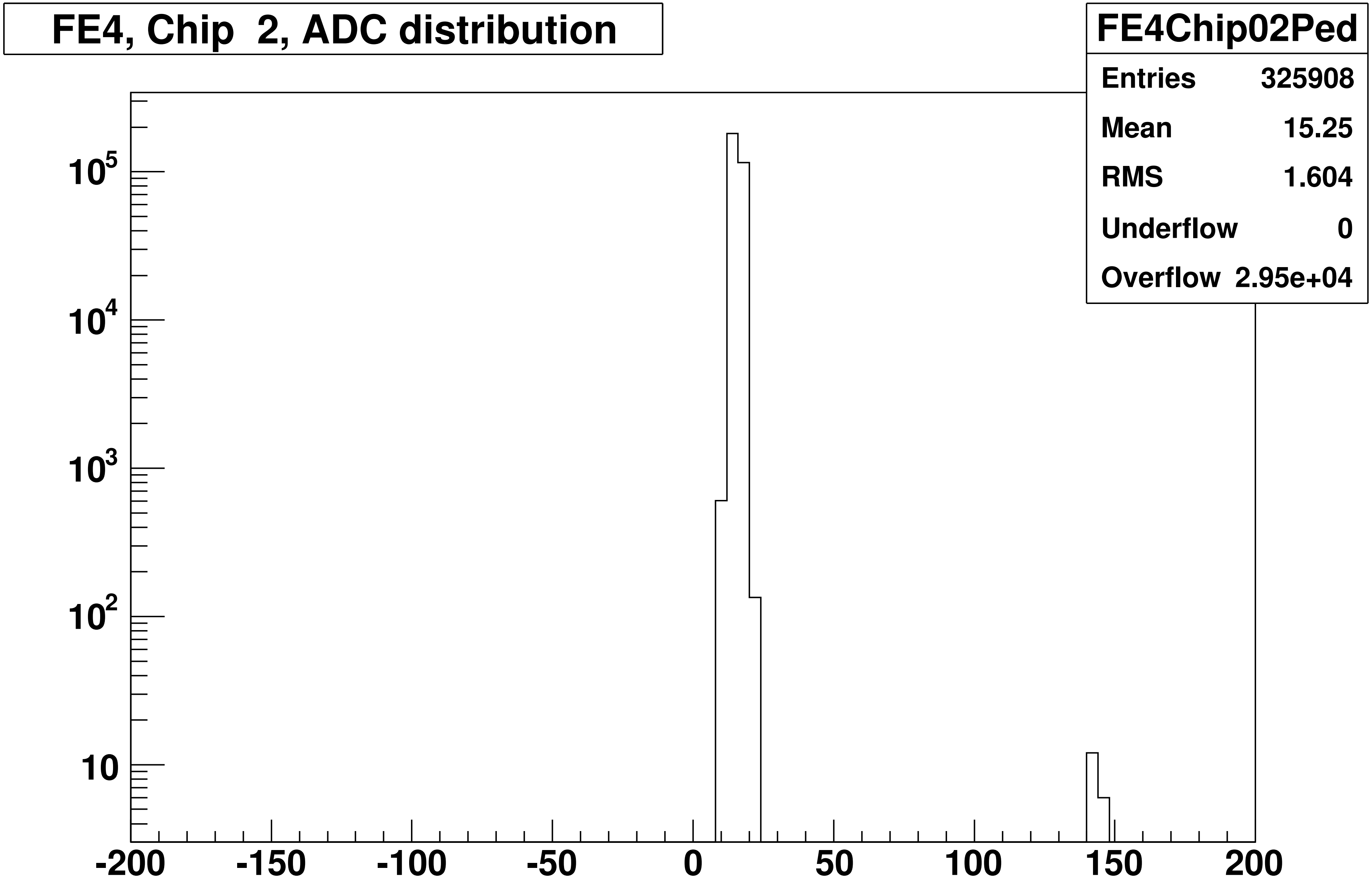
25000


FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 325908
Mean 15.25
RMS 1.604
Underflow 0
Overflow 2.95e+04
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10^4
10^3
10^2
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-200
-150
-100
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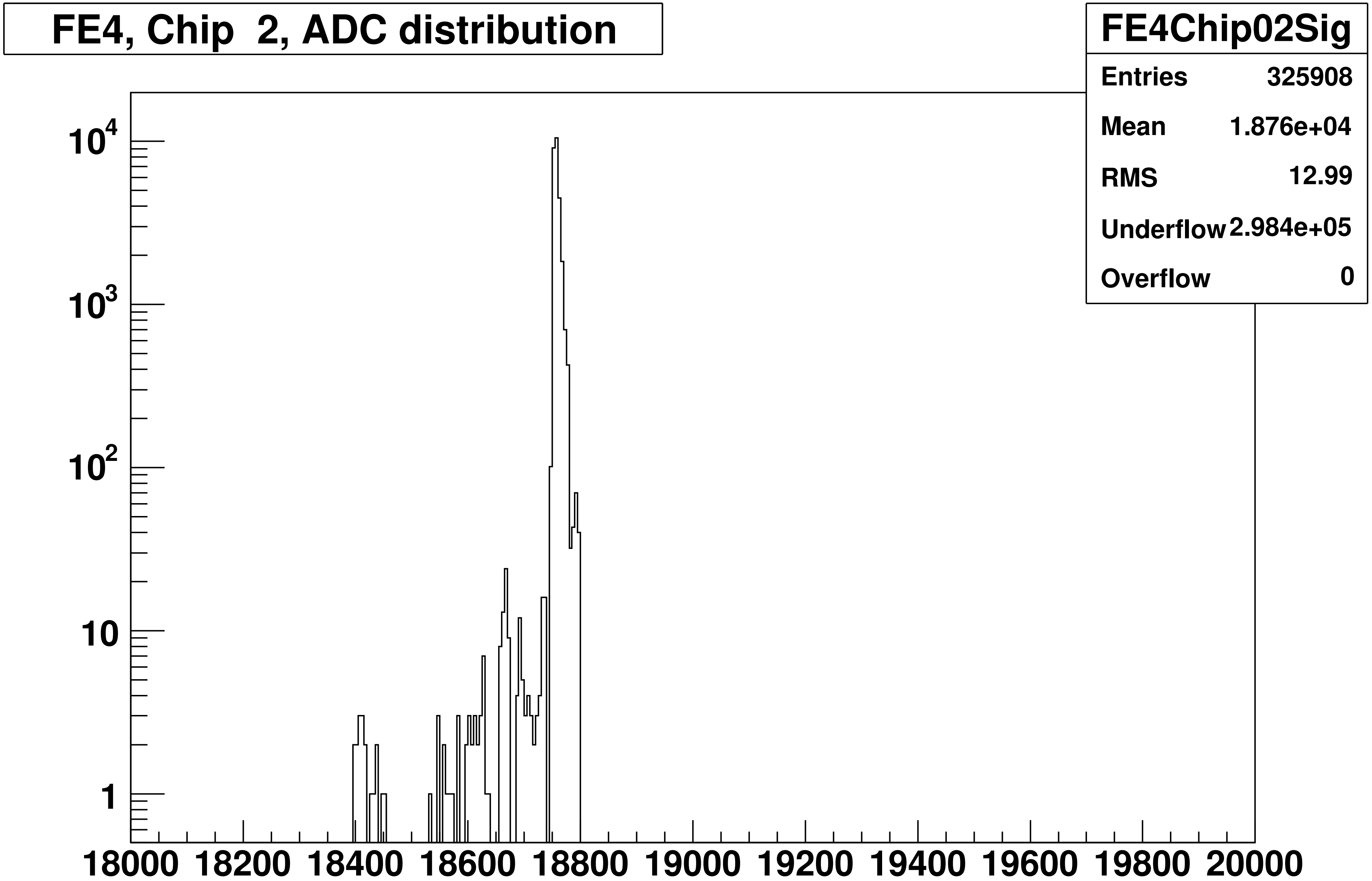
100
150
200


FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 325908
Mean 1.876e+04
RMS 12.99
Underflow 2.984e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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19000
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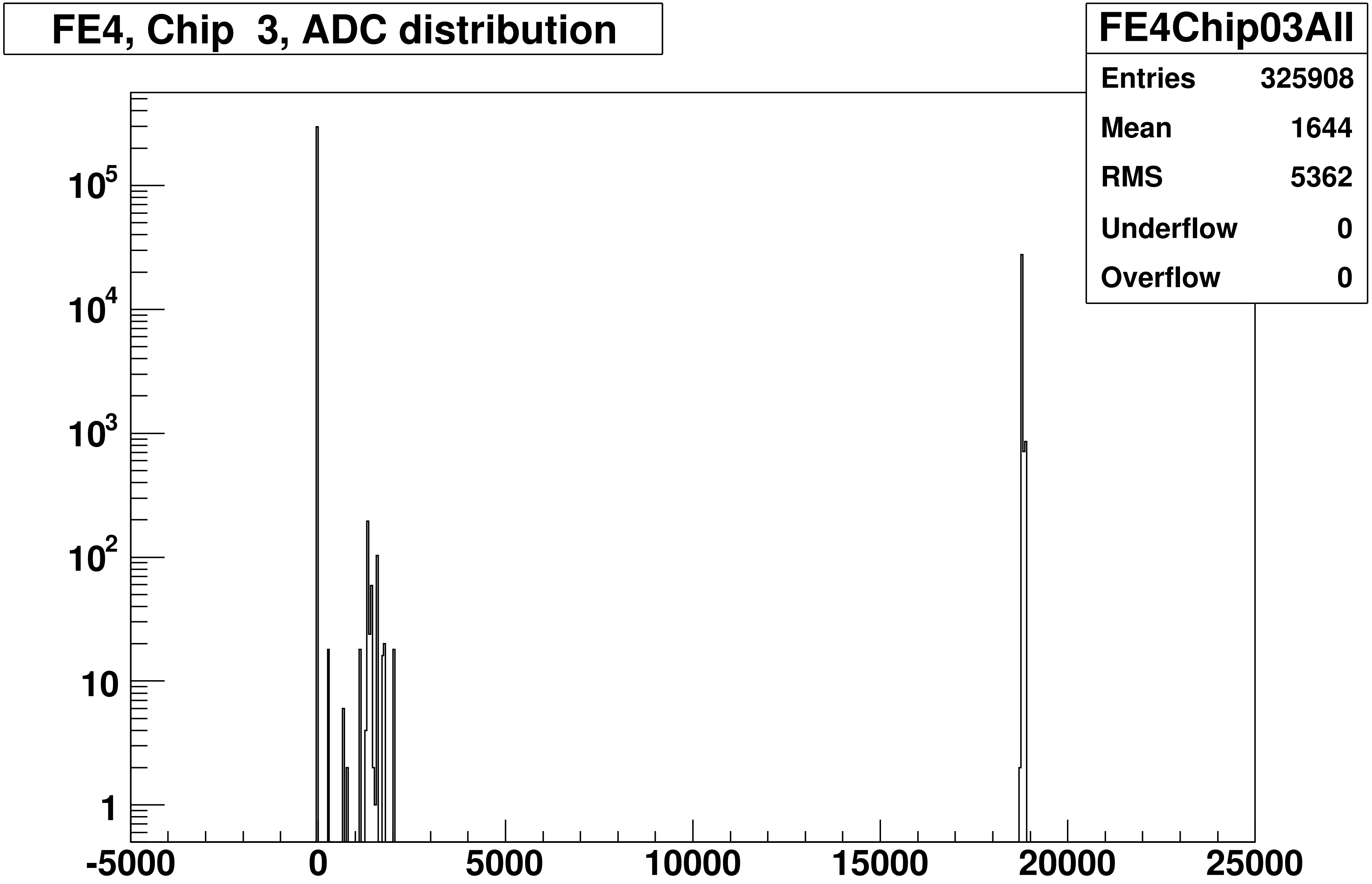
19600
19800
20000



FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 325908
Mean 1644
RMS 5362
Underflow 0
Overflow 0
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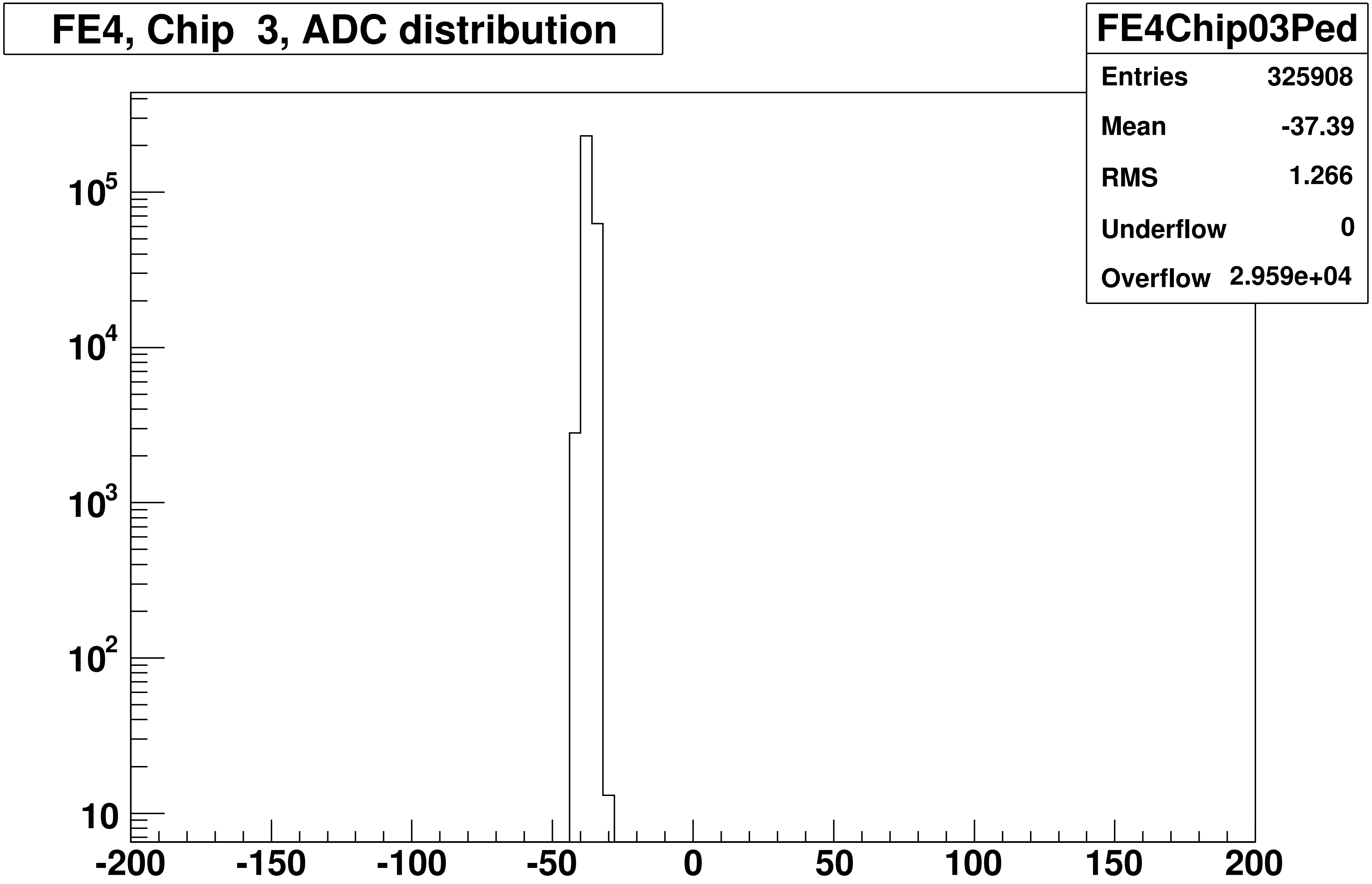
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 325908
Mean -37.39
RMS 1.266
Underflow 0
Overflow 2.959e+04
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10^4
10^3
10^2
10
-200
-150
-100
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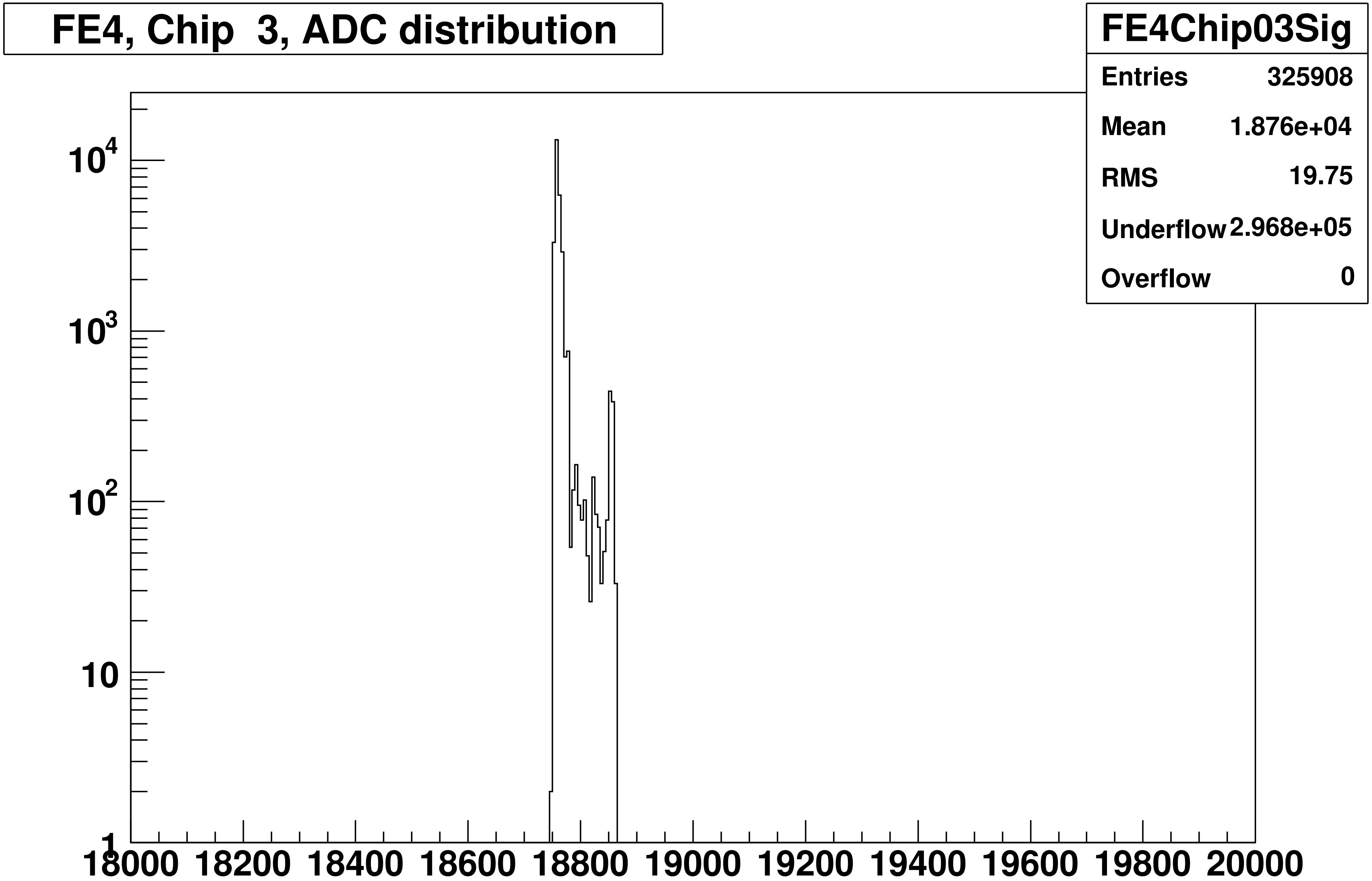
50
100
150
200



FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 325908
Mean 1.876e+04
RMS 19.75
Underflow 2.968e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
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19000
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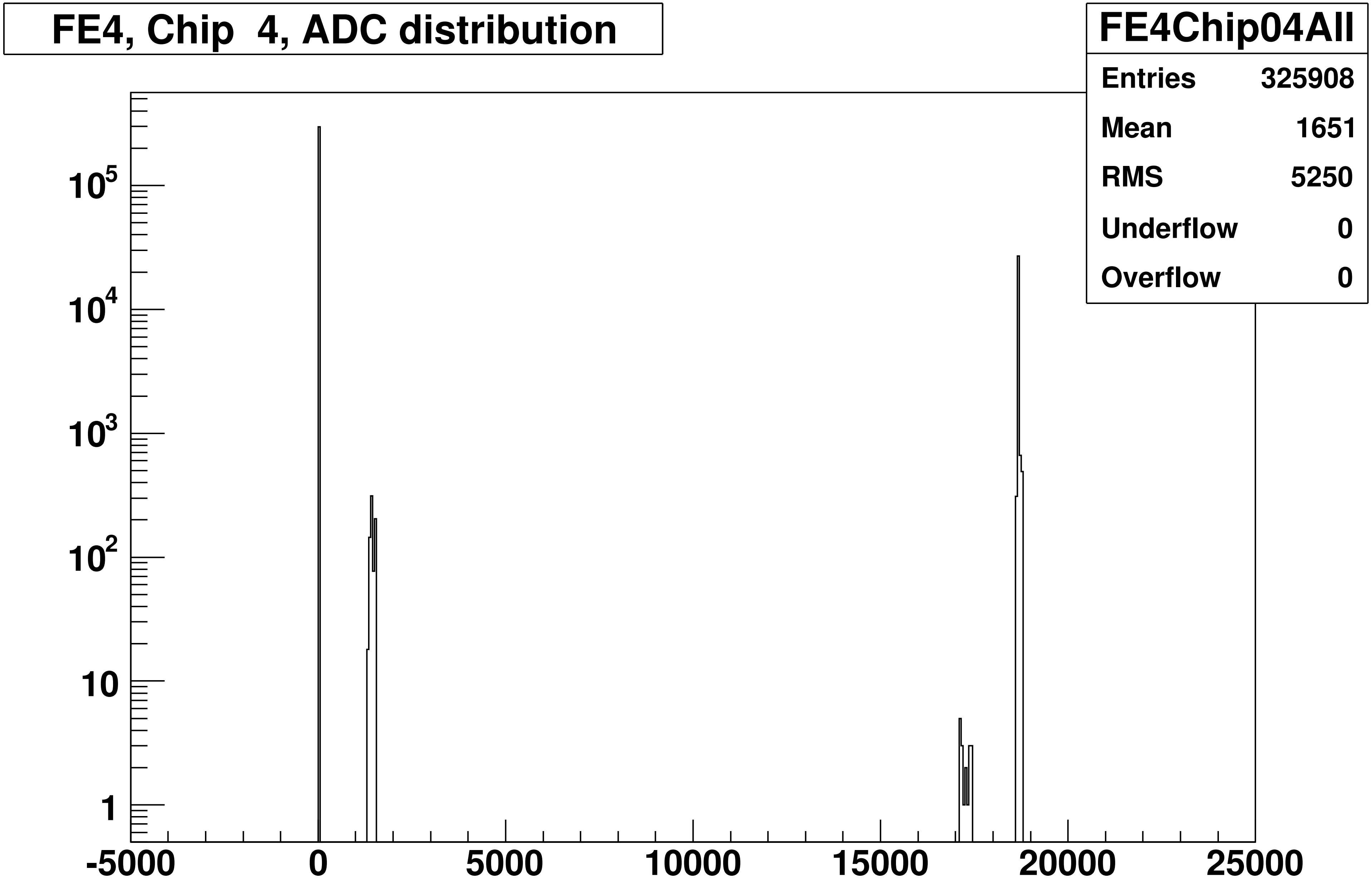
19800
20000



FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 325908
Mean 1651
RMS 5250
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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-5000
0
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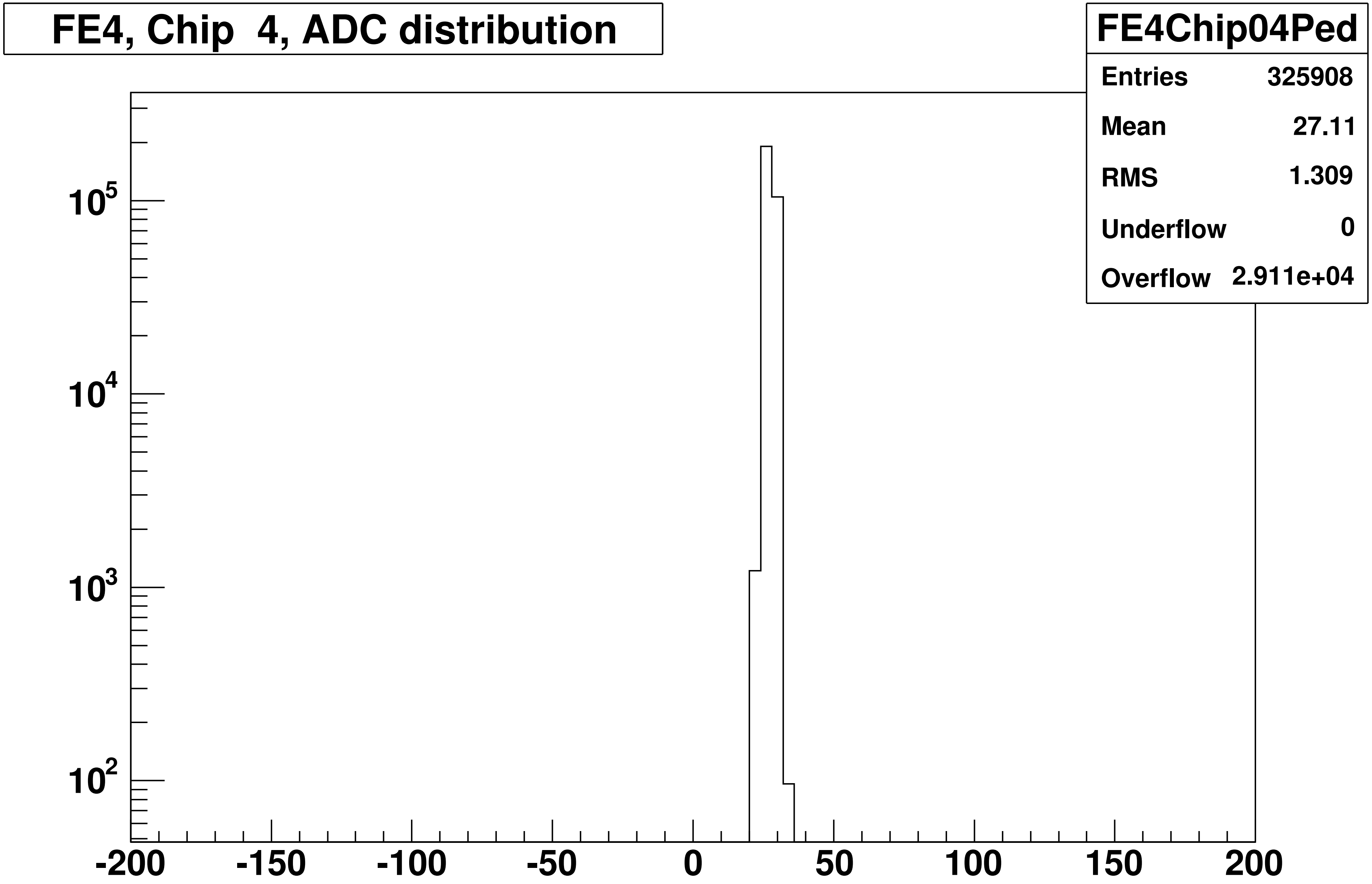
10000
15000
20000
25000


FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 325908
Mean 27.11
RMS 1.309
Underflow 0
Overflow 2.911e+04
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10^4
10^3
10^2
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-100
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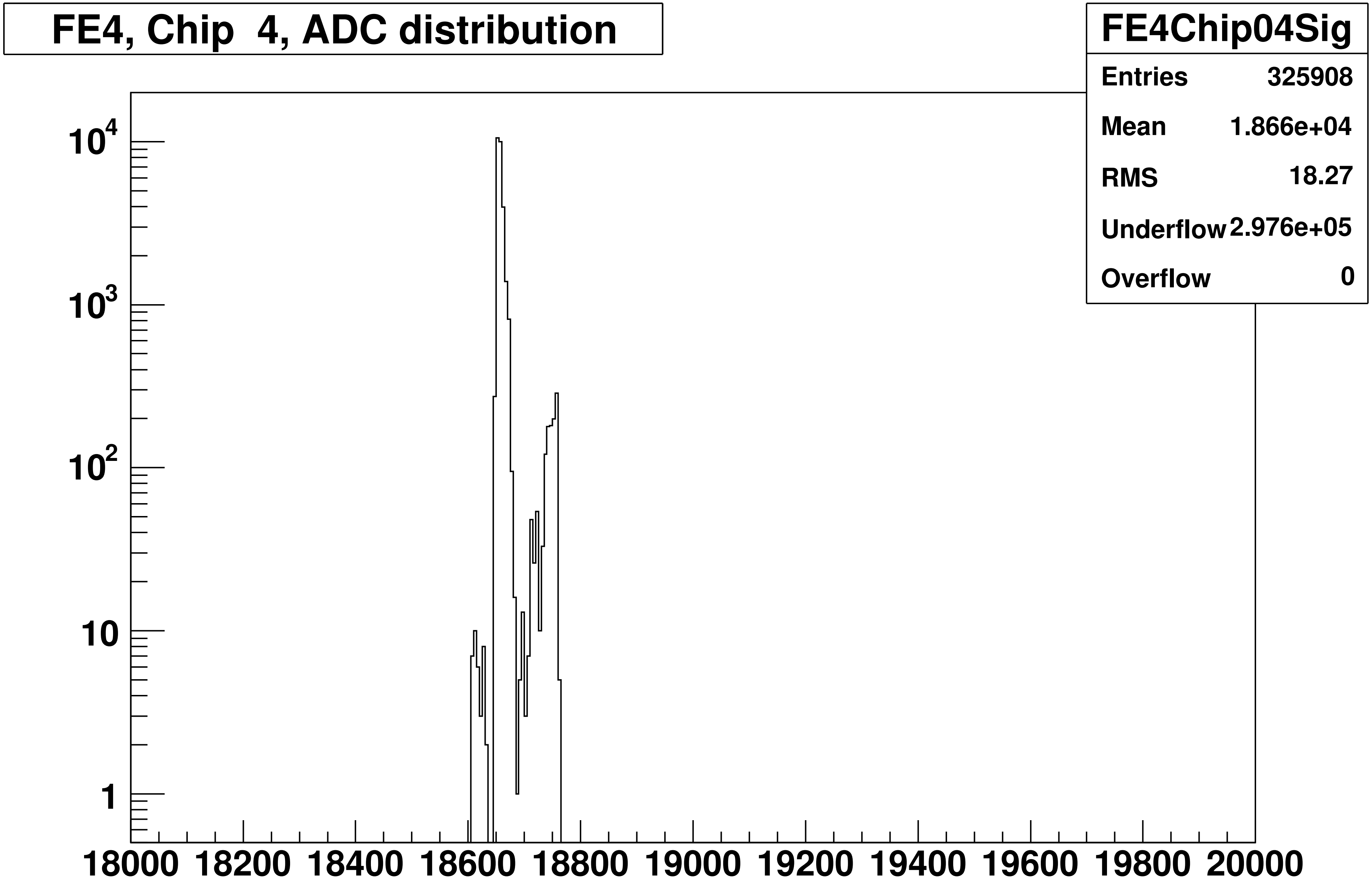
0
50
100
150
200


FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 325908
Mean 1.866e+04
RMS 18.27
Underflow 2.976e+05
Overflow 0
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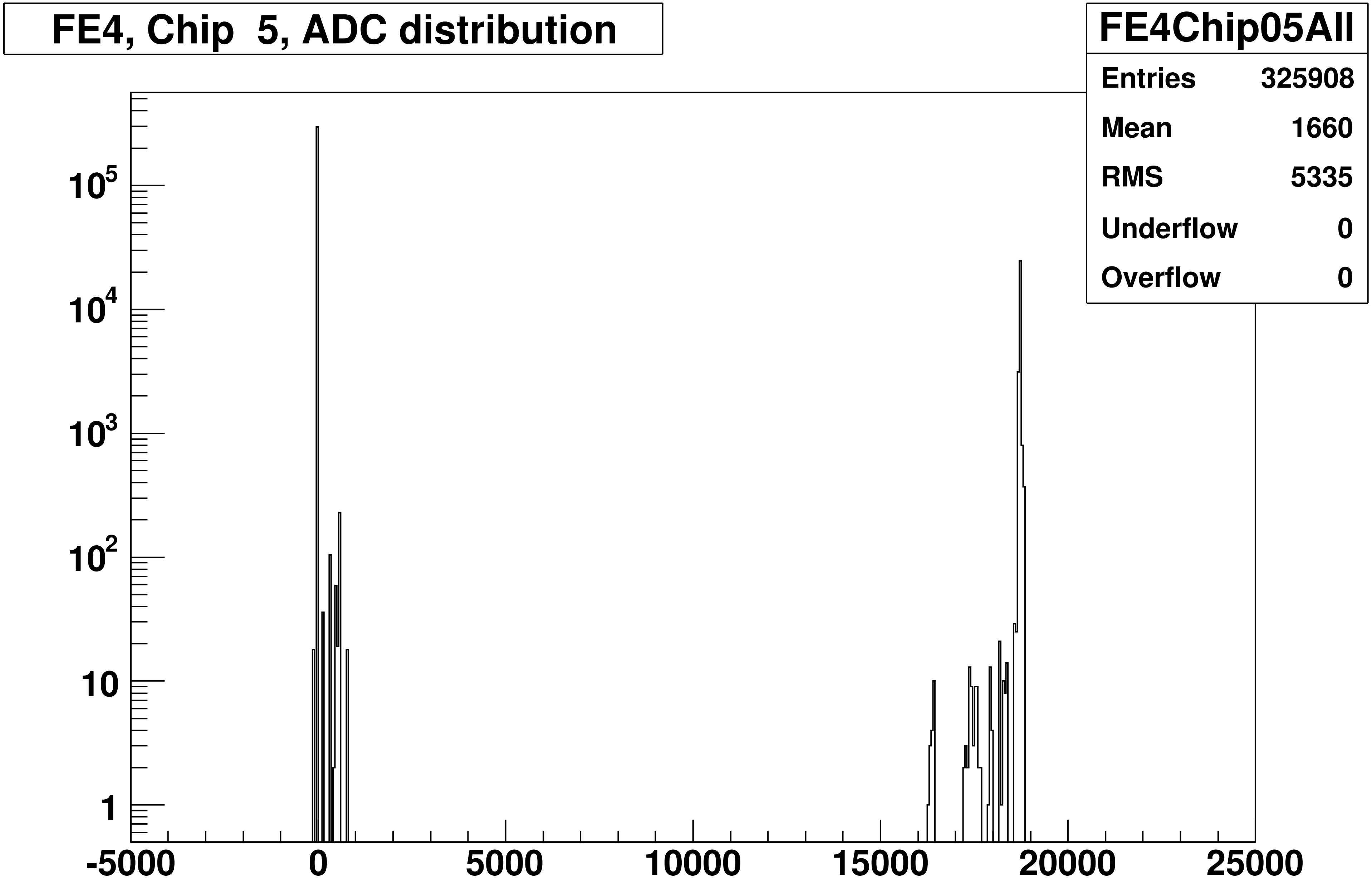
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 325908
Mean 1660
RMS 5335
Underflow 0
Overflow 0
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10^4
10^3
10^2
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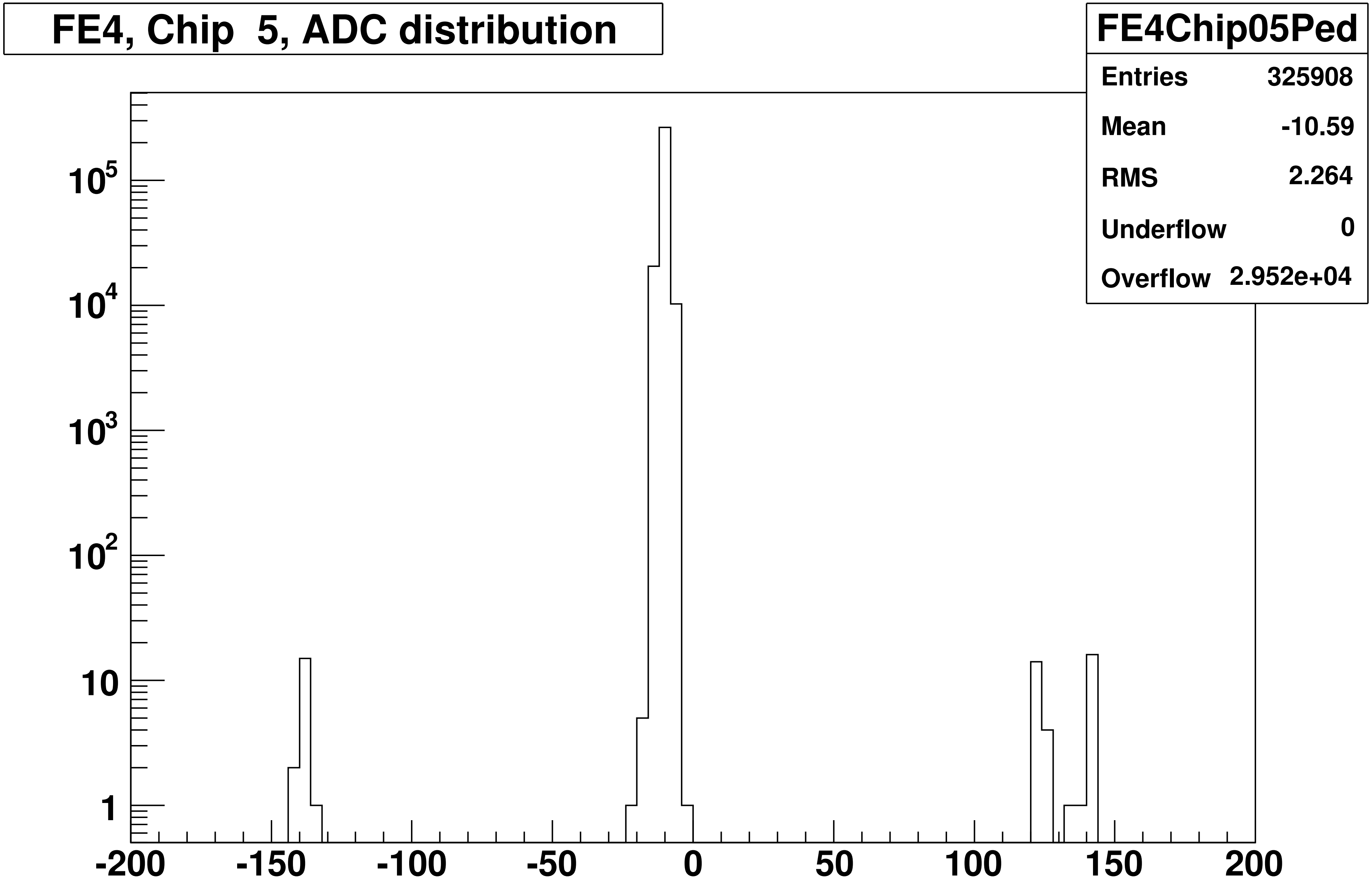
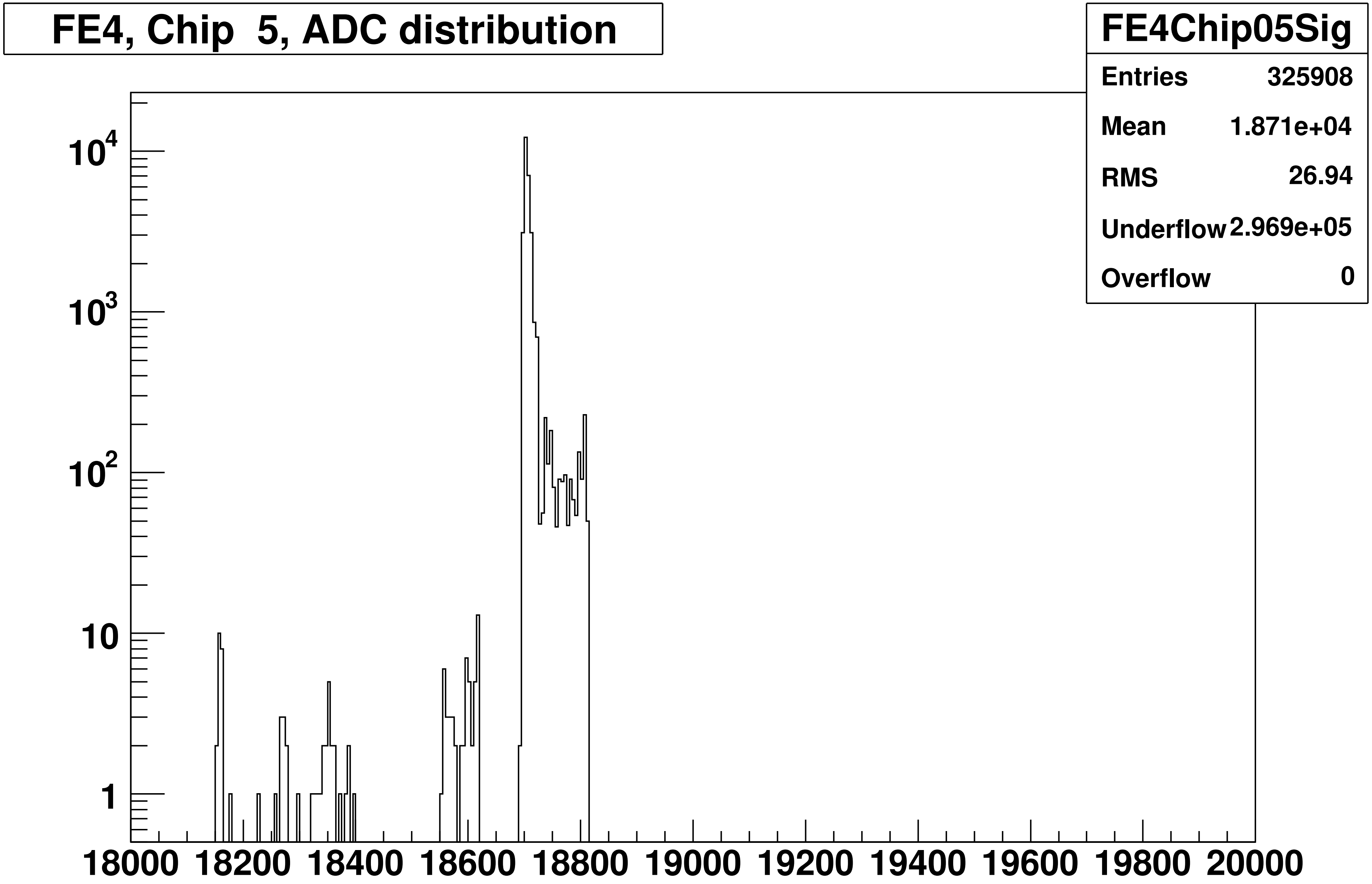
20000
25000


FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 325908
Mean -10.59
RMS 2.264
Underflow 0
Overflow 2.952e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 325908
Mean 1.871e+04
RMS 26.94
Underflow 2.969e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
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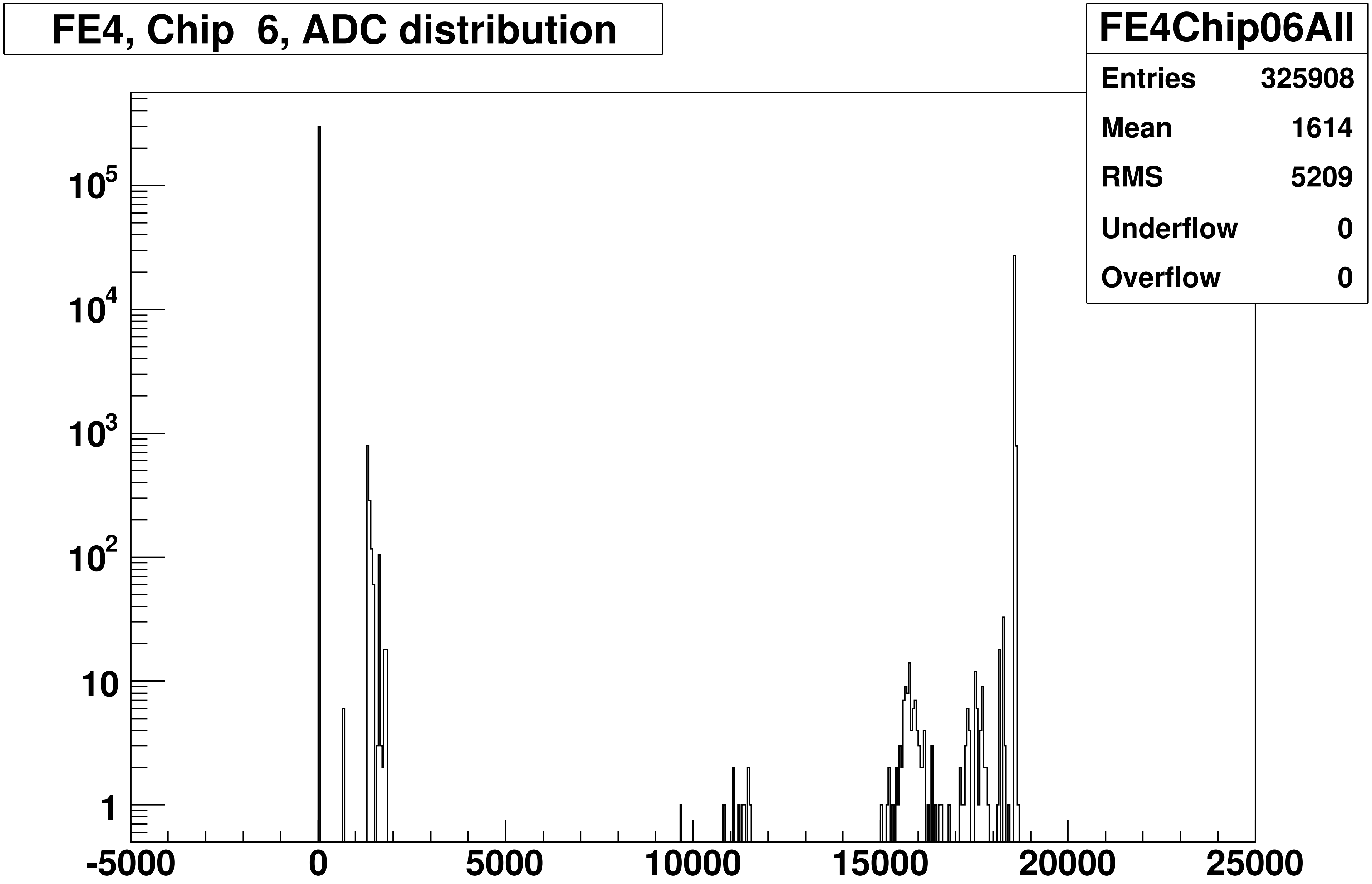

FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 325908
Mean 1614
RMS 5209
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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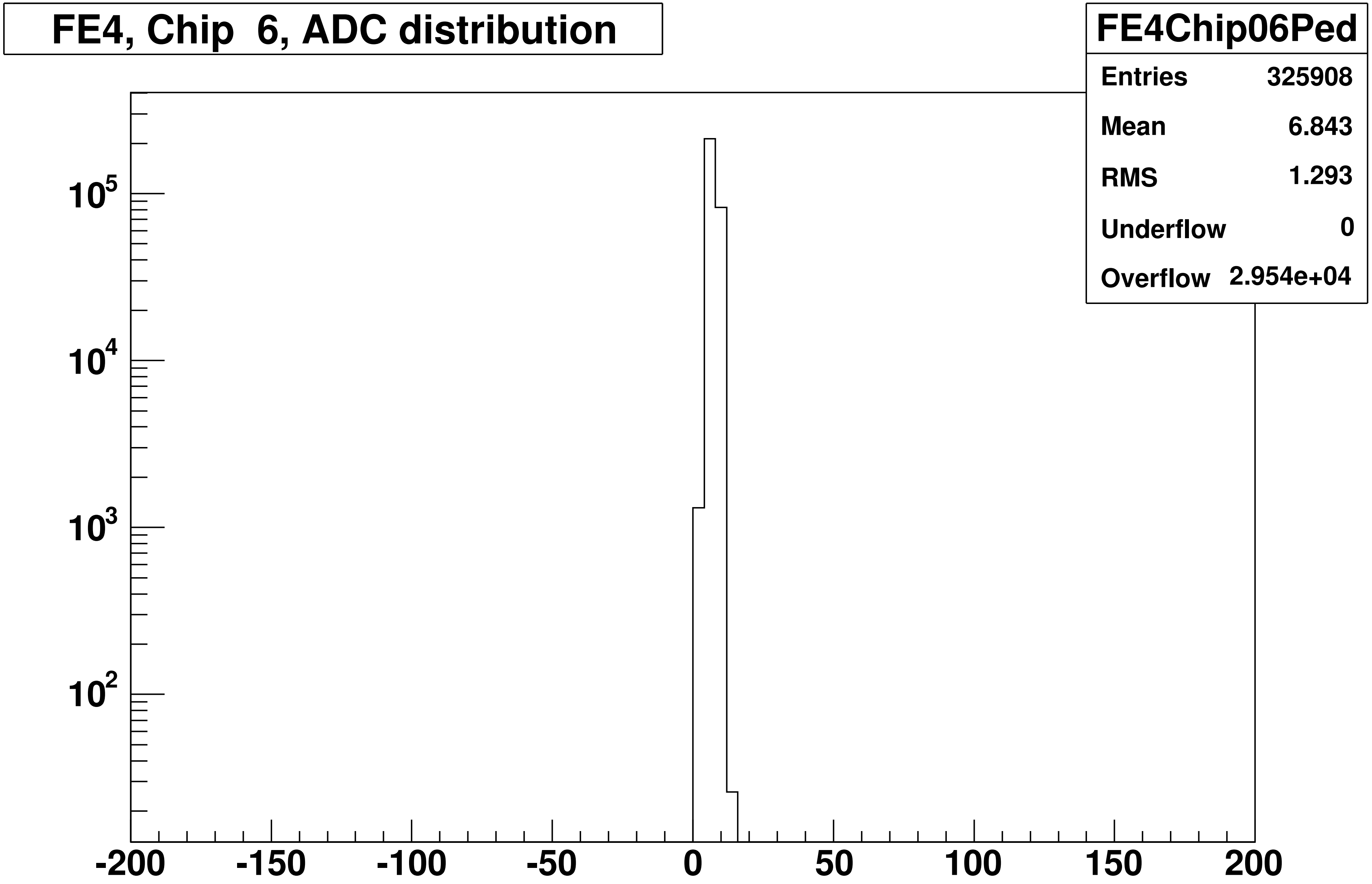

FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 325908
Mean 6.843
RMS 1.293
Underflow 0
Overflow 2.954e+04
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10^4
10^3
10^2
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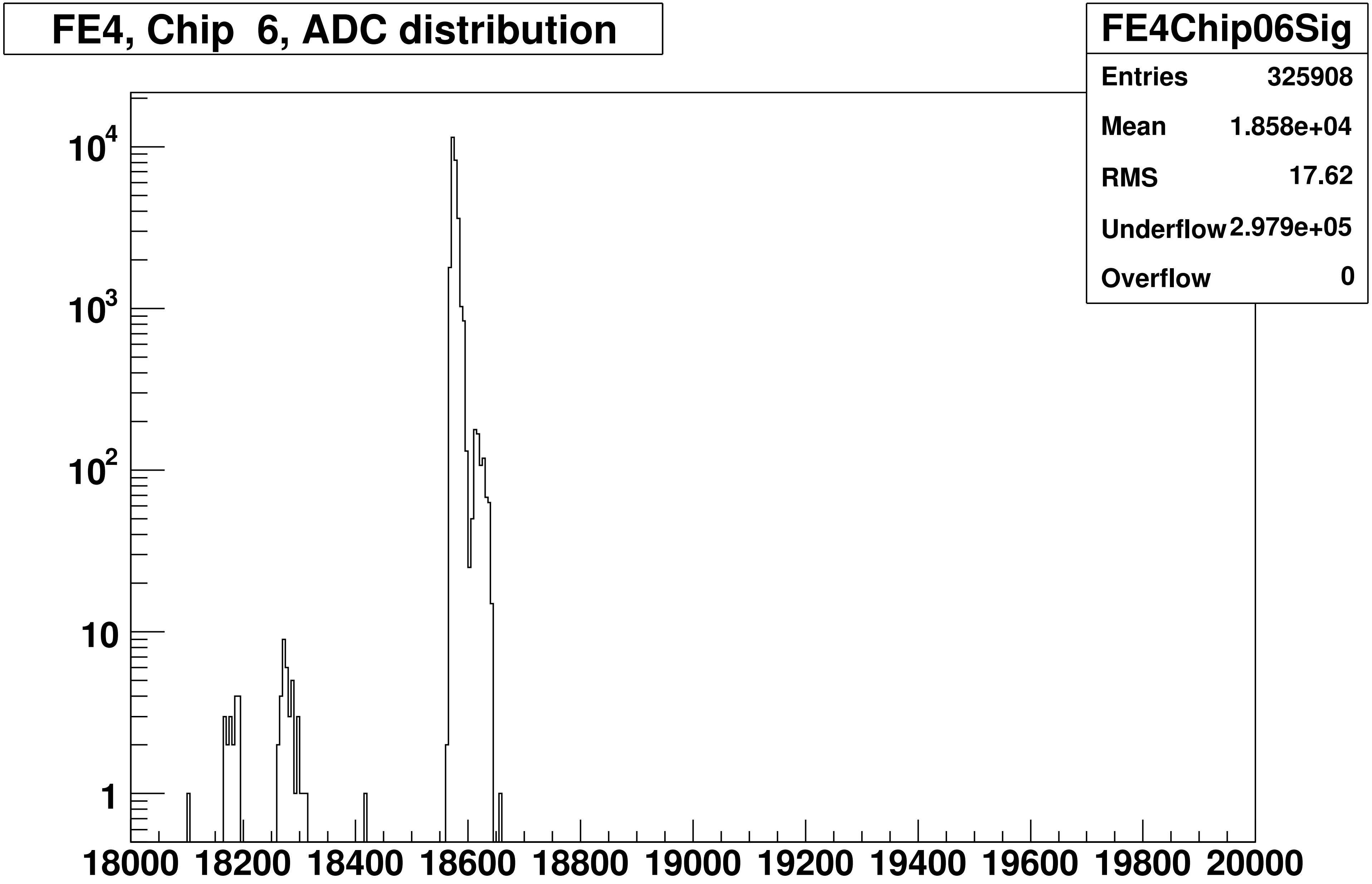
-150
-100
-50
0
50
100
150
200


FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 325908
Mean 1.858e+04
RMS 17.62
Underflow 2.979e+05
Overflow 0
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10^3
10^2
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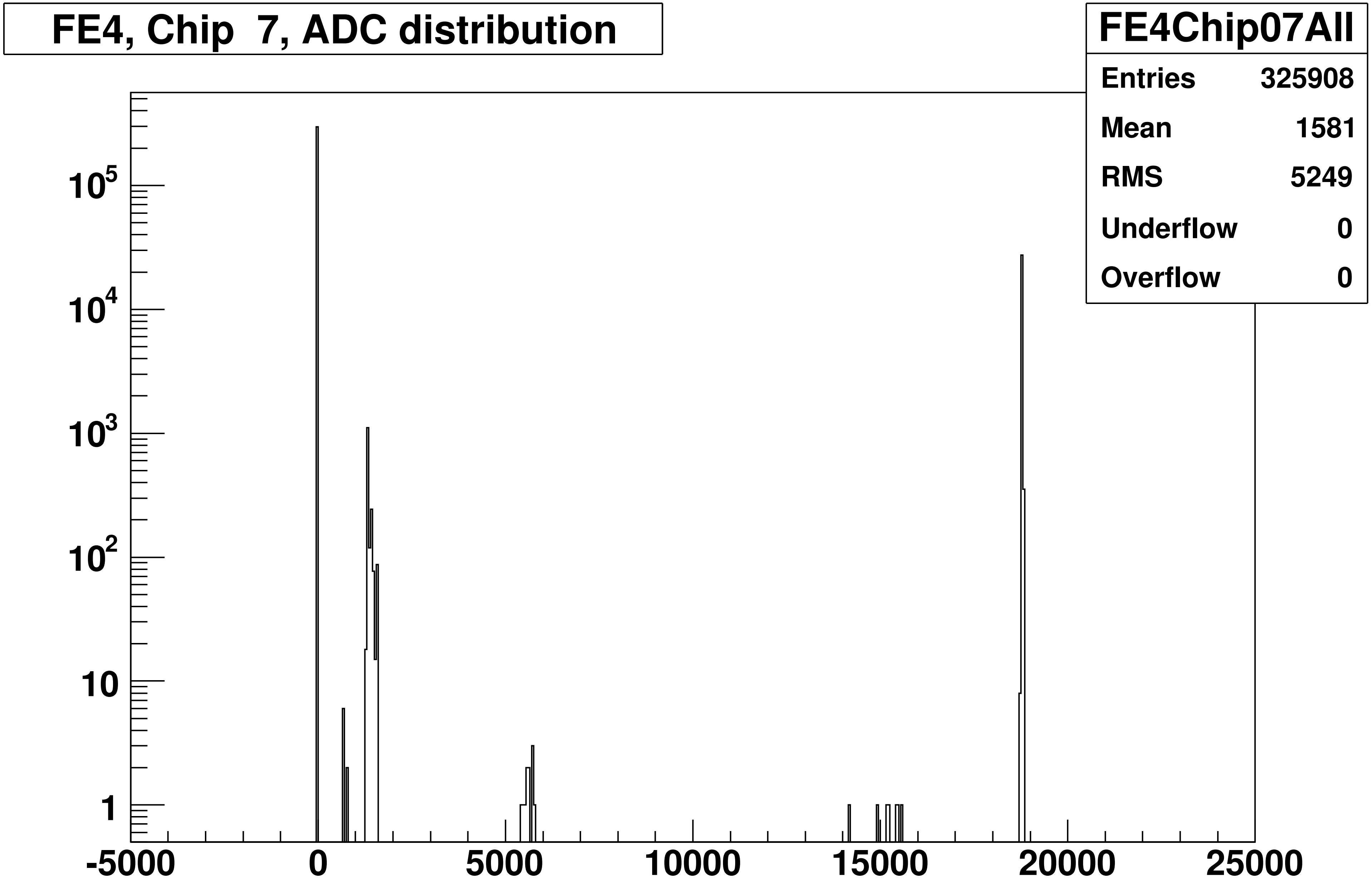
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 325908
Mean 1581
RMS 5249
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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10000
15000
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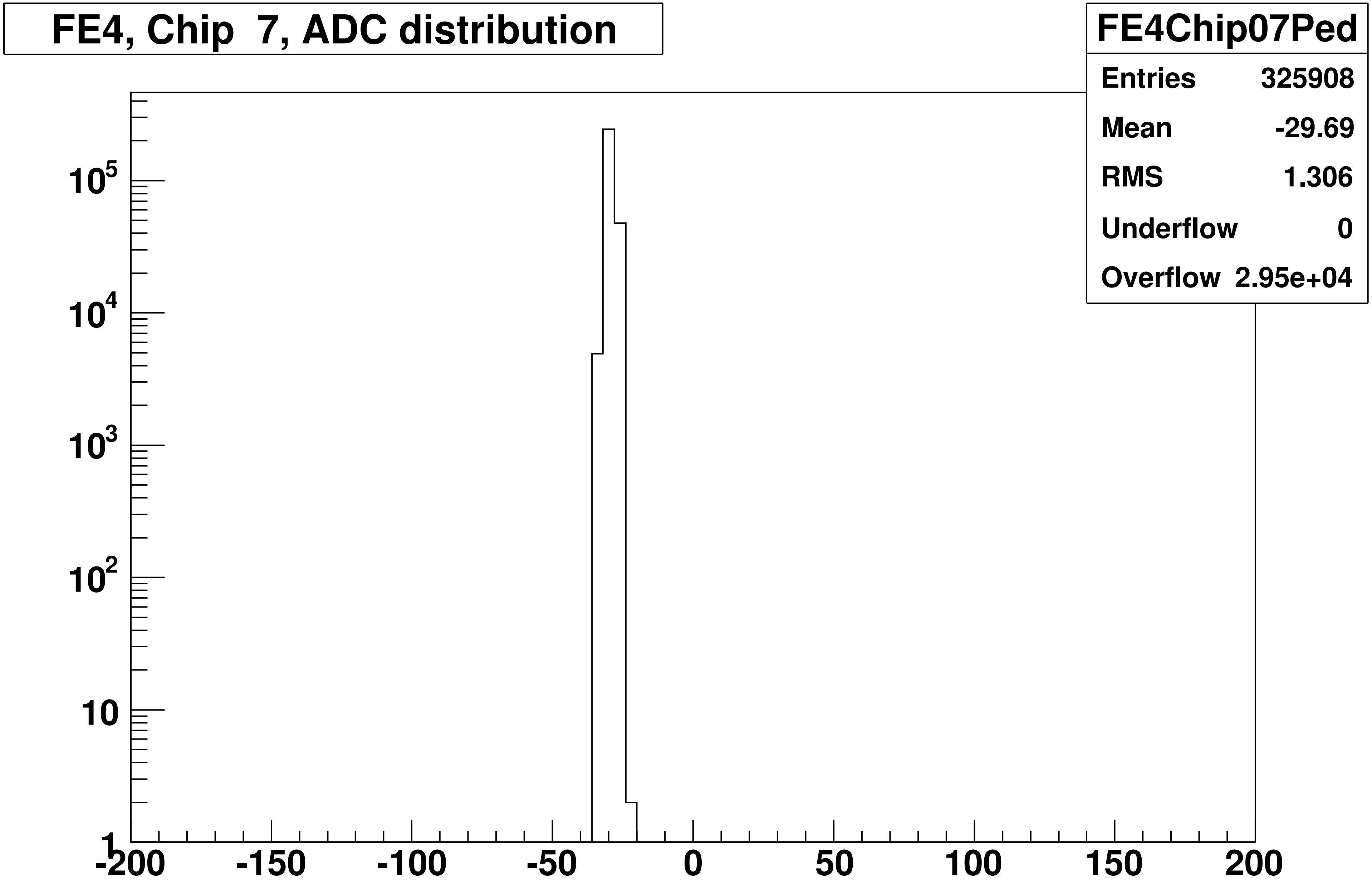
25000



FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 325908
Mean -29.69
RMS 1.306
Underflow 0
Overflow 2.95e+04
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10^3
10^2
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1
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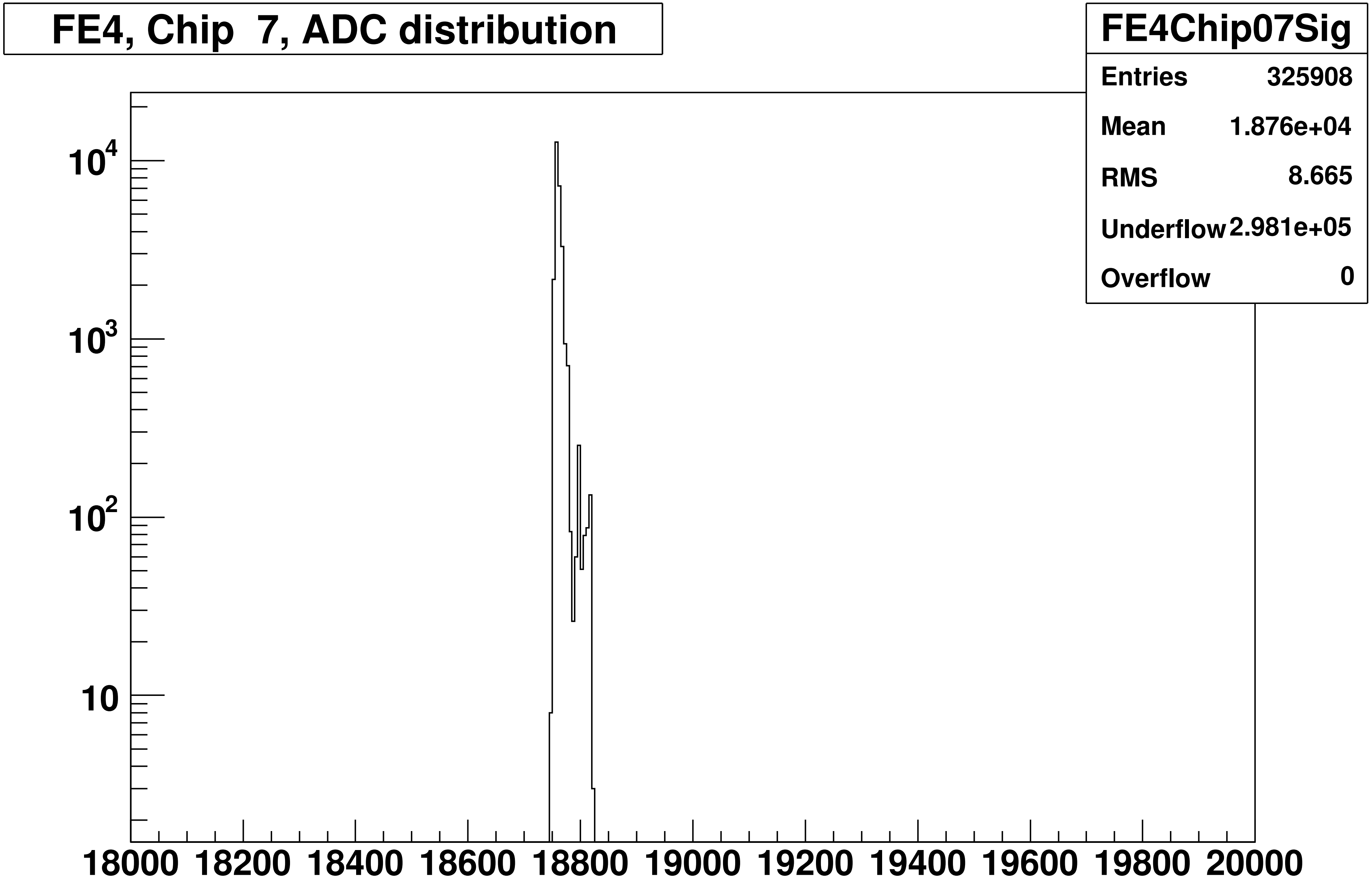
150
200



FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 325908
Mean 1.876e+04
RMS 8.665
Underflow 2.981e+05
Overflow 0
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10^3
10^2
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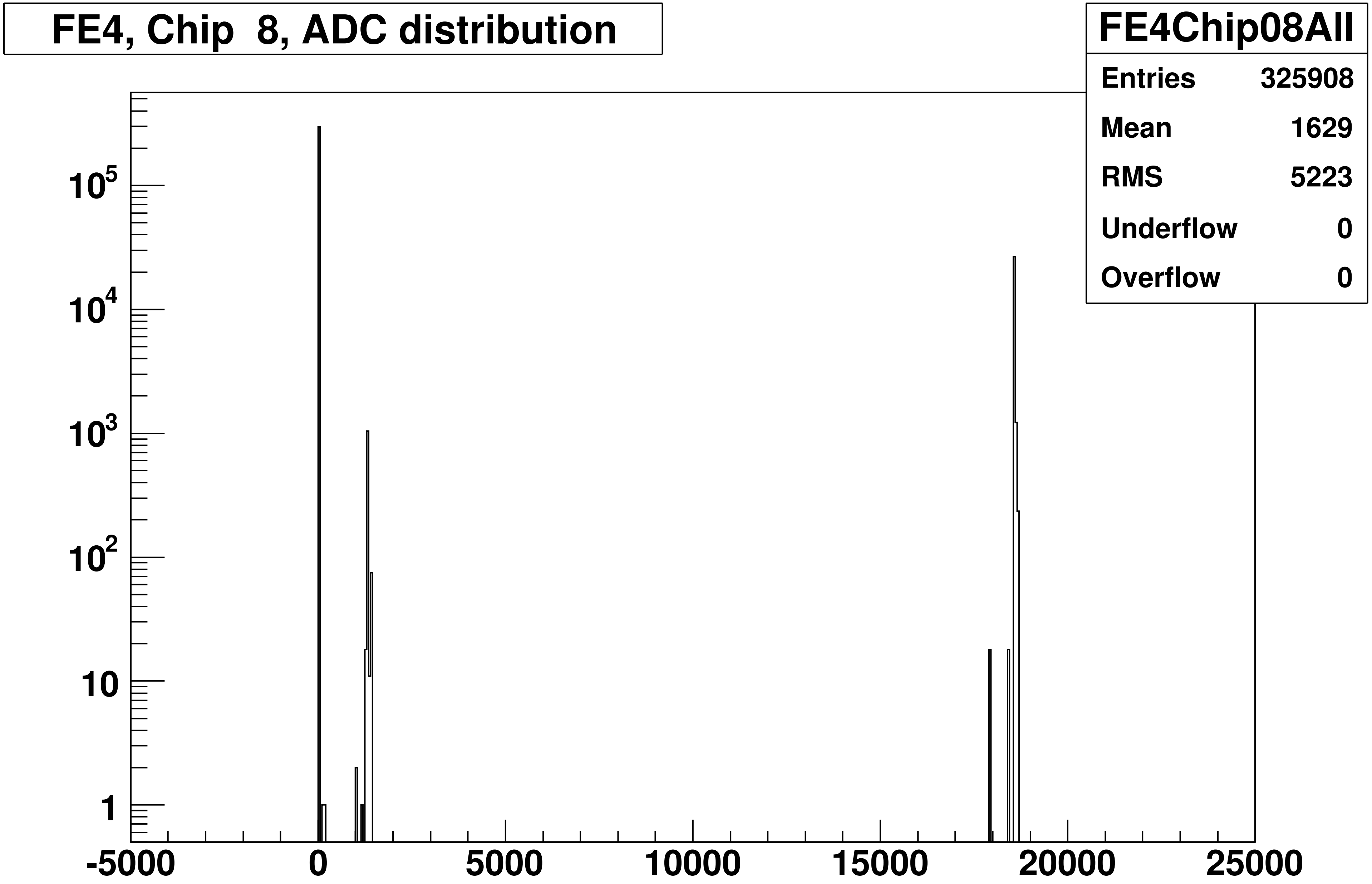
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 325908
Mean 1629
RMS 5223
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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-5000
0
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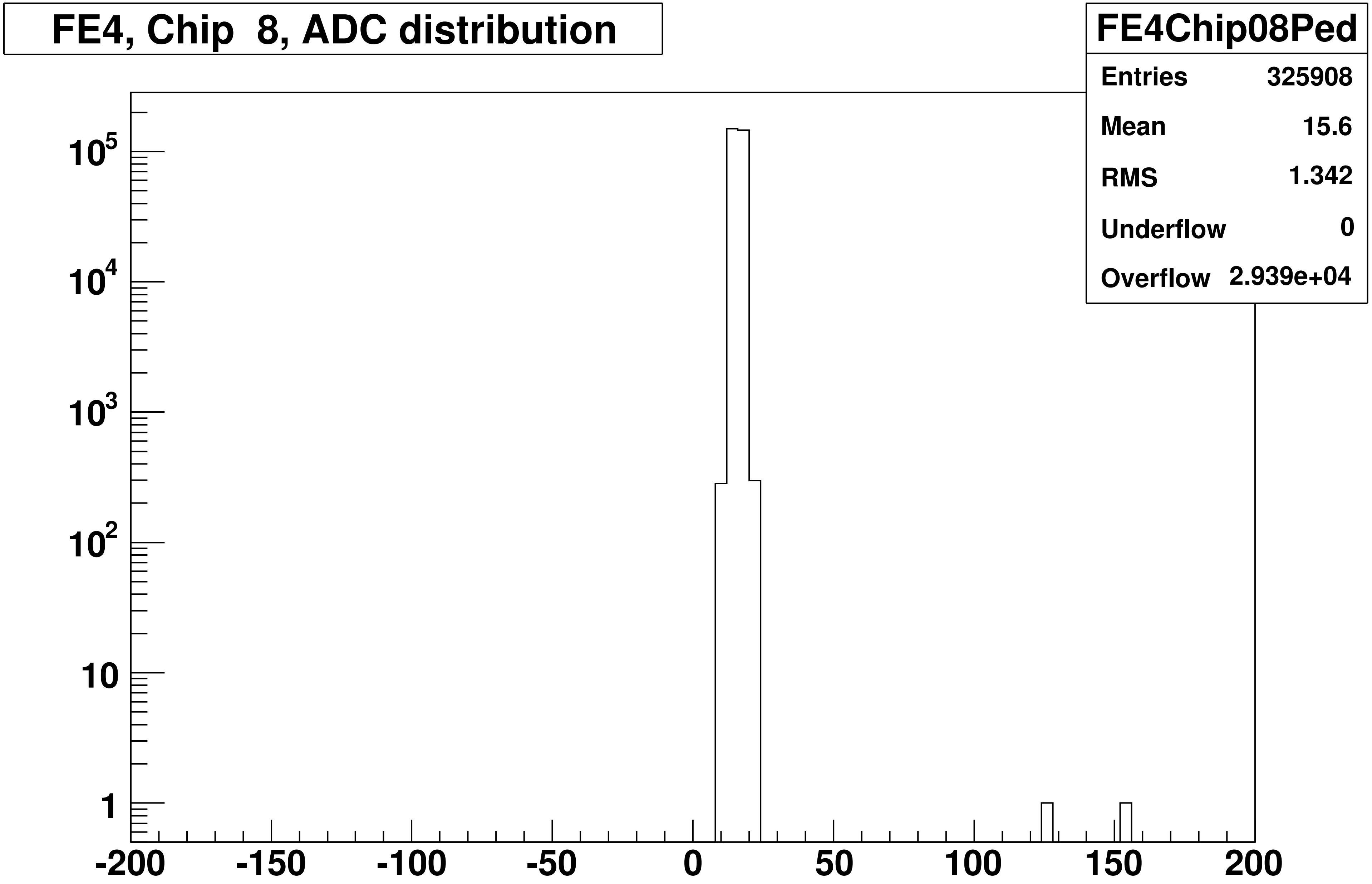
10000
15000
20000
25000



FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 325908
Mean 15.6
RMS 1.342
Underflow 0
Overflow 2.939e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
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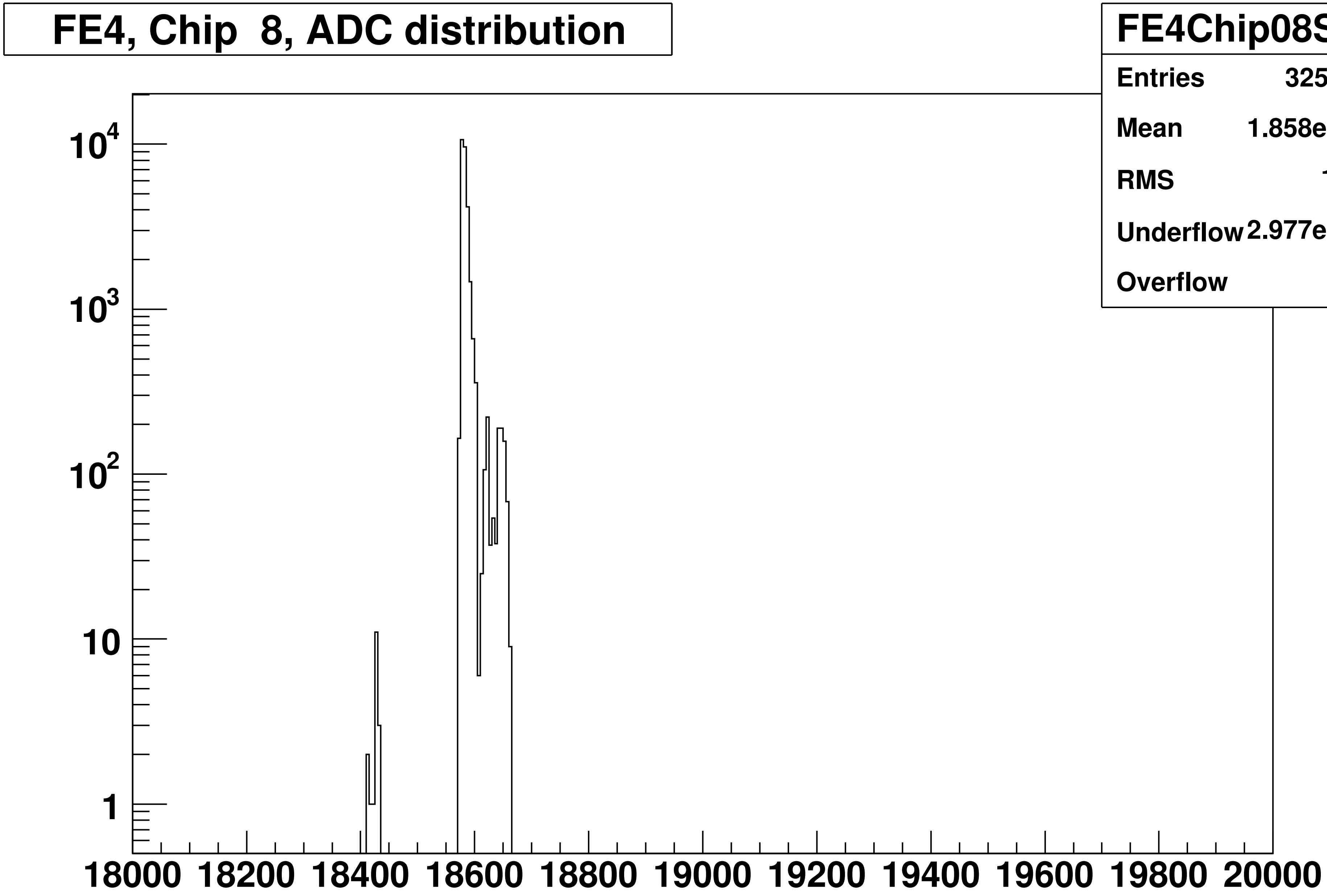
0
50
100
150
200



FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 325908
Mean 1.858e+04
RMS 12.8
Underflow 2.977e+05
Overflow 0
10^4
10^3
10^2
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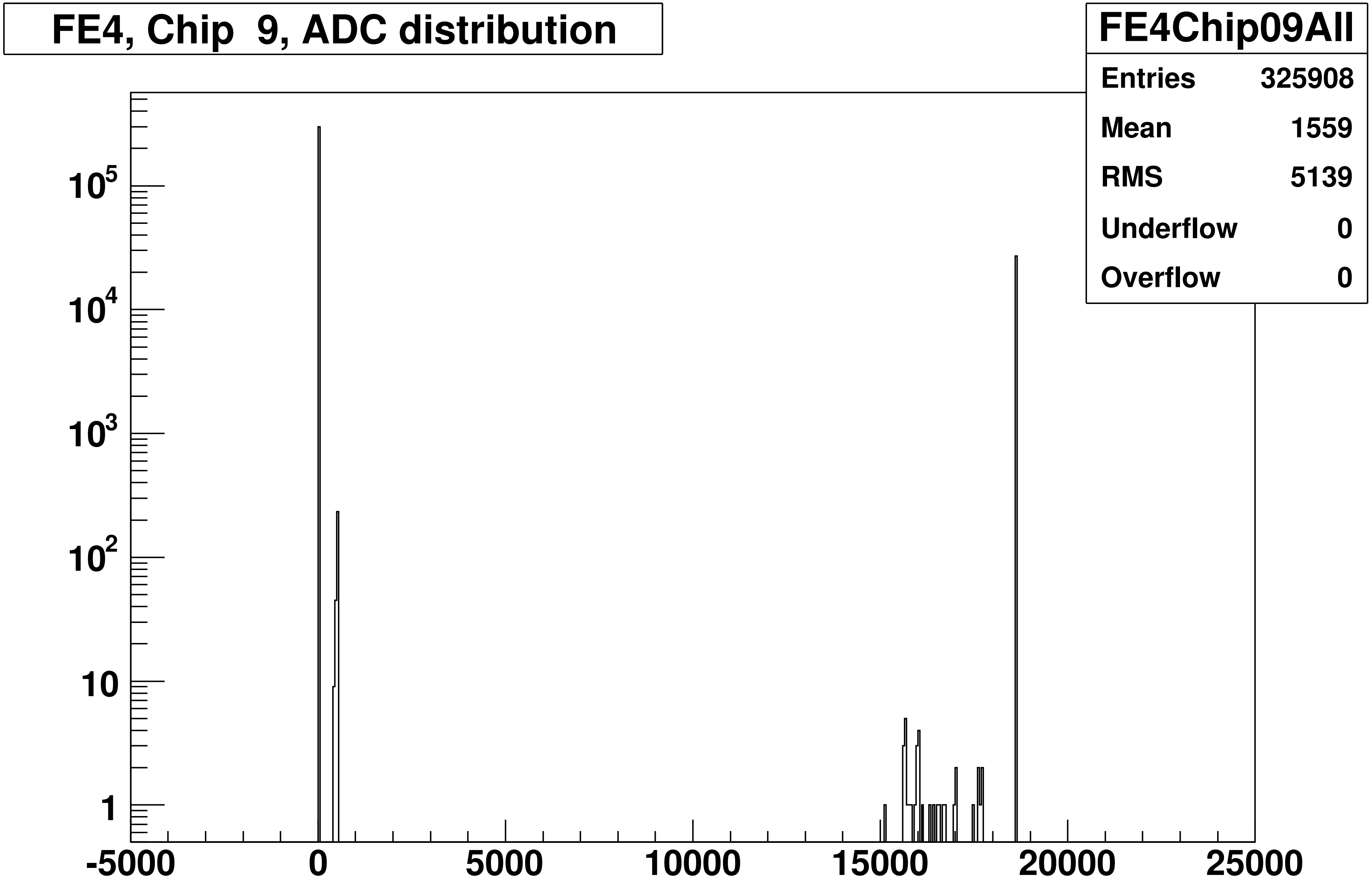
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 325908
Mean 1559
RMS 5139
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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-5000
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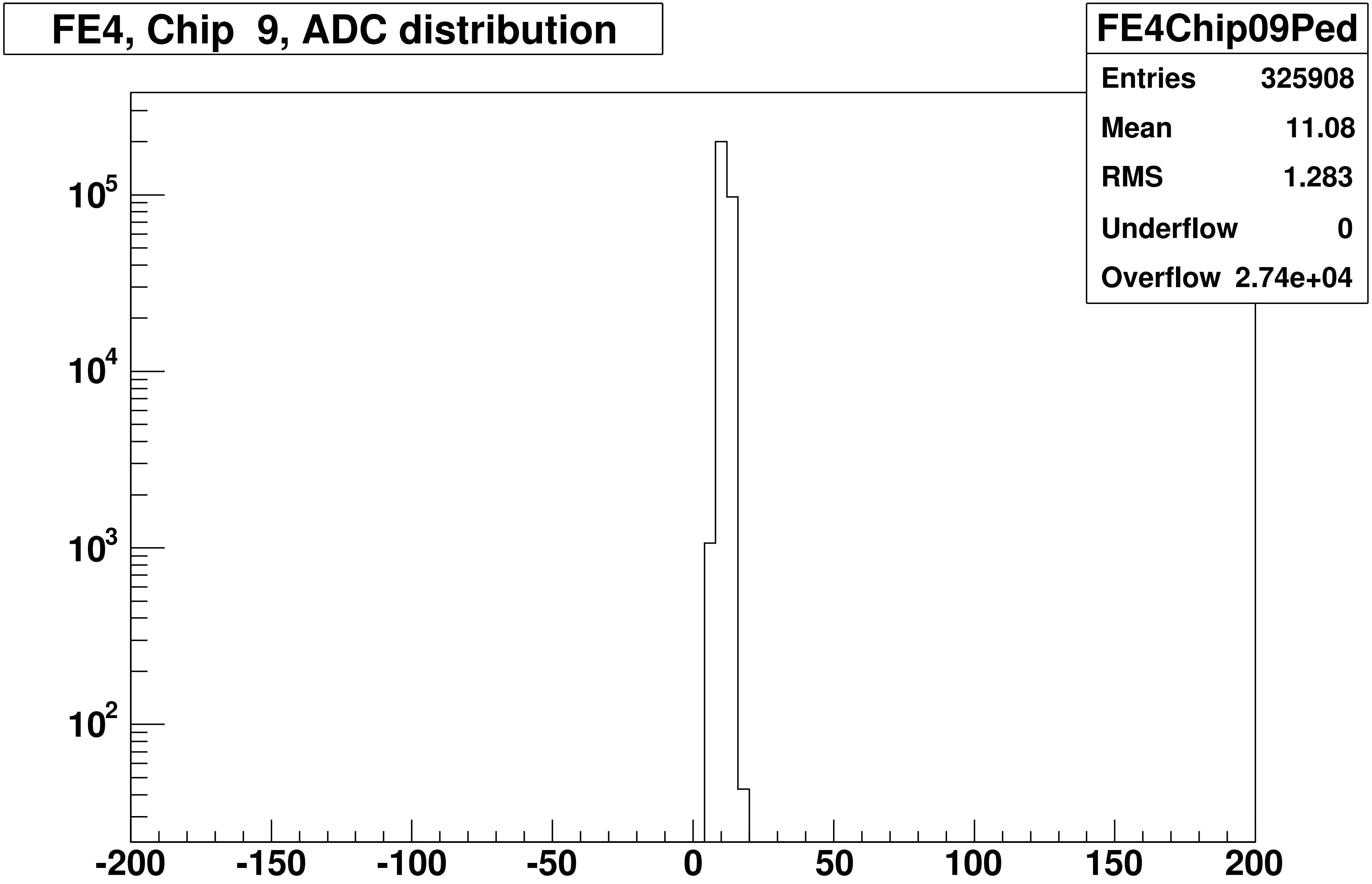
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 325908
Mean 11.08
RMS 1.283
Underflow 0
Overflow 2.74e+04
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10^4
10^3
10^2
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-100
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0
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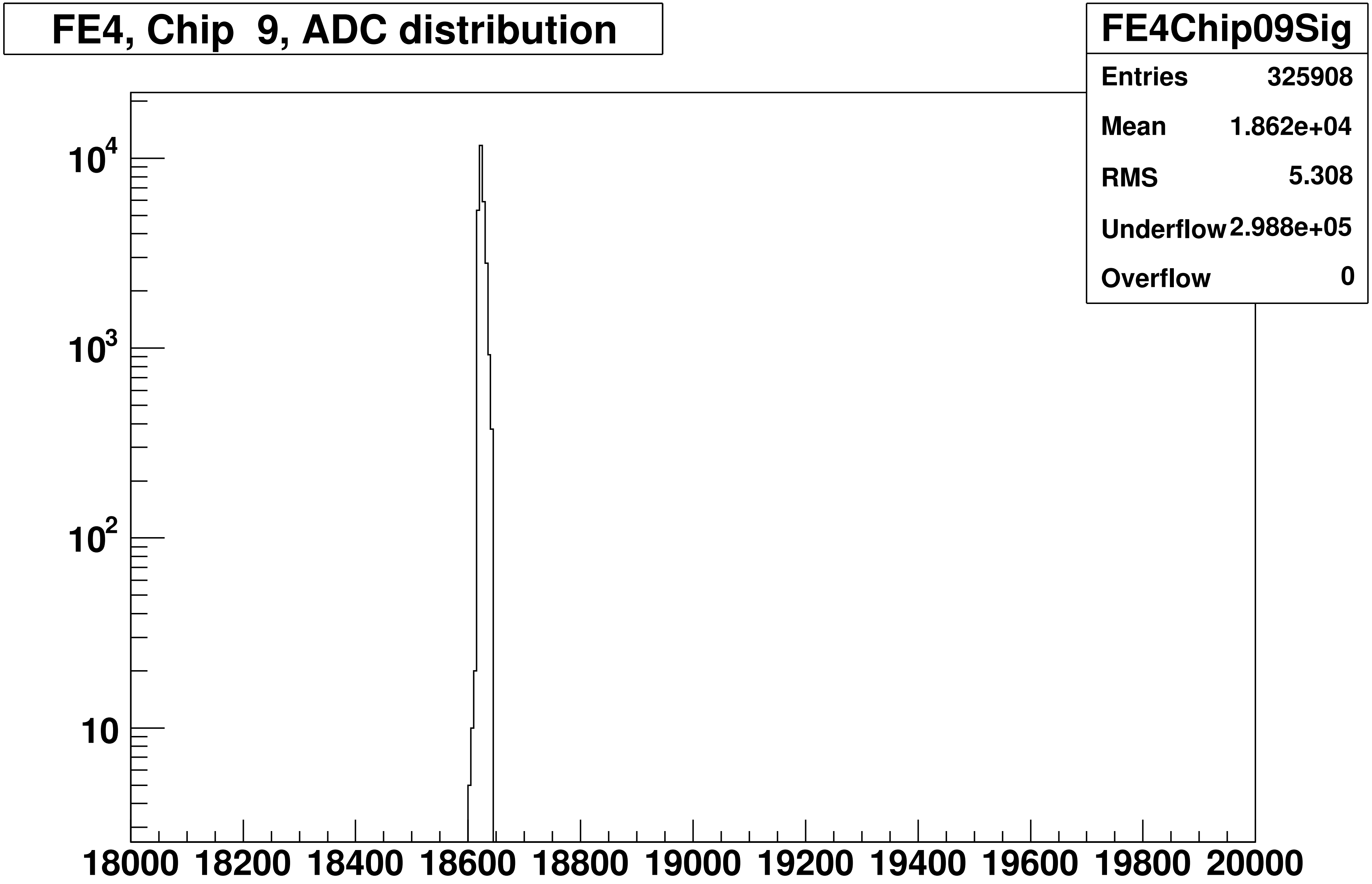
150
200


FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 325908
Mean 1.862e+04
RMS 5.308
Underflow 2.988e+05
Overflow 0
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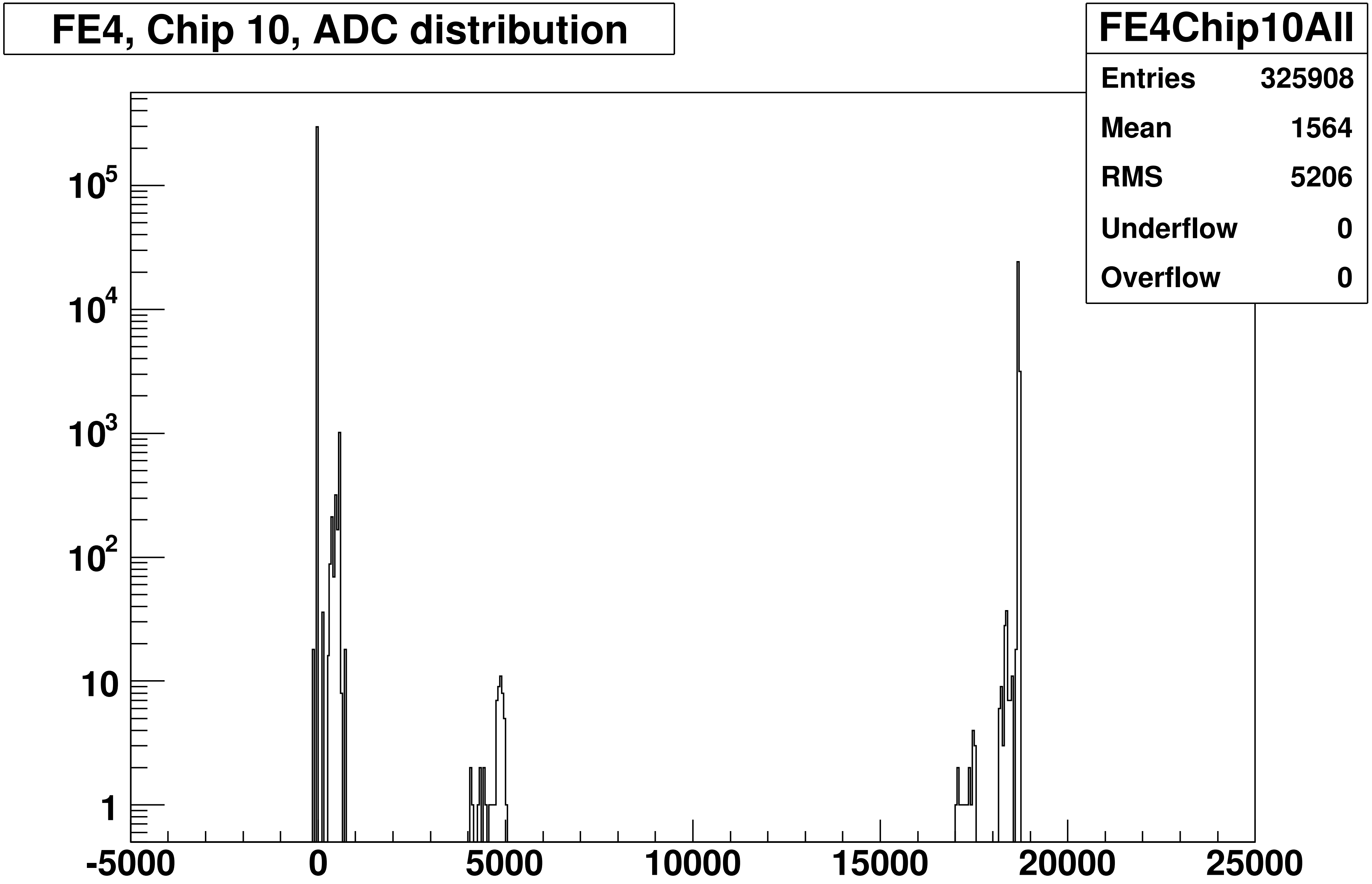
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 325908
Mean 1564
RMS 5206
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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0
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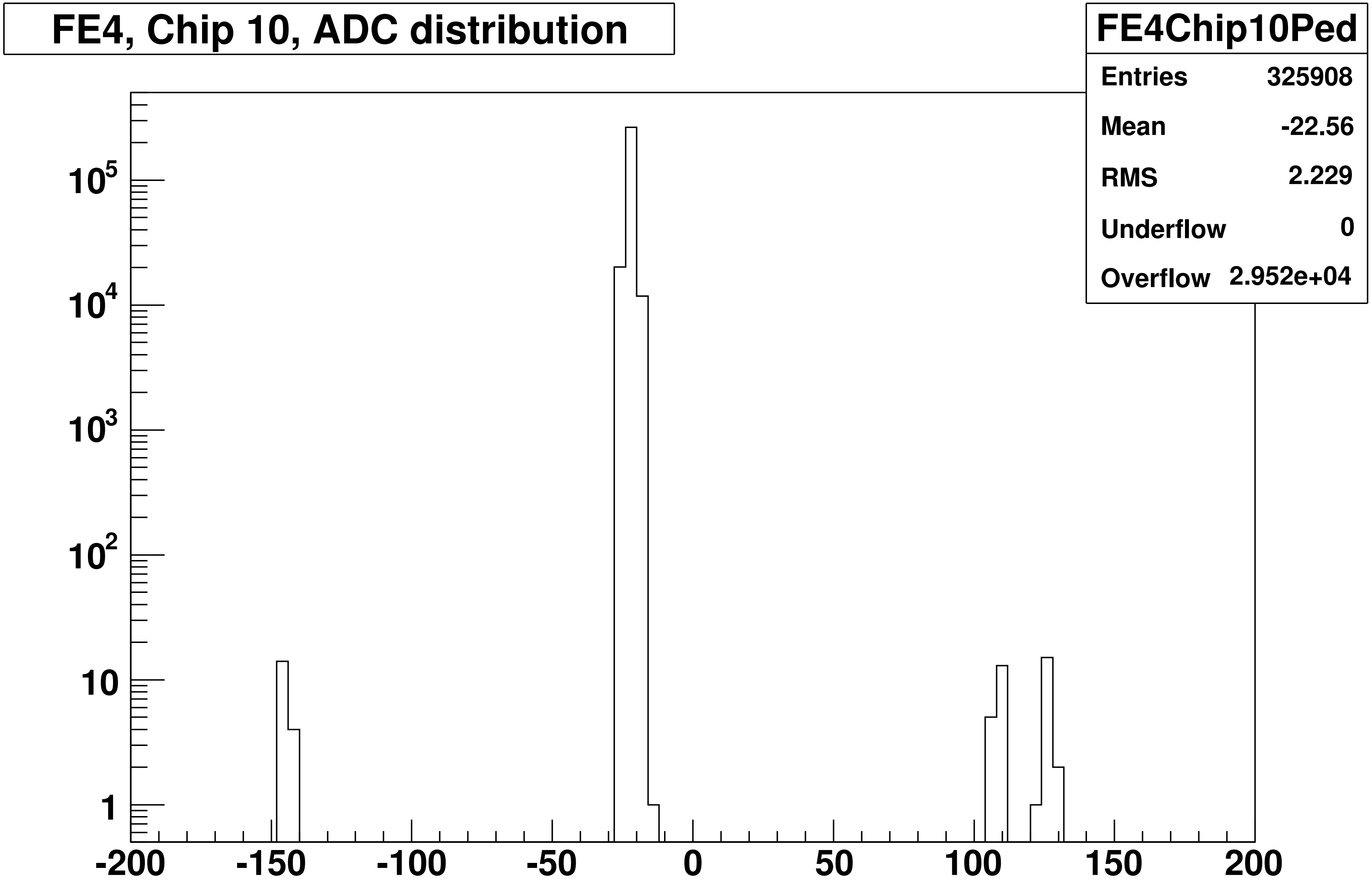
10000
15000
20000
25000



FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 325908
Mean -22.56
RMS 2.229
Underflow 0
Overflow 2.952e+04
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10^4
10^3
10^2
10
1
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-100
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0
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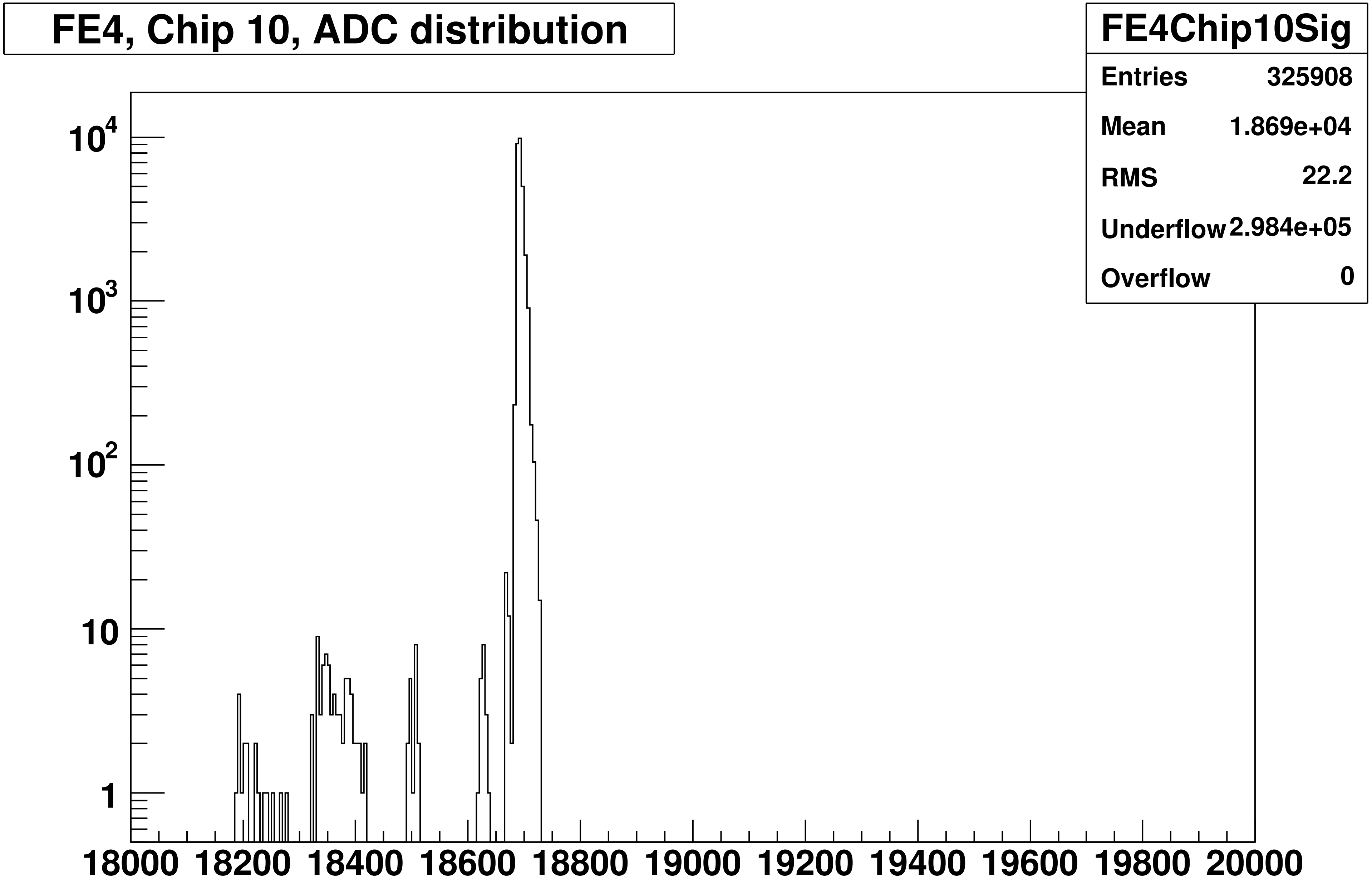
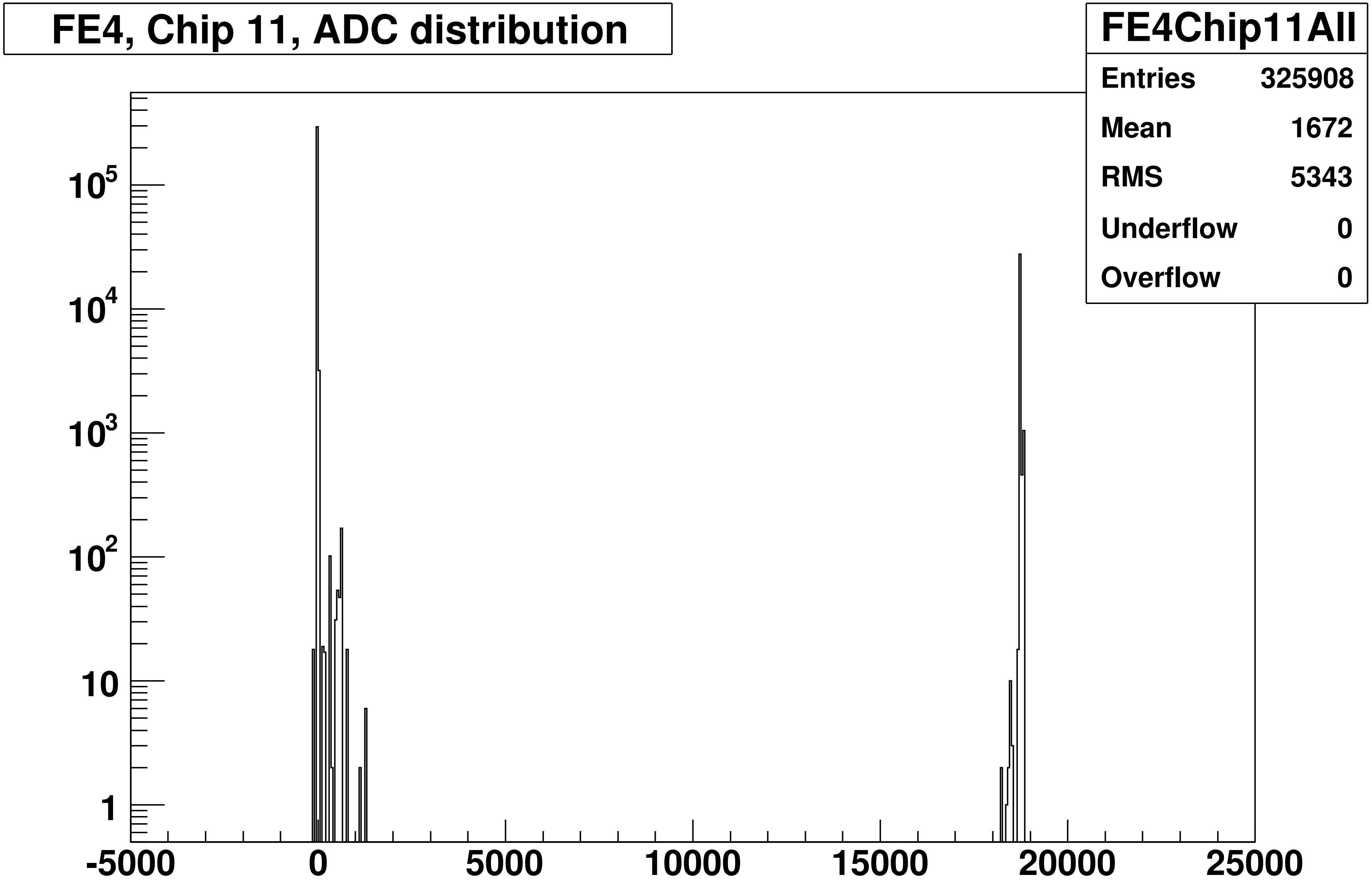
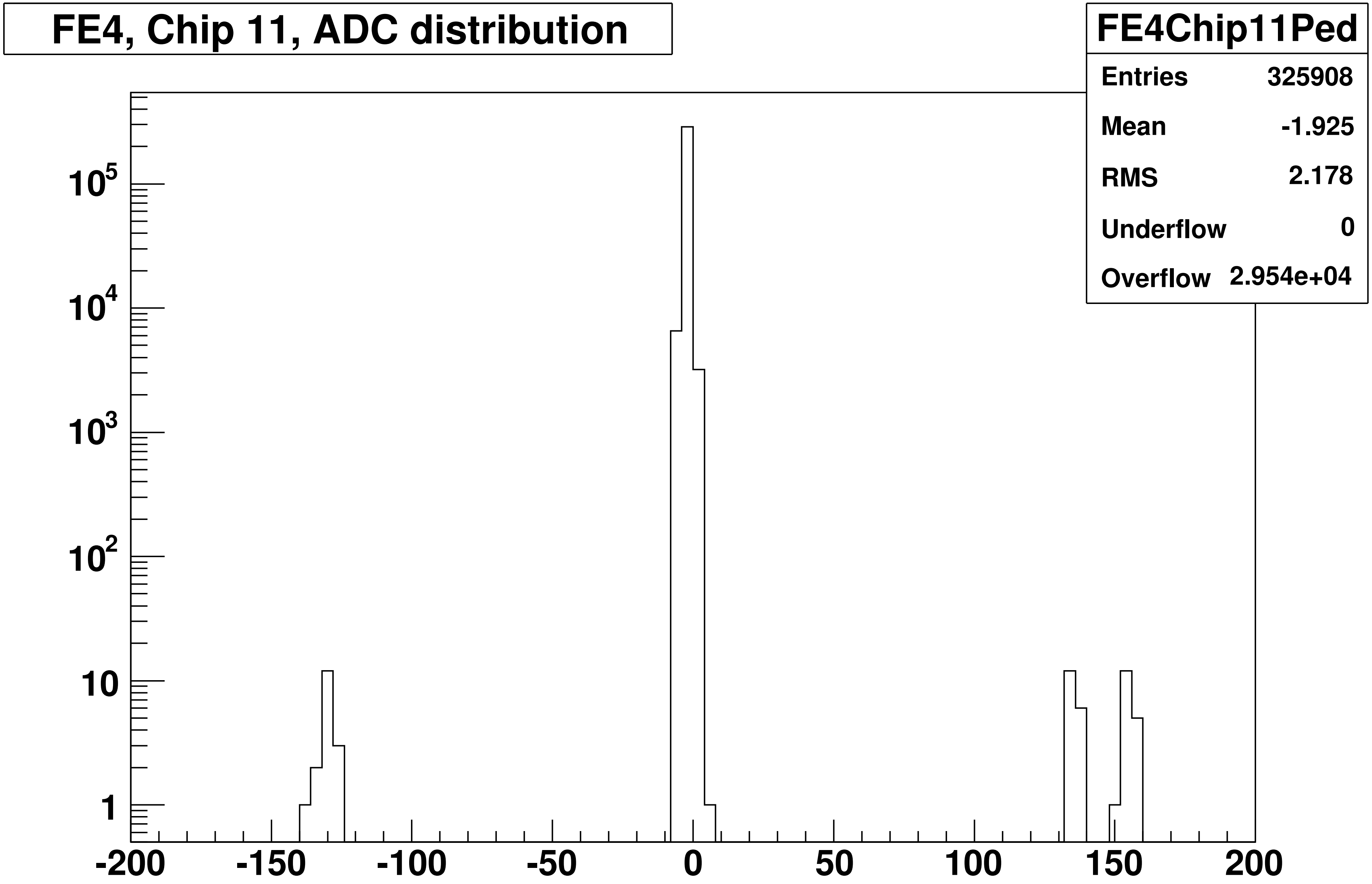
100
150
200


FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 325908
Mean 1.869e+04
RMS 22.2
Underflow 2.984e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 325908
Mean 1672
RMS 5343
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 325908
Mean -1.925
RMS 2.178
Underflow 0
Overflow 2.954e+04
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10^4
10^3
10^2
10
1
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-100
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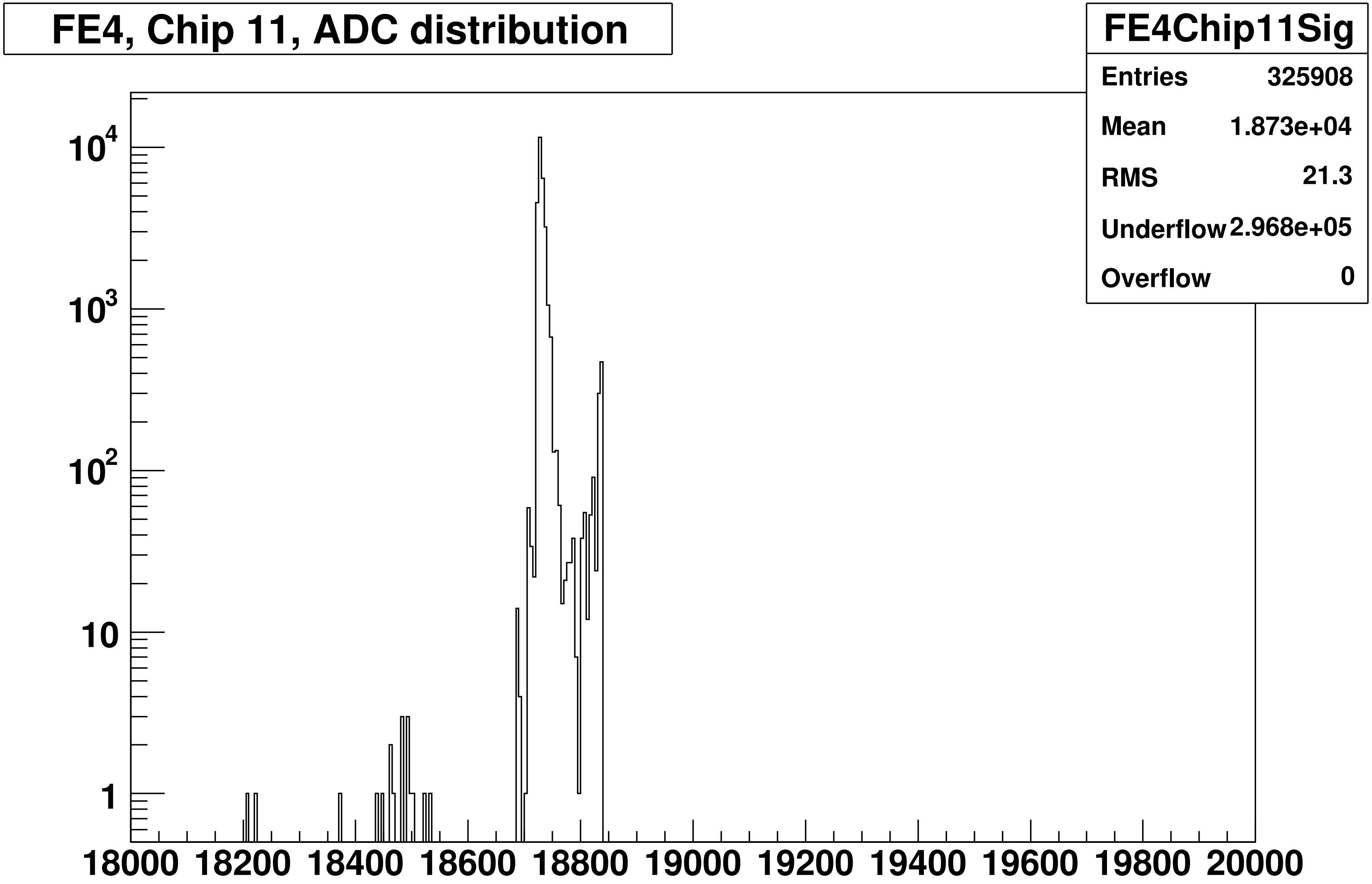
150
200



FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 325908
Mean 1.873e+04
RMS 21.3
Underflow 2.968e+05
Overflow 0
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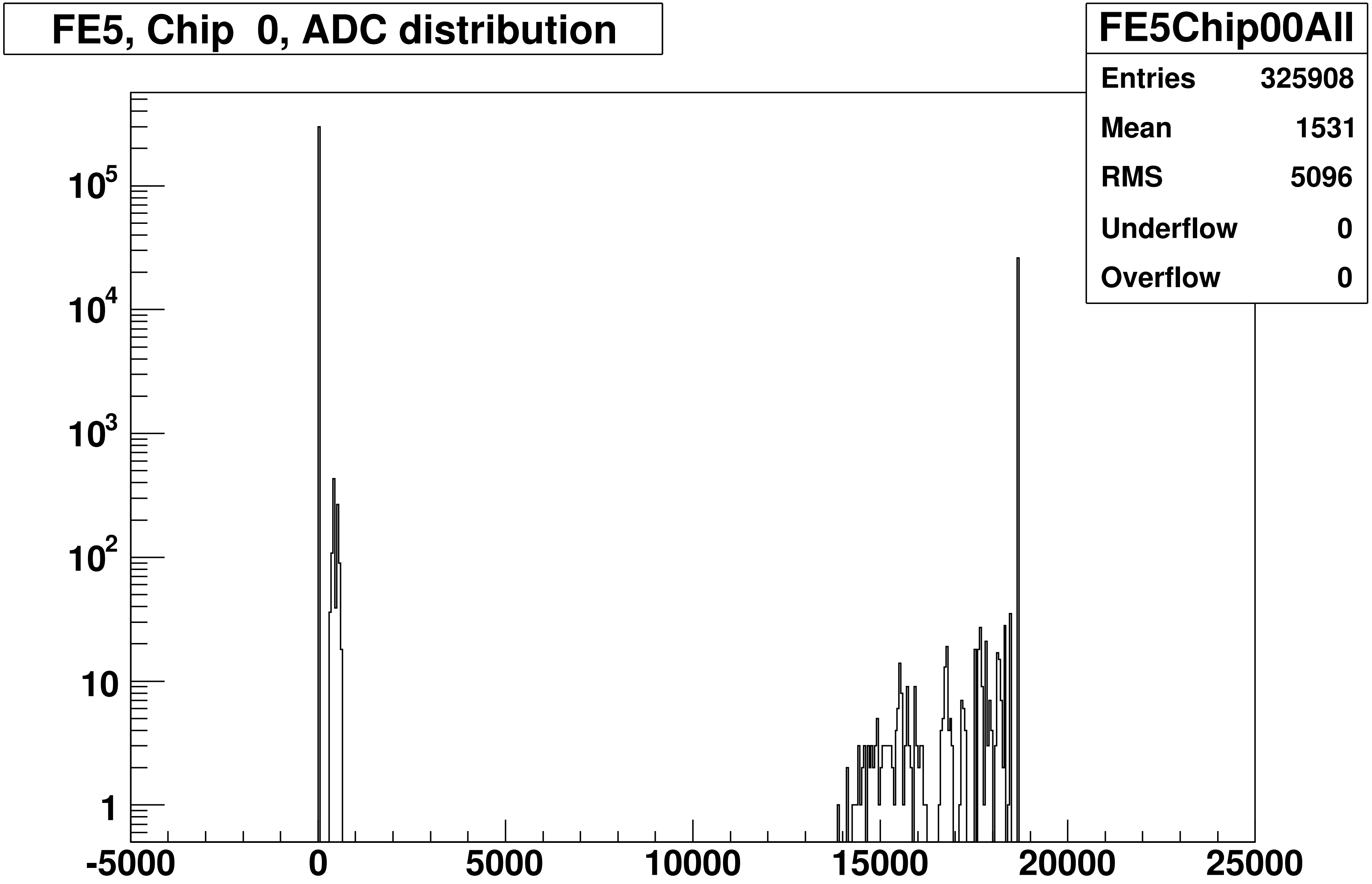
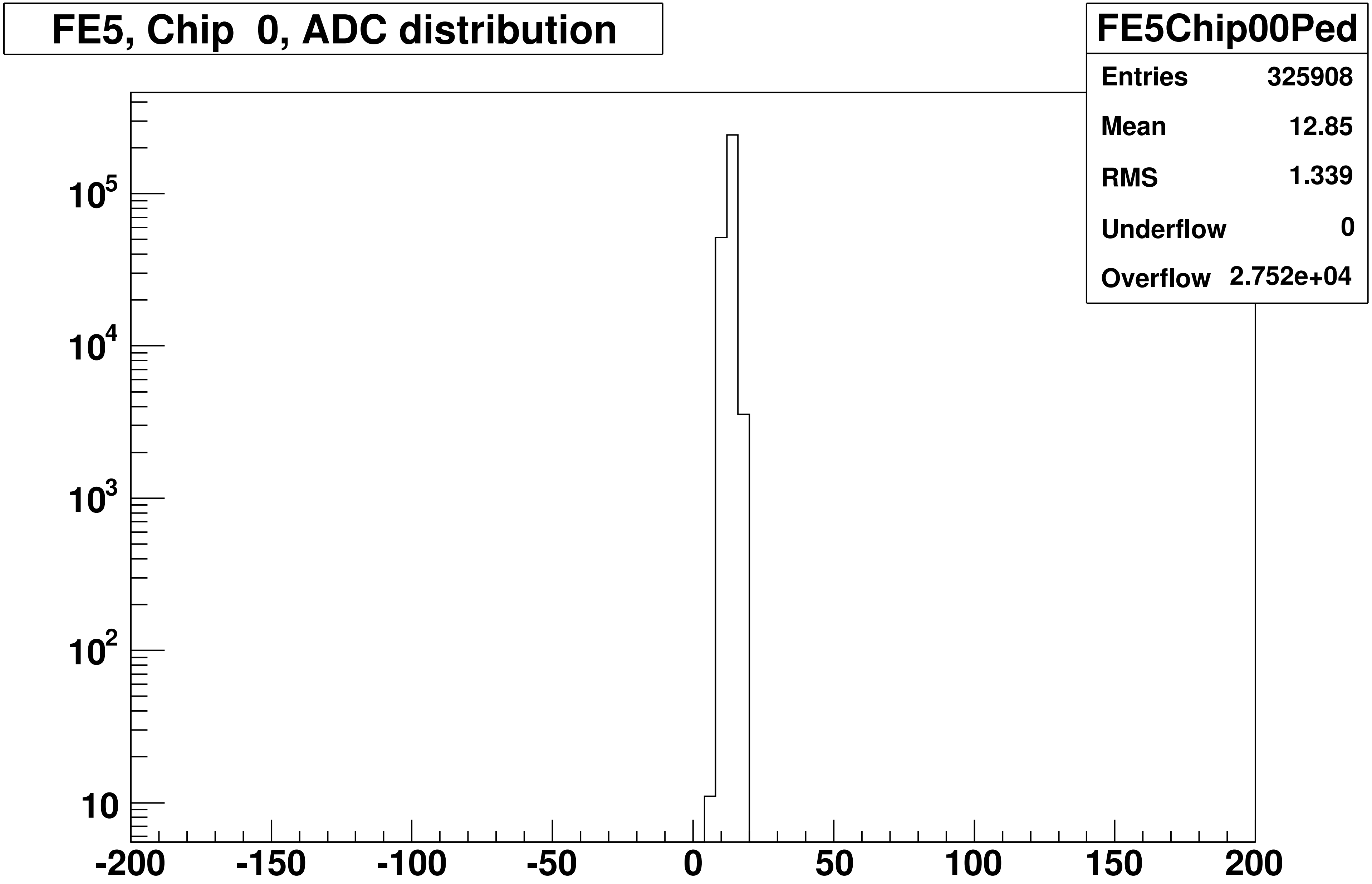
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 325908
Mean 1531
RMS 5096
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 325908
Mean 12.85
RMS 1.339
Underflow 0
Overflow 2.752e+04
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10^4
10^3
10^2
10
-200
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-100
-50
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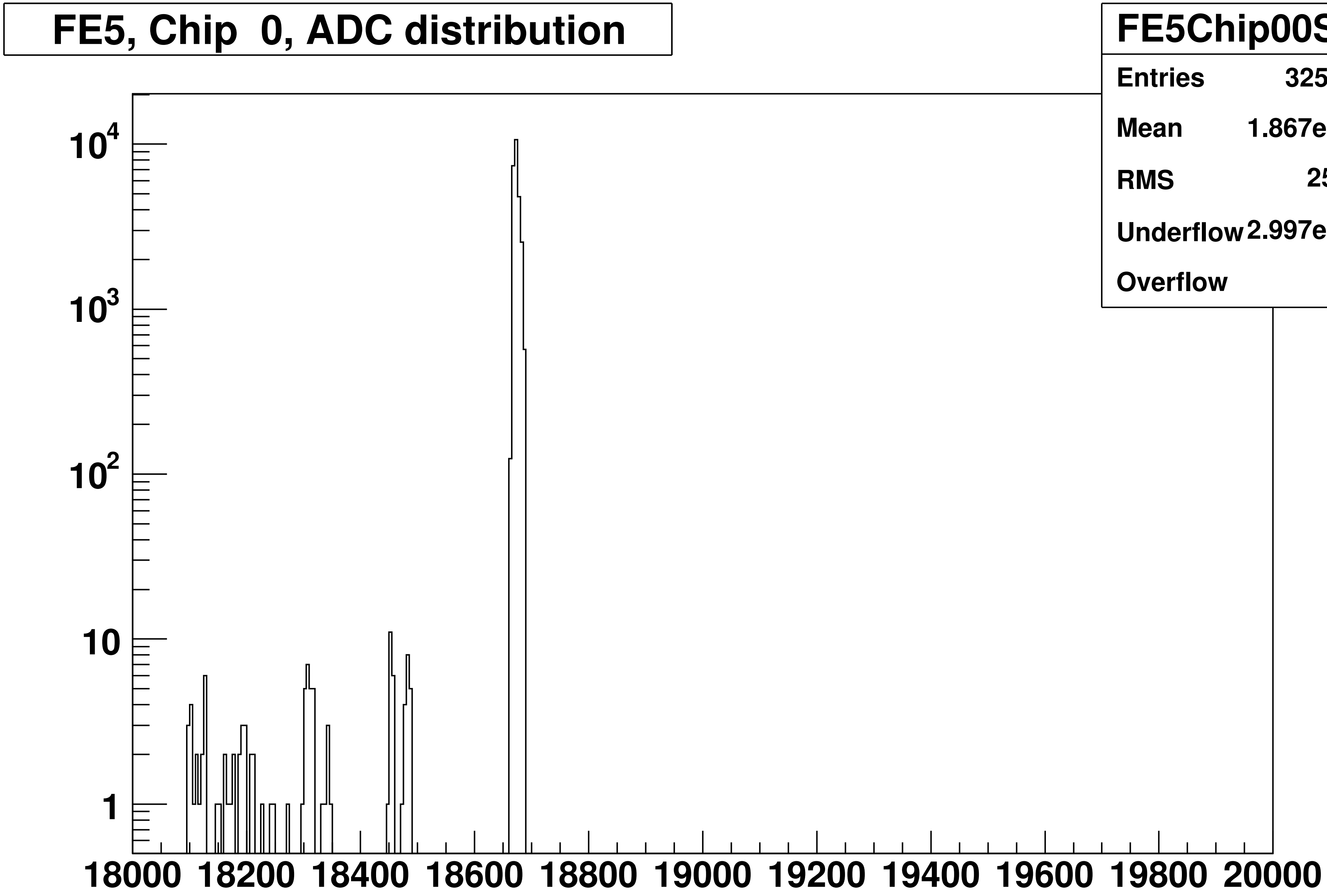
50
100
150
200


FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 325908
Mean 1.867e+04
RMS 25.64
Underflow 2.997e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
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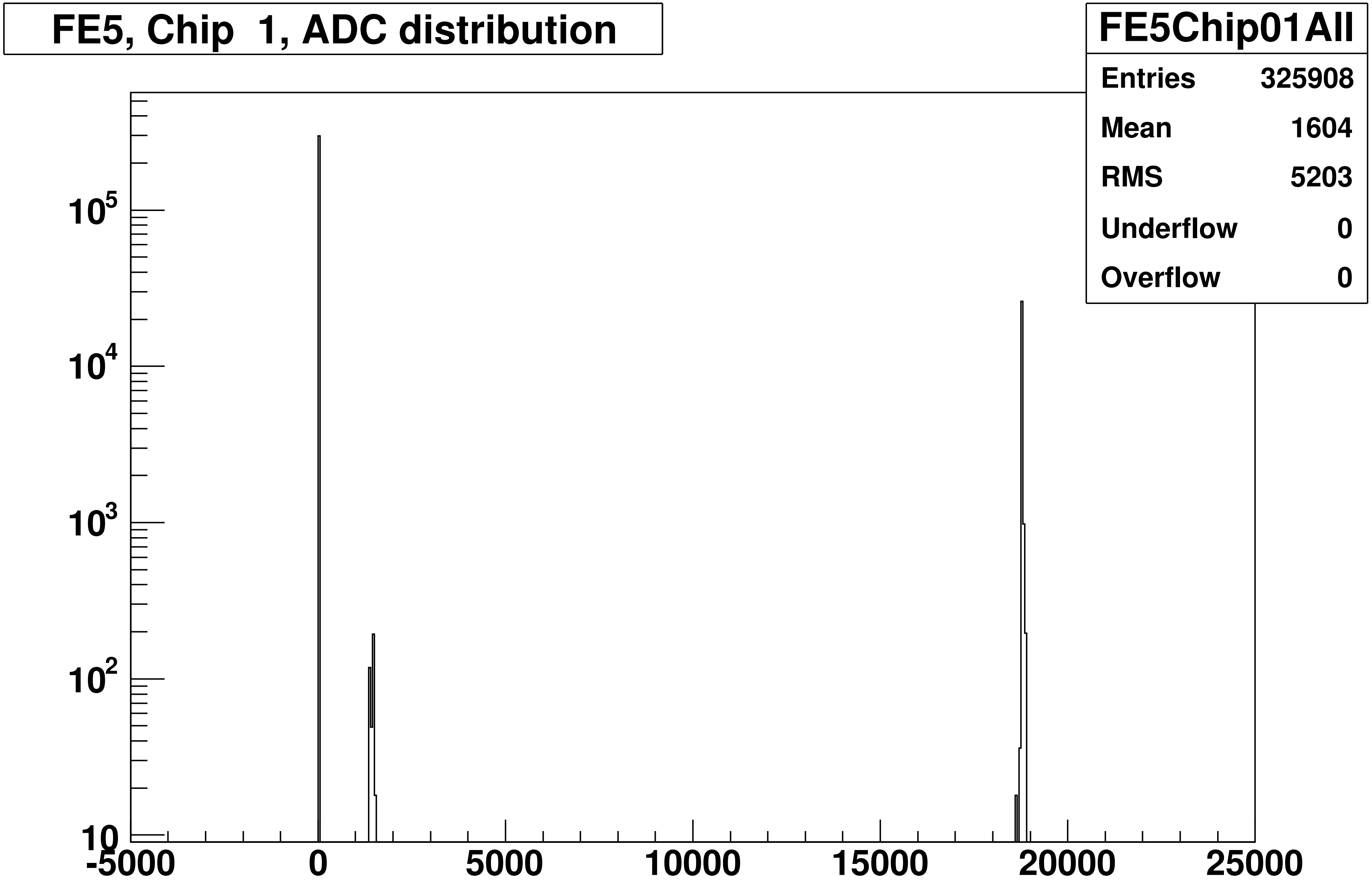
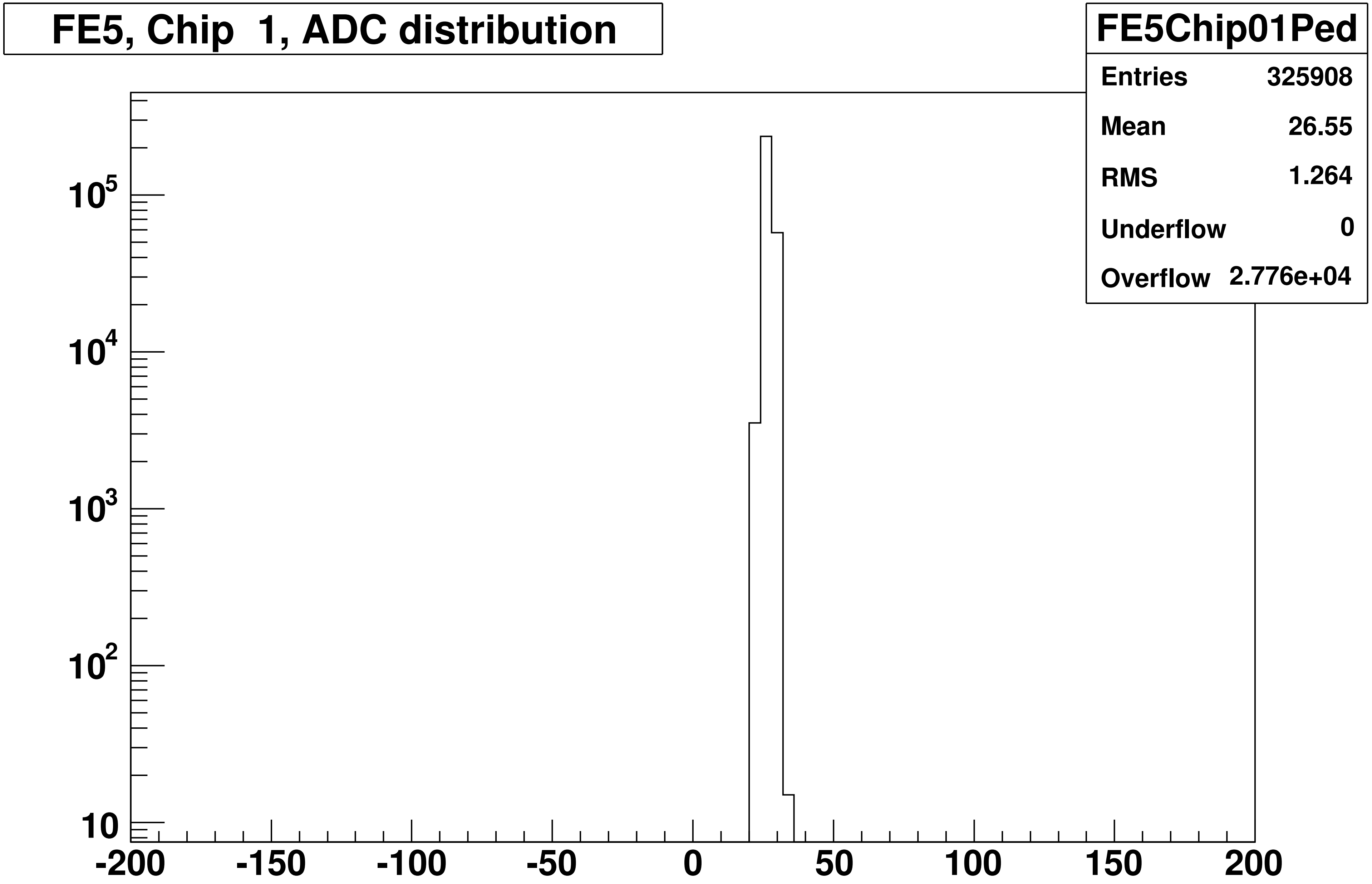
19400
19600
19800
20000


FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 325908
Mean 1604
RMS 5203
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 325908
Mean 26.55
RMS 1.264
Underflow 0
Overflow 2.776e+04
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10^2
10^3
10^4
10^5
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-100
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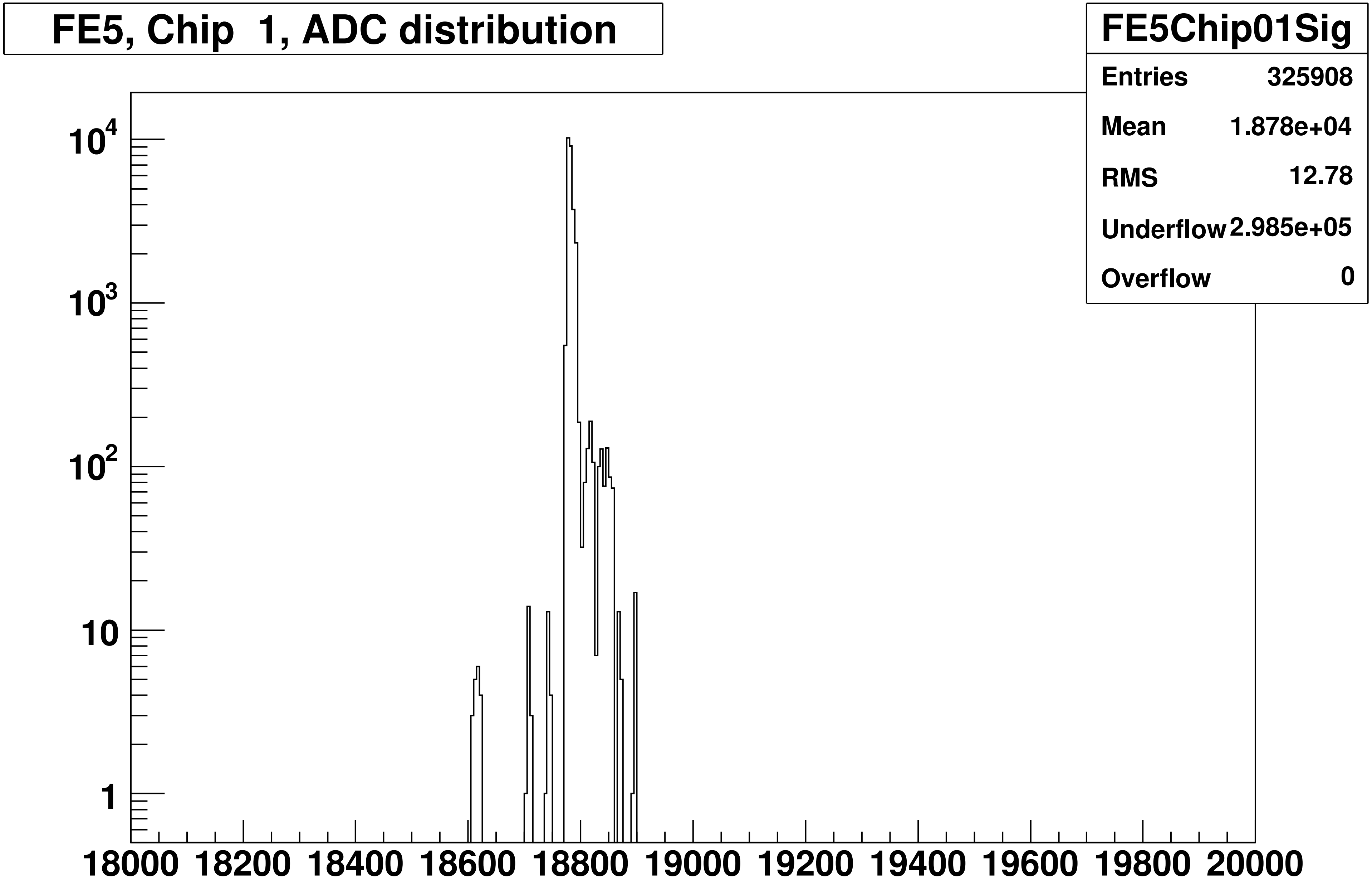
100
150
200


FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 325908
Mean 1.878e+04
RMS 12.78
Underflow 2.985e+05
Overflow 0
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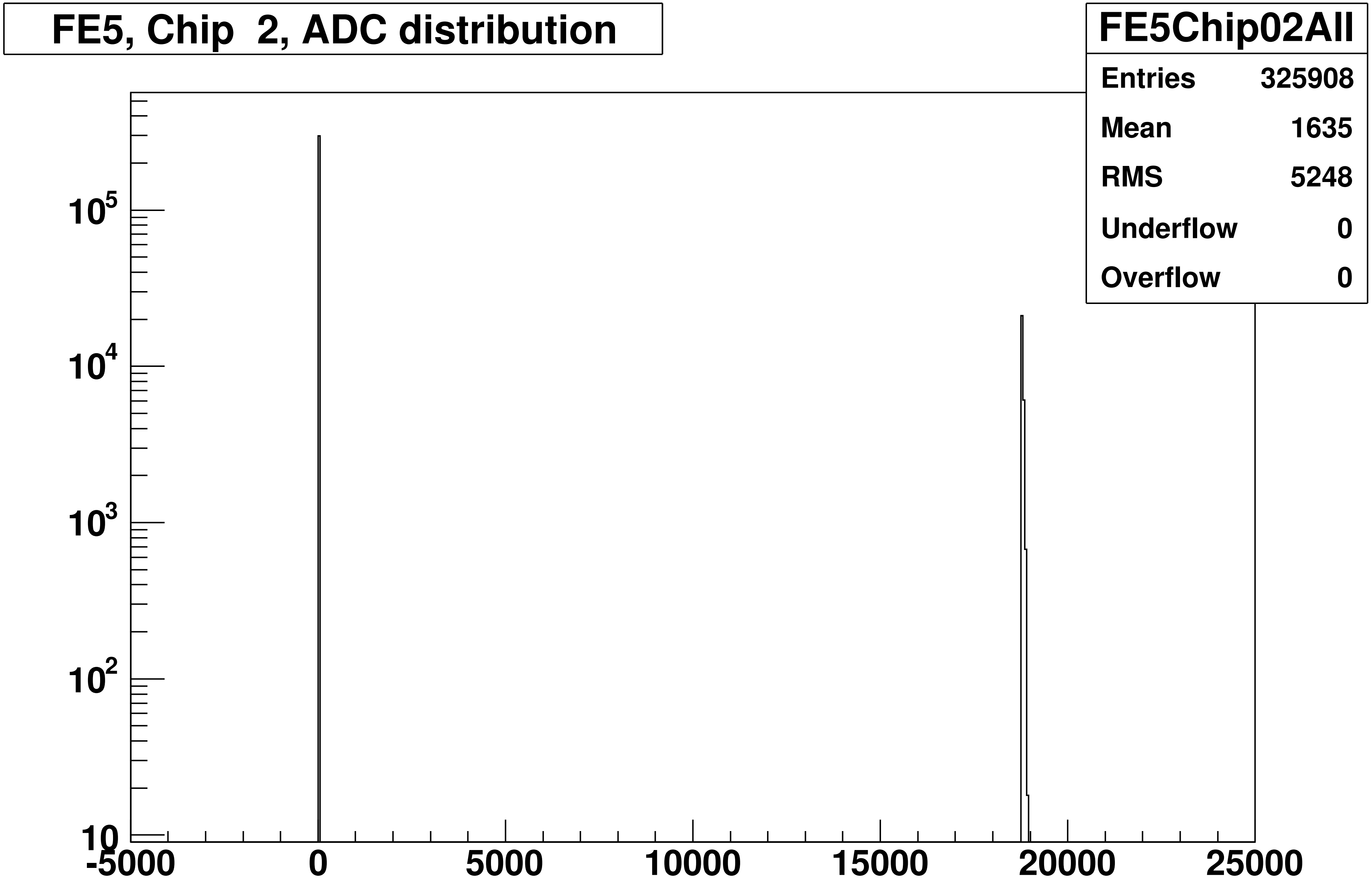
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 325908
Mean 1635
RMS 5248
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
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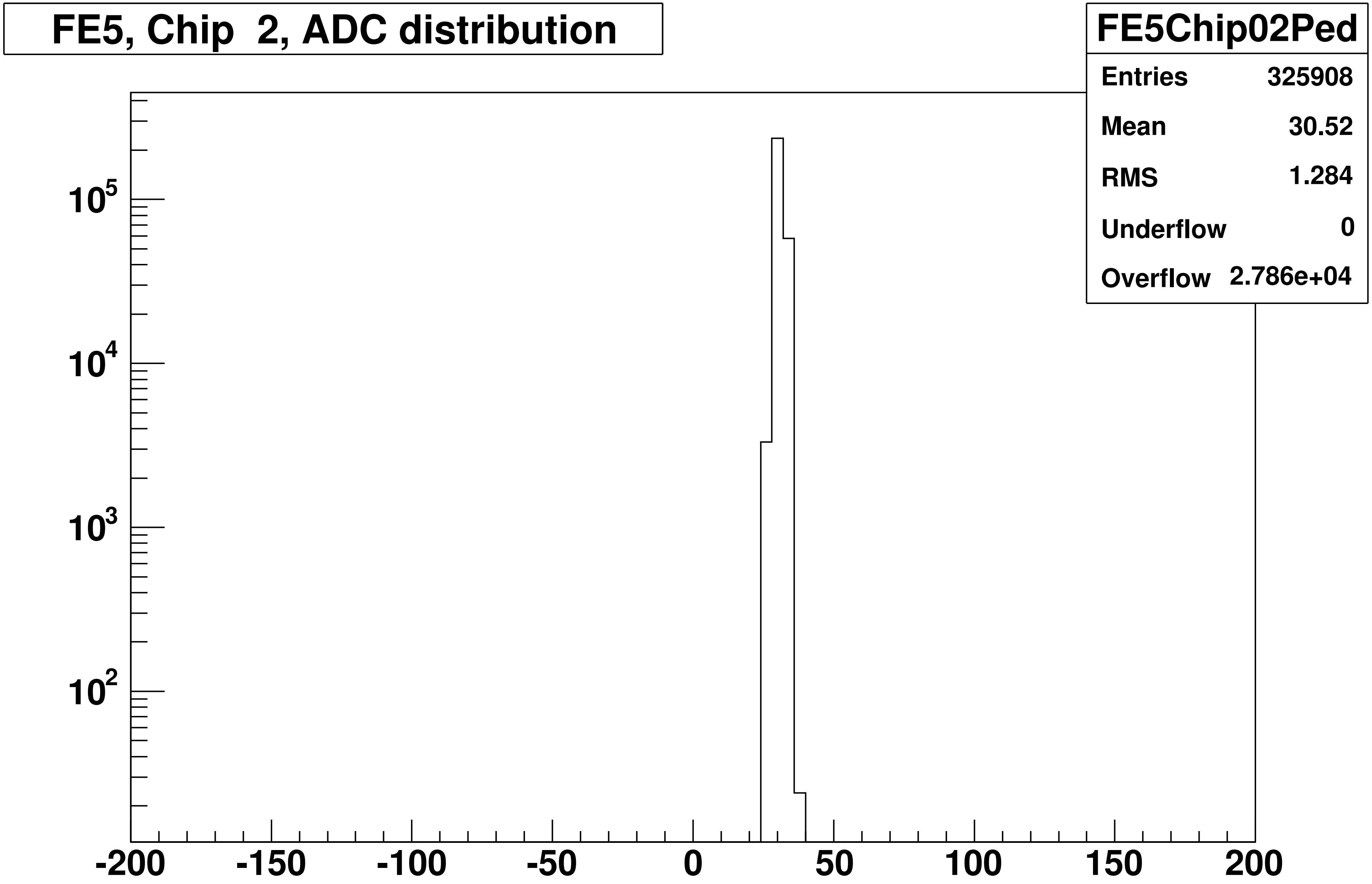

FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 325908
Mean 30.52
RMS 1.284
Underflow 0
Overflow 2.786e+04
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10^4
10^3
10^2
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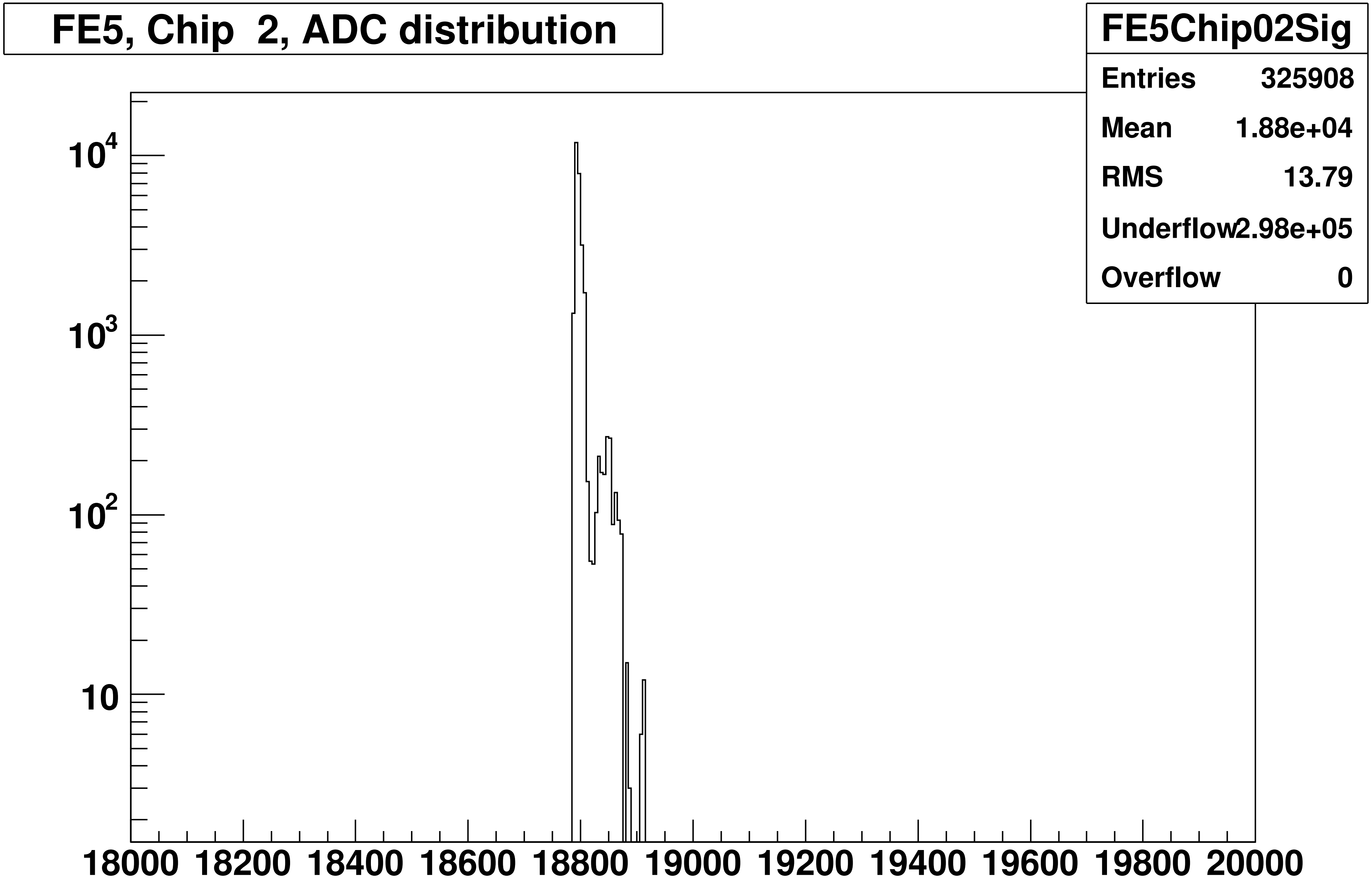
150
200



FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 325908
Mean 1.88e+04
RMS 13.79
Underflow 2.98e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
18800
19000
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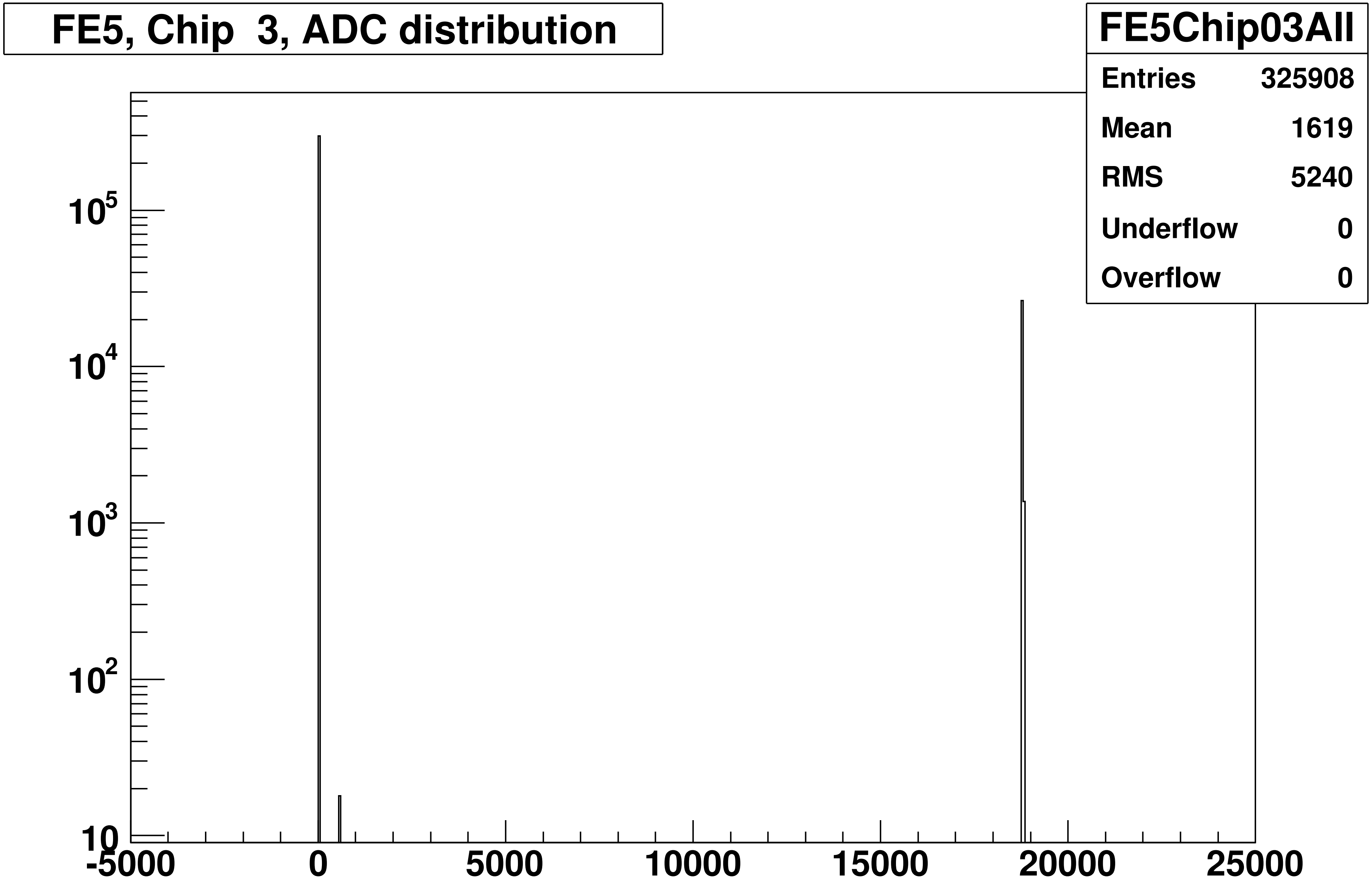
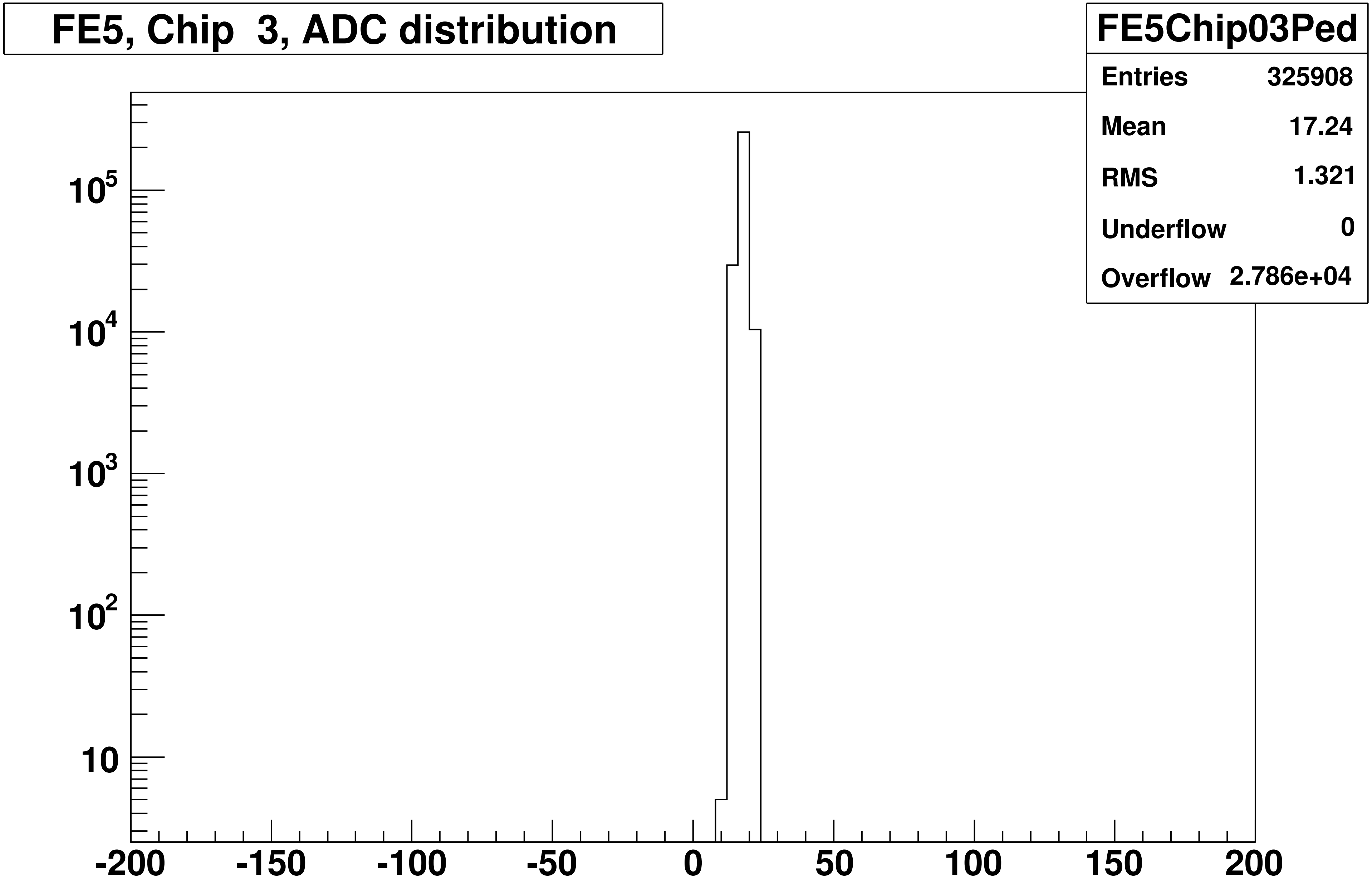
19400
19600
19800
20000



FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 325908
Mean 1619
RMS 5240
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 325908
Mean 17.24
RMS 1.321
Underflow 0
Overflow 2.786e+04
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10^4
10^3
10^2
10
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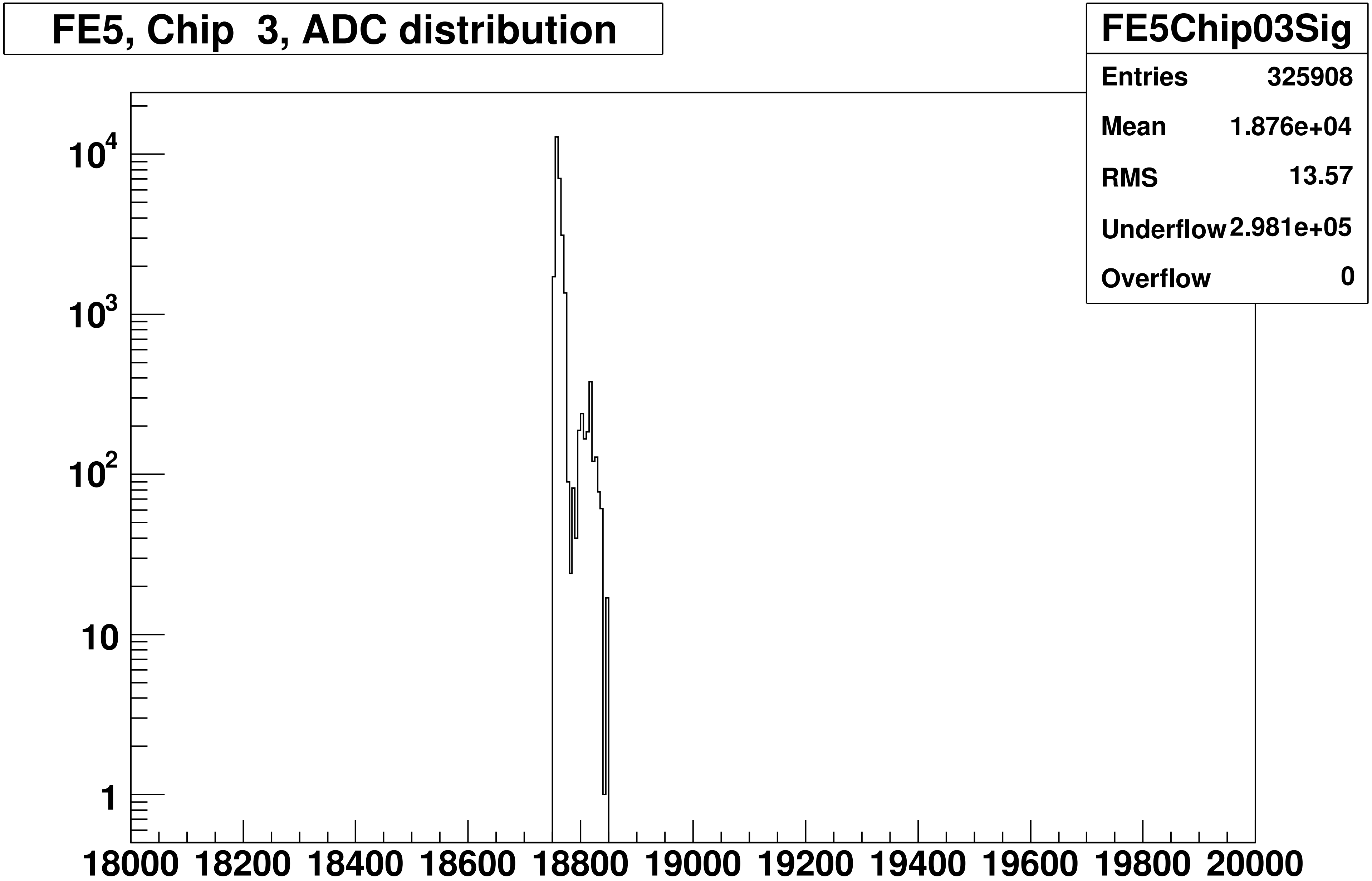
150
200


FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 325908
Mean 1.876e+04
RMS 13.57
Underflow 2.981e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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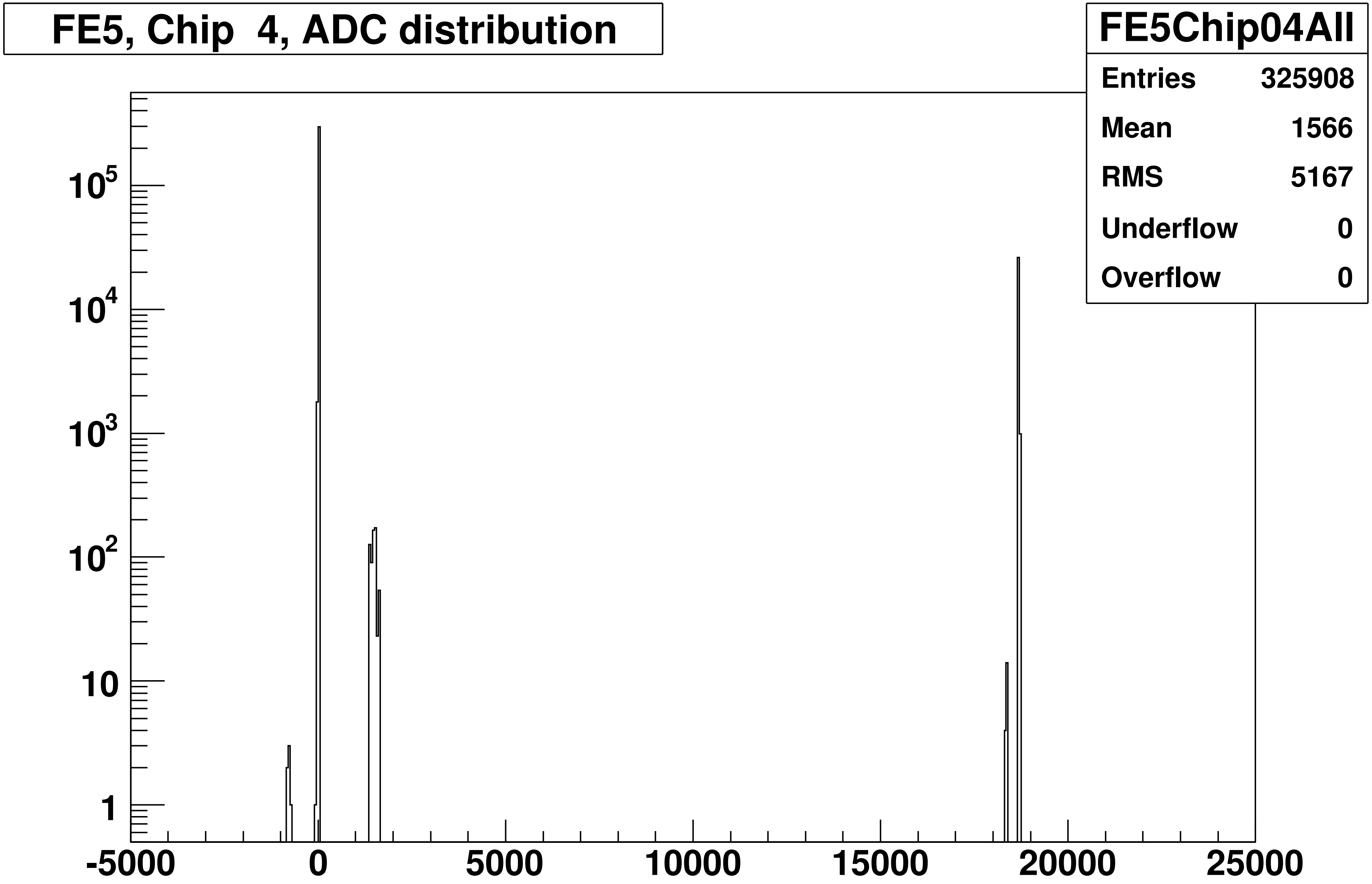
20000


FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 325908
Mean 1566
RMS 5167
Underflow 0
Overflow 0
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10^3
10^2
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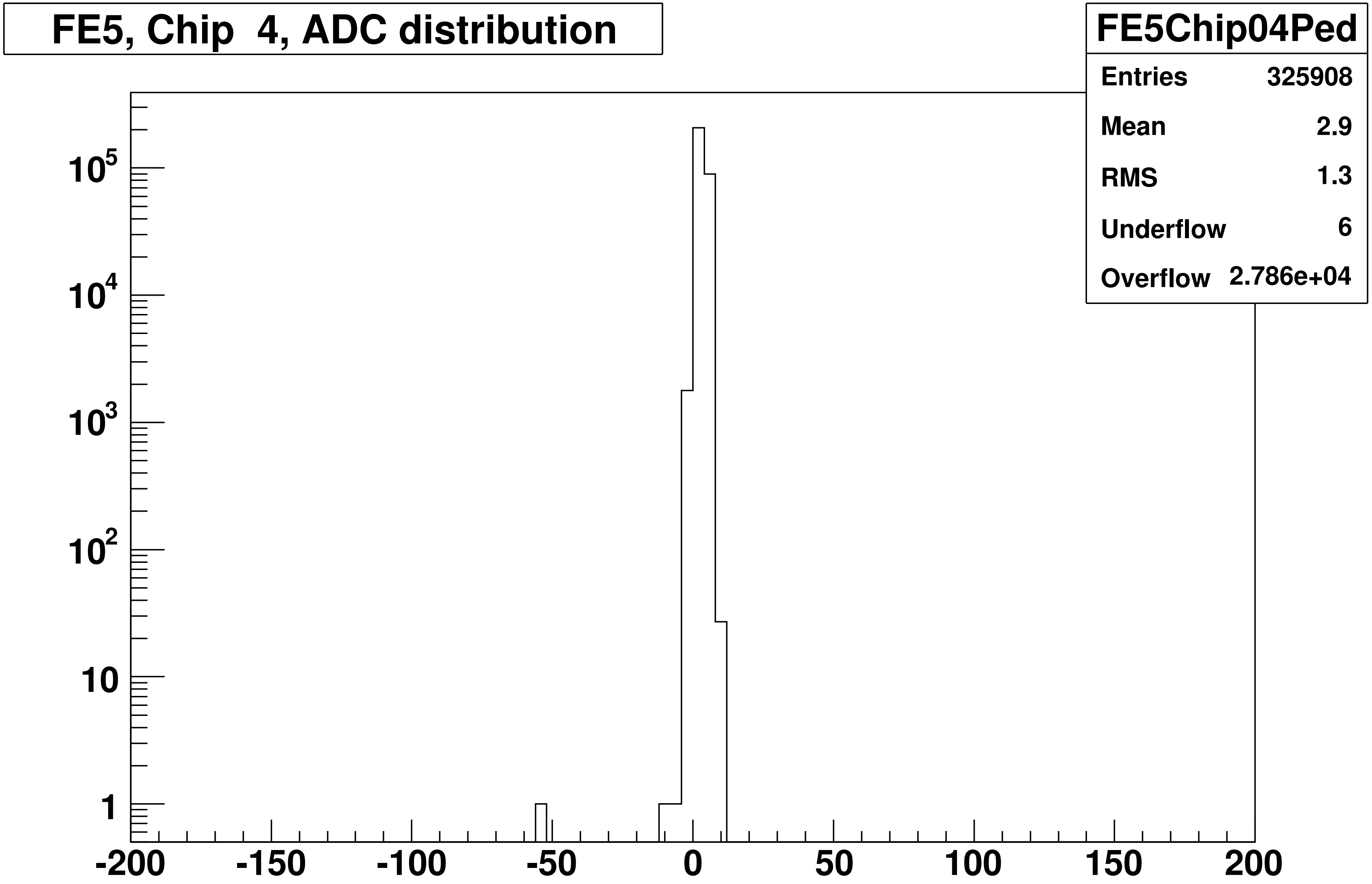
FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 325908
Mean 2.9
RMS 1.3
Underflow 6
Overflow 2.786e+04
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10^3
10^2
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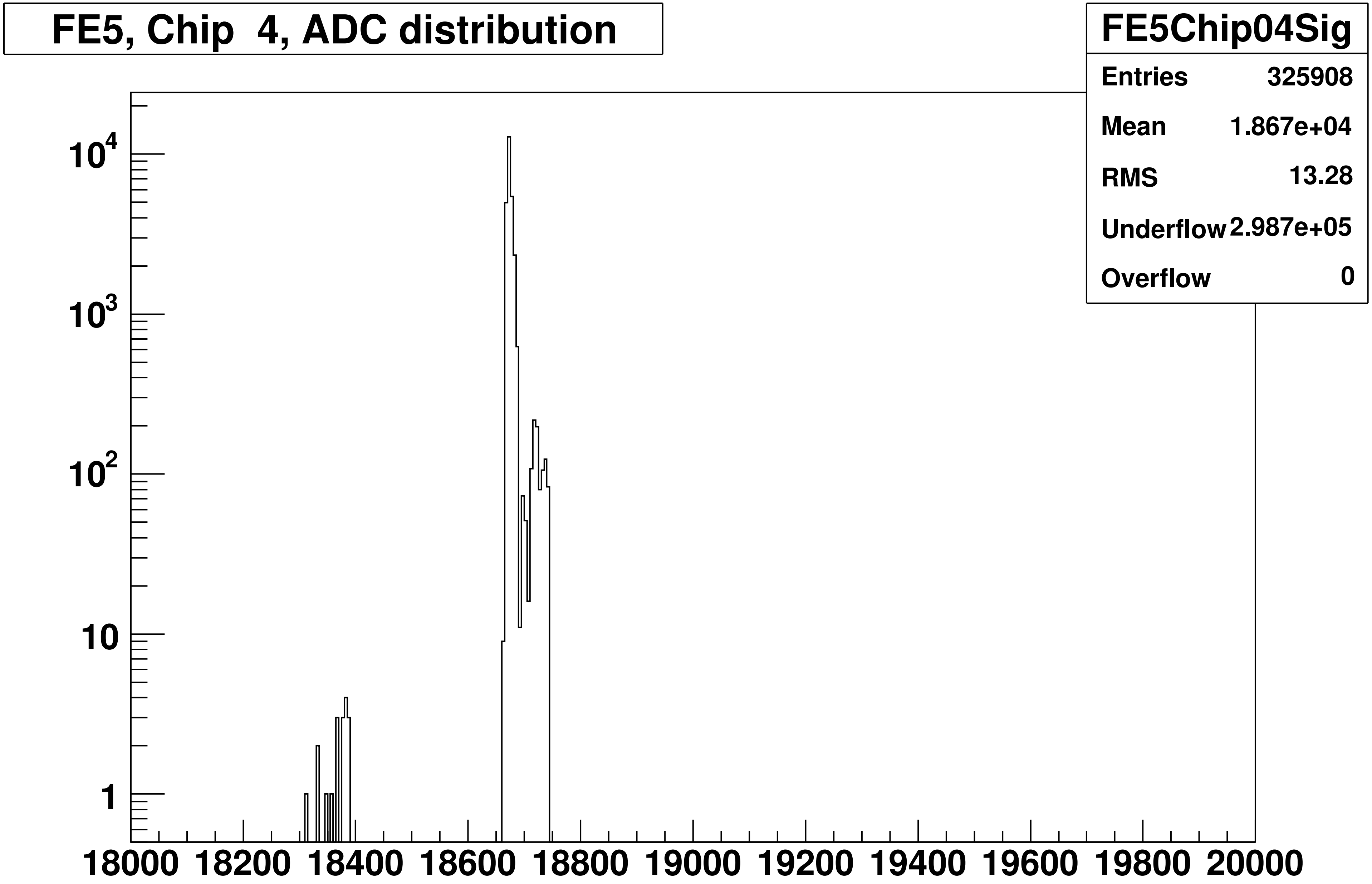
150
200


FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 325908
Mean 1.867e+04
RMS 13.28
Underflow 2.987e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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19000
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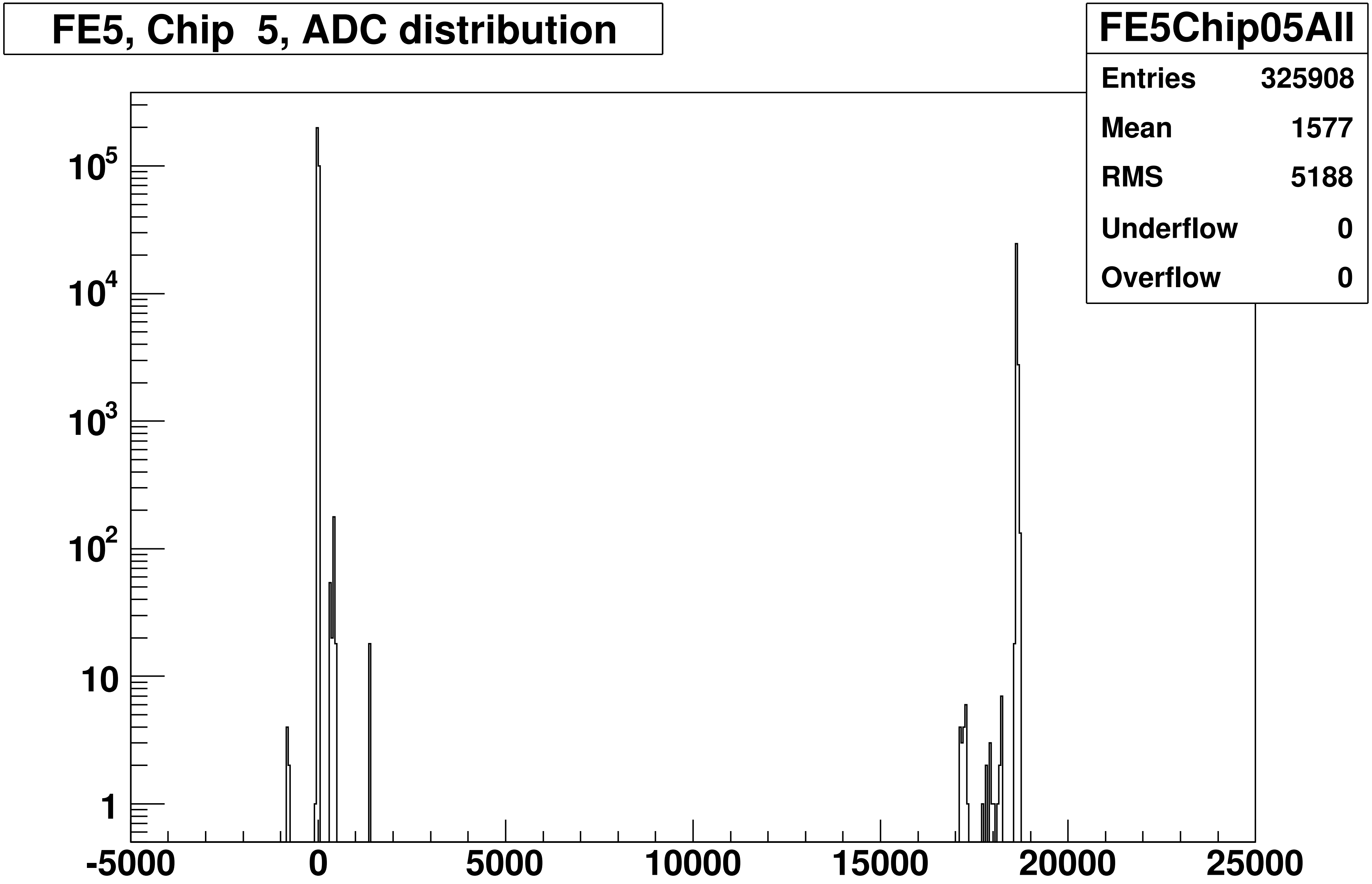
20000



FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 325908
Mean 1577
RMS 5188
Underflow 0
Overflow 0
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10^4
10^3
10^2
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0
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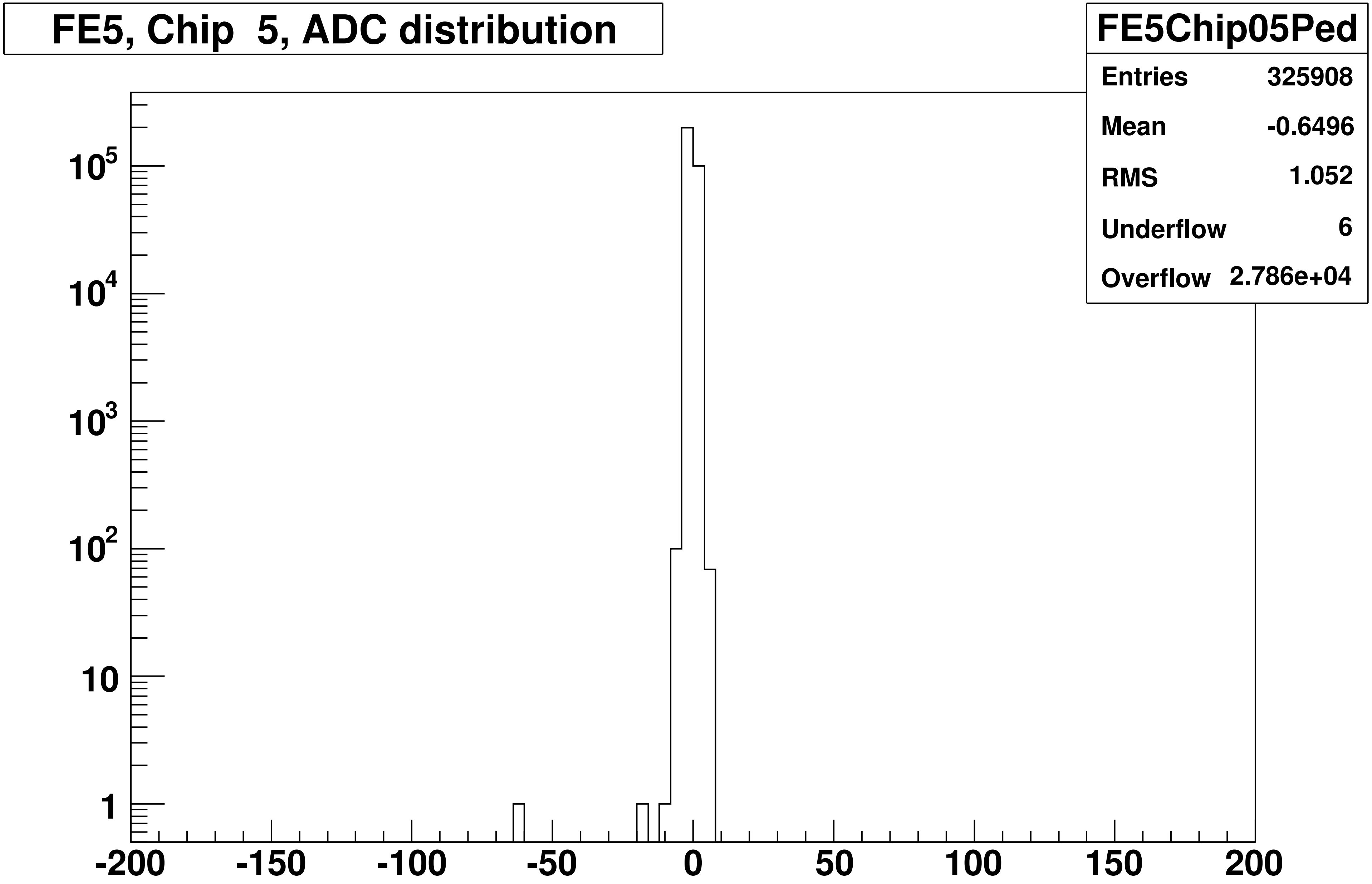

FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 325908
Mean -0.6496
RMS 1.052
Underflow 6
Overflow 2.786e+04
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10^3
10^2
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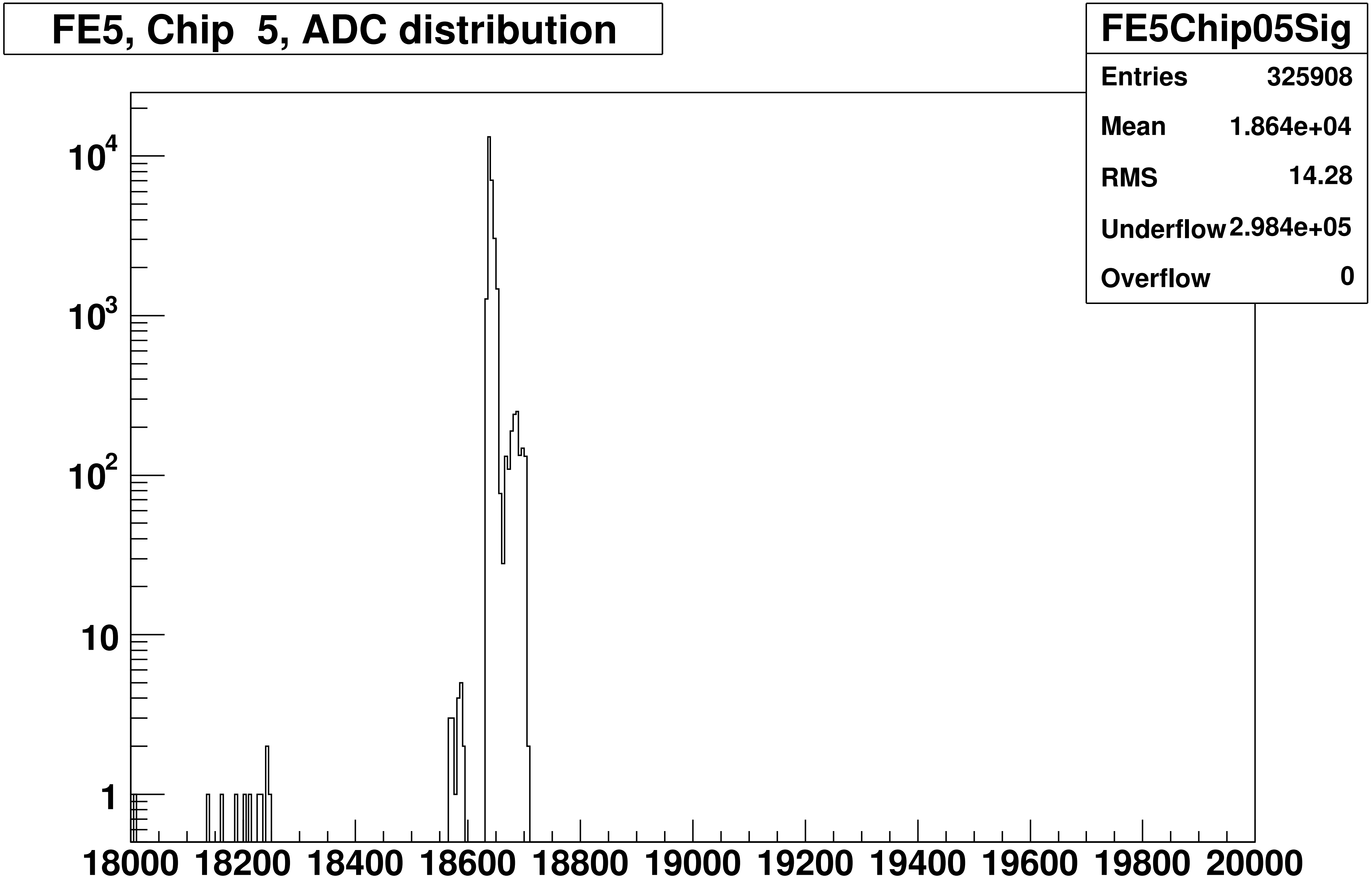
100
150
200


FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 325908
Mean 1.864e+04
RMS 14.28
Underflow 2.984e+05
Overflow 0
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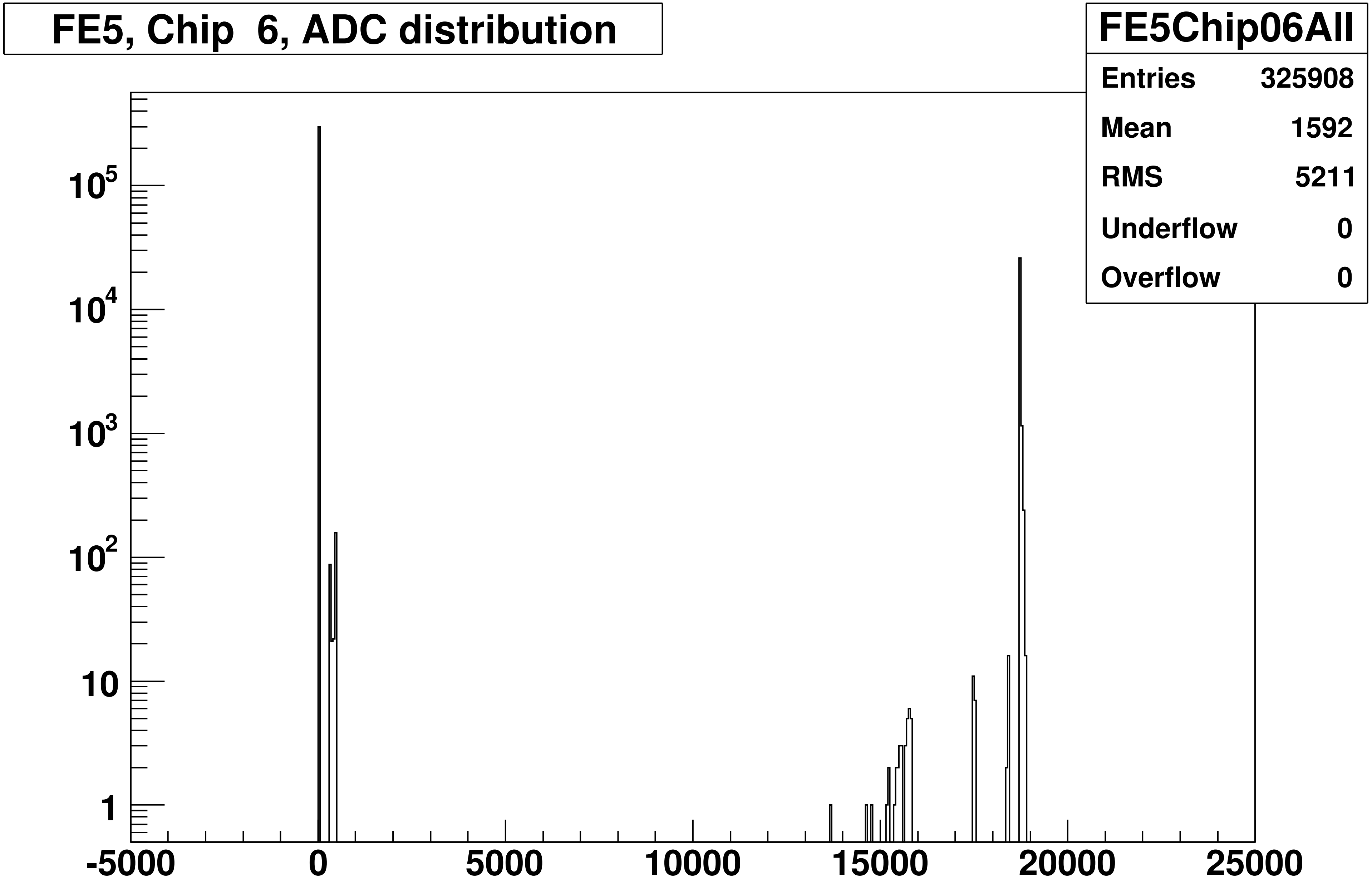
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 325908
Mean 1592
RMS 5211
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
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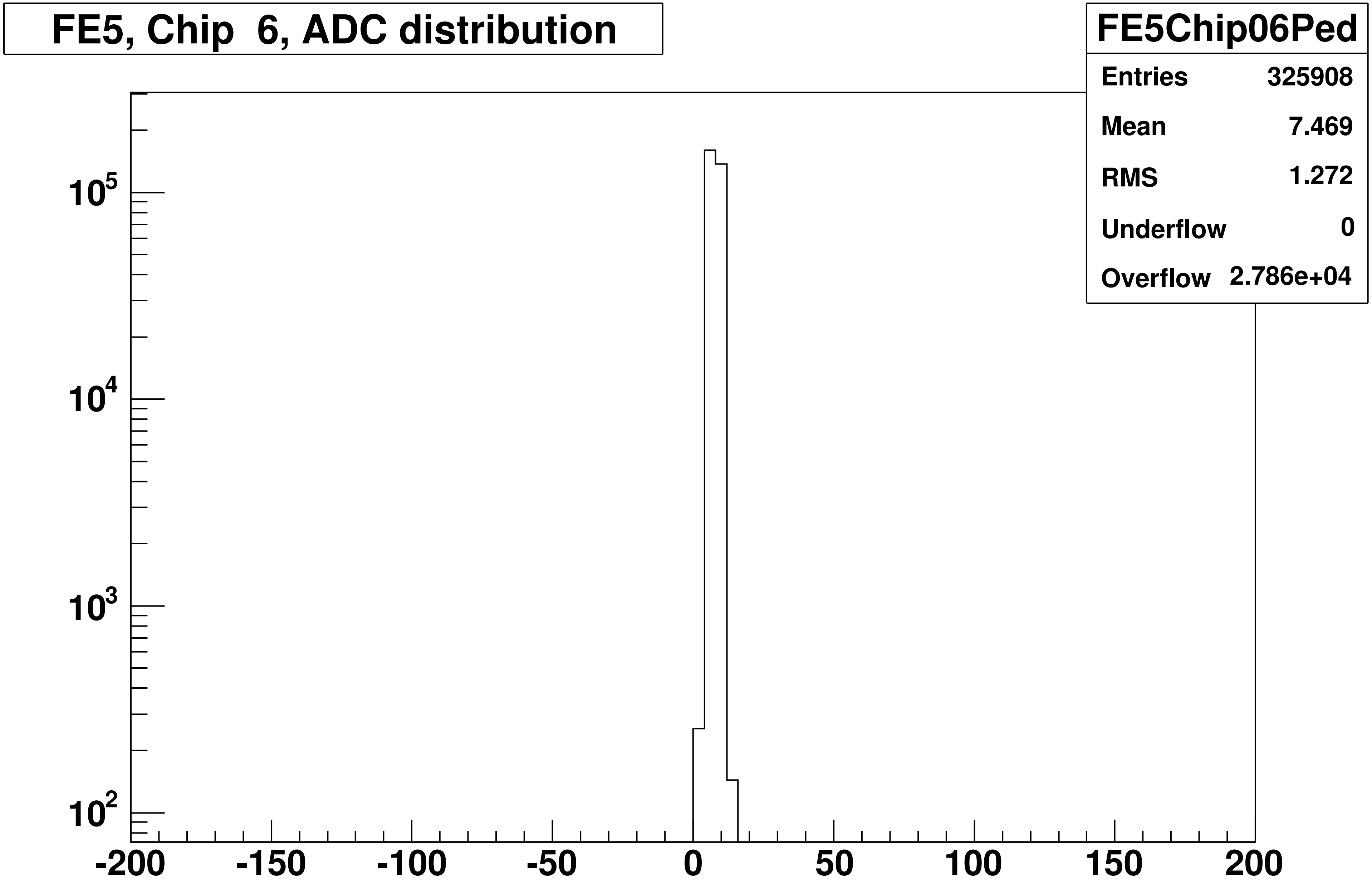
20000
25000


FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 325908
Mean 7.469
RMS 1.272
Underflow 0
Overflow 2.786e+04
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10^4
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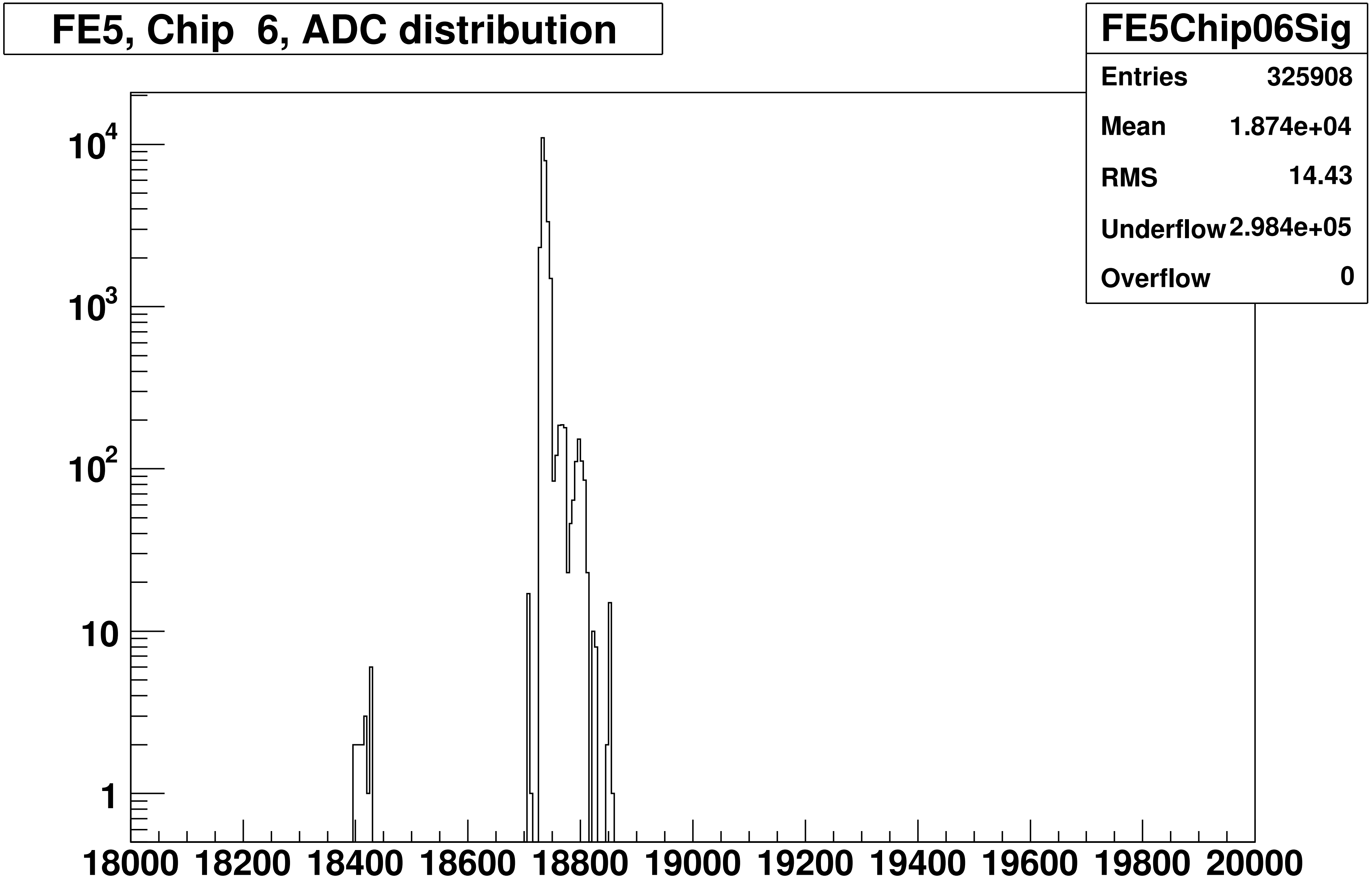
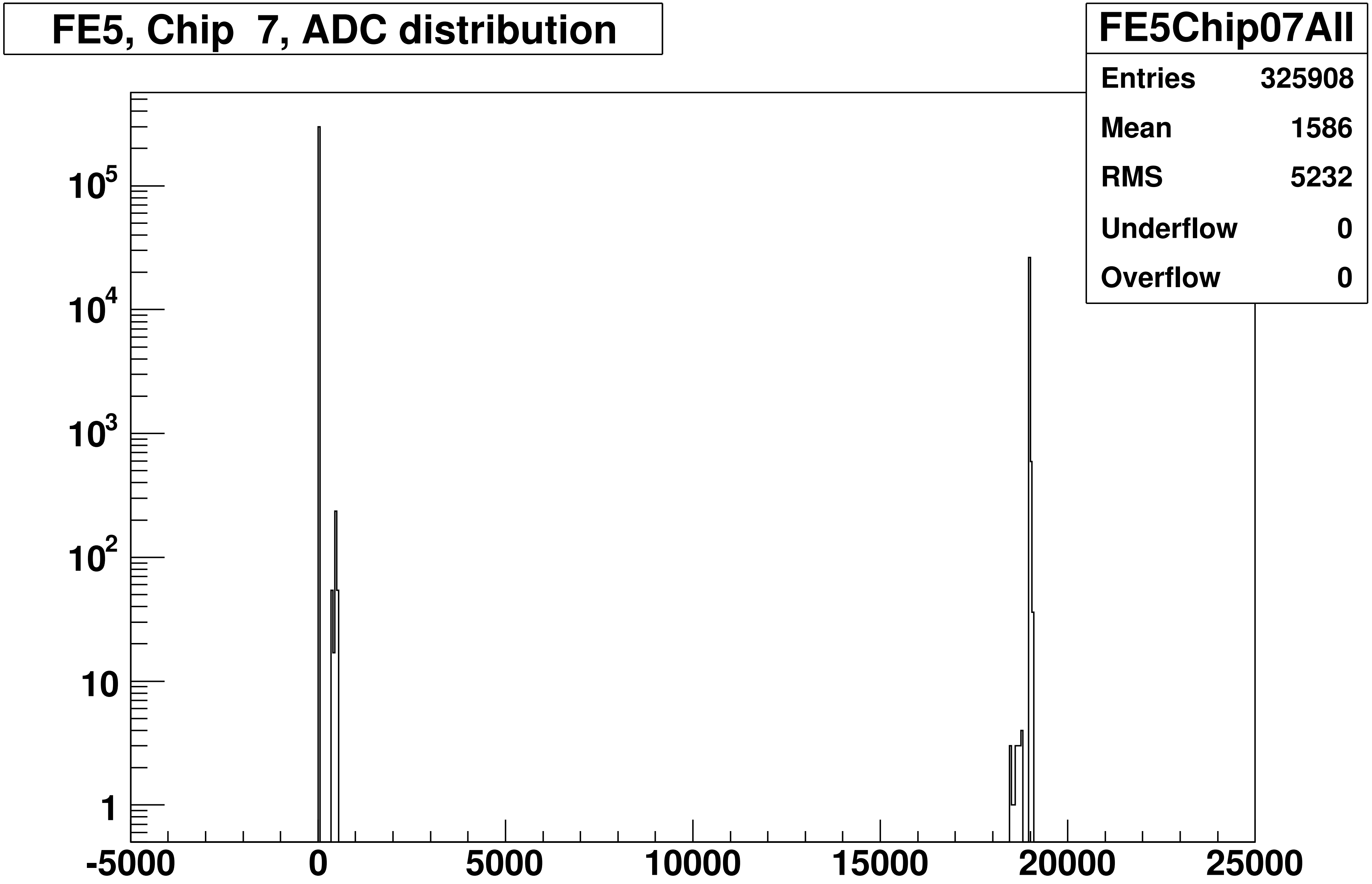
150
200



FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 325908
Mean 1.874e+04
RMS 14.43
Underflow 2.984e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 325908
Mean 1586
RMS 5232
Underflow 0
Overflow 0
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0
5000
10000
15000
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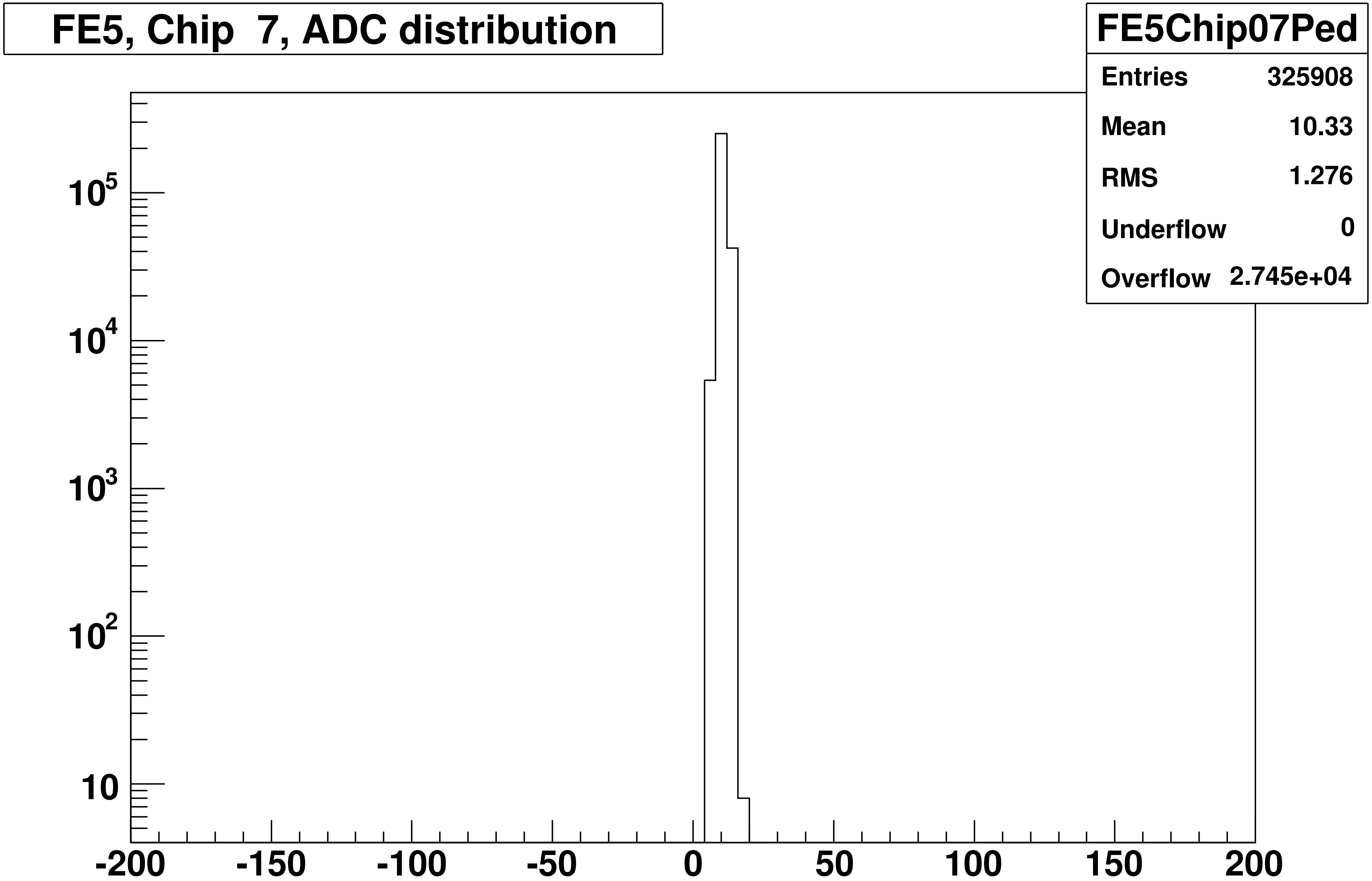
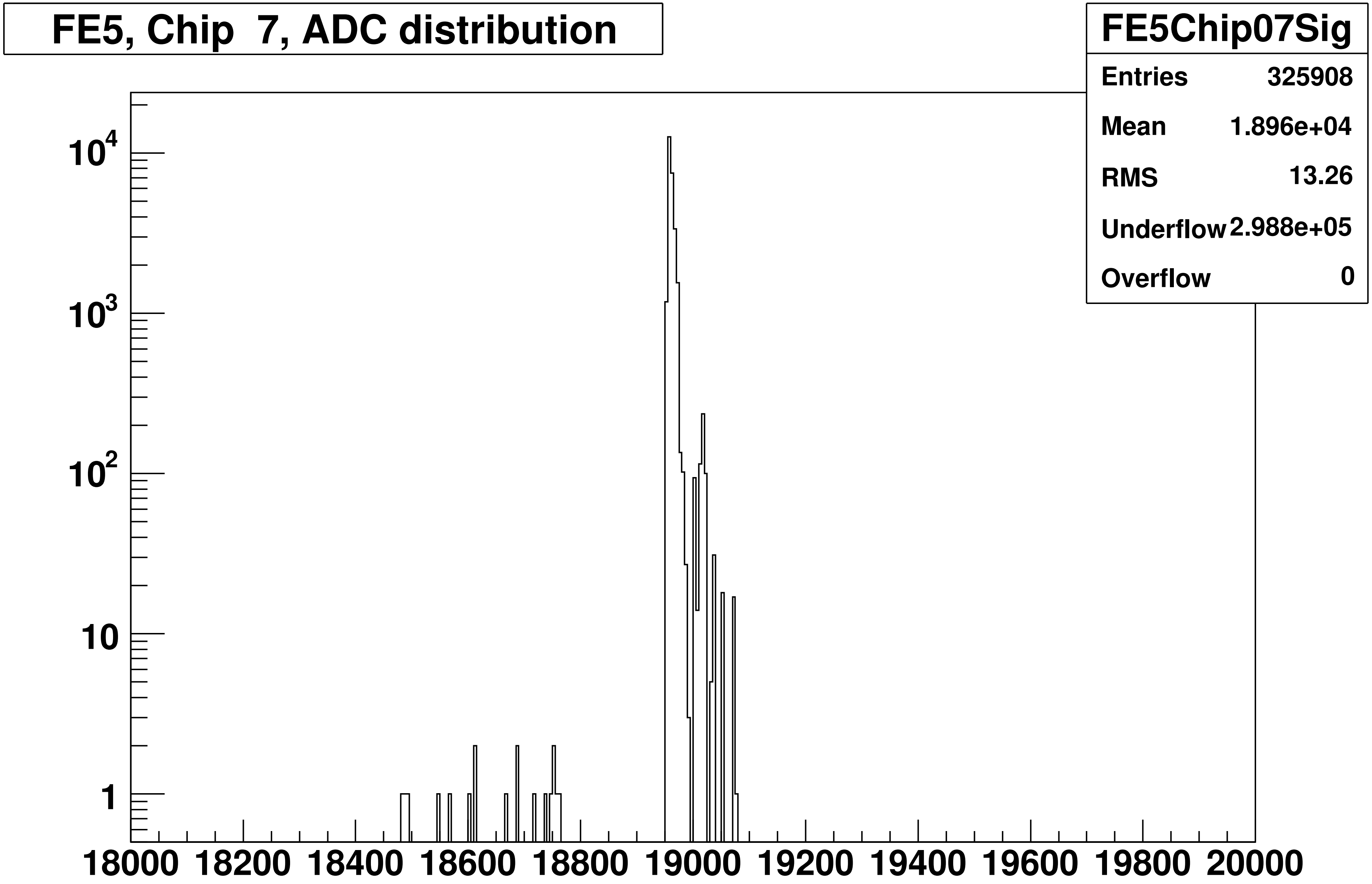
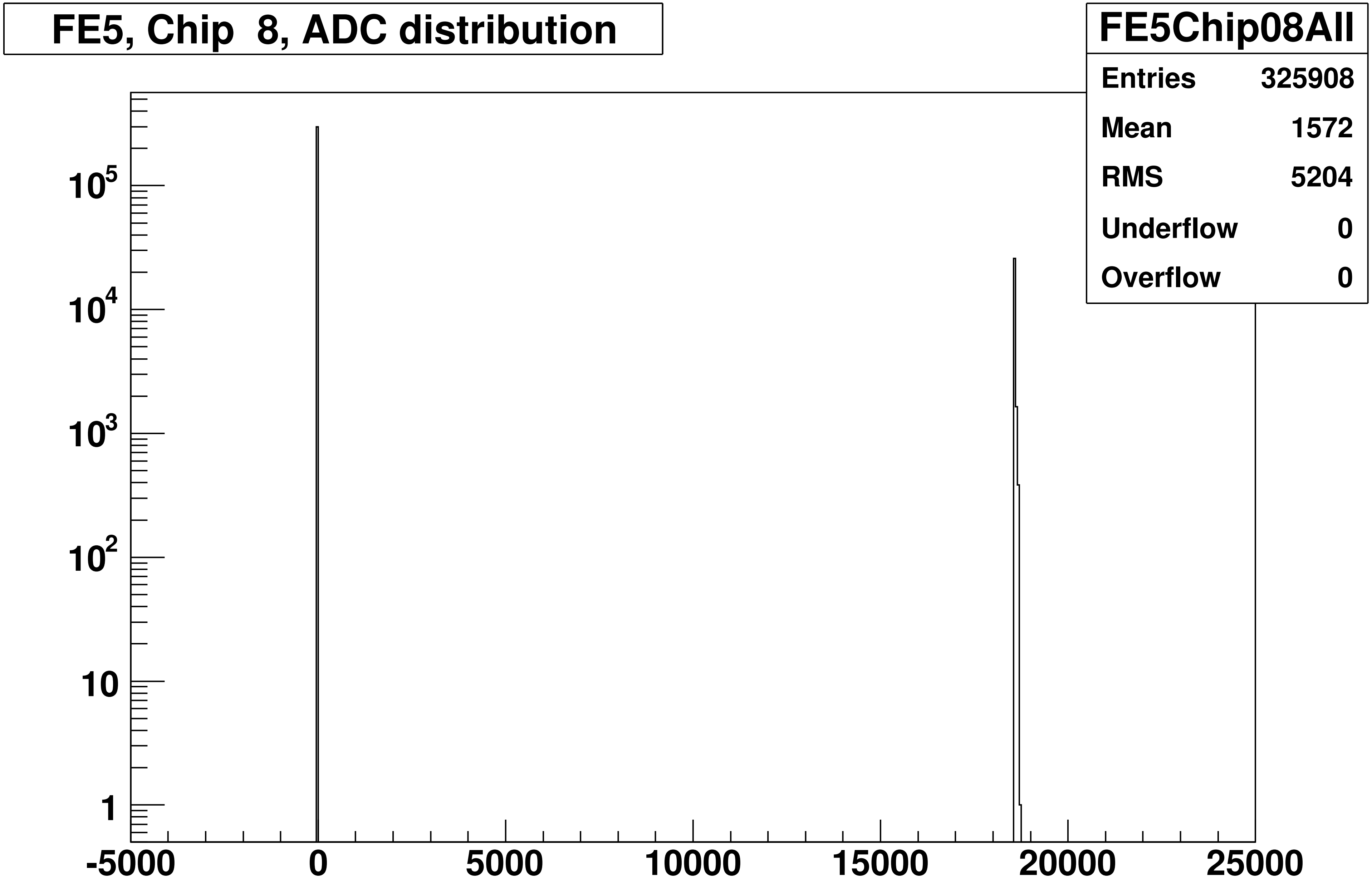
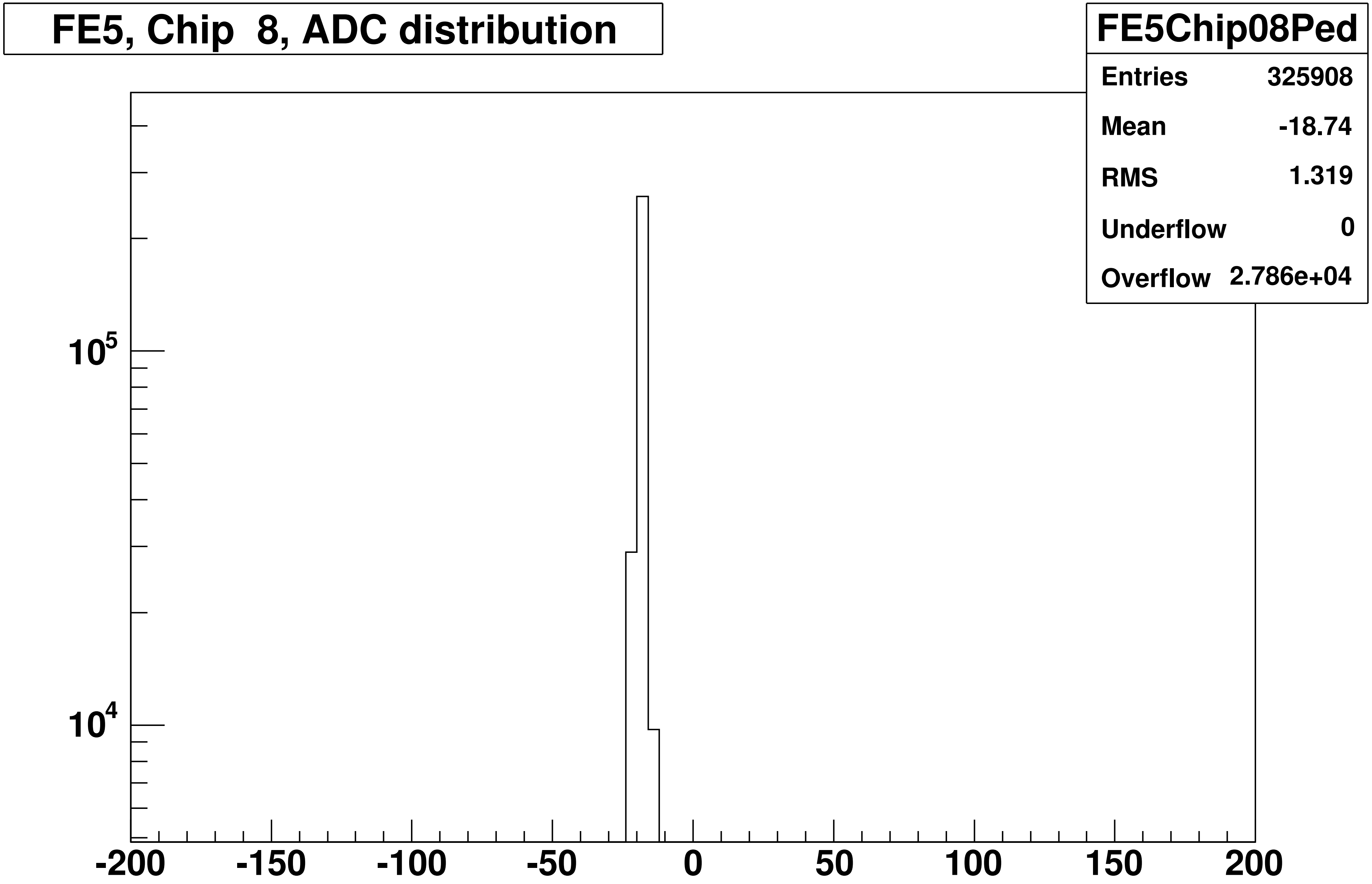
10^3
10^4
10^5


FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 325908
Mean 10.33
RMS 1.276
Underflow 0
Overflow 2.745e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

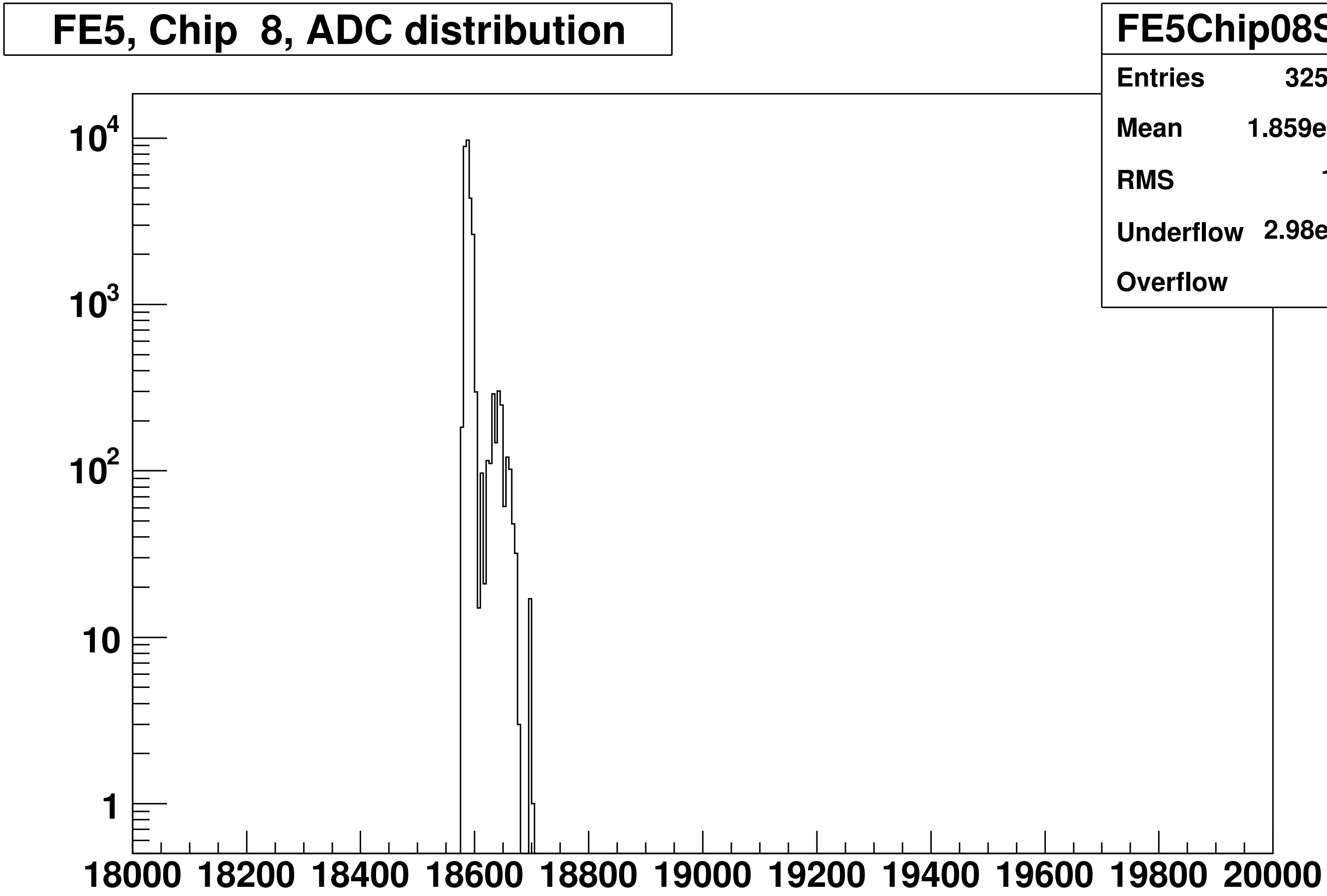

FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 325908
Mean 1.896e+04
RMS 13.26
Underflow 2.988e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

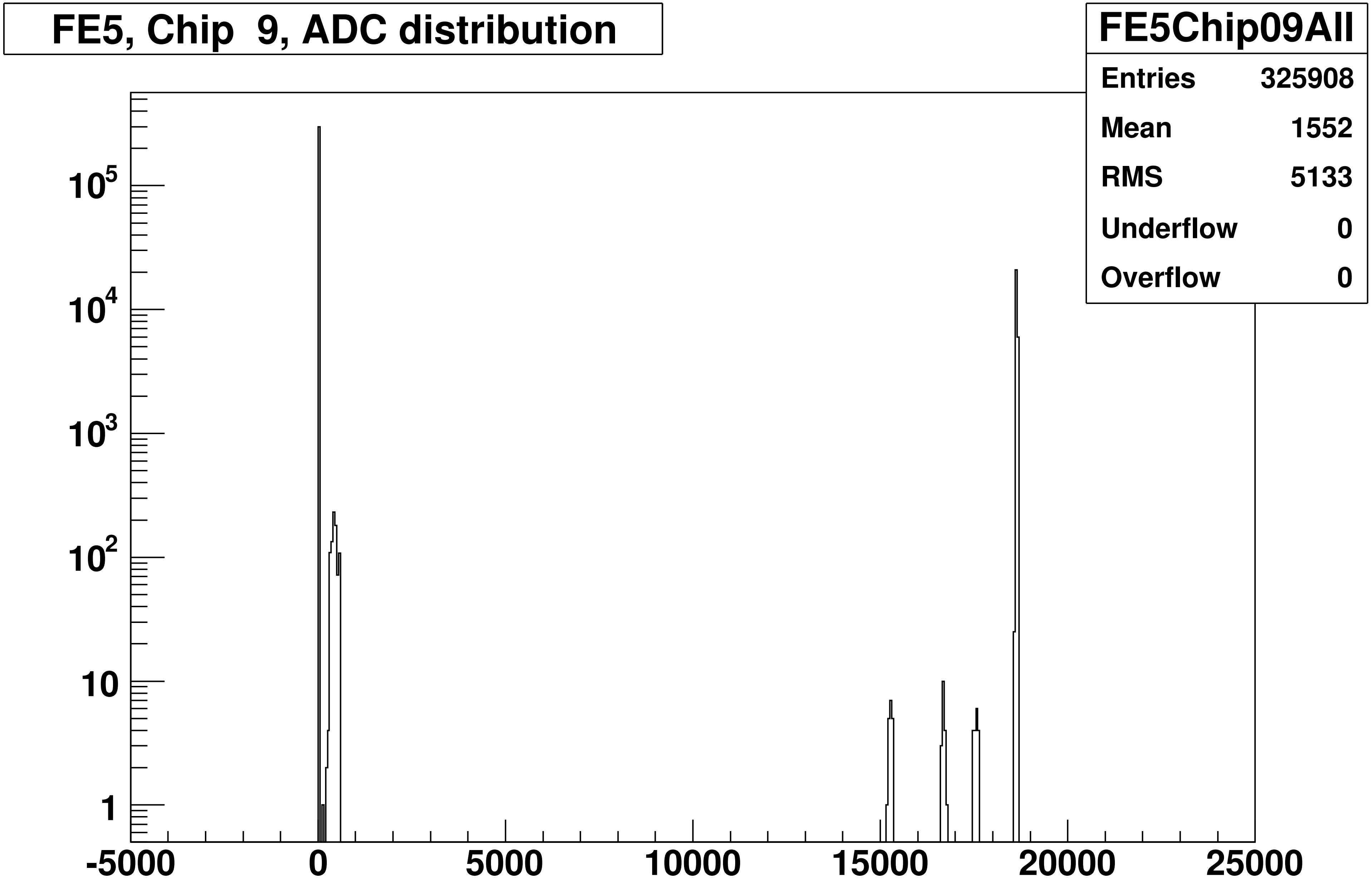


FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 325908
Mean 1572
RMS 5204
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

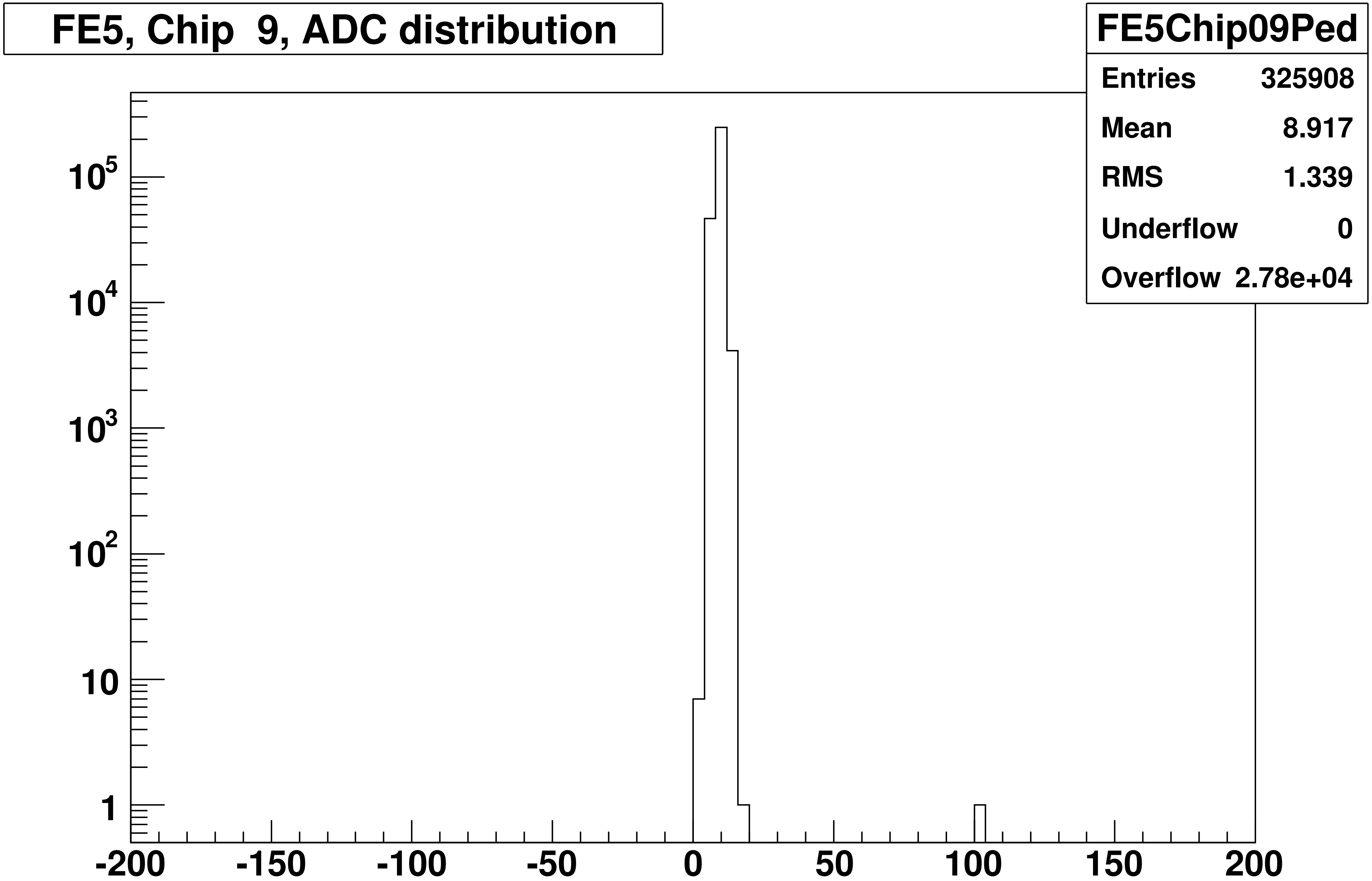


FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 325908
Mean -18.74
RMS 1.319
Underflow 0
Overflow 2.786e+04
10^5
10^4
-200 -150 -100 -50 0 50 100 150 200


FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 325908
Mean 1.859e+04
RMS 14.2
Underflow 2.98e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 325908
Mean 1552
RMS 5133
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 325908
Mean 8.917
RMS 1.339
Underflow 0
Overflow 2.78e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
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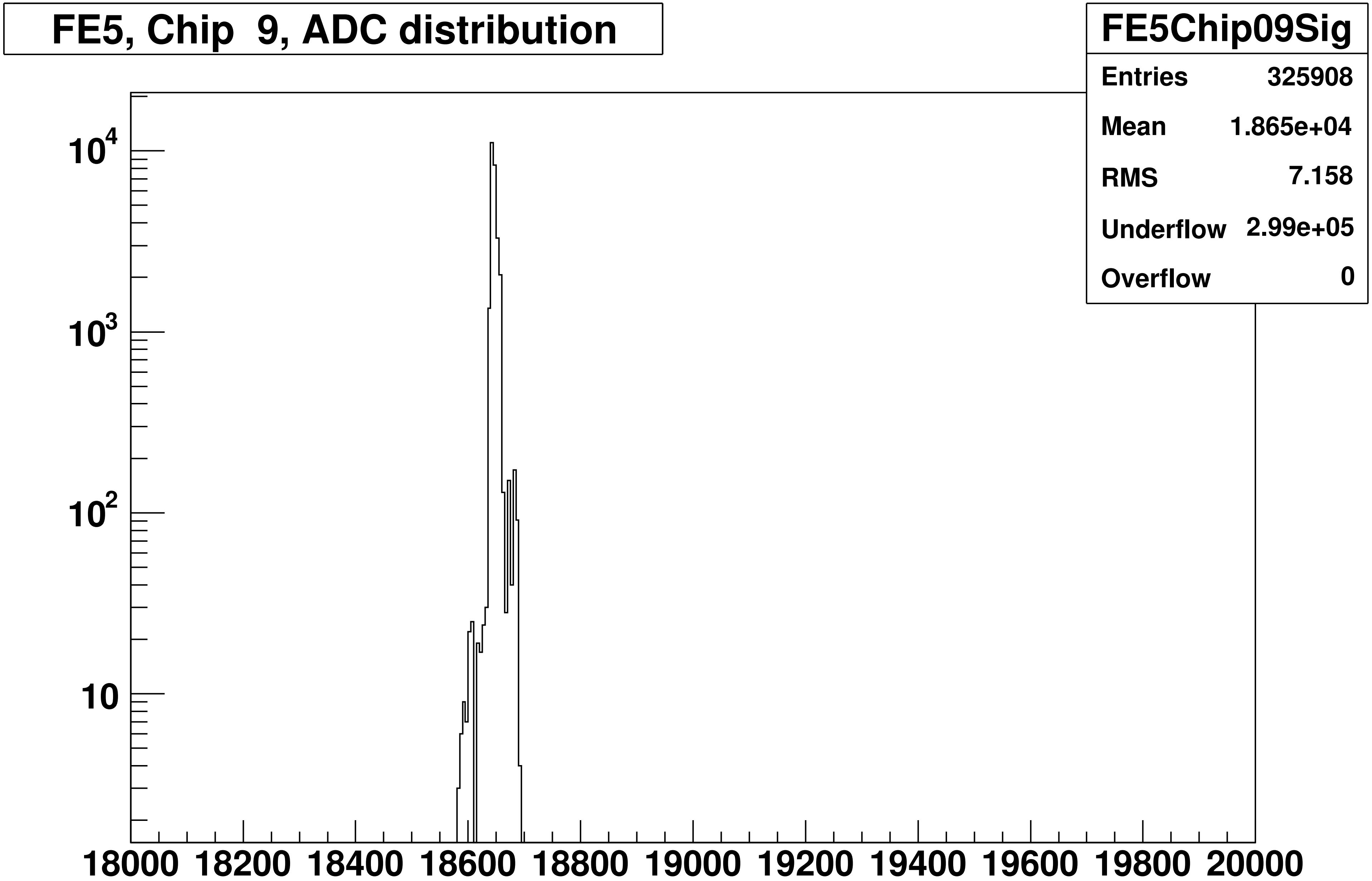
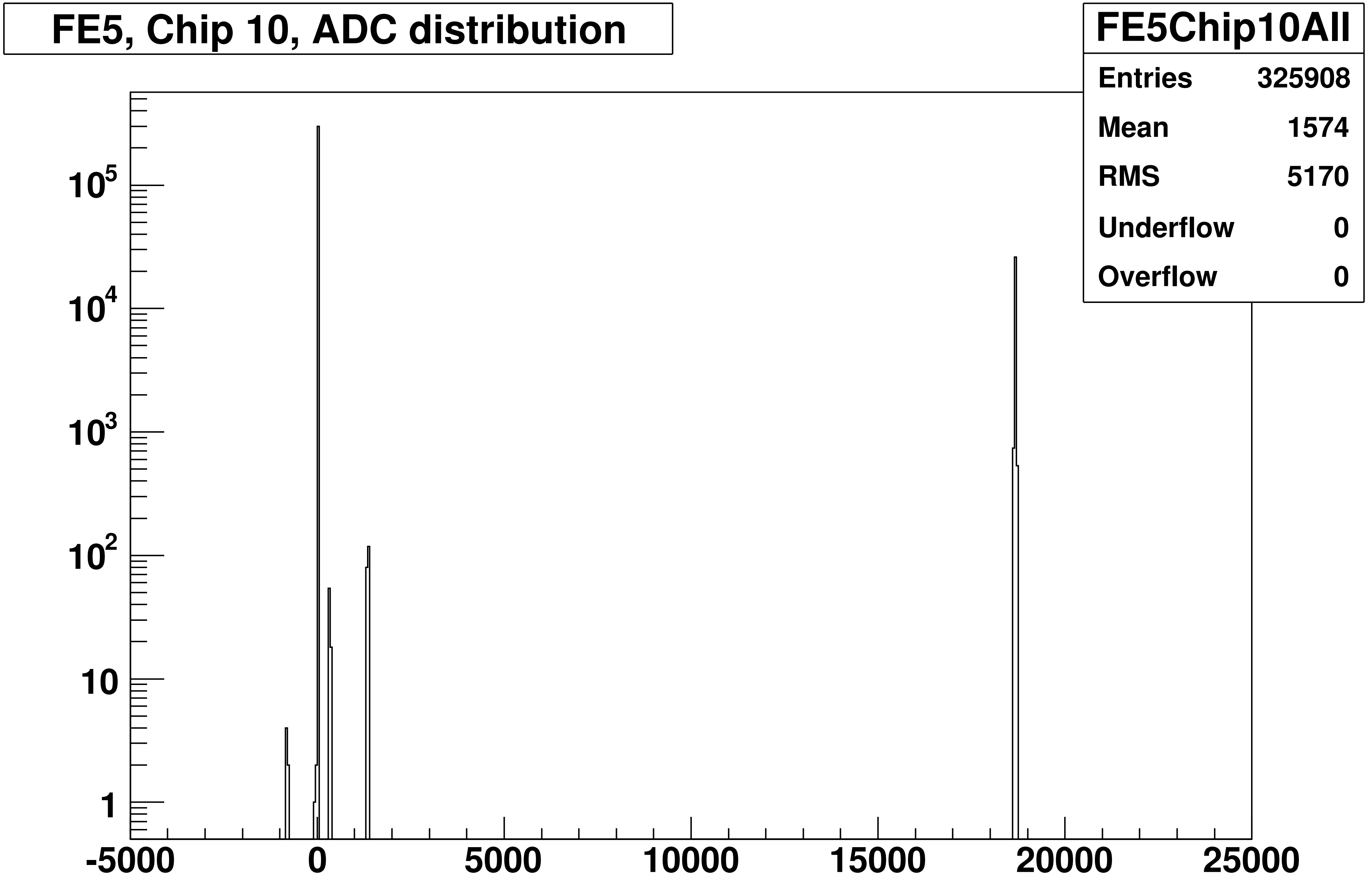
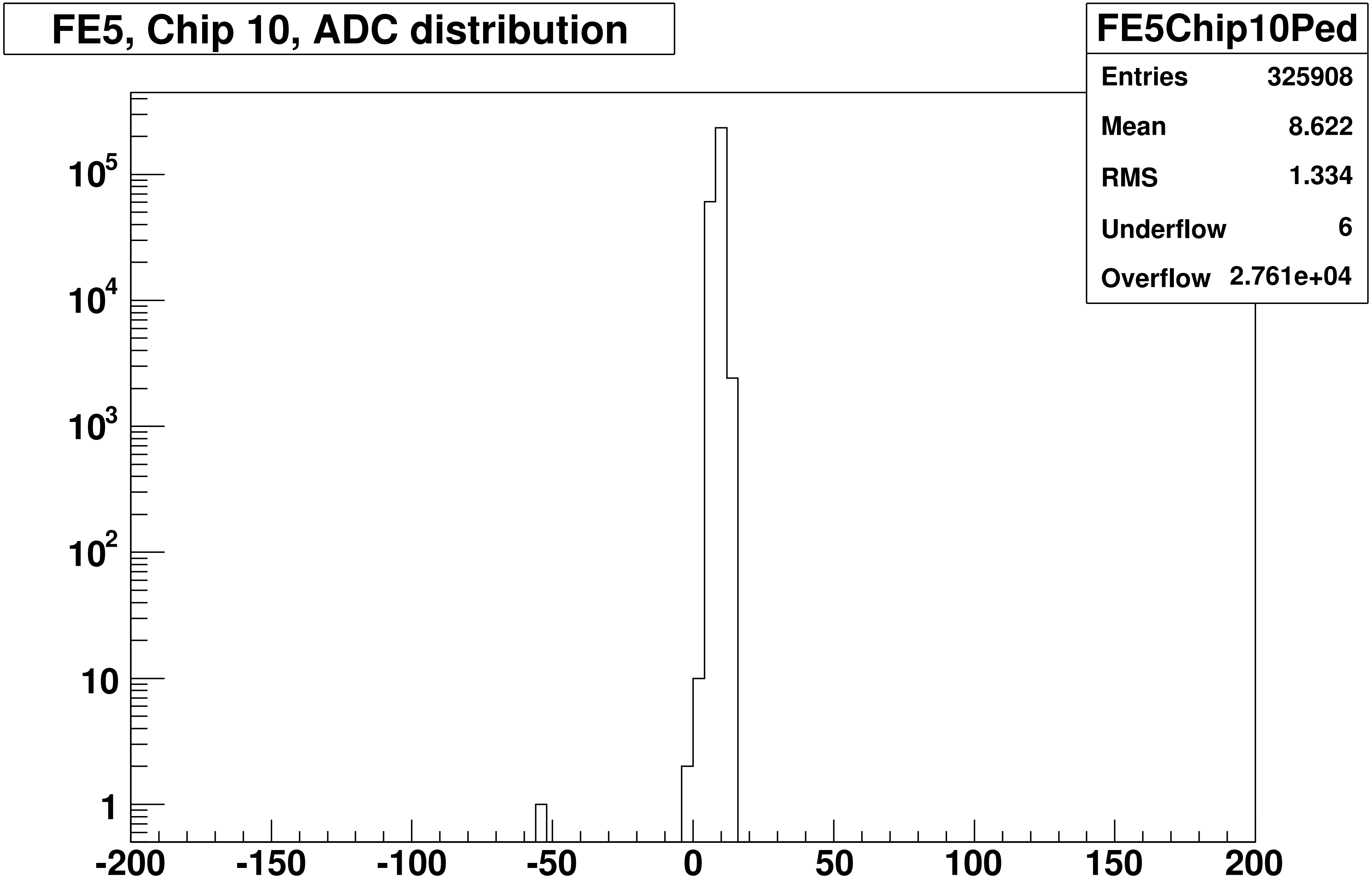
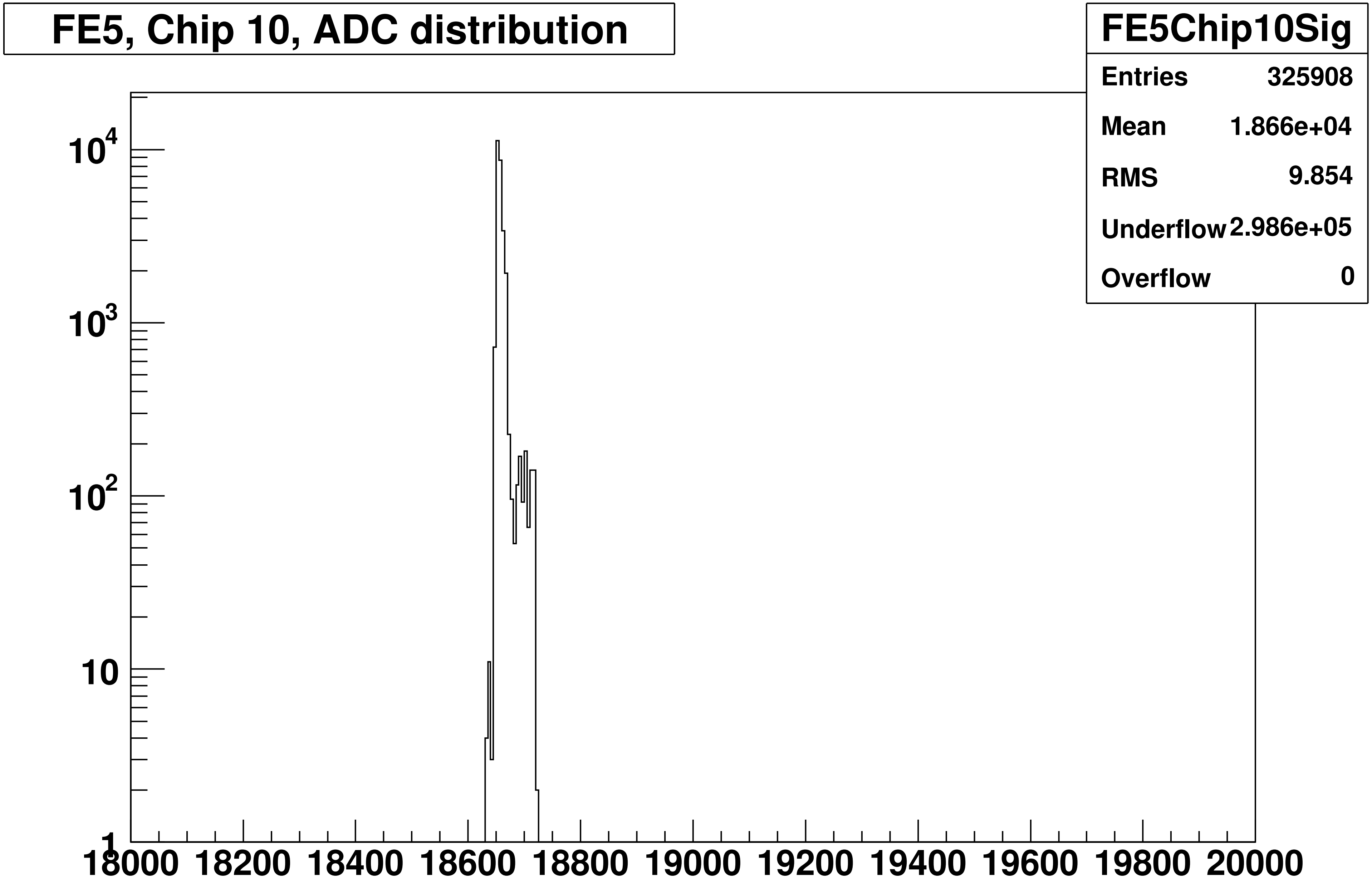
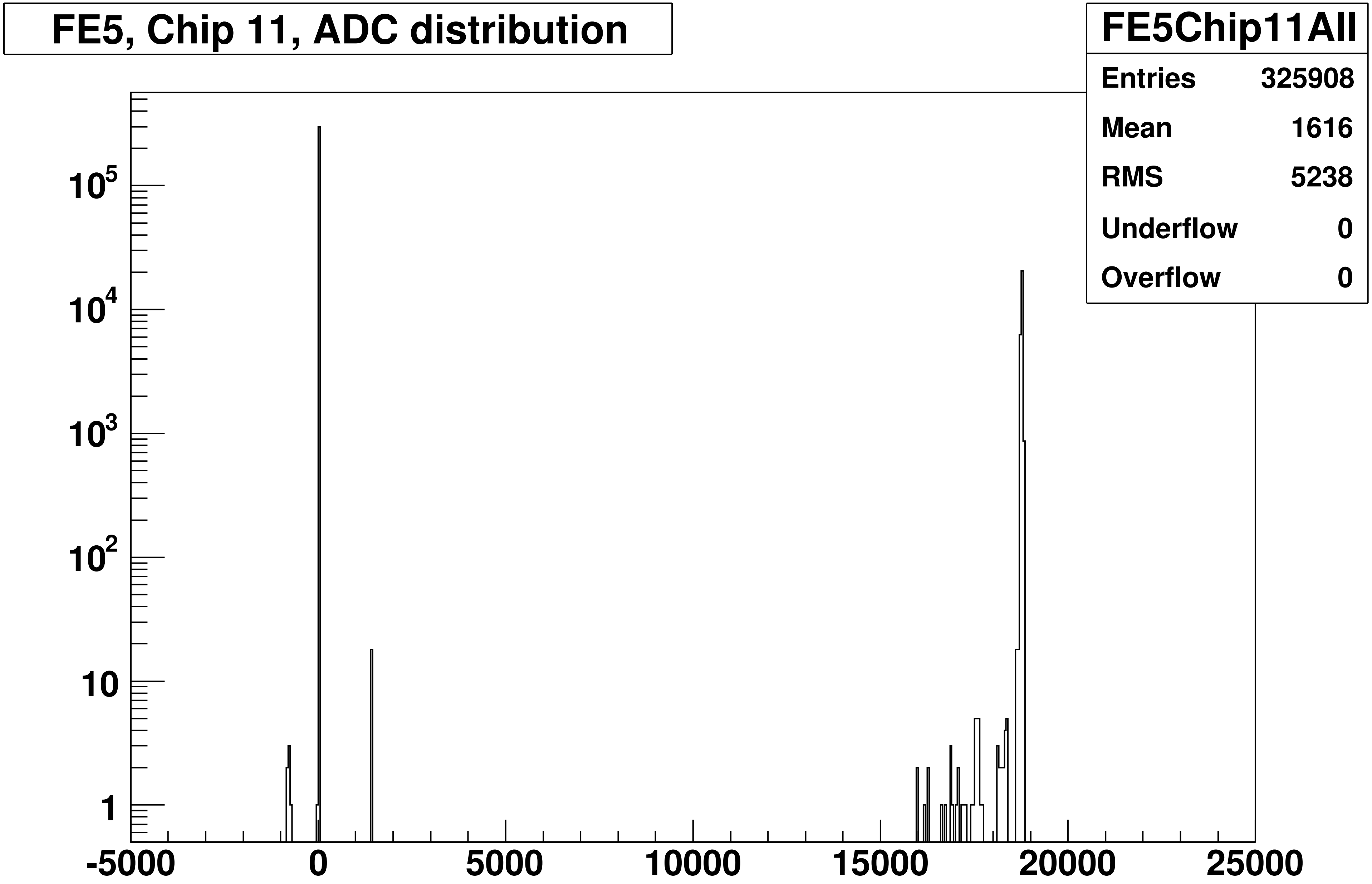
0
50
100
150
200

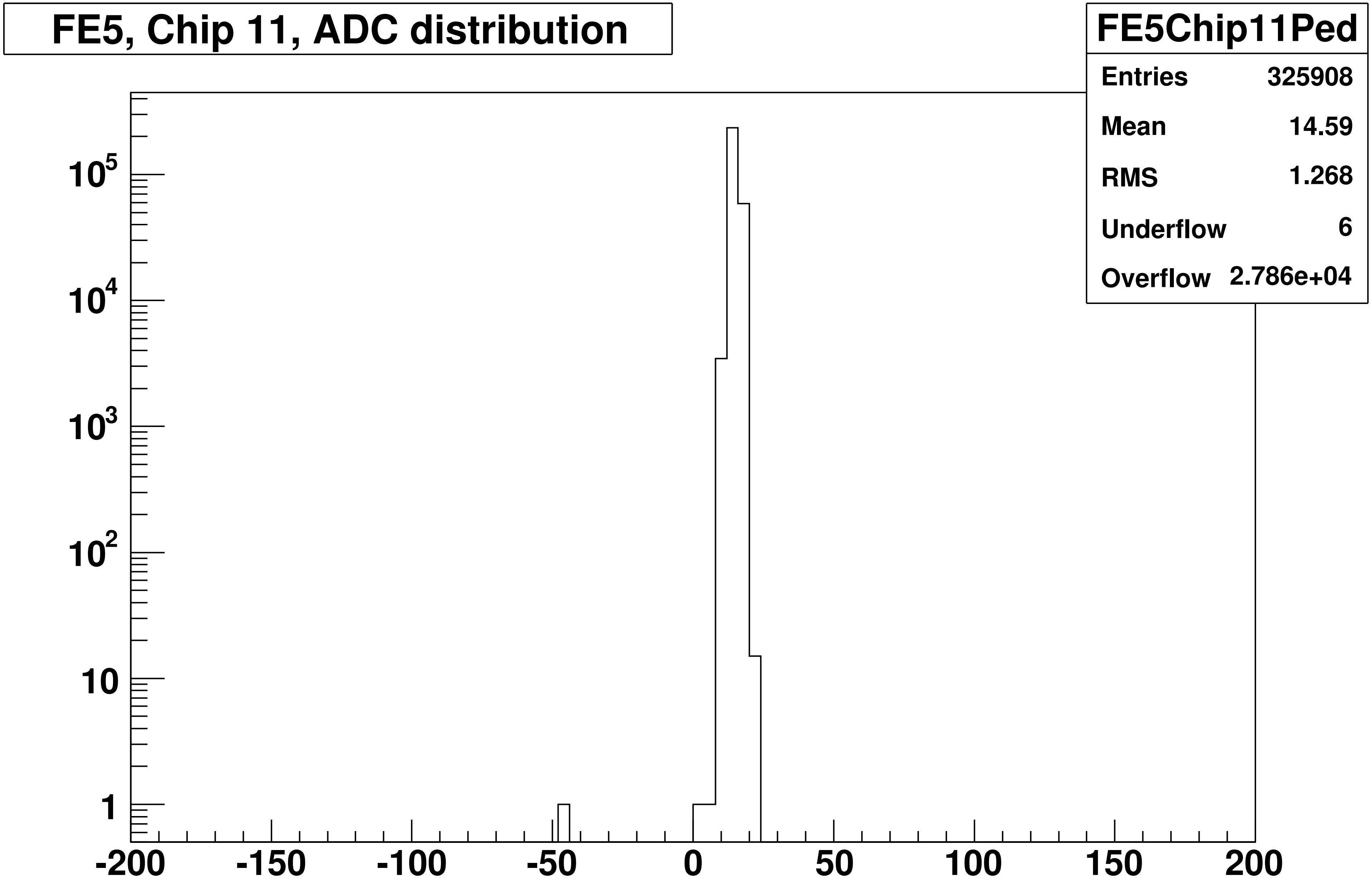

FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 325908
Mean 1.865e+04
RMS 7.158
Underflow 2.99e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 325908
Mean 1574
RMS 5170
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 325908
Mean 8.622
RMS 1.334
Underflow 6
Overflow 2.761e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 325908
Mean 1.866e+04
RMS 9.854
Underflow 2.986e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 325908
Mean 1616
RMS 5238
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 325908
Mean 14.59
RMS 1.268
Underflow 6
Overflow 2.786e+04
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10^4
10^3
10^2
10
1
-200
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-100
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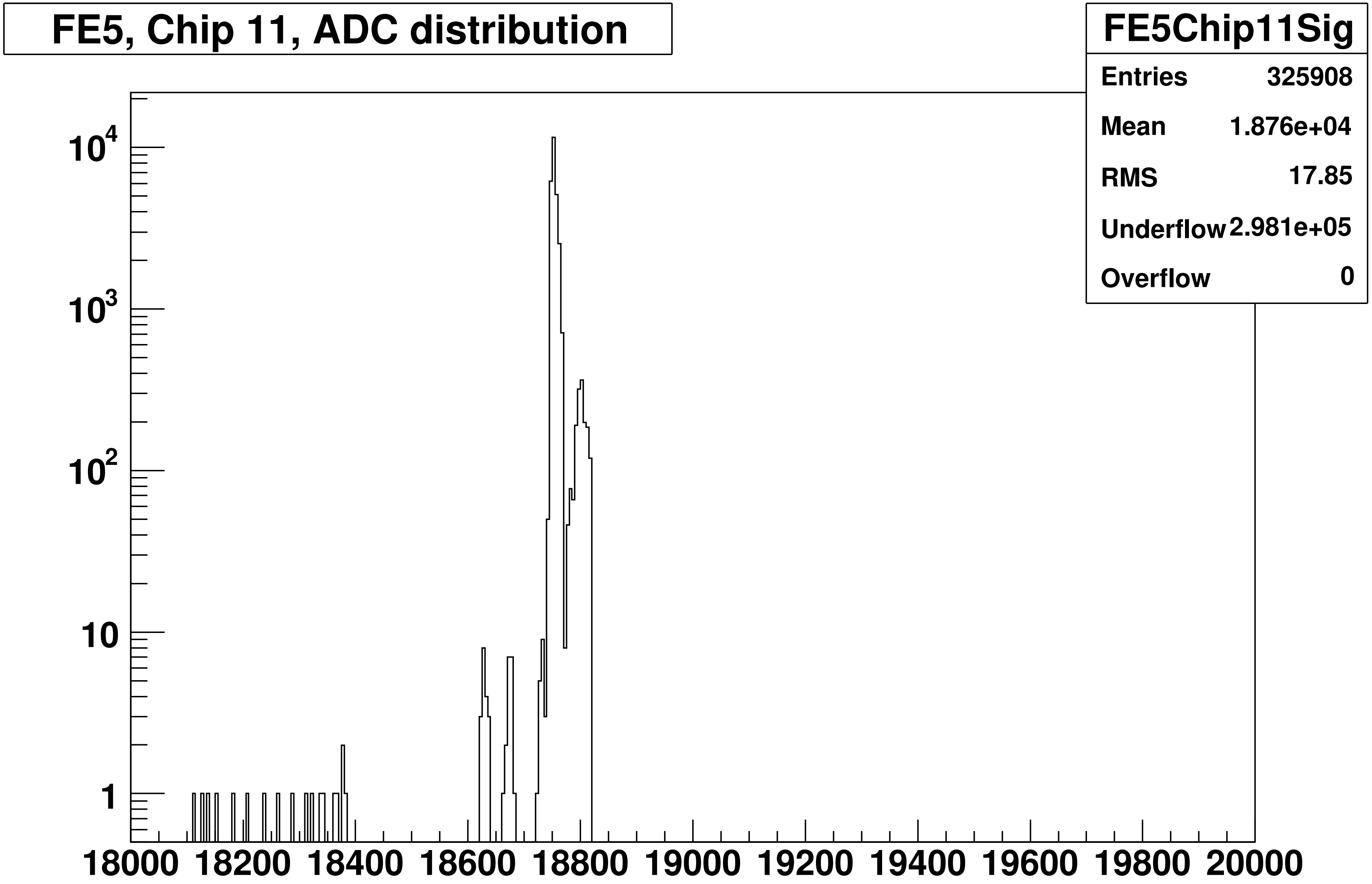
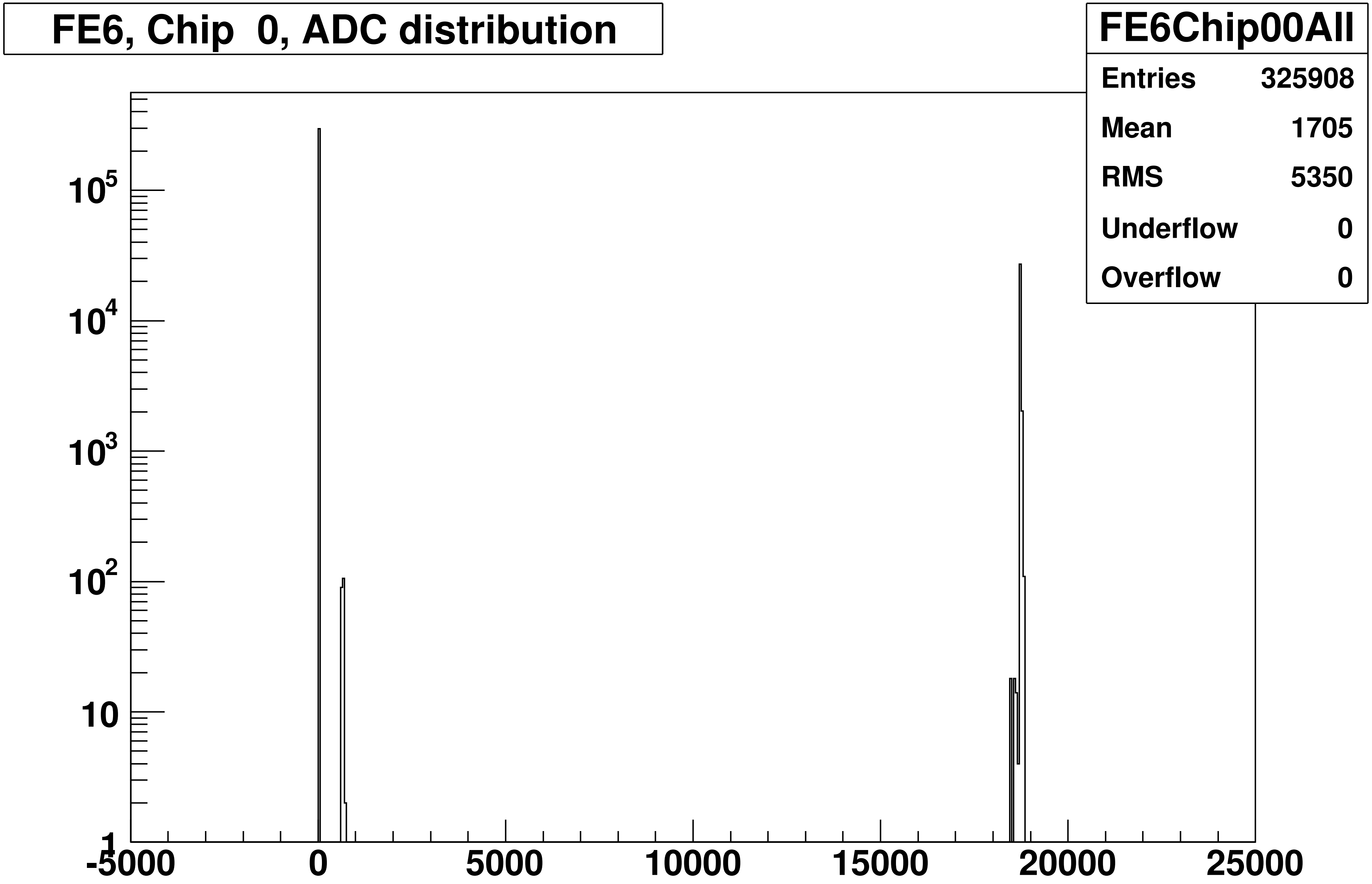
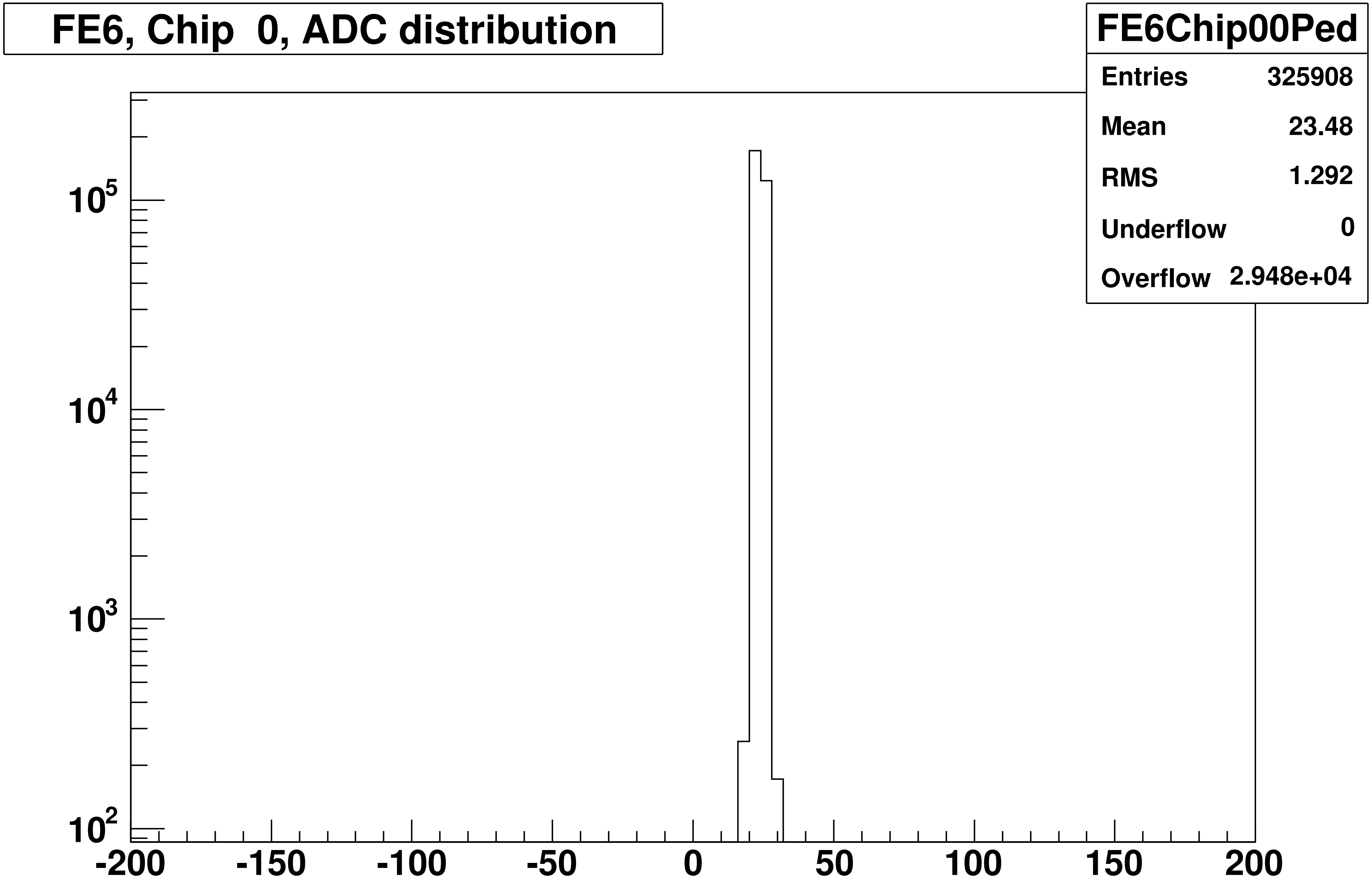
50
100
150
200



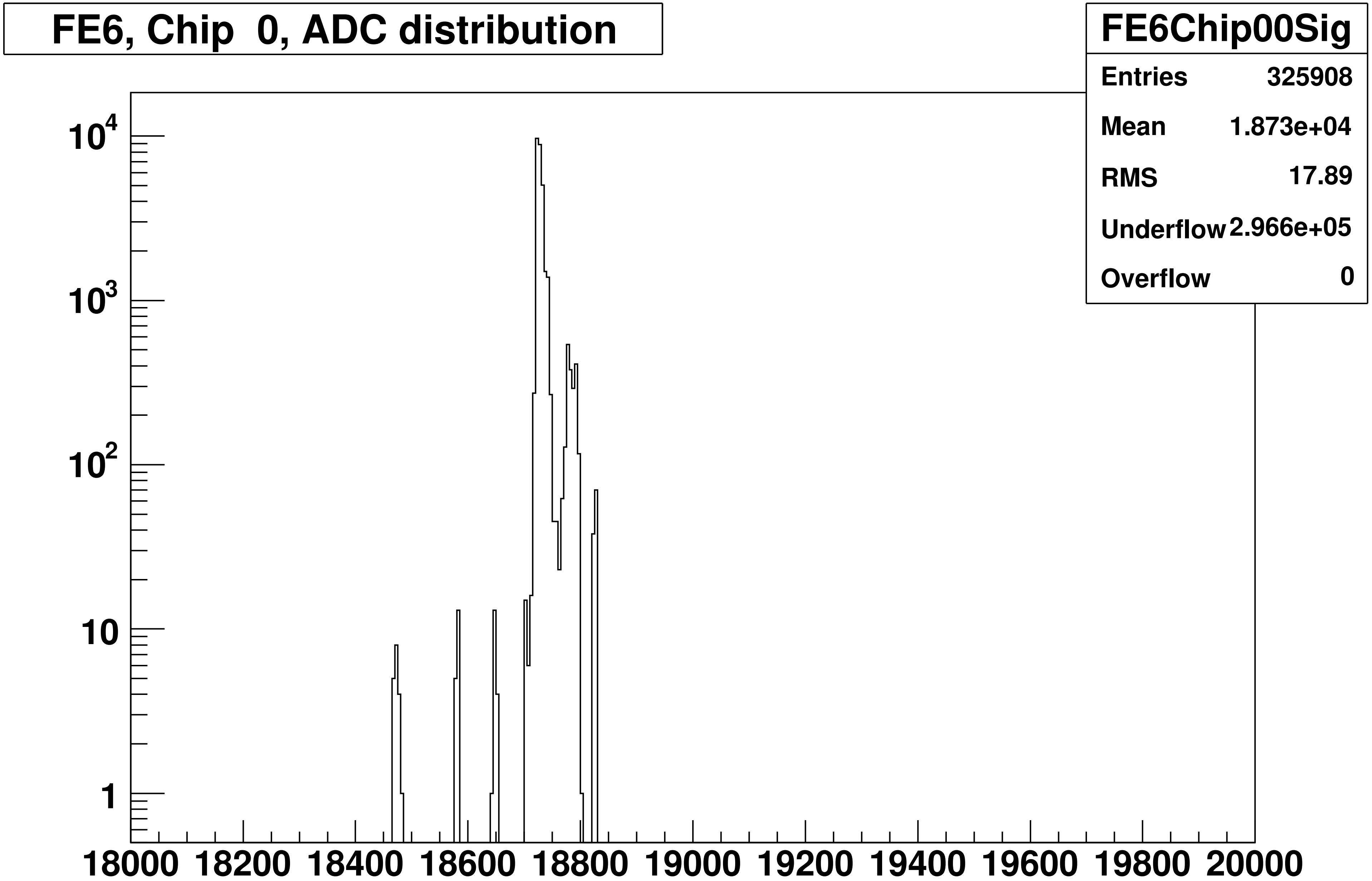
FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 325908
Mean 1.876e+04
RMS 17.85
Underflow 2.981e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

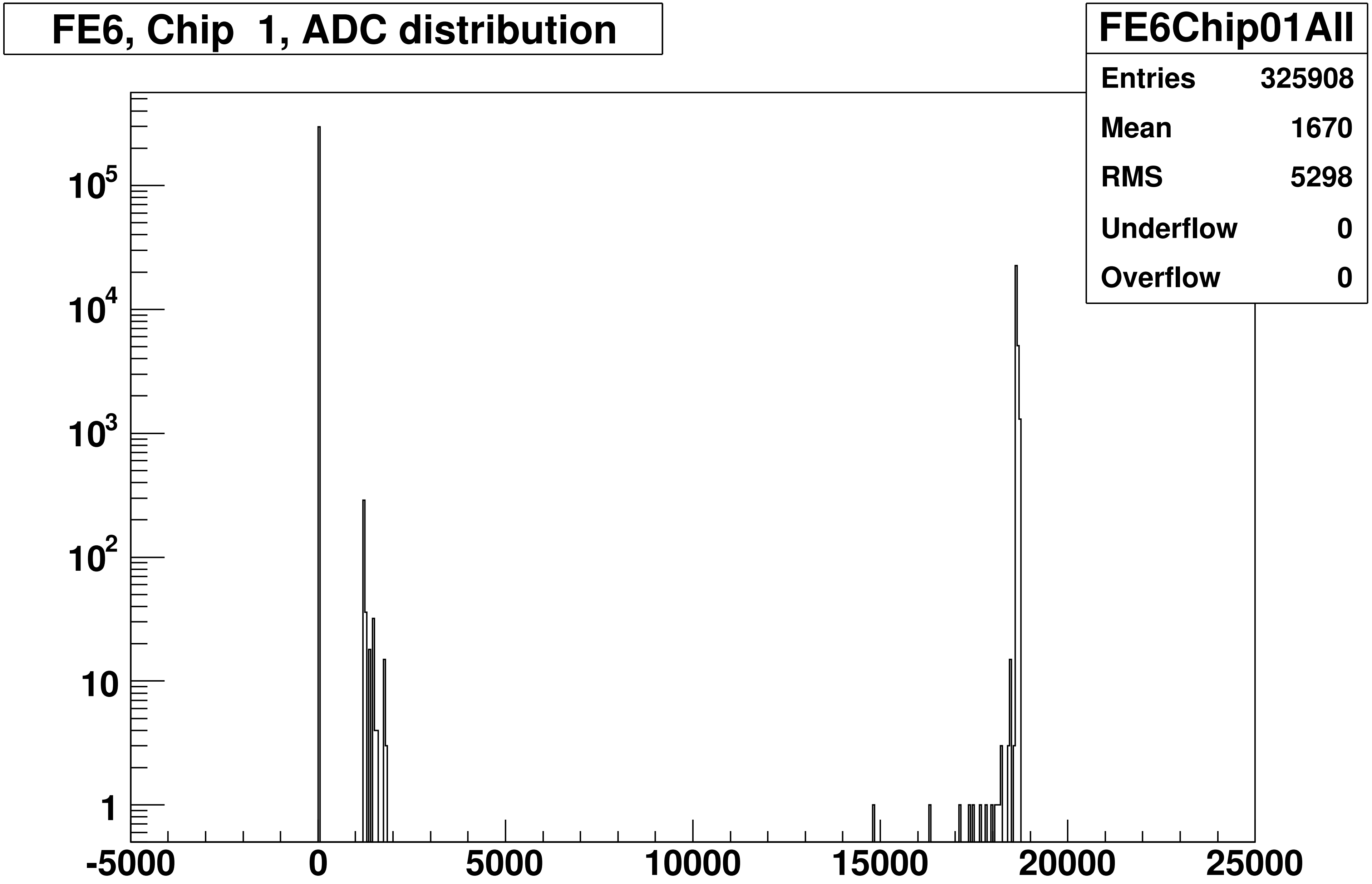


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 325908
Mean 1705
RMS 5350
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

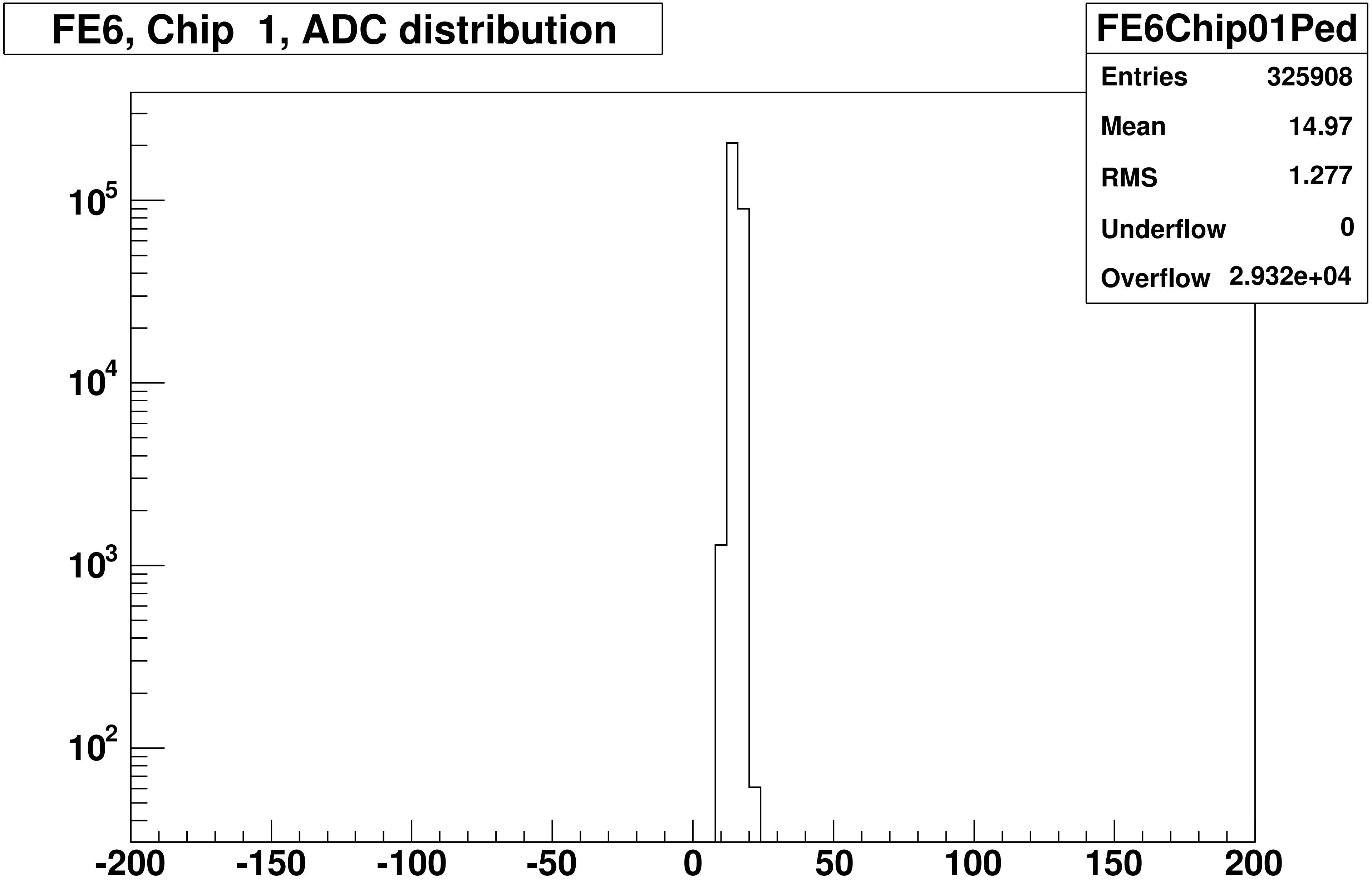


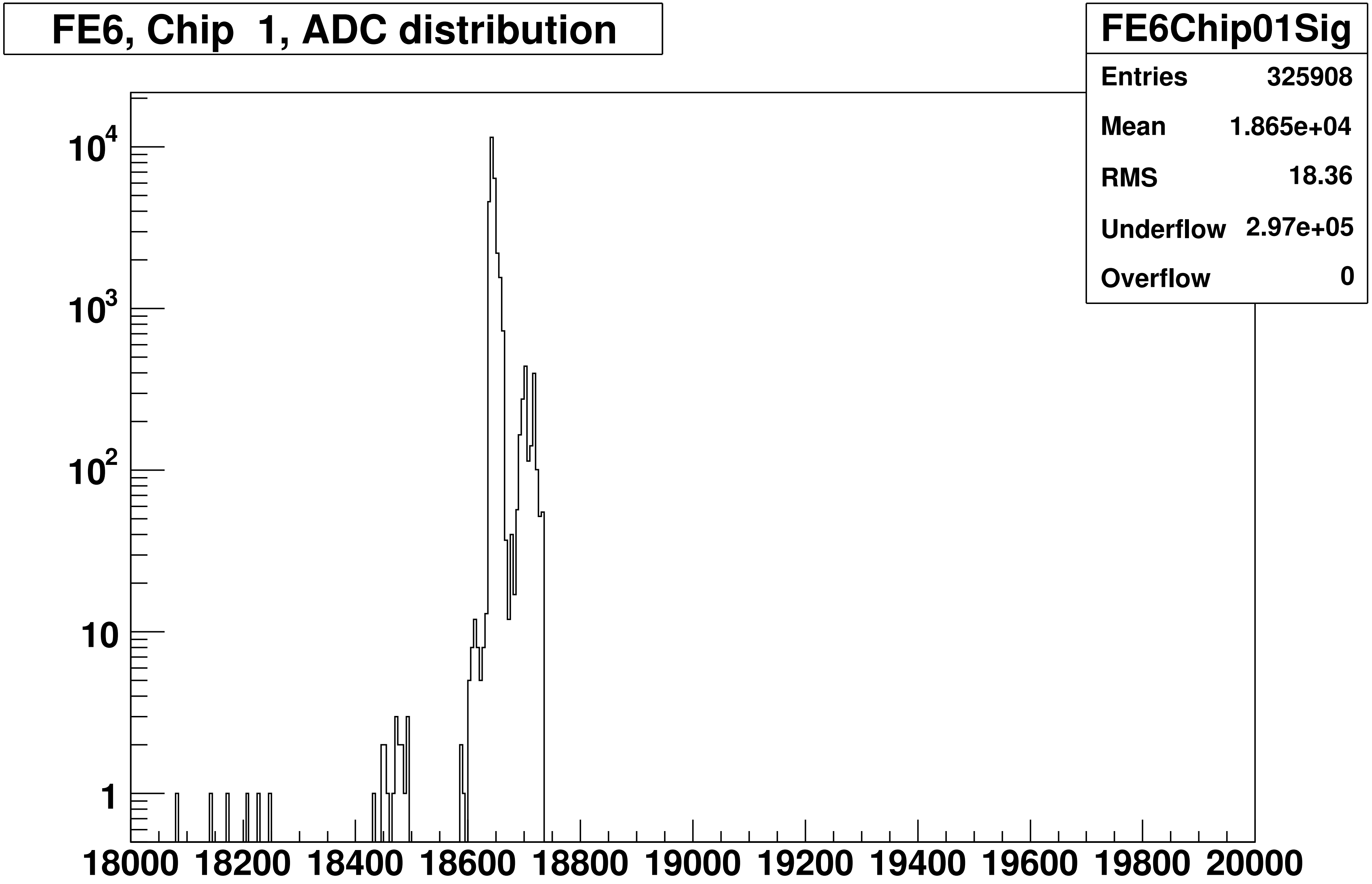
FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 325908
Mean 23.48
RMS 1.292
Underflow 0
Overflow 2.948e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

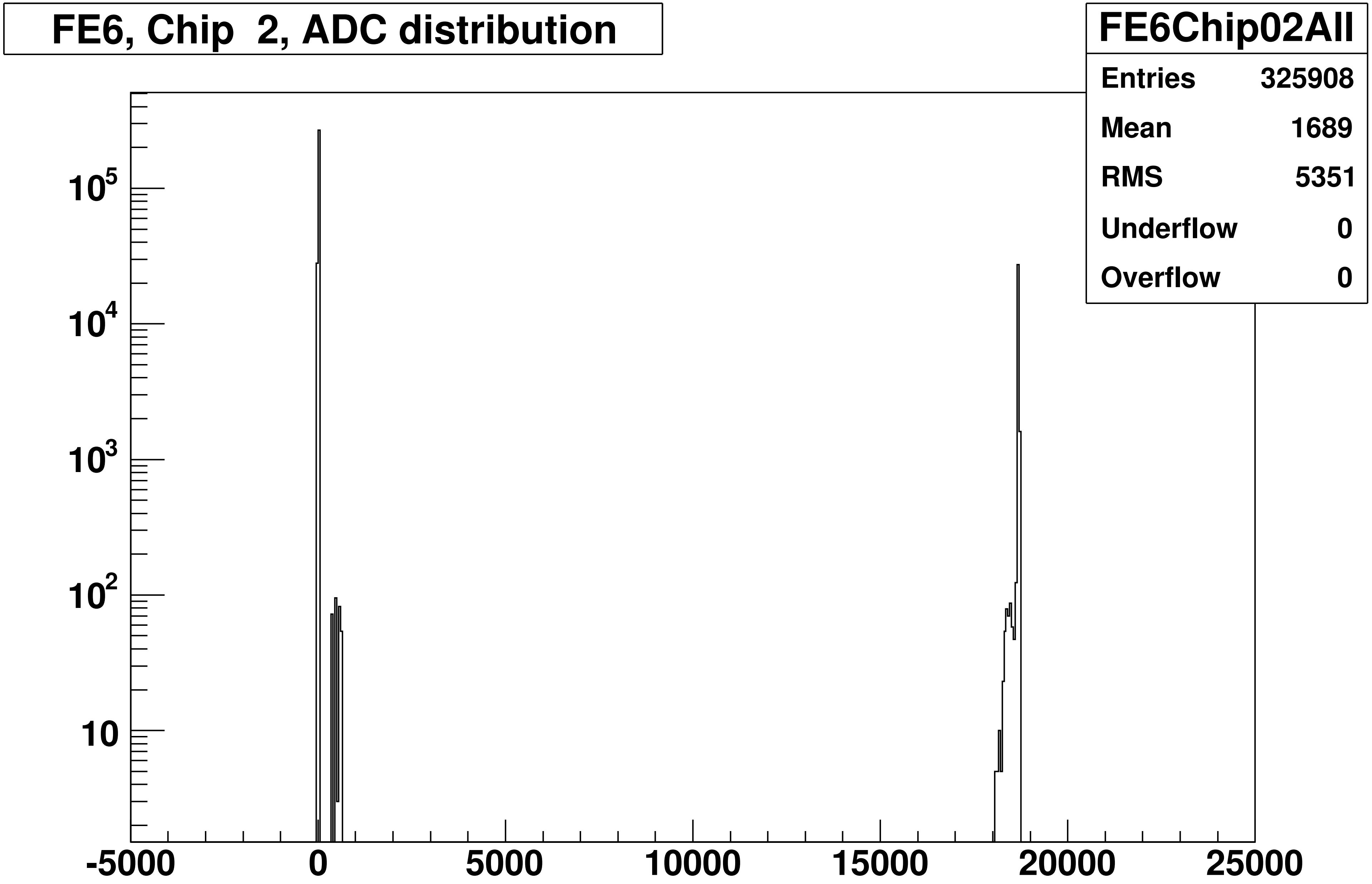

FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 325908
Mean 1.873e+04
RMS 17.89
Underflow 2.966e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



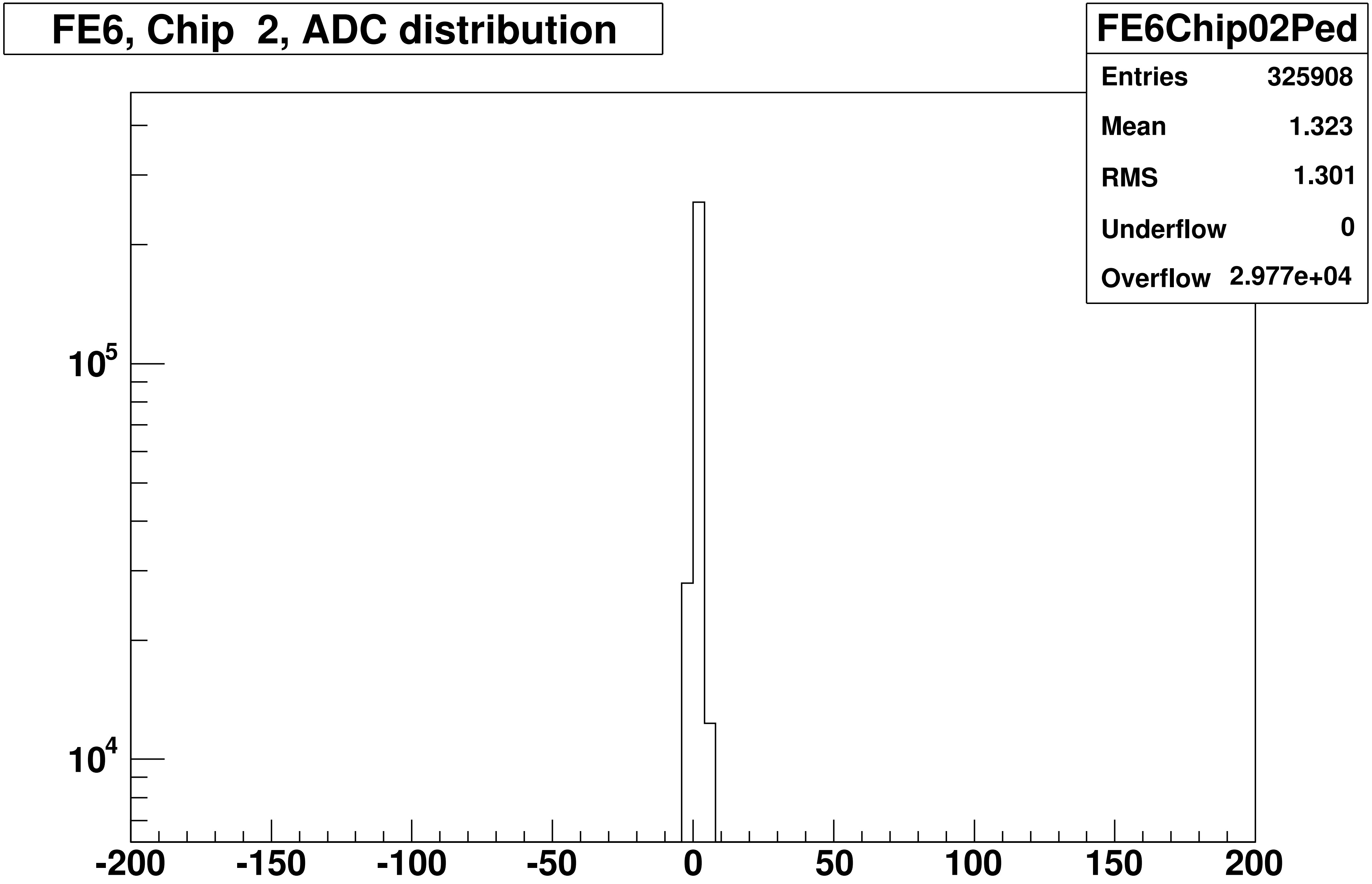
FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 325908
Mean 1670
RMS 5298
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

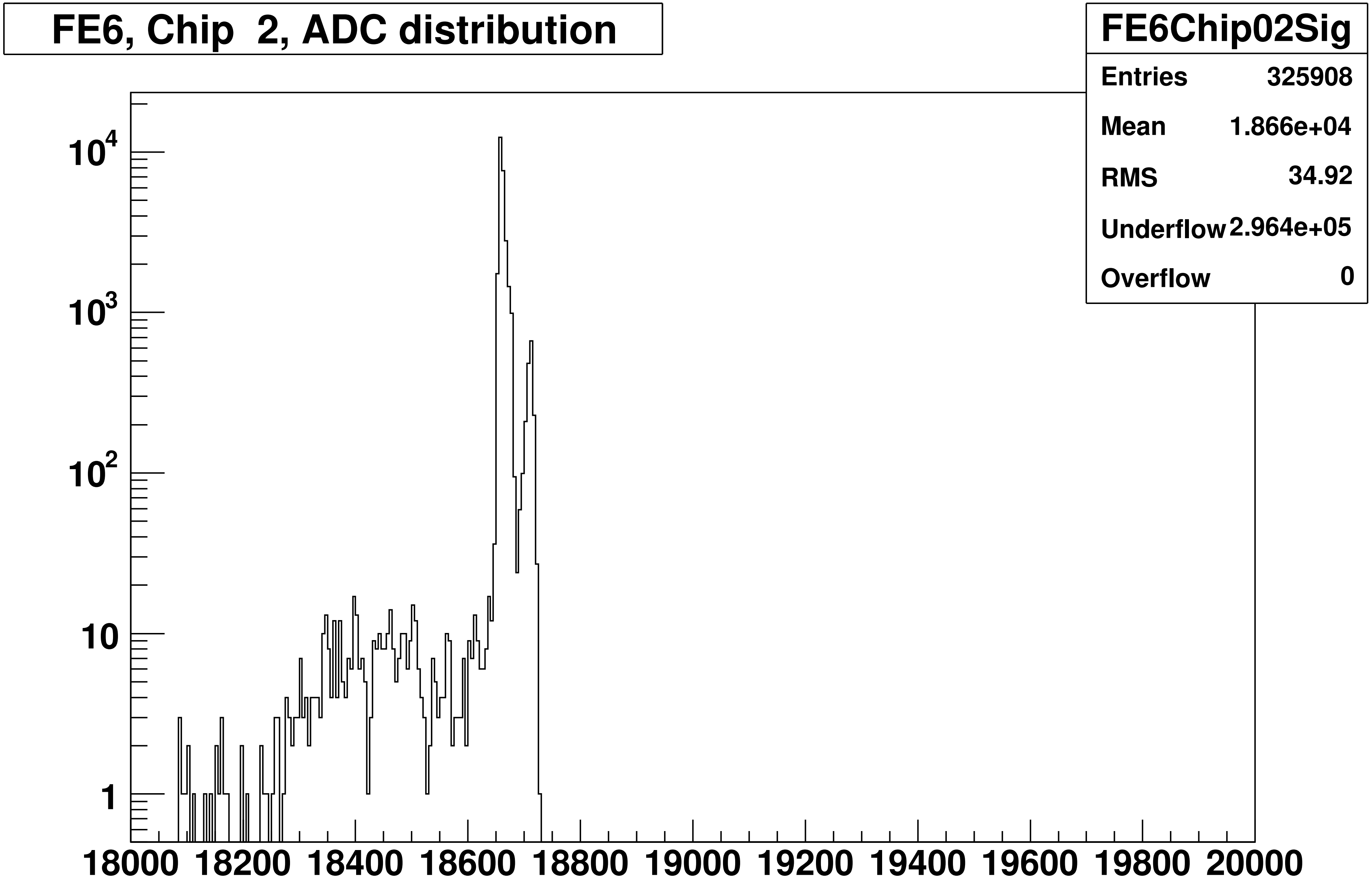

FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 325908
Mean 14.97
RMS 1.277
Underflow 0
Overflow 2.932e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

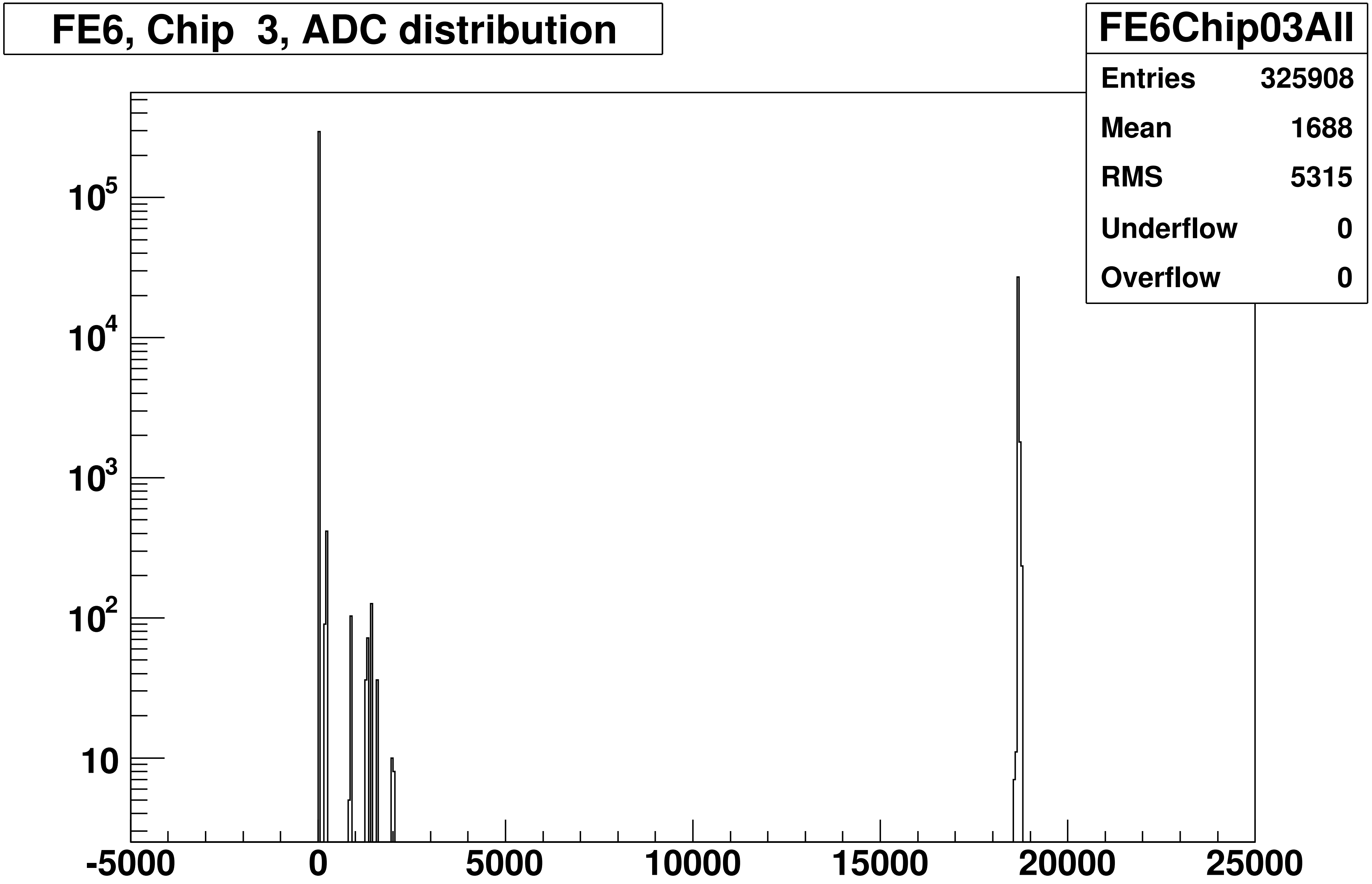

FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 325908
Mean 1.865e+04
RMS 18.36
Underflow 2.97e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

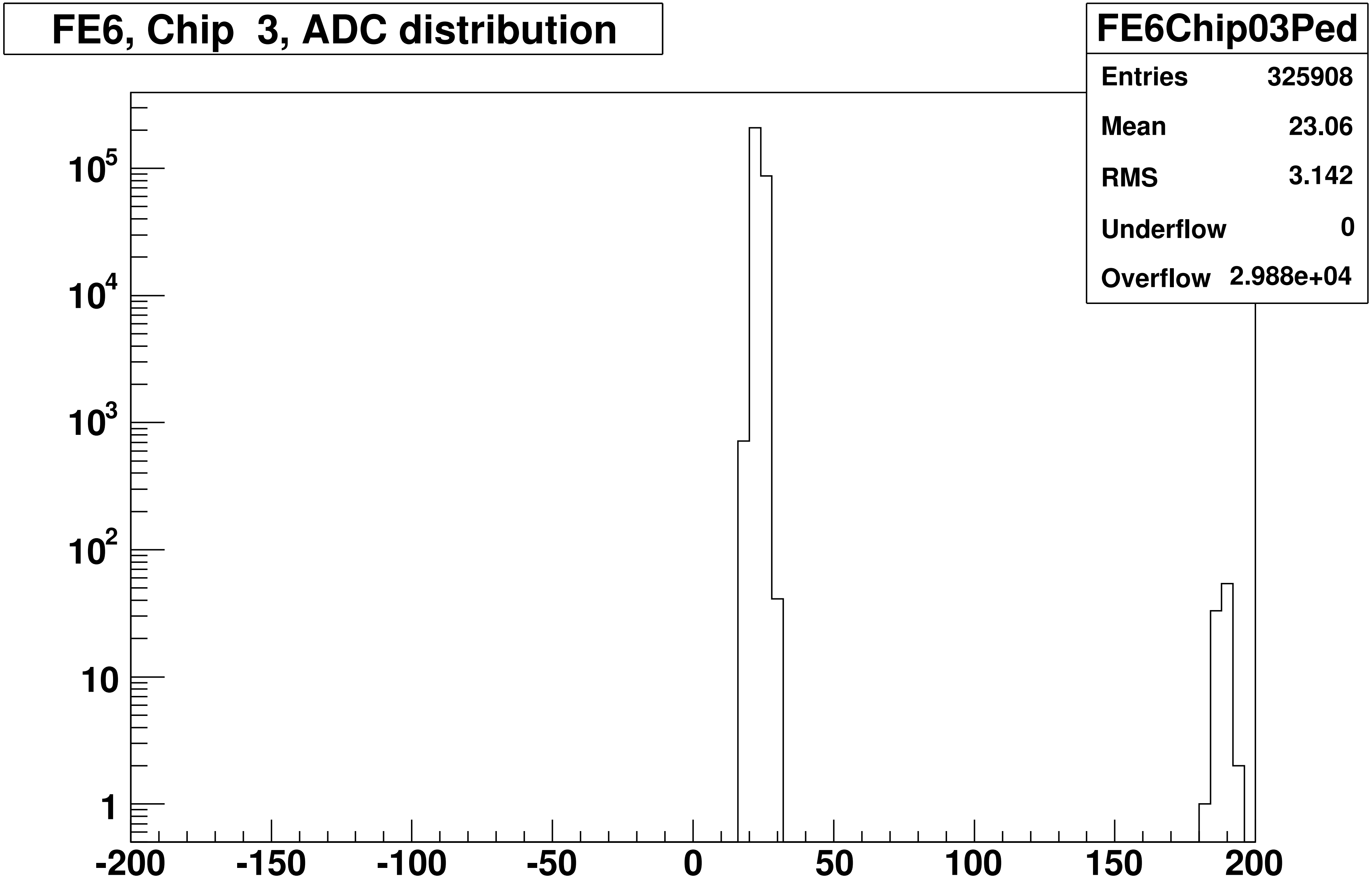


FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 325908
Mean 1689
RMS 5351
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 325908
Mean 1.323
RMS 1.301
Underflow 0
Overflow 2.977e+04
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 325908
Mean 1.866e+04
RMS 34.92
Underflow 2.964e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 325908
Mean 1688
RMS 5315
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 325908
Mean 23.06
RMS 3.142
Underflow 0
Overflow 2.988e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
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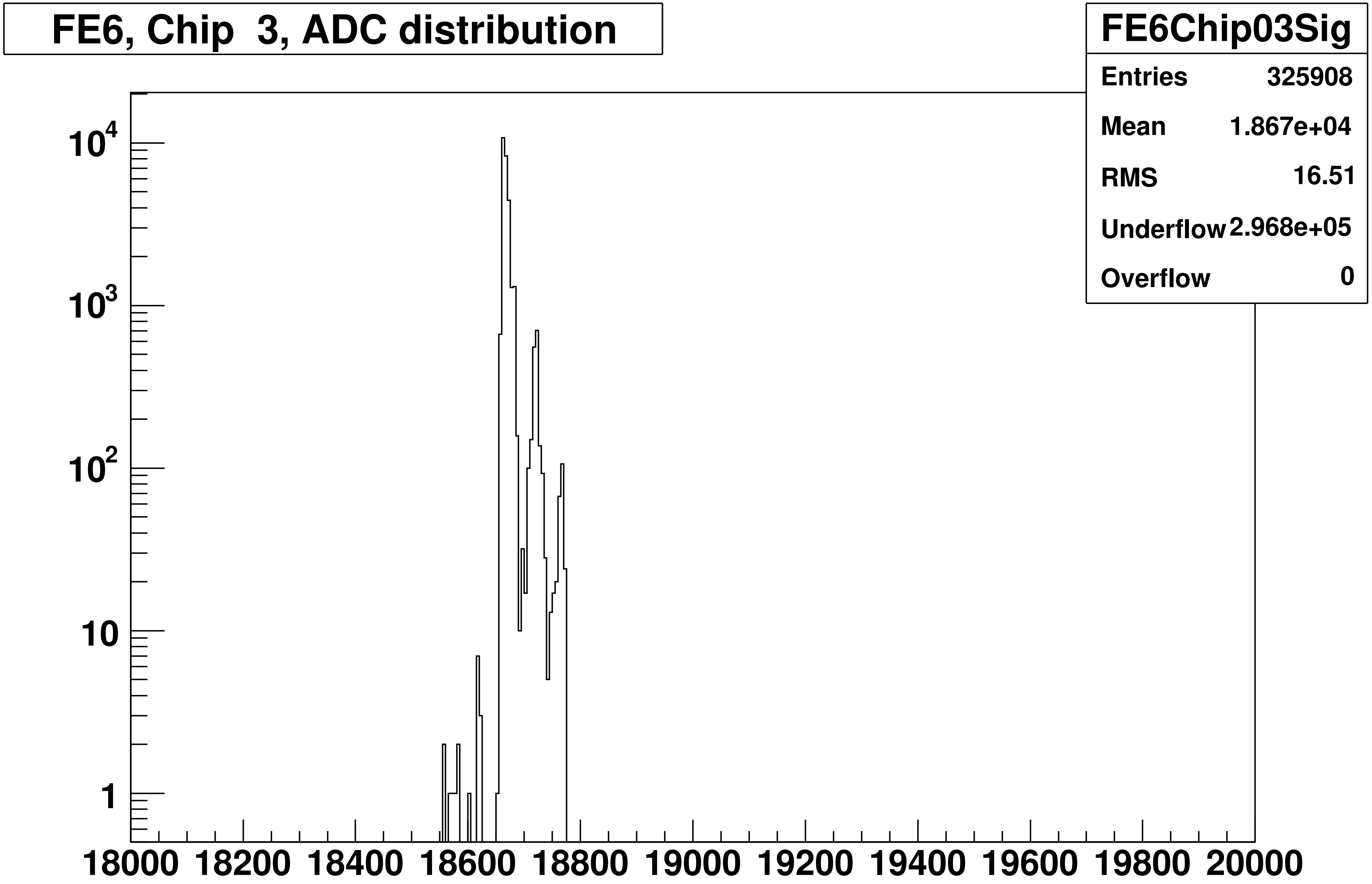
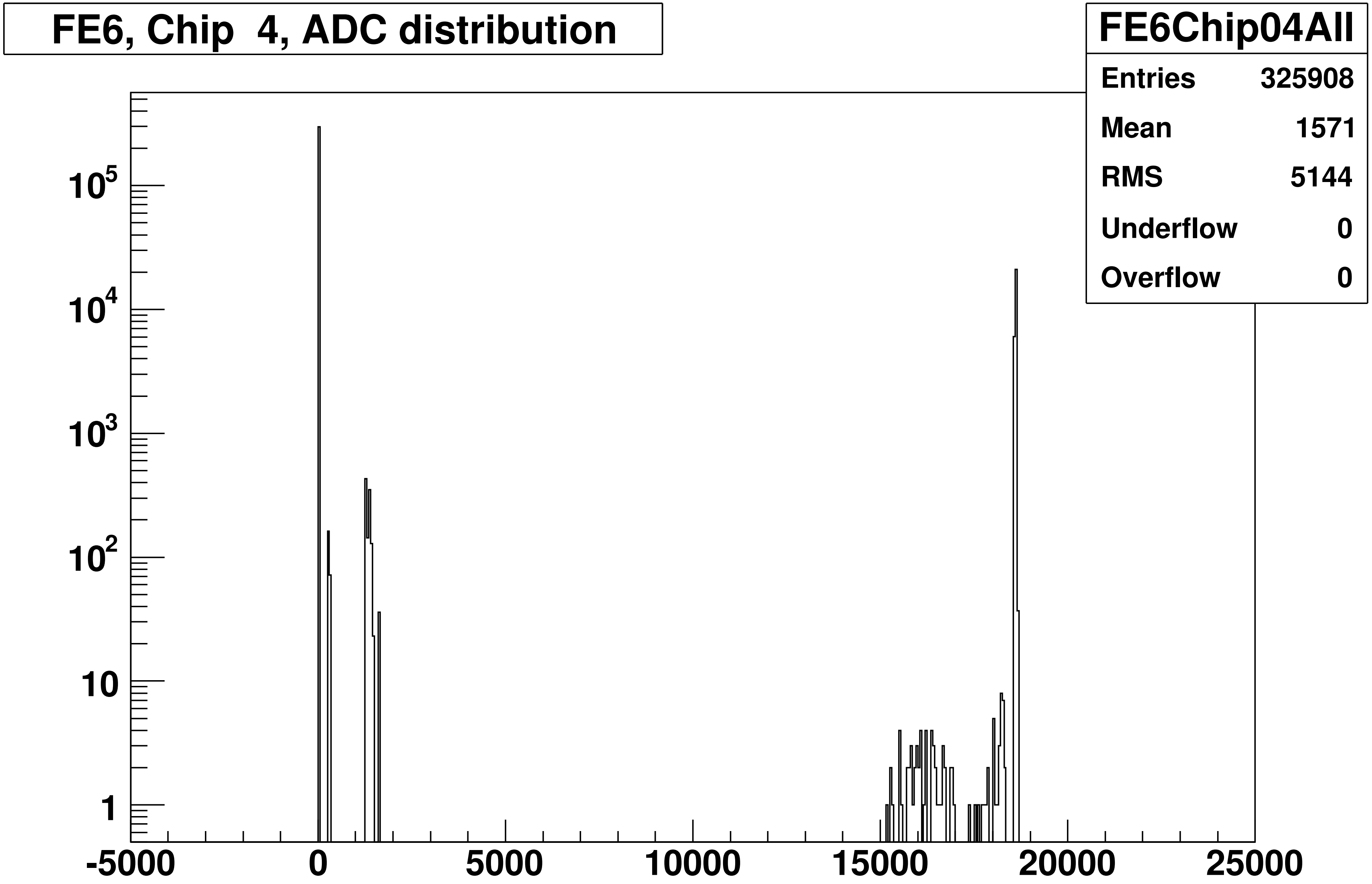
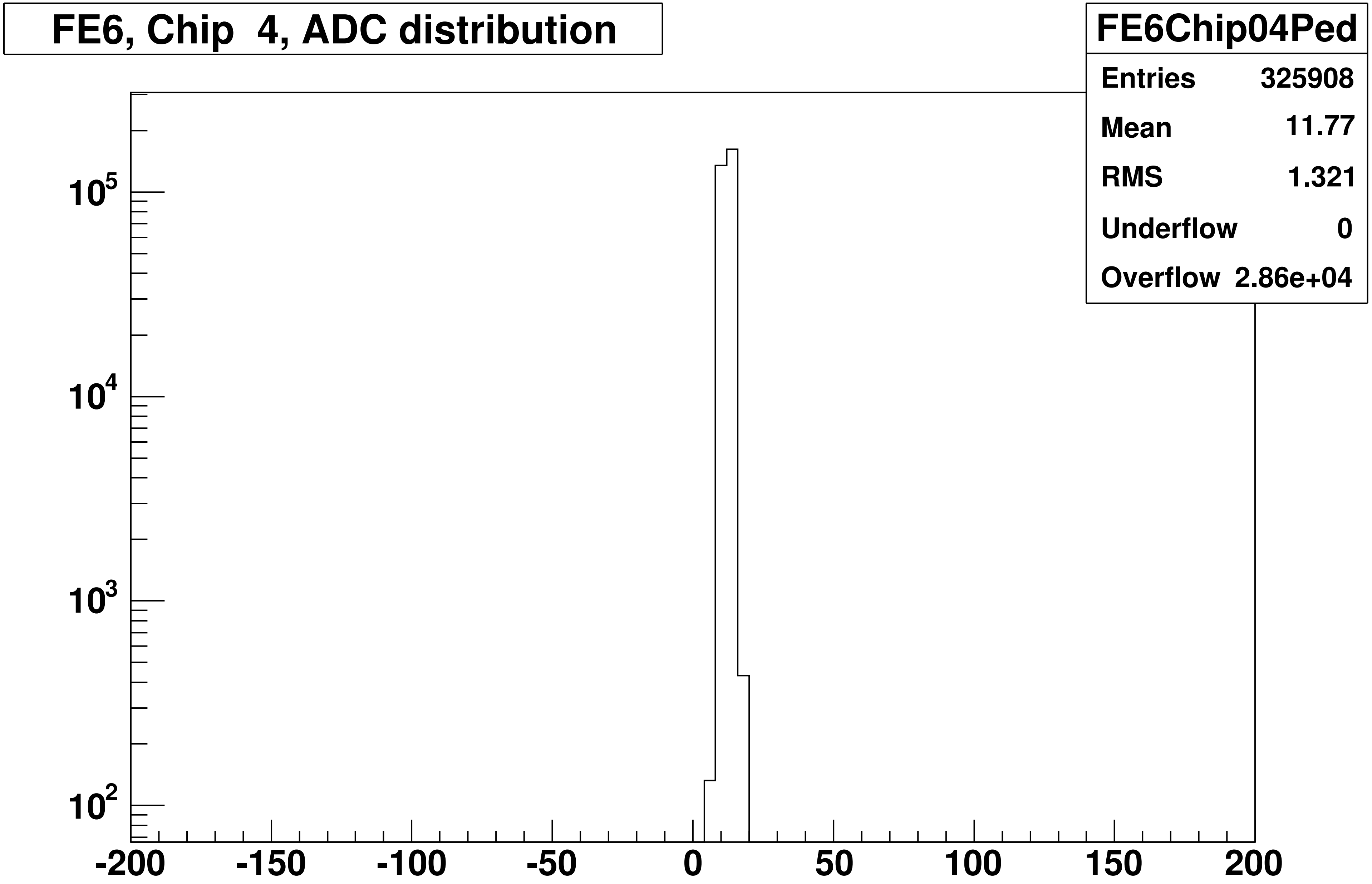
50
100
150
200



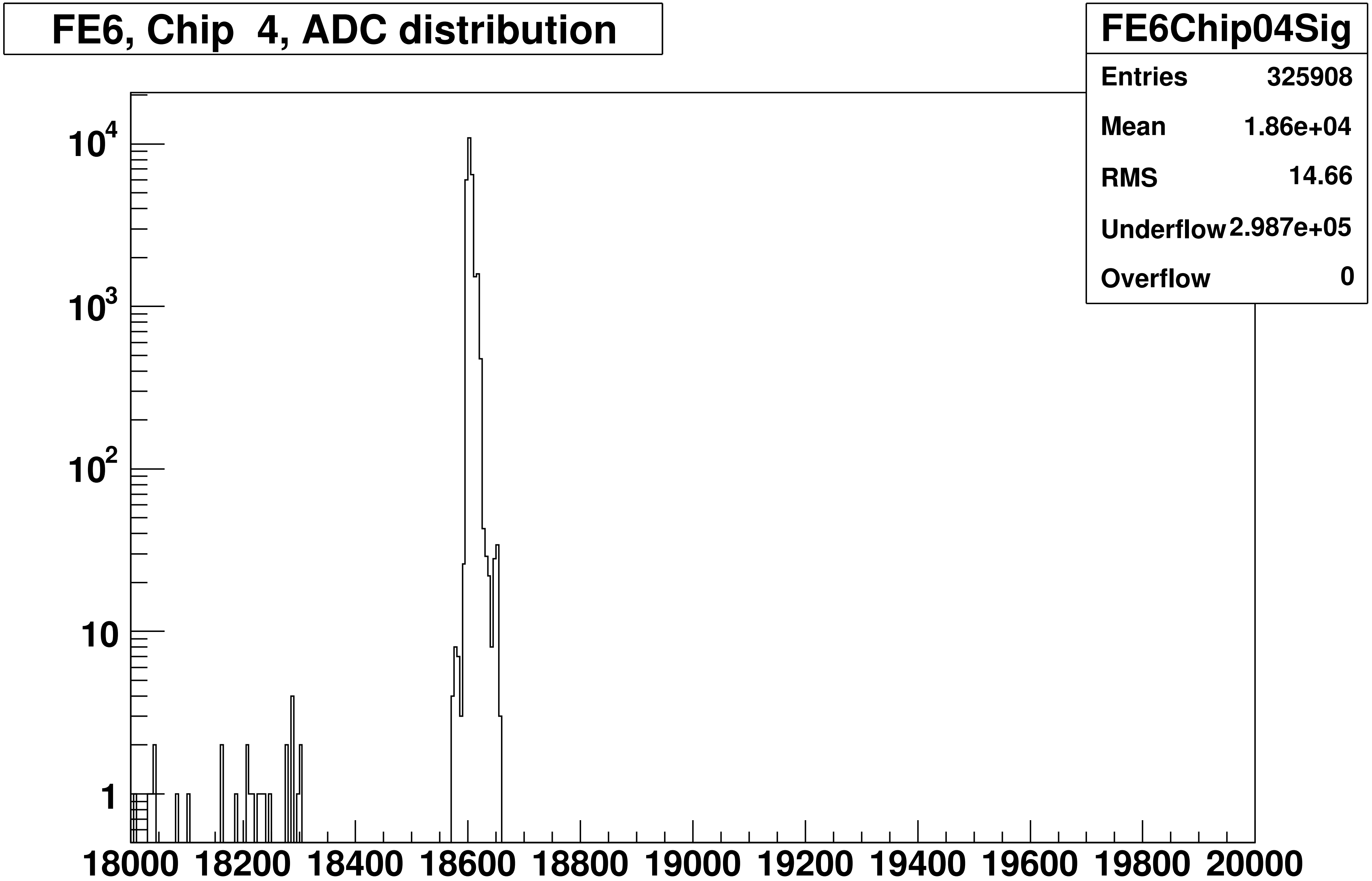
FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 325908
Mean 1.867e+04
RMS 16.51
Underflow 2.968e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

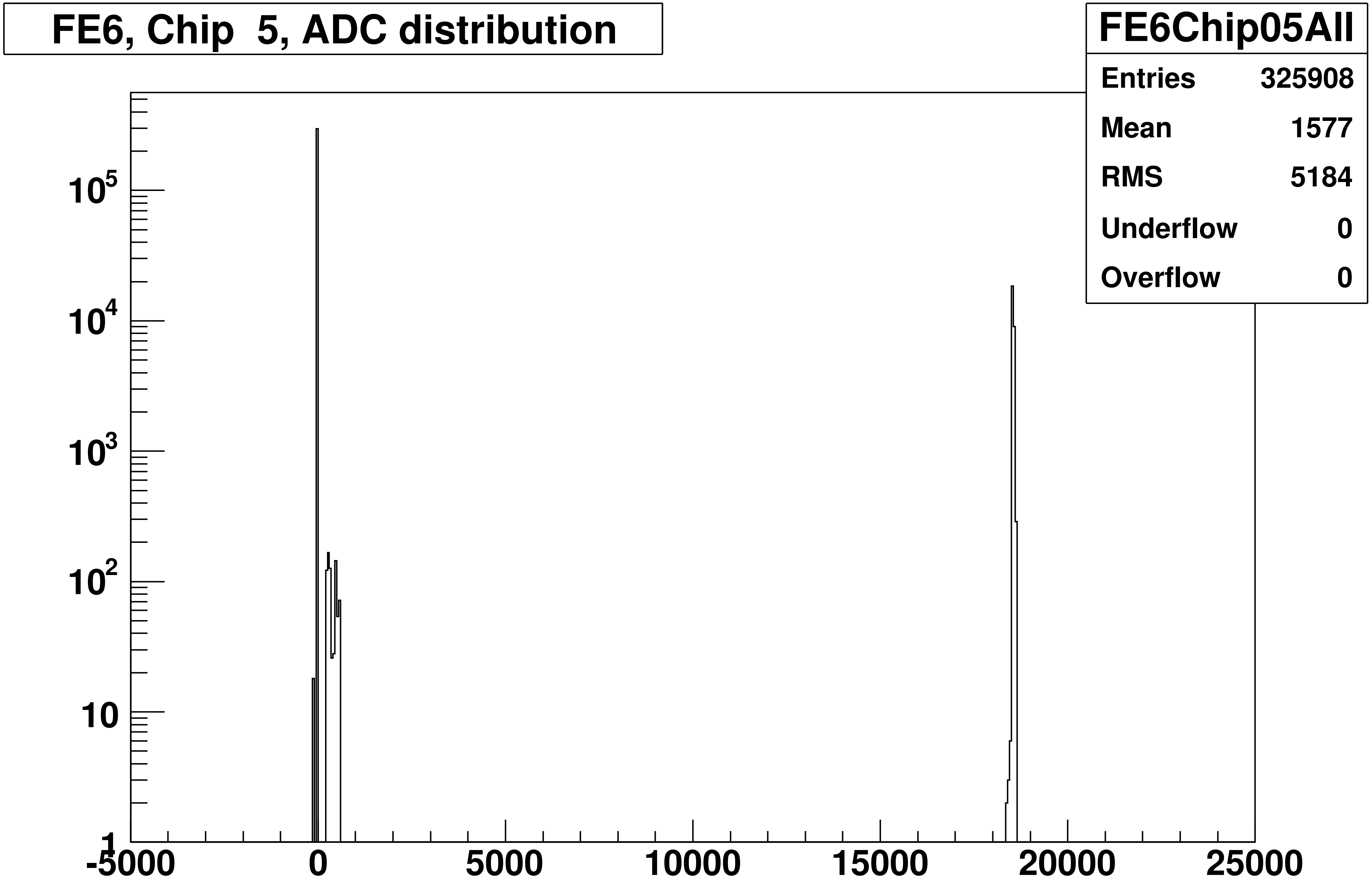


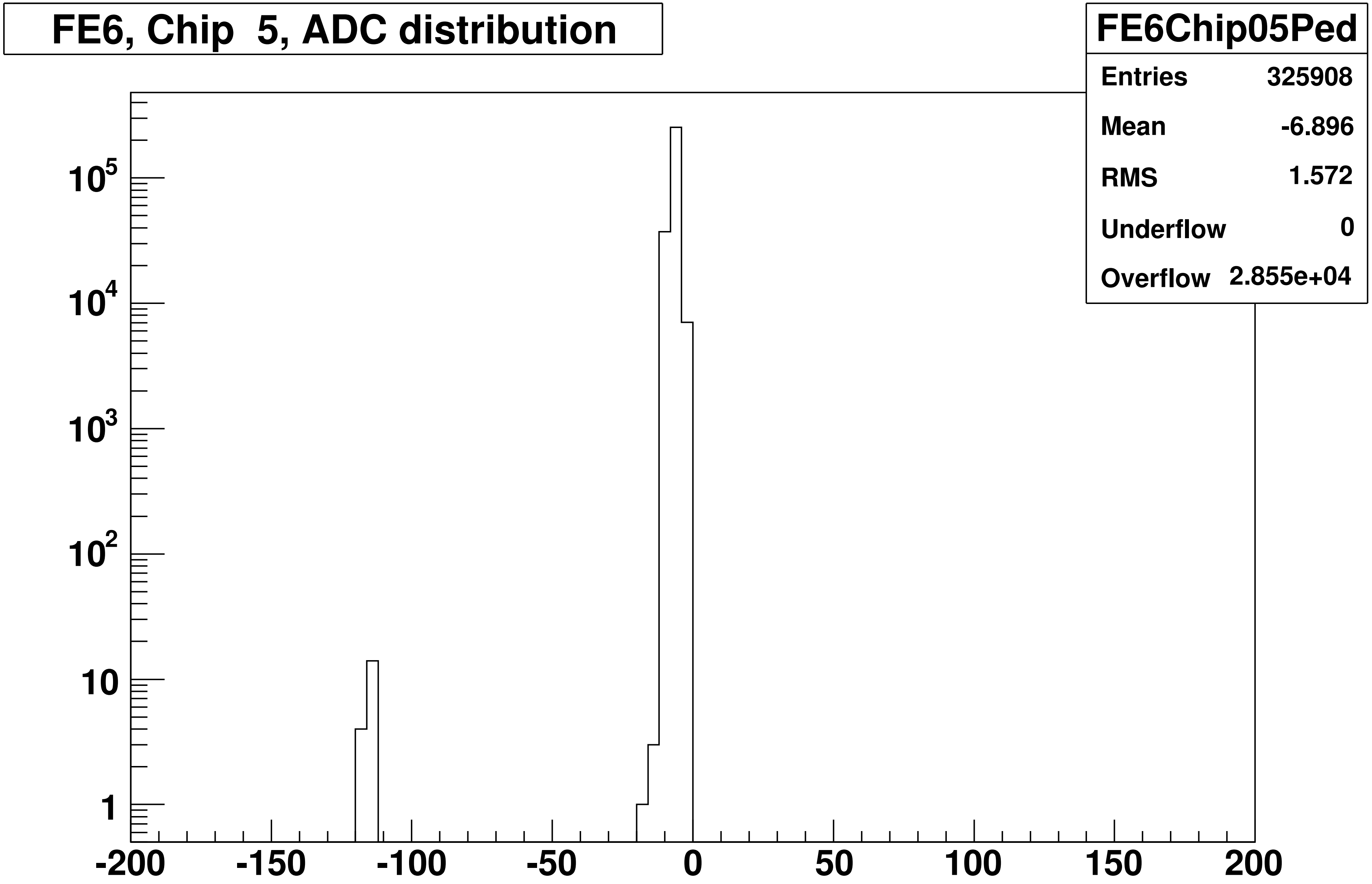
FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 325908
Mean 1571
RMS 5144
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 325908
Mean 11.77
RMS 1.321
Underflow 0
Overflow 2.86e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 325908
Mean 1.86e+04
RMS 14.66
Underflow 2.987e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 325908
Mean 1577
RMS 5184
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 325908
Mean -6.896
RMS 1.572
Underflow 0
Overflow 2.855e+04
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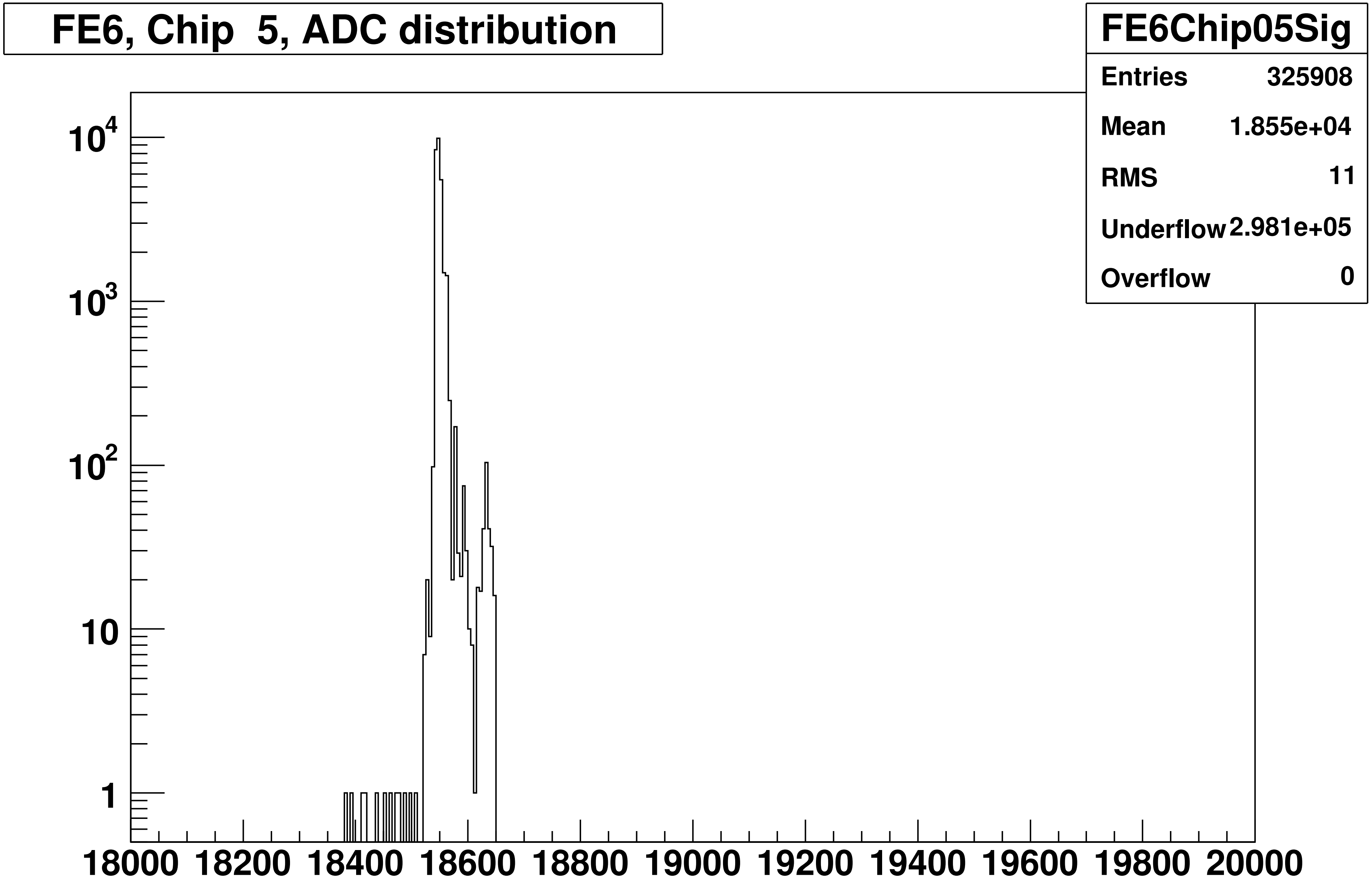
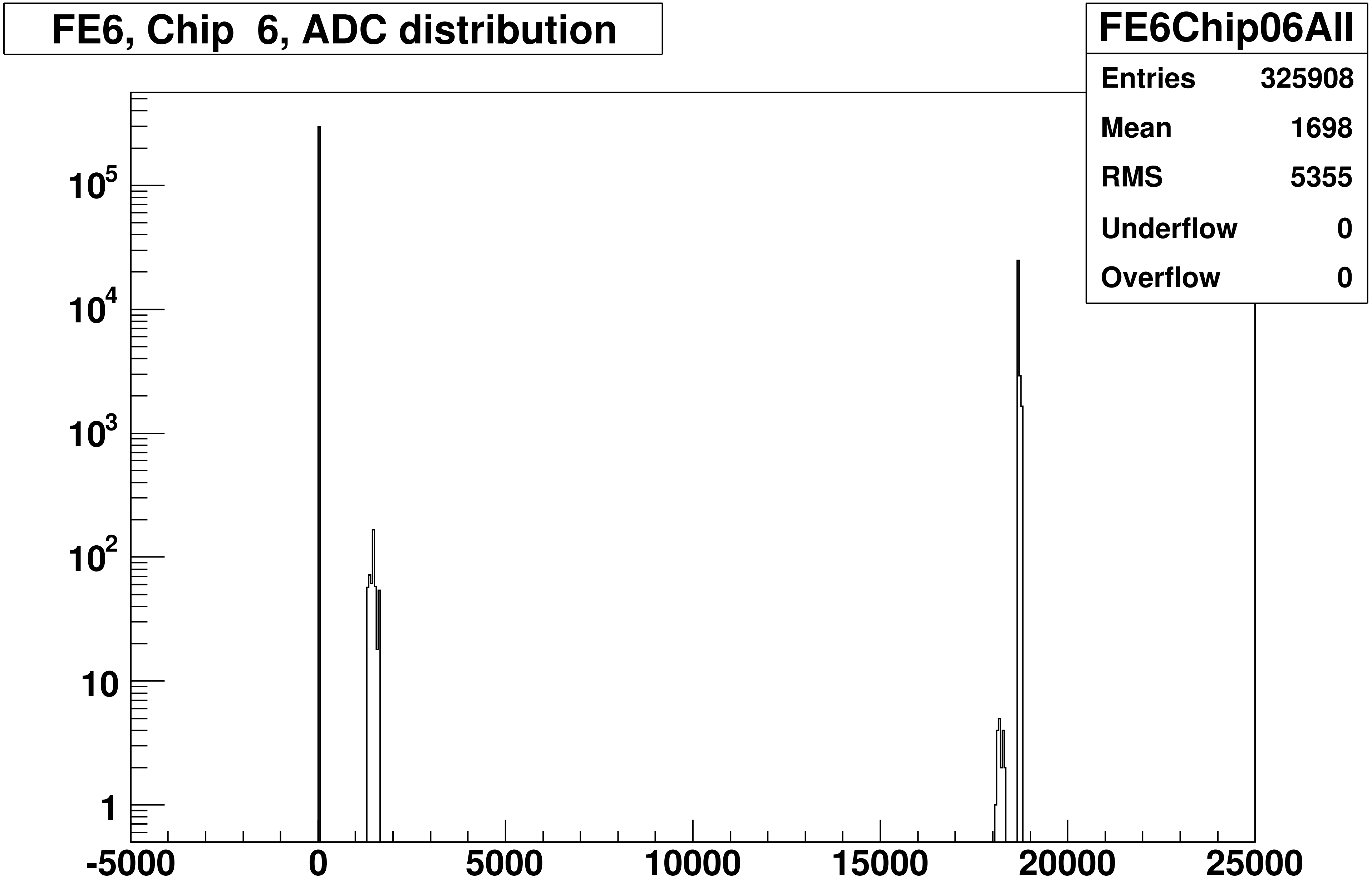
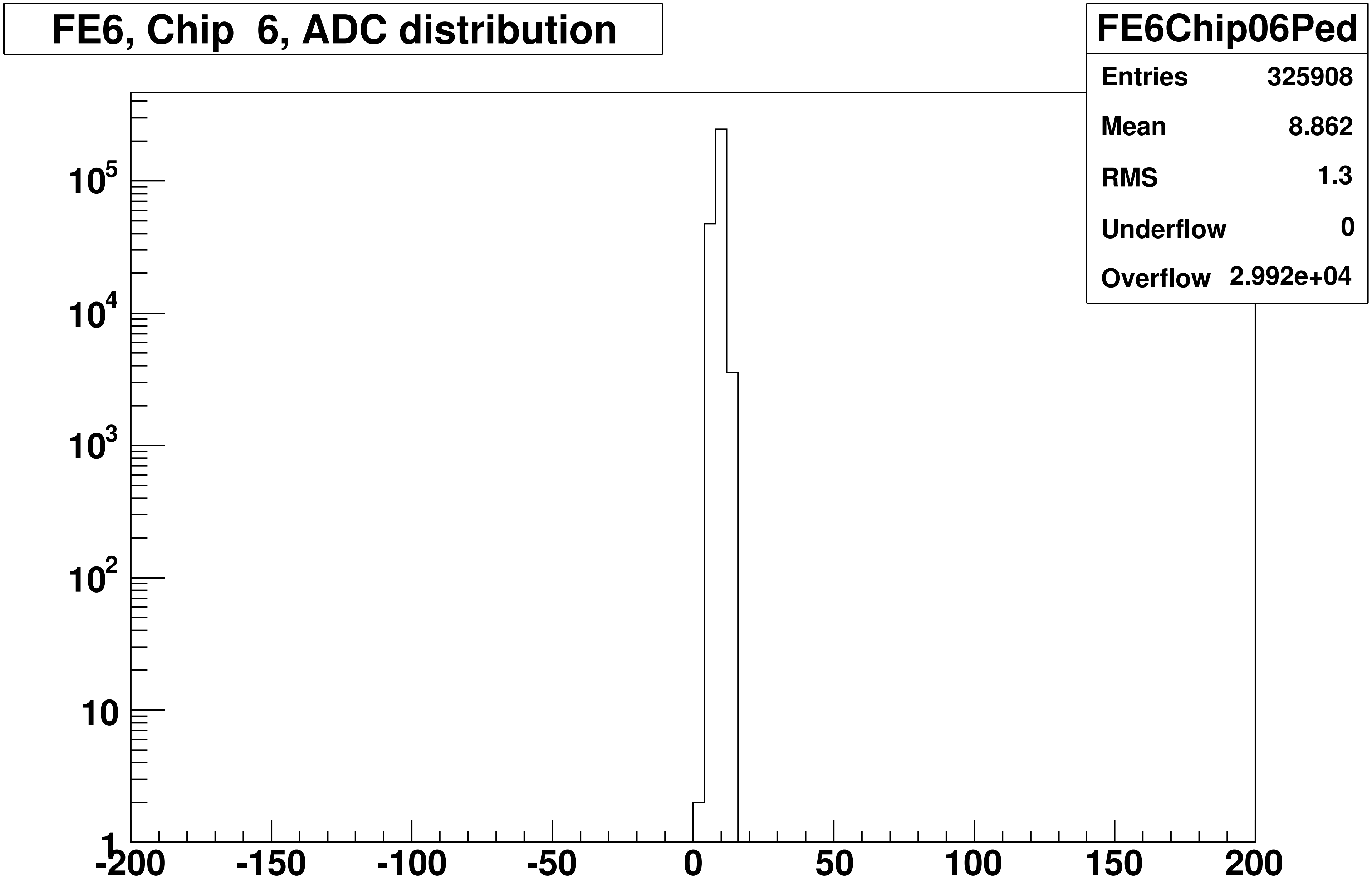
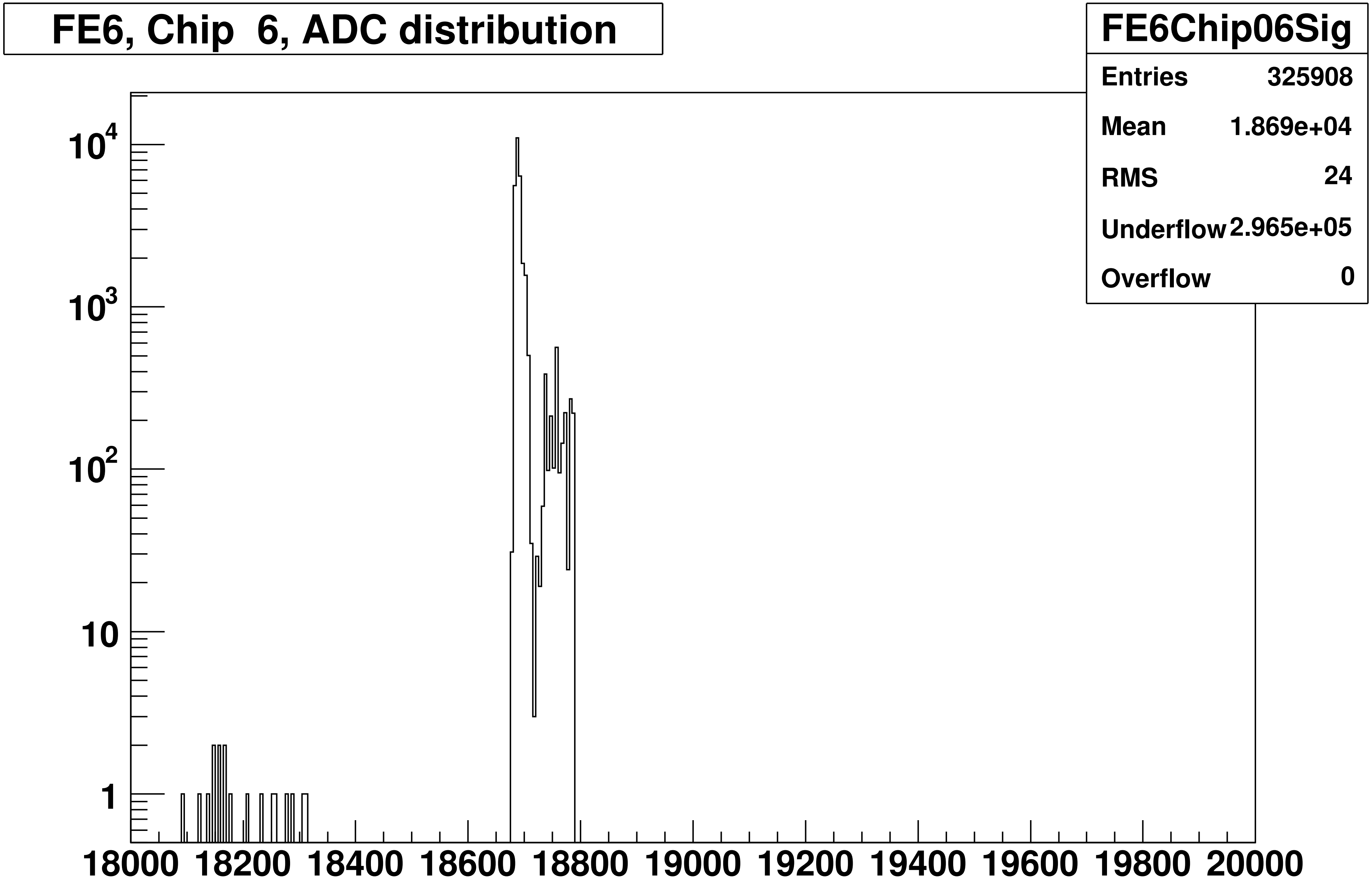
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 325908
Mean 1.855e+04
RMS 11
Underflow 2.981e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

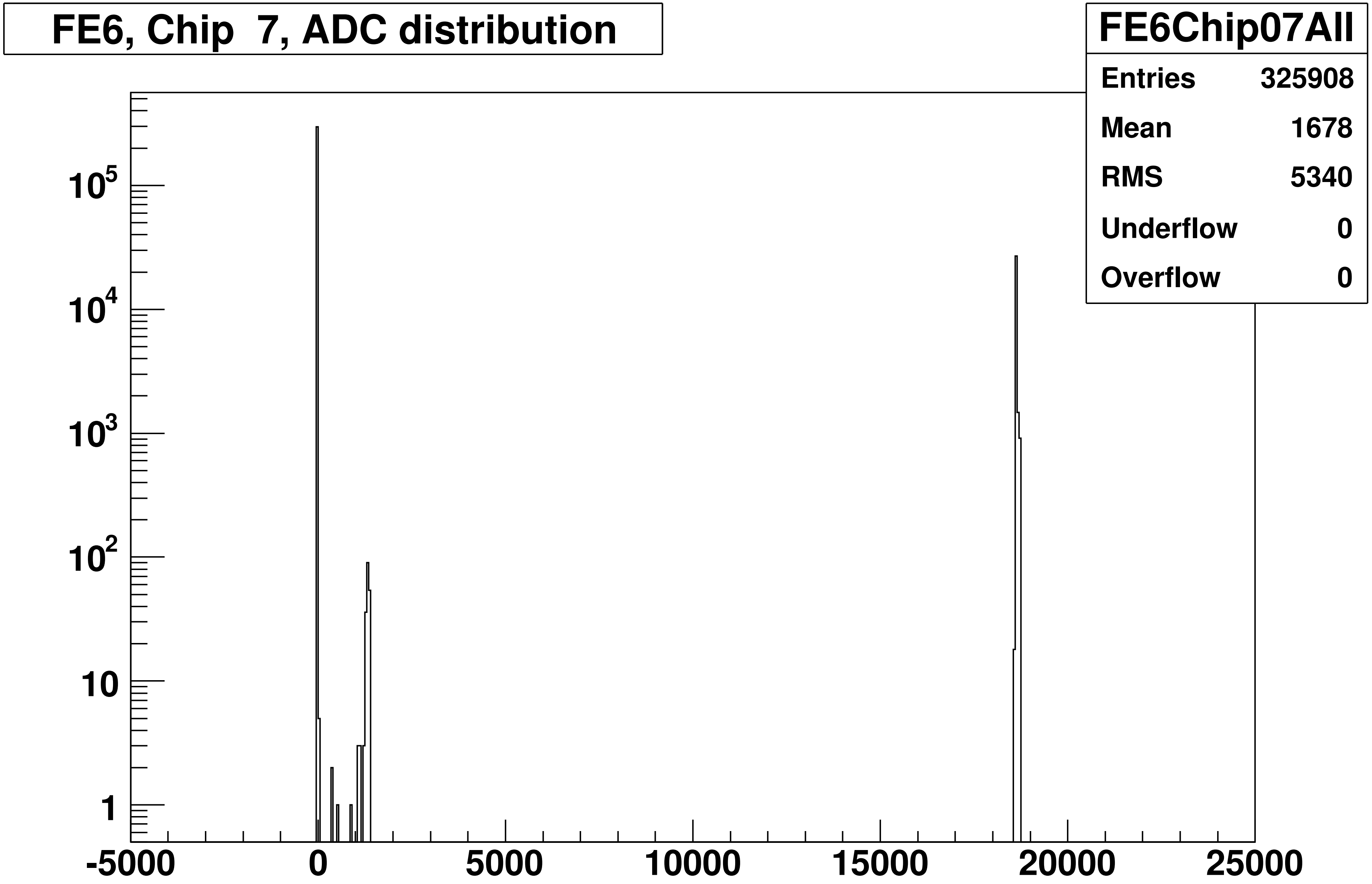


FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 325908
Mean 1698
RMS 5355
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 325908
Mean 8.862
RMS 1.3
Underflow 0
Overflow 2.992e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 325908
Mean 1.869e+04
RMS 24
Underflow 2.965e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 325908
Mean 1678
RMS 5340
Underflow 0
Overflow 0
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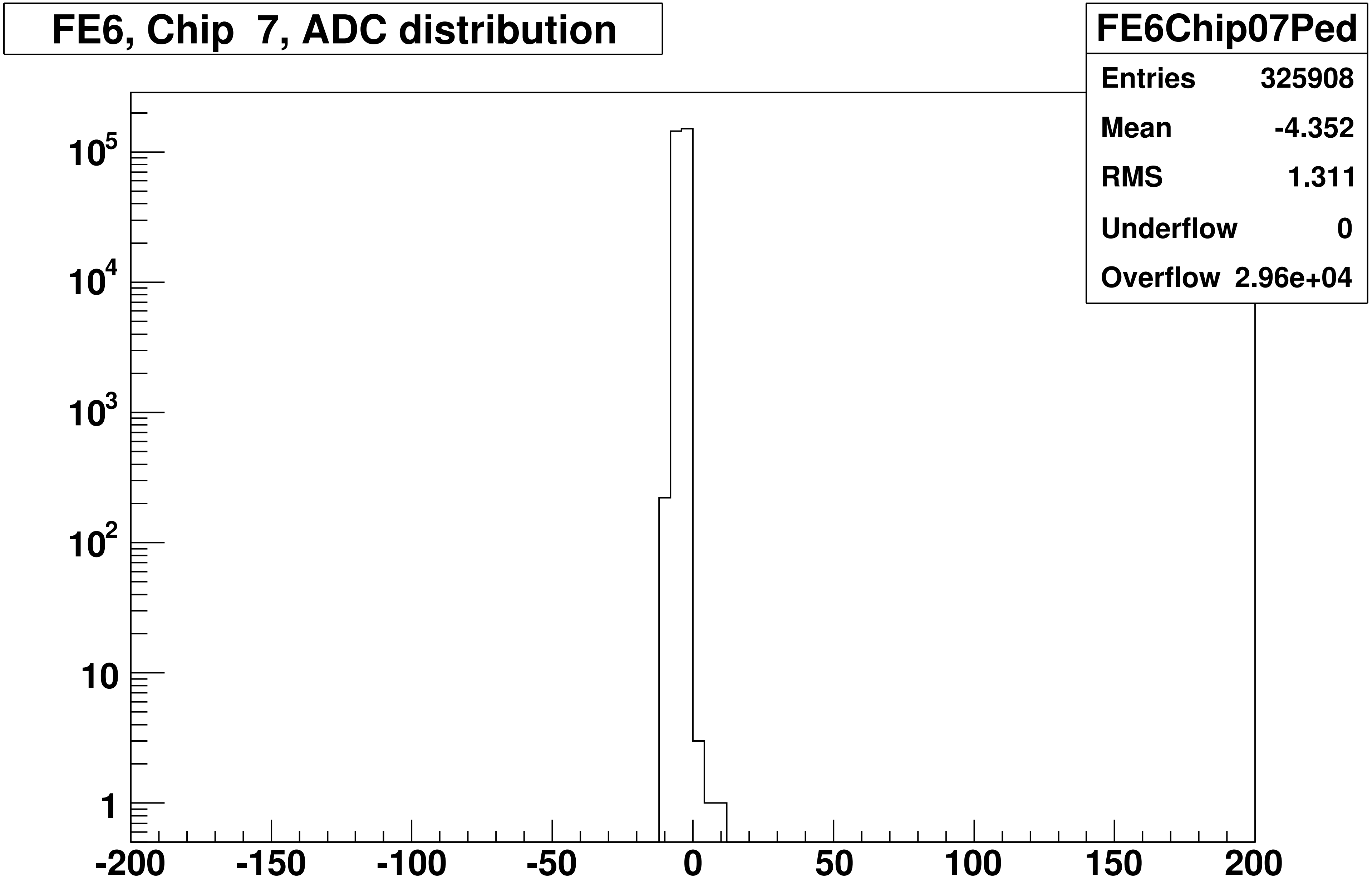
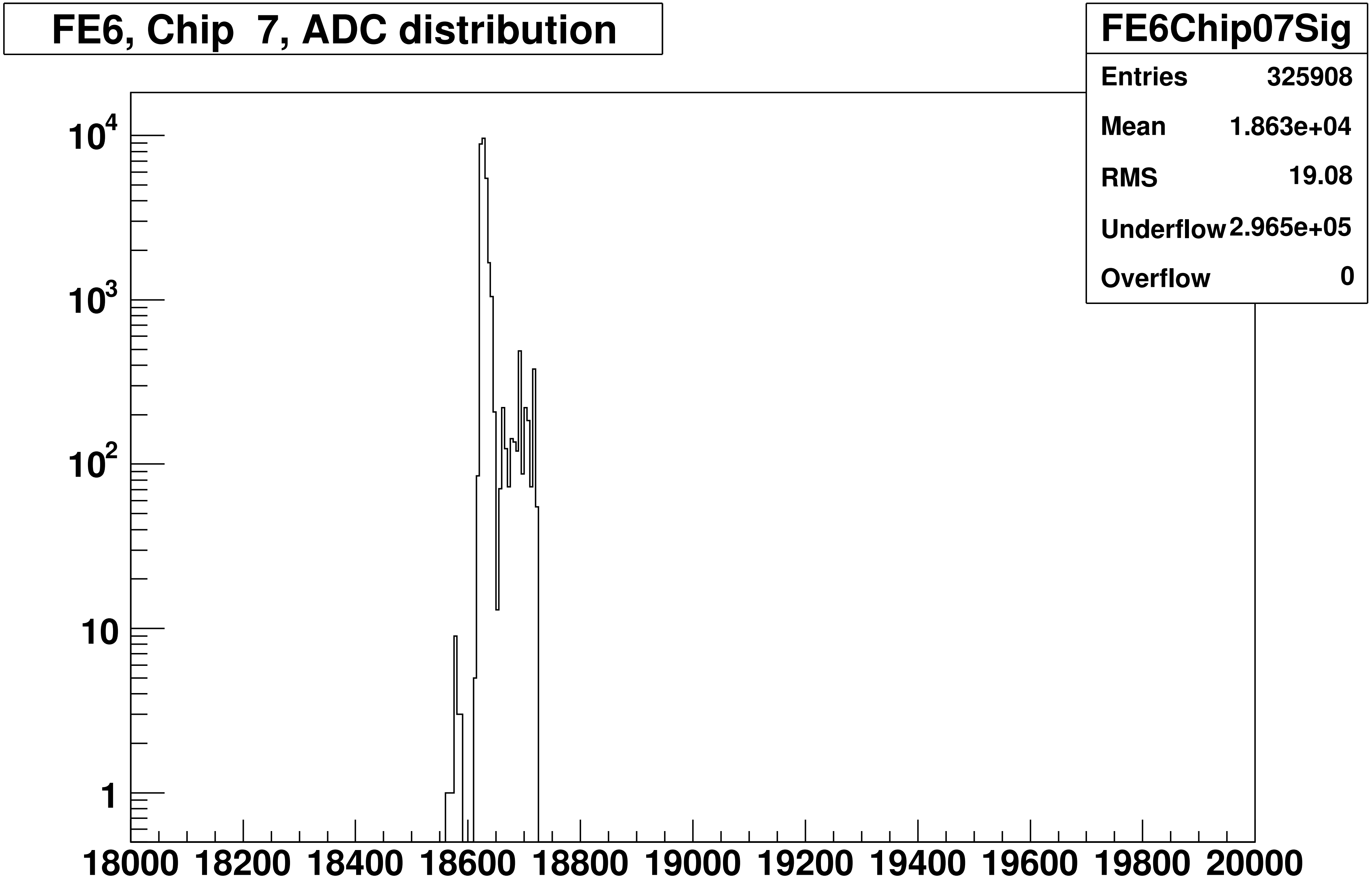
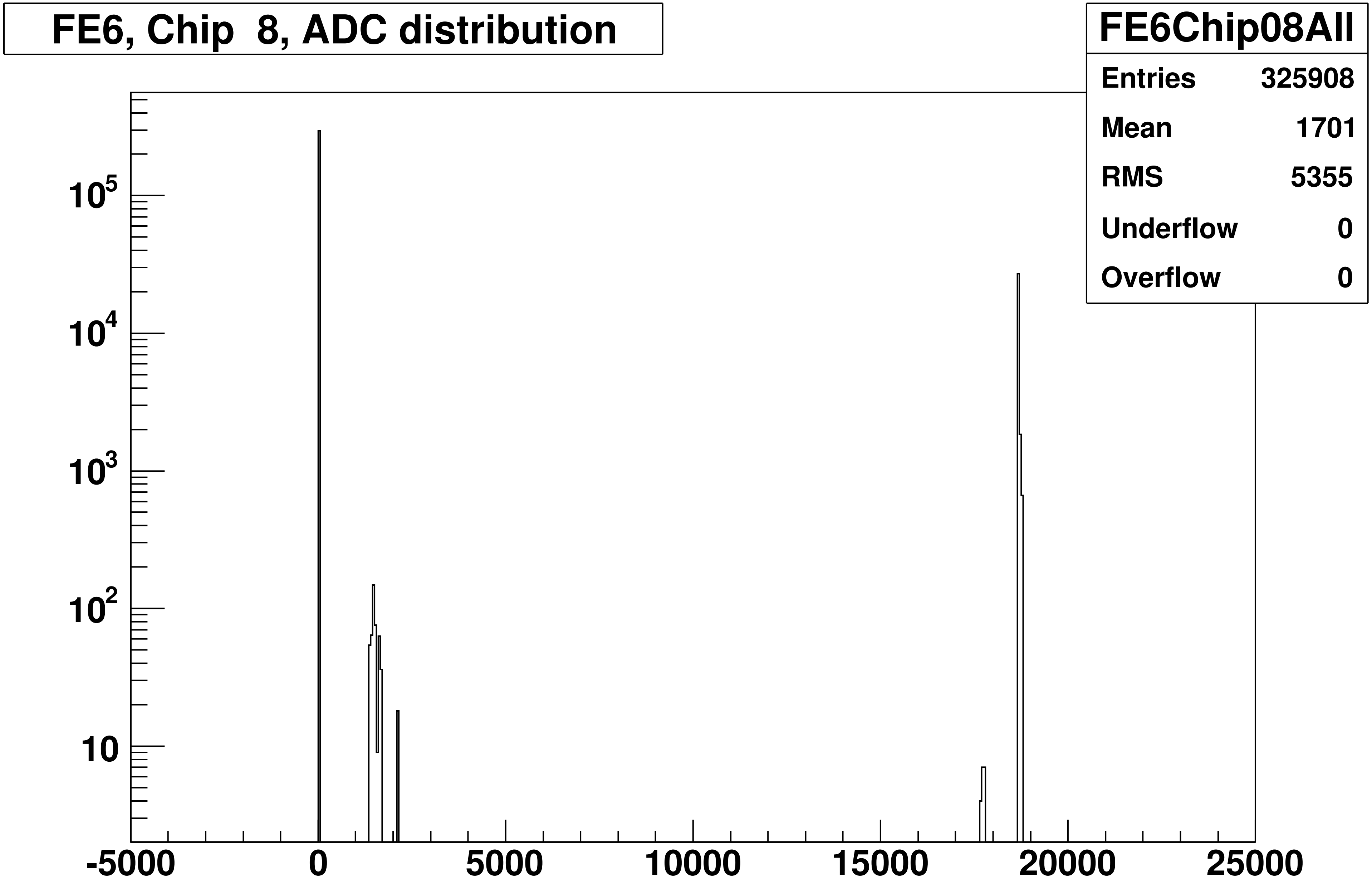
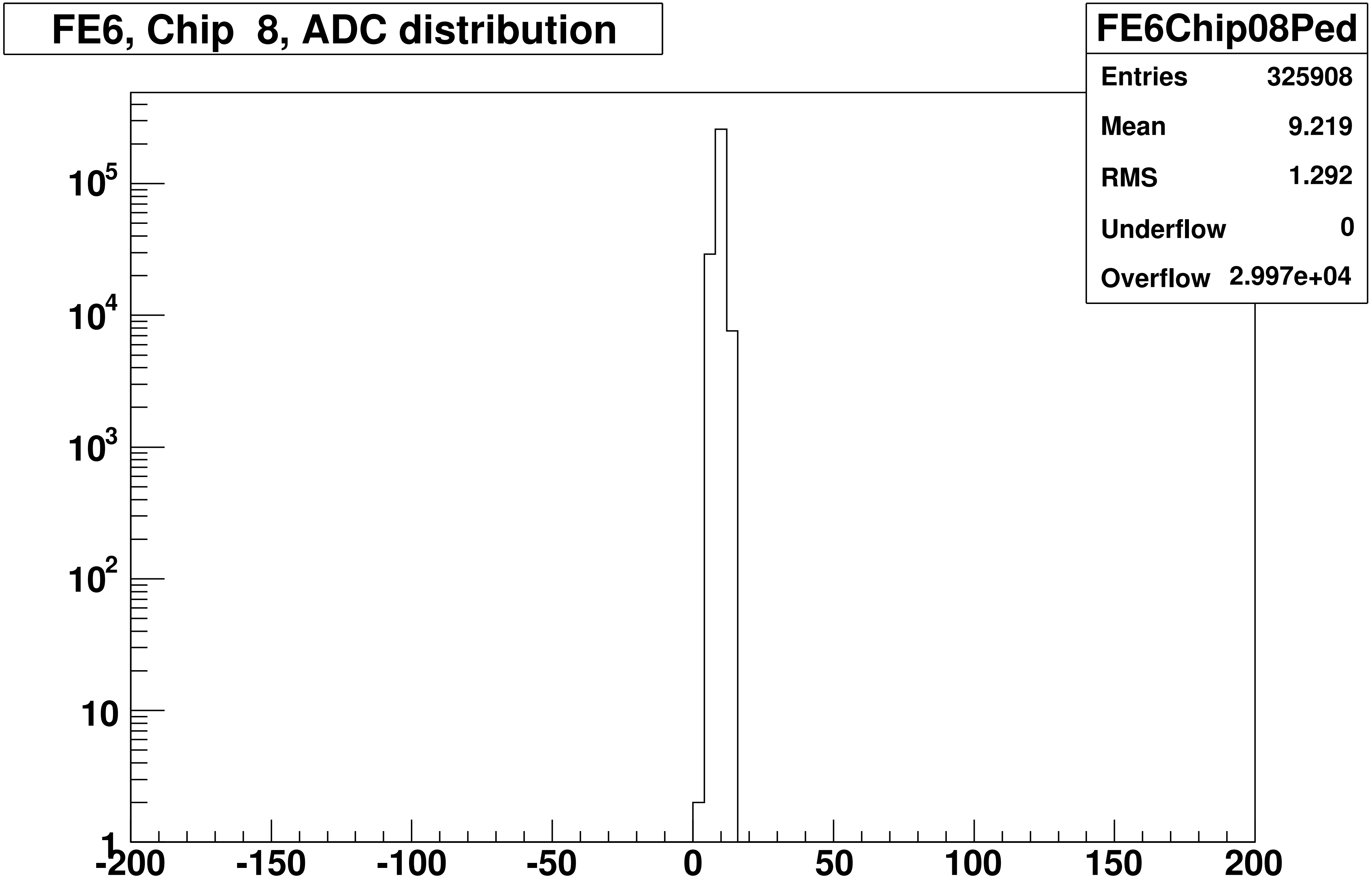
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

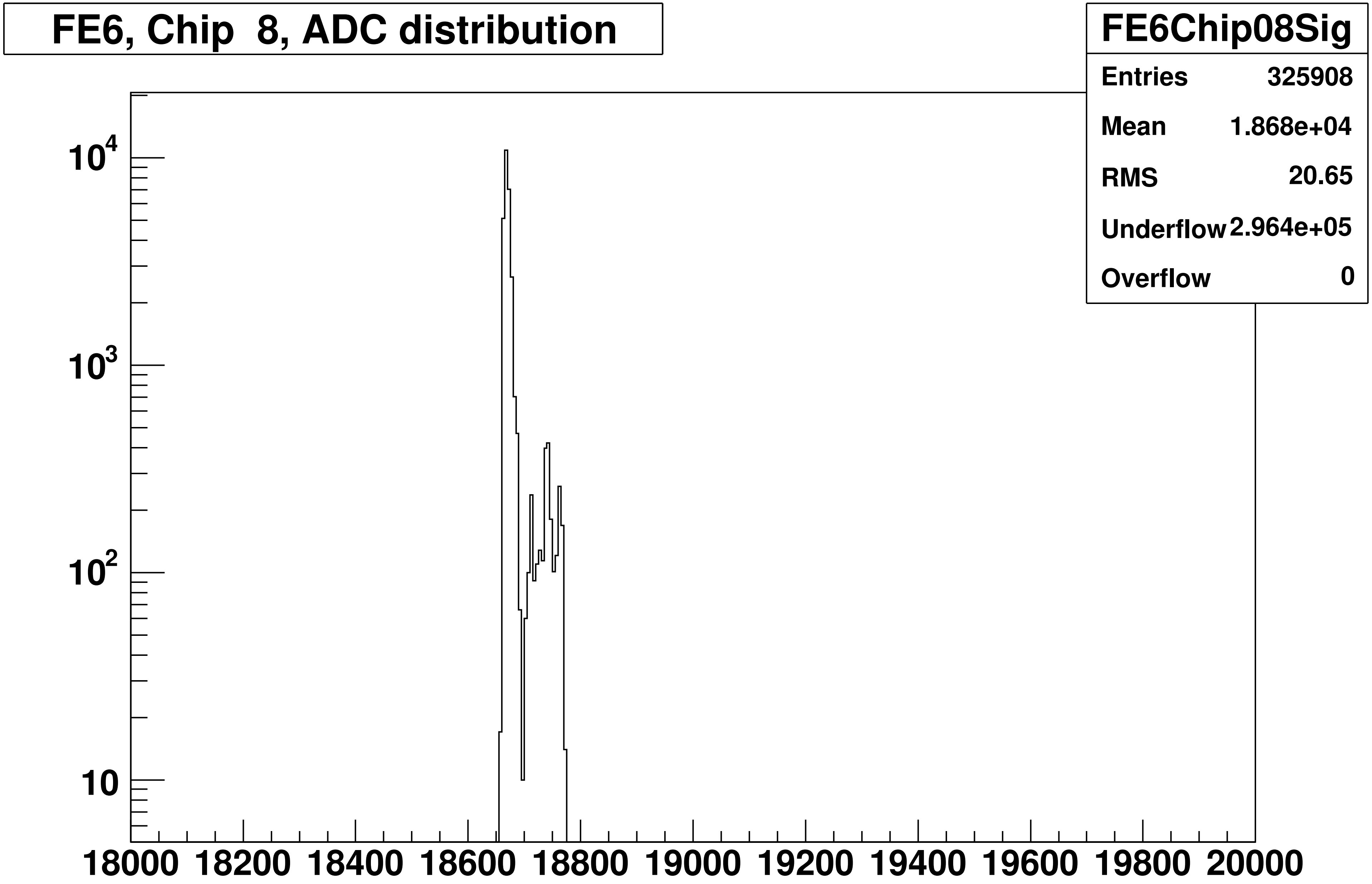

FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 325908
Mean -4.352
RMS 1.311
Underflow 0
Overflow 2.96e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 325908
Mean 1.863e+04
RMS 19.08
Underflow 2.965e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 325908
Mean 1701
RMS 5355
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 325908
Mean 9.219
RMS 1.292
Underflow 0
Overflow 2.997e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 325908
Mean 1.868e+04
RMS 20.65
Underflow 2.964e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
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19000
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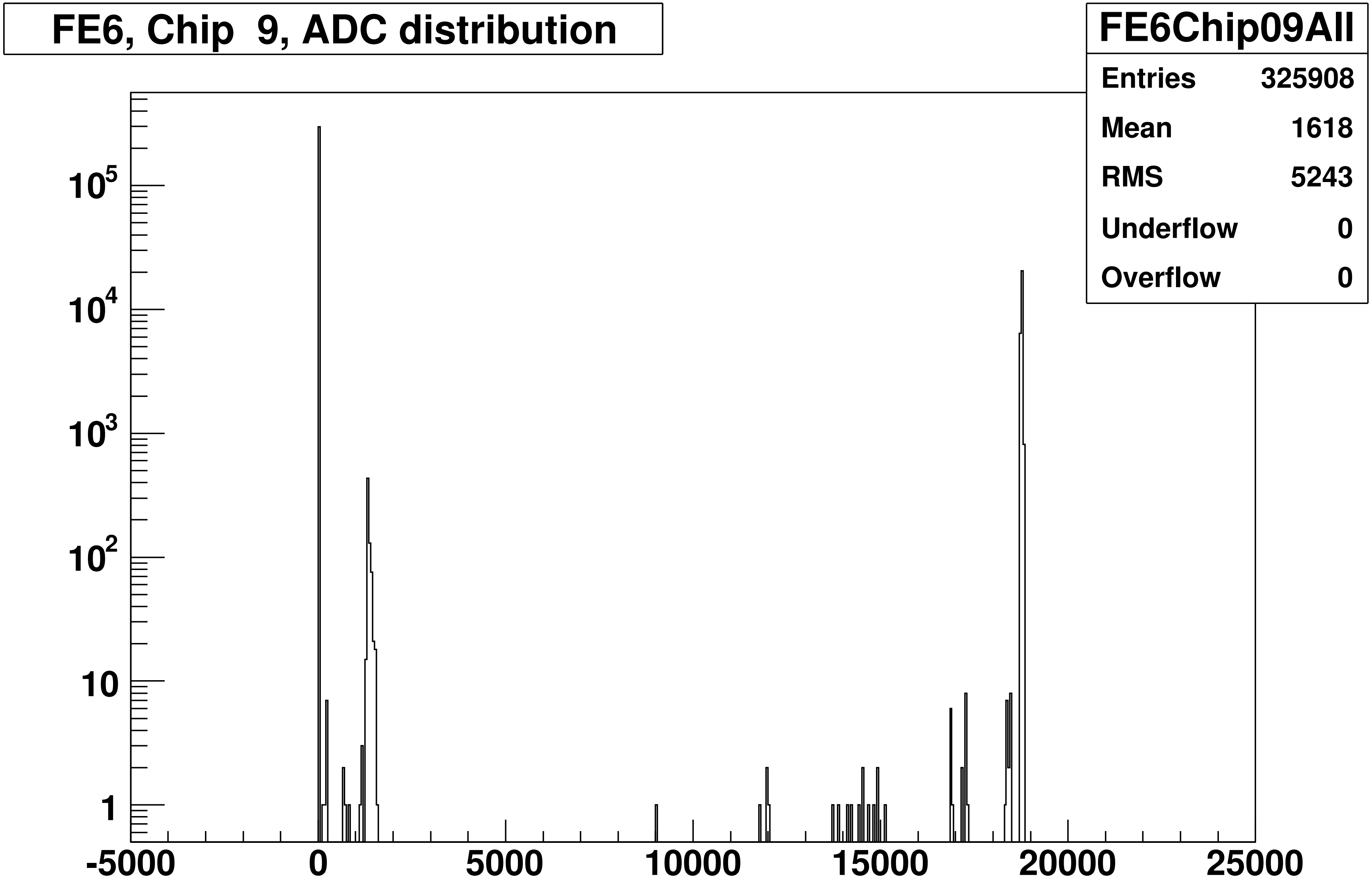
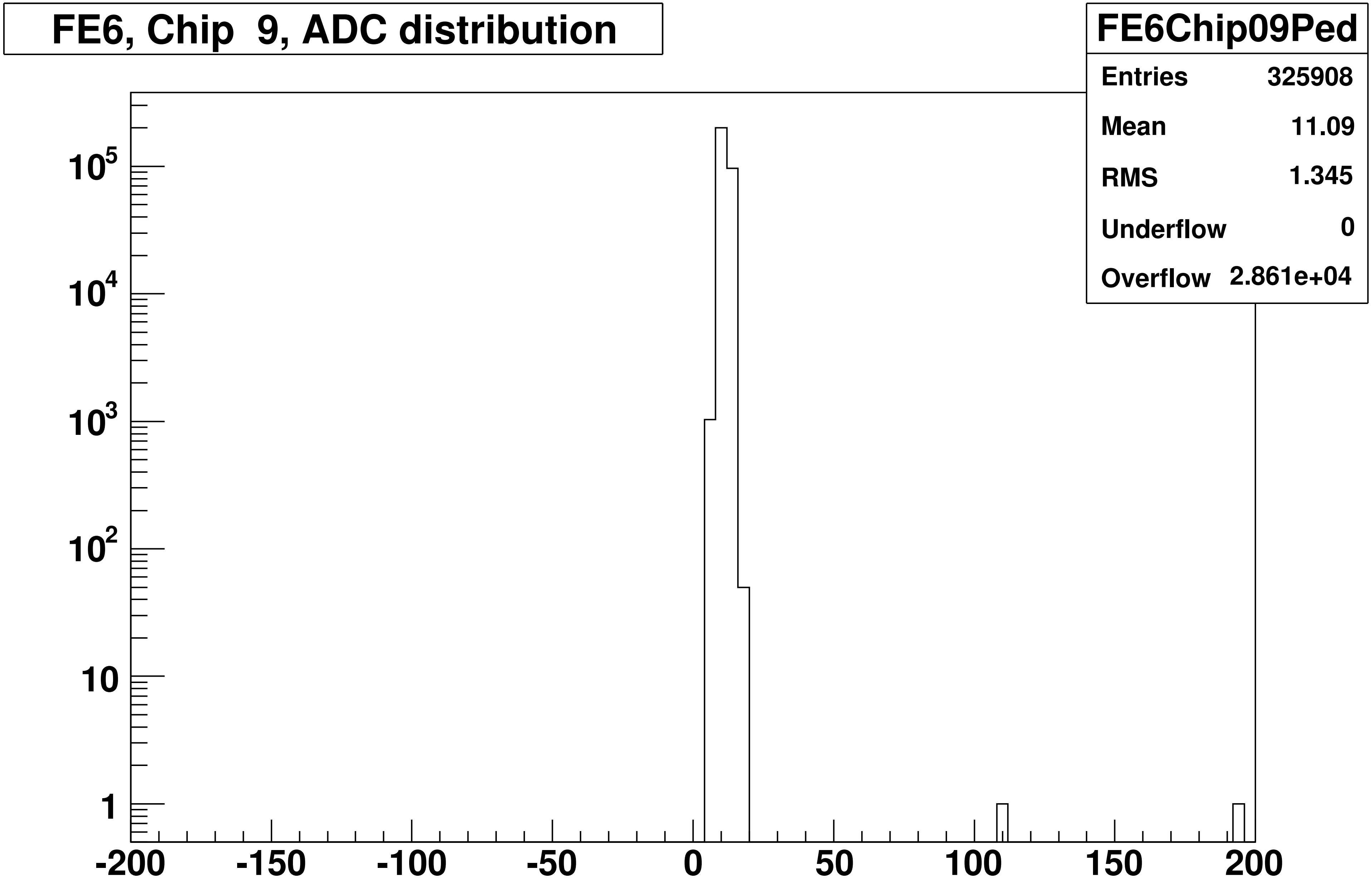
19600
19800
20000


FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 325908
Mean 1618
RMS 5243
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 325908
Mean 11.09
RMS 1.345
Underflow 0
Overflow 2.861e+04
10^5
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10^3
10^2
10
1
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-100
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0
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100
150
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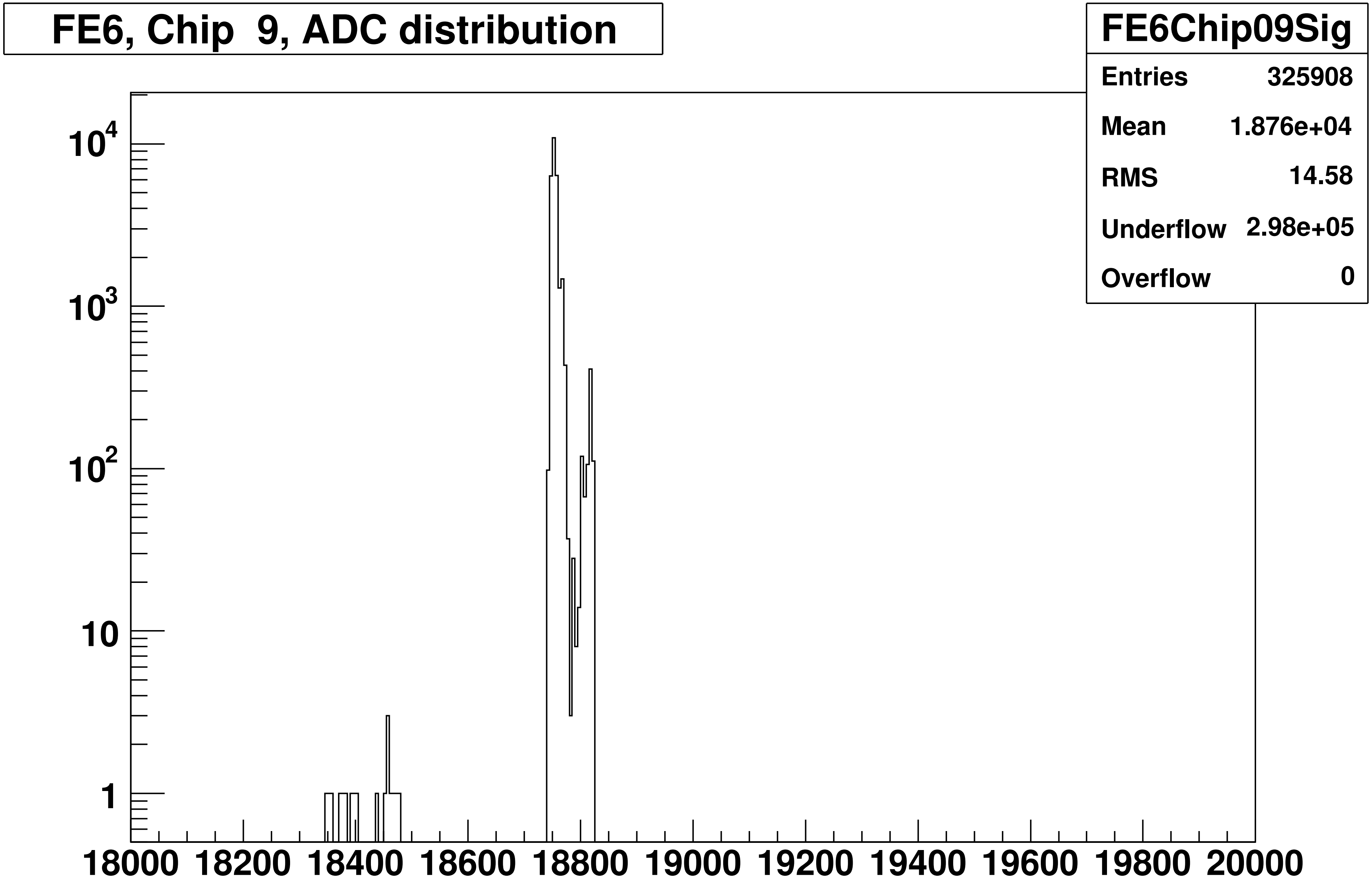
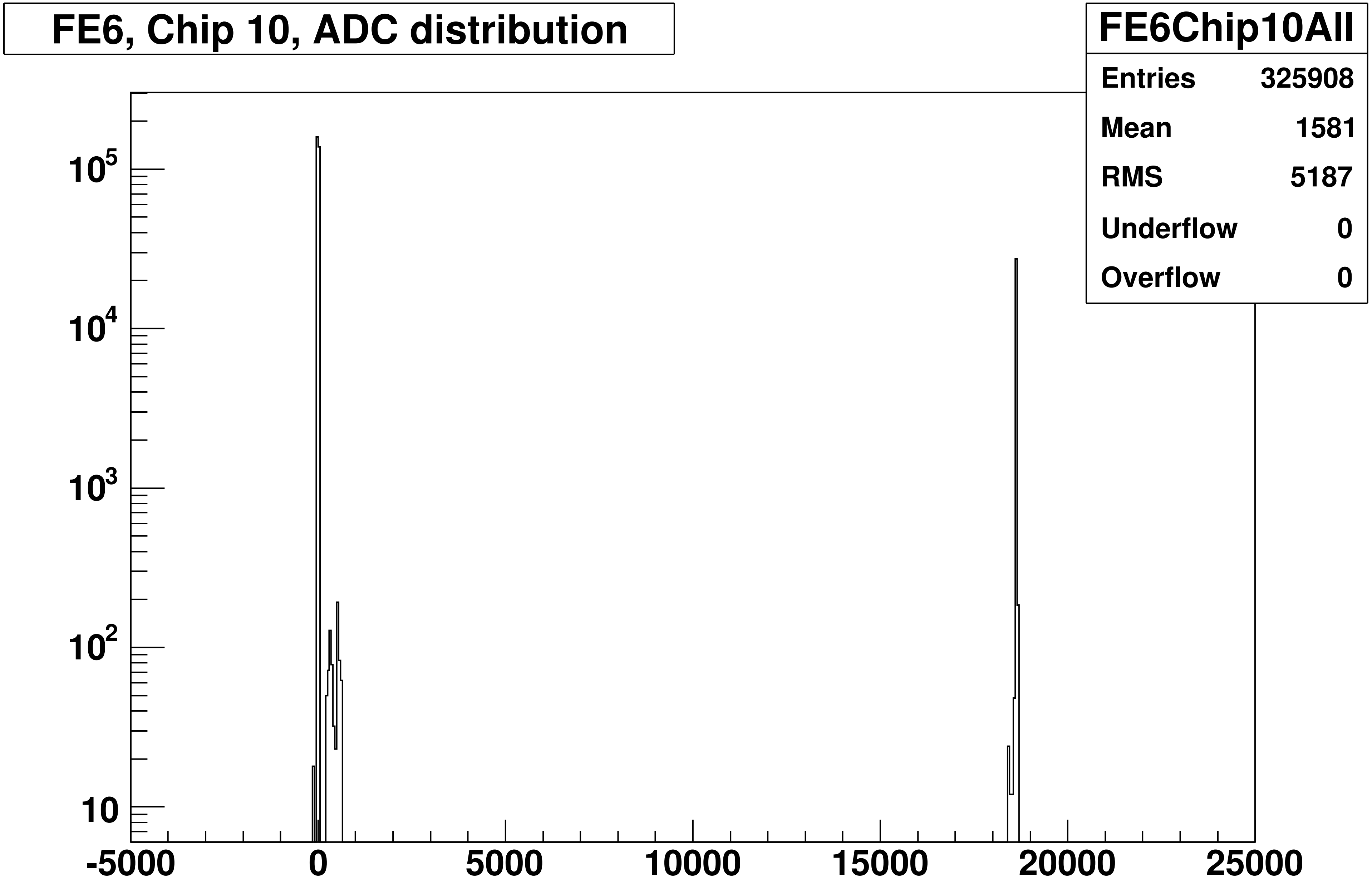

FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 325908
Mean 1.876e+04
RMS 14.58
Underflow 2.98e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 325908
Mean 1581
RMS 5187
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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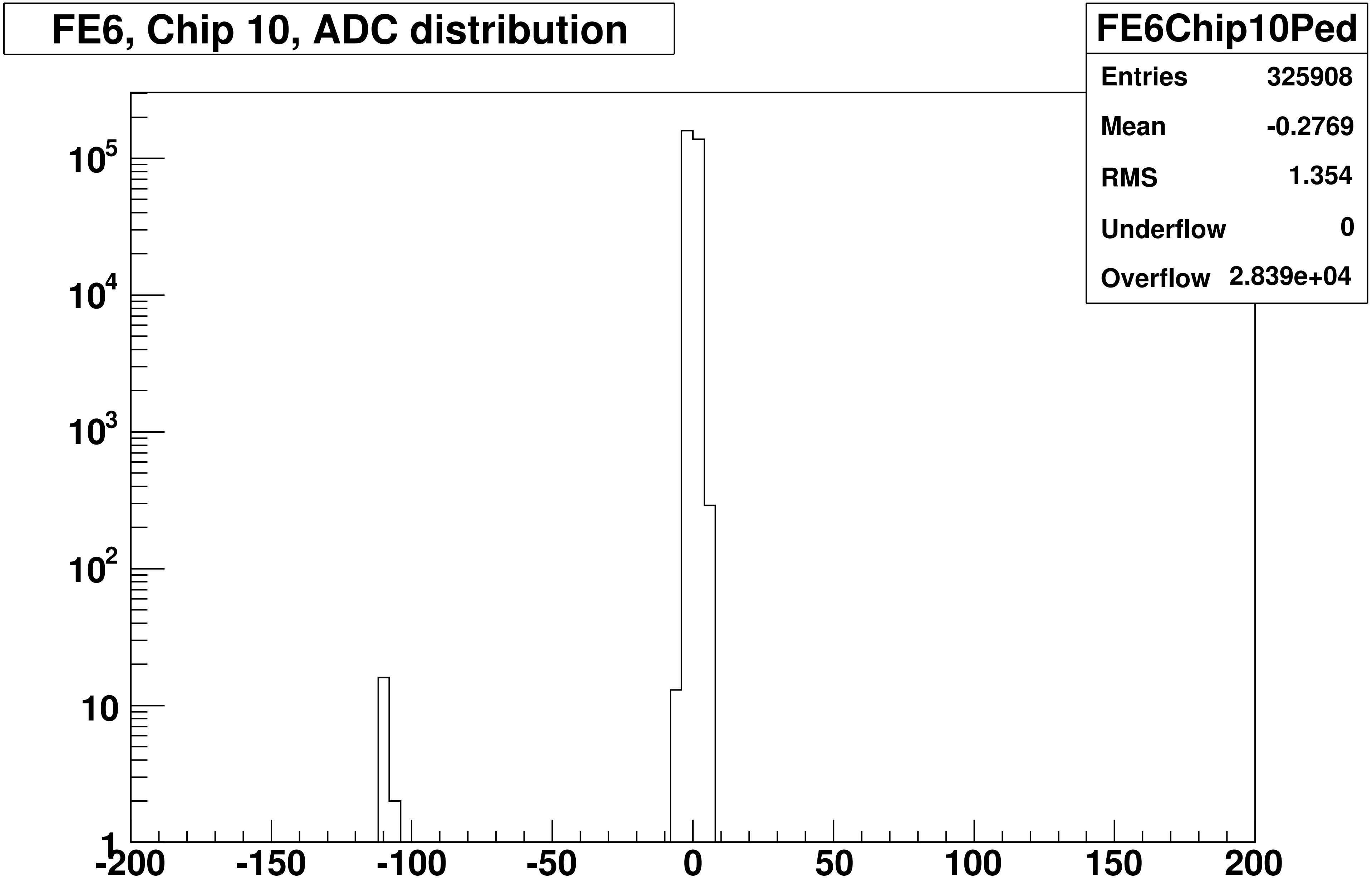
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 325908
Mean -0.2769
RMS 1.354
Underflow 0
Overflow 2.839e+04
10^5
10^4
10^3
10^2
10
1
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-150
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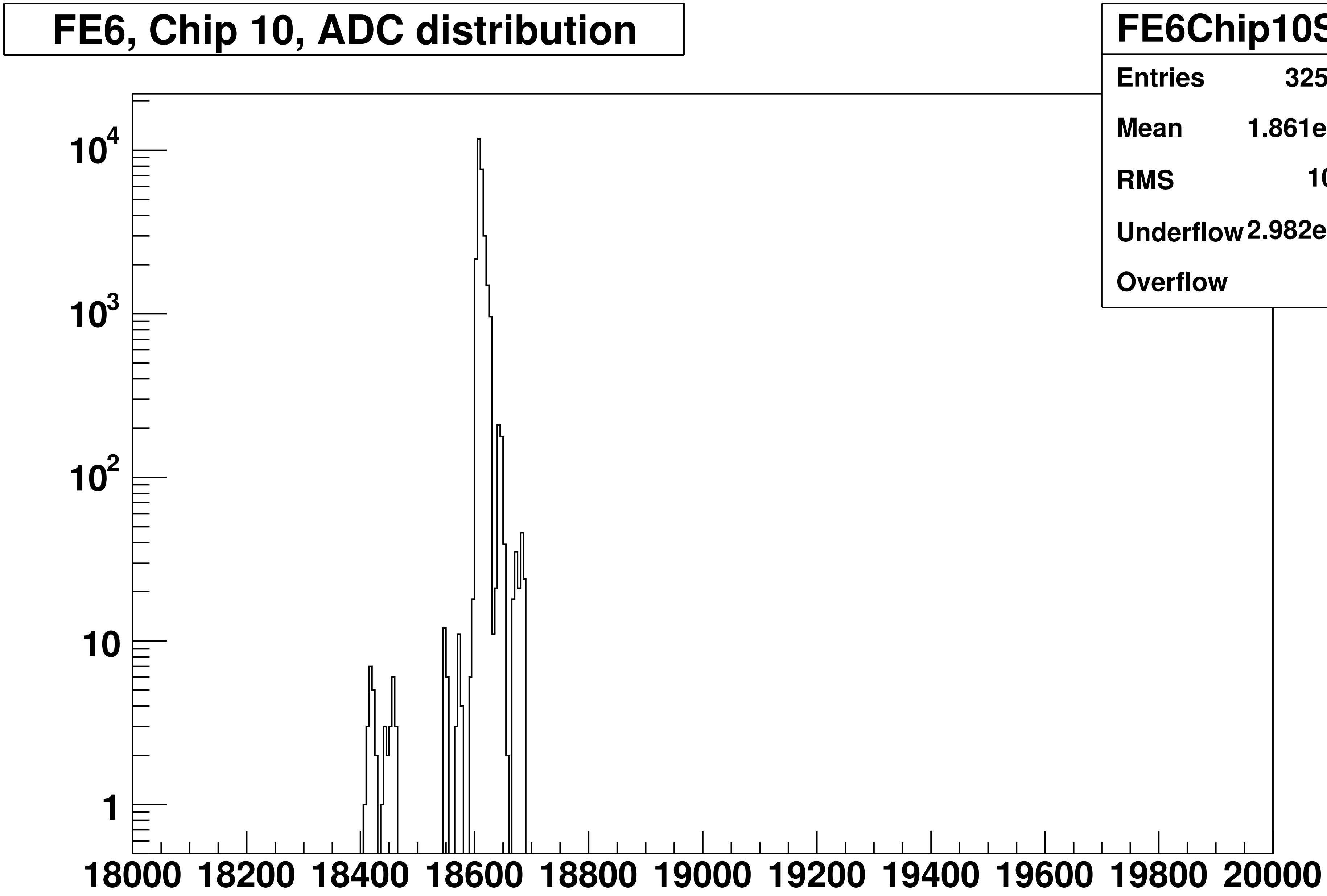
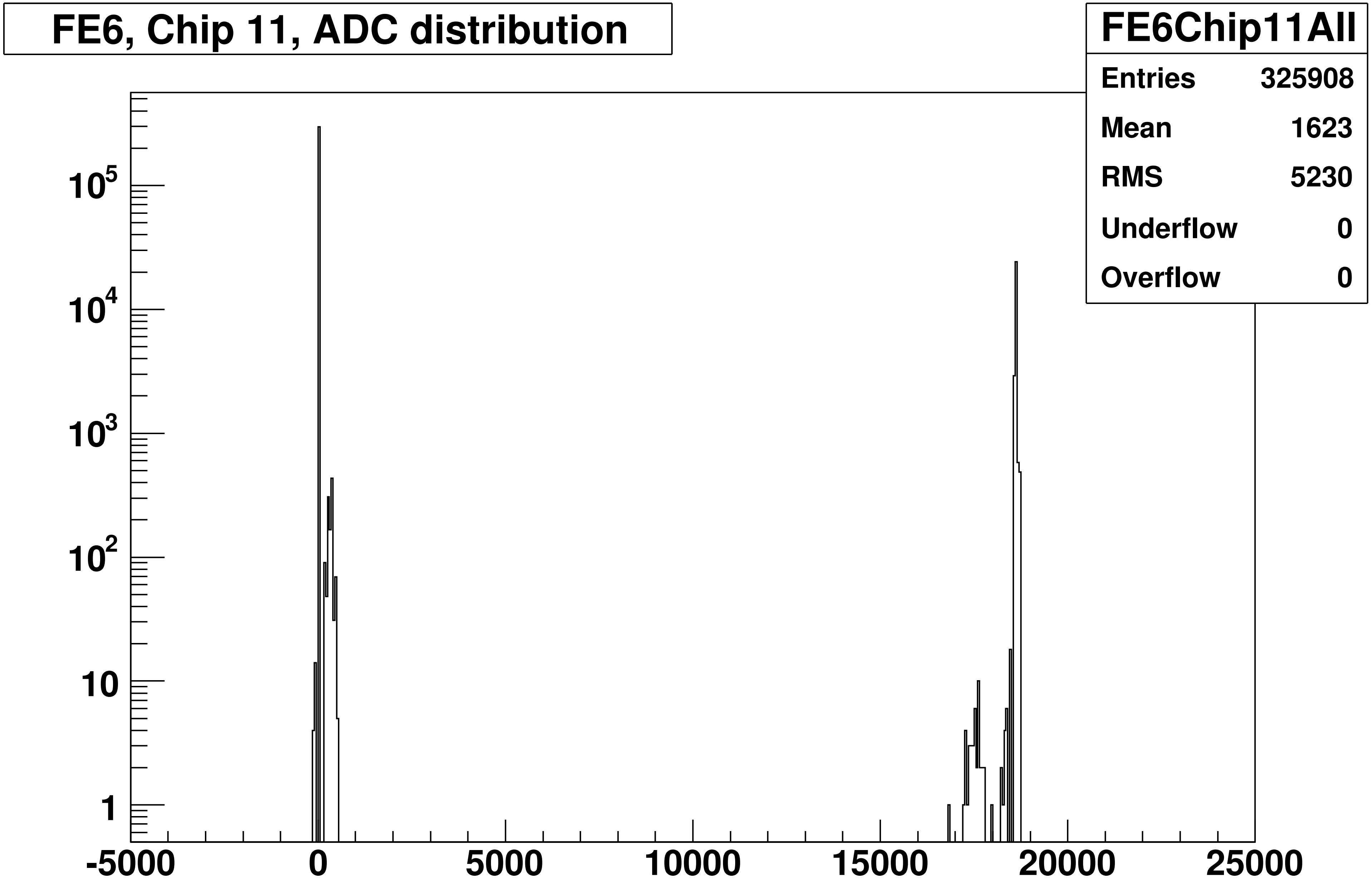
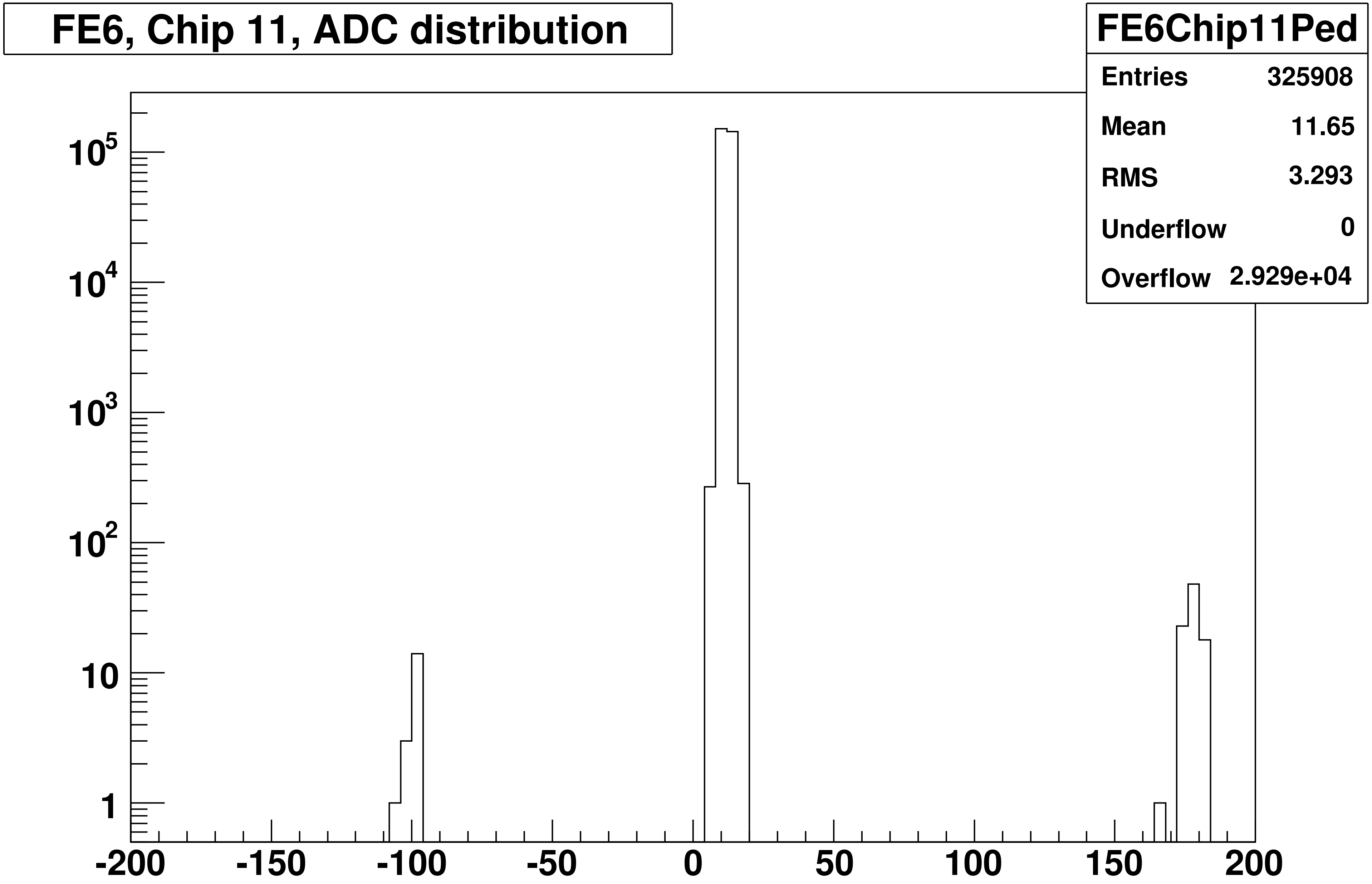
0
50
100
150
200



FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 325908
Mean 1.861e+04
RMS 10.83
Underflow 2.982e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 325908
Mean 1623
RMS 5230
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 325908
Mean 11.65
RMS 3.293
Underflow 0
Overflow 2.929e+04
10^5
10^4
10^3
10^2
10
1
-200
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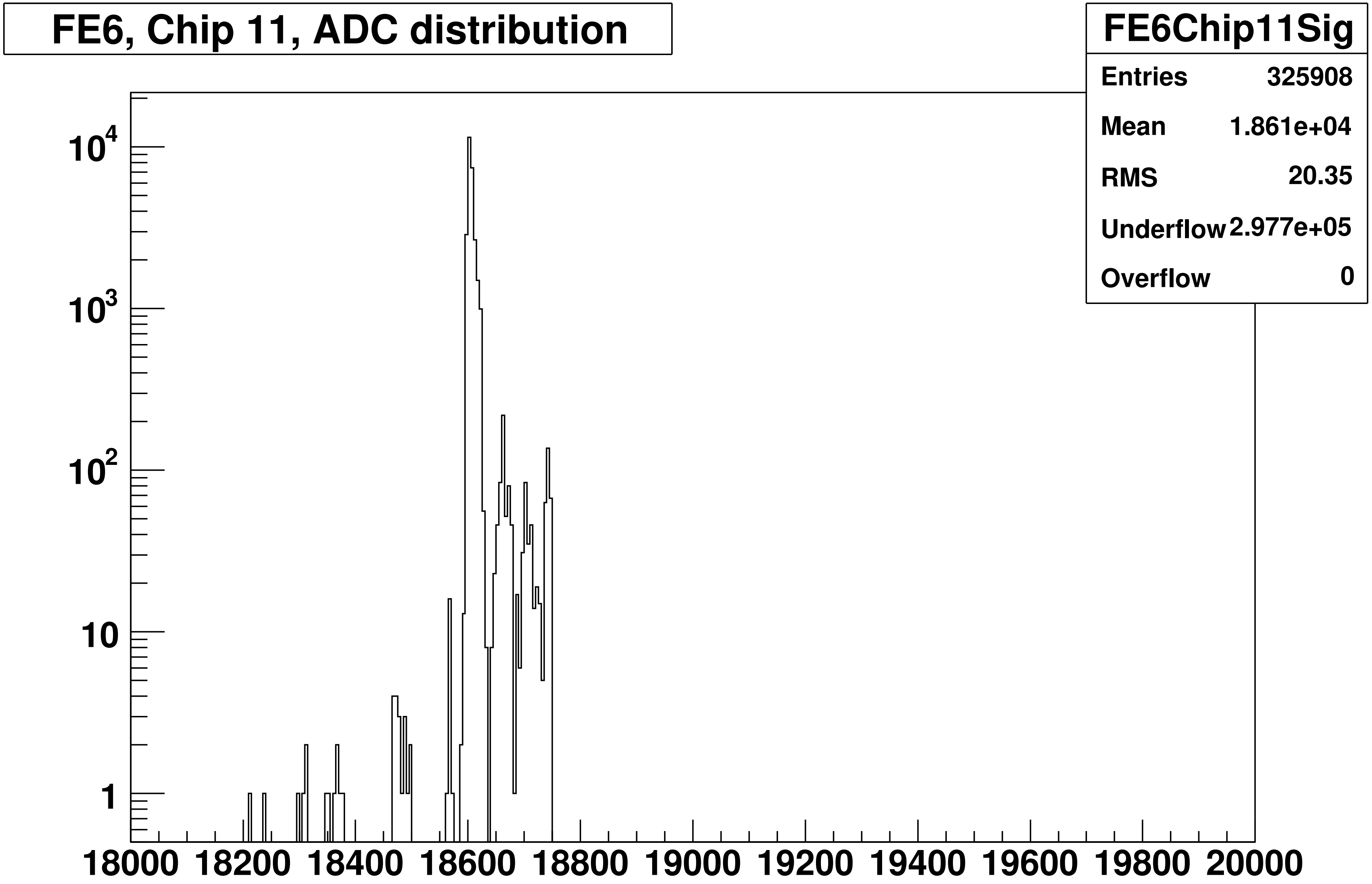
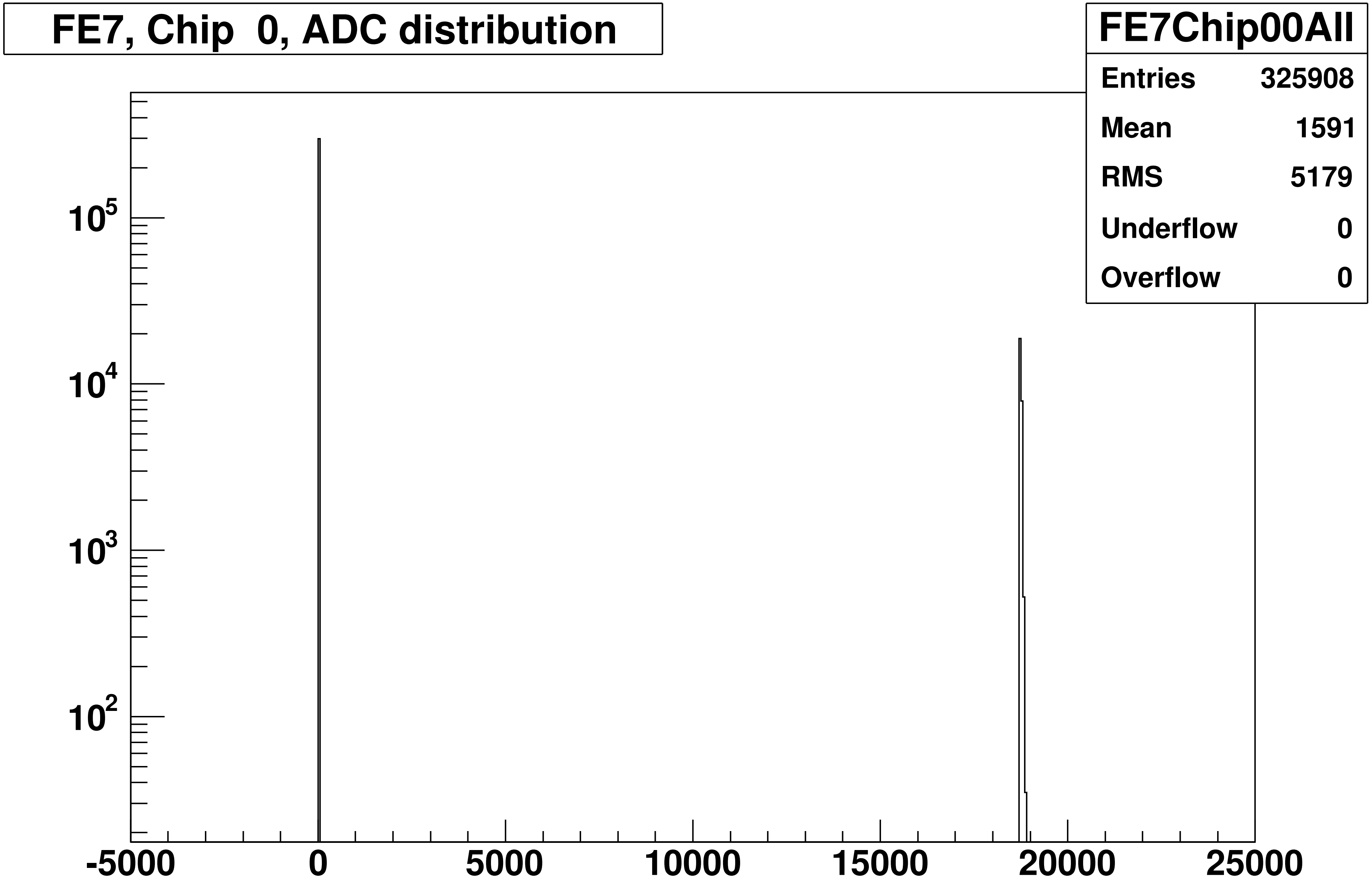
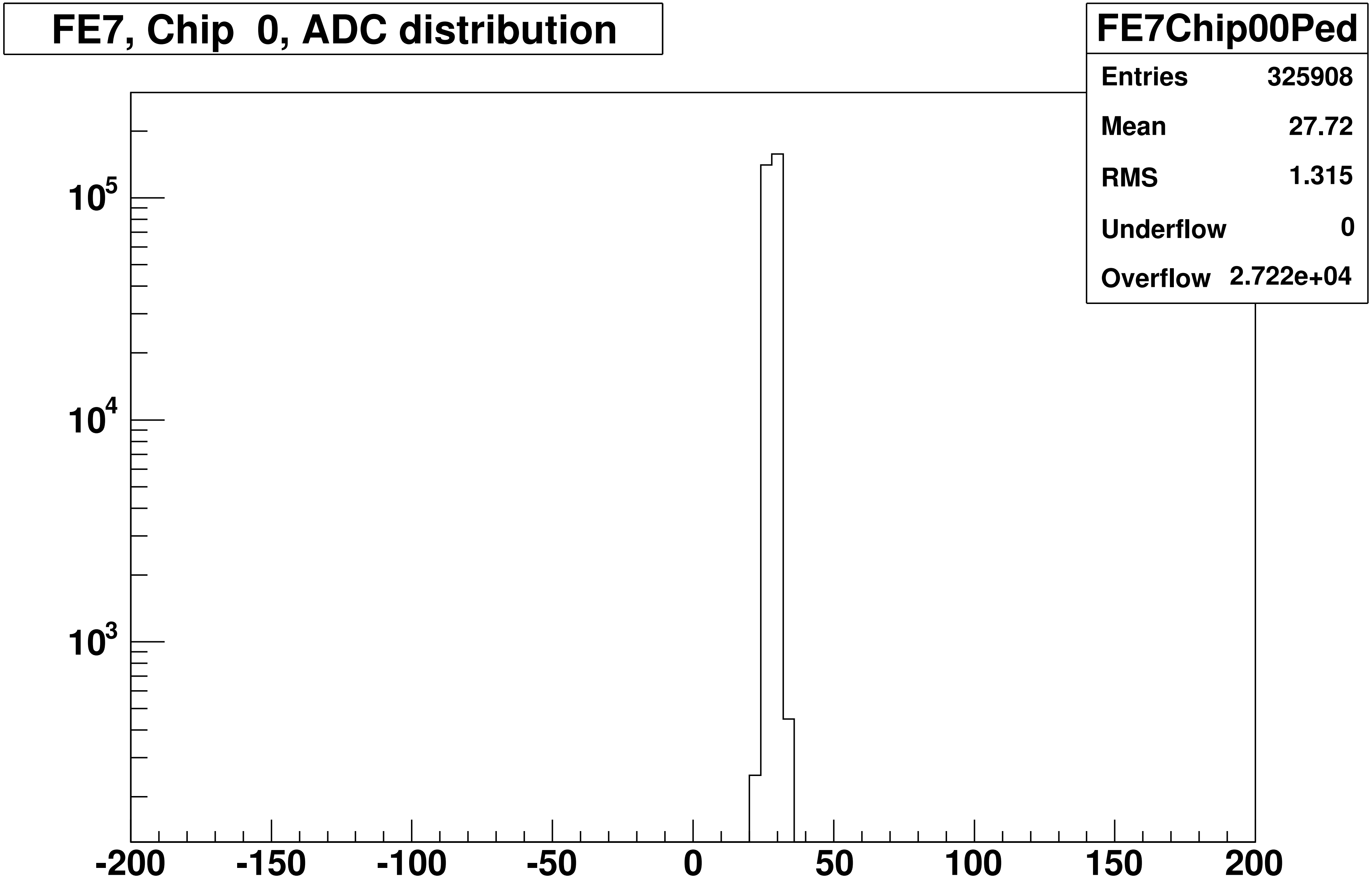
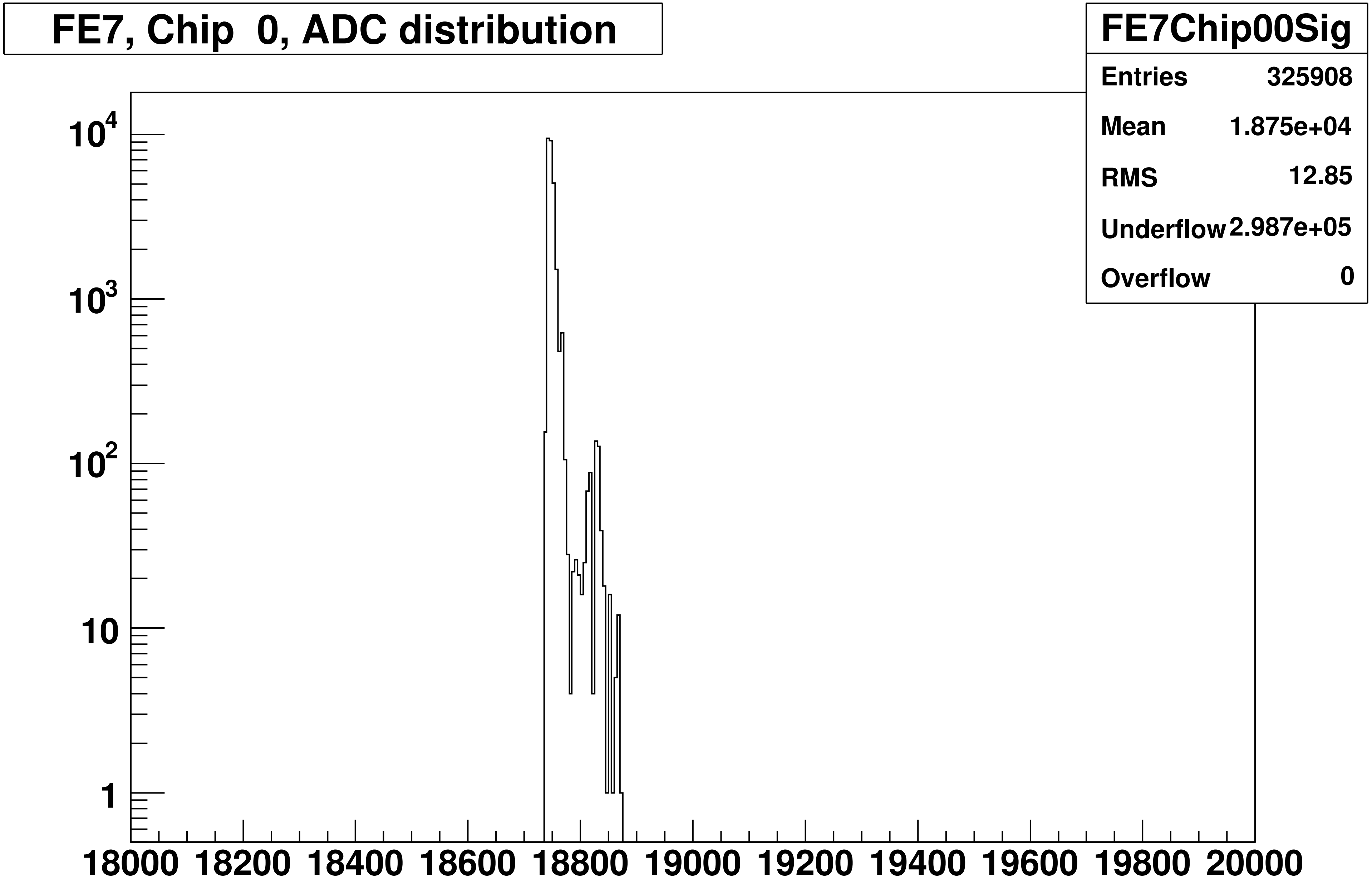
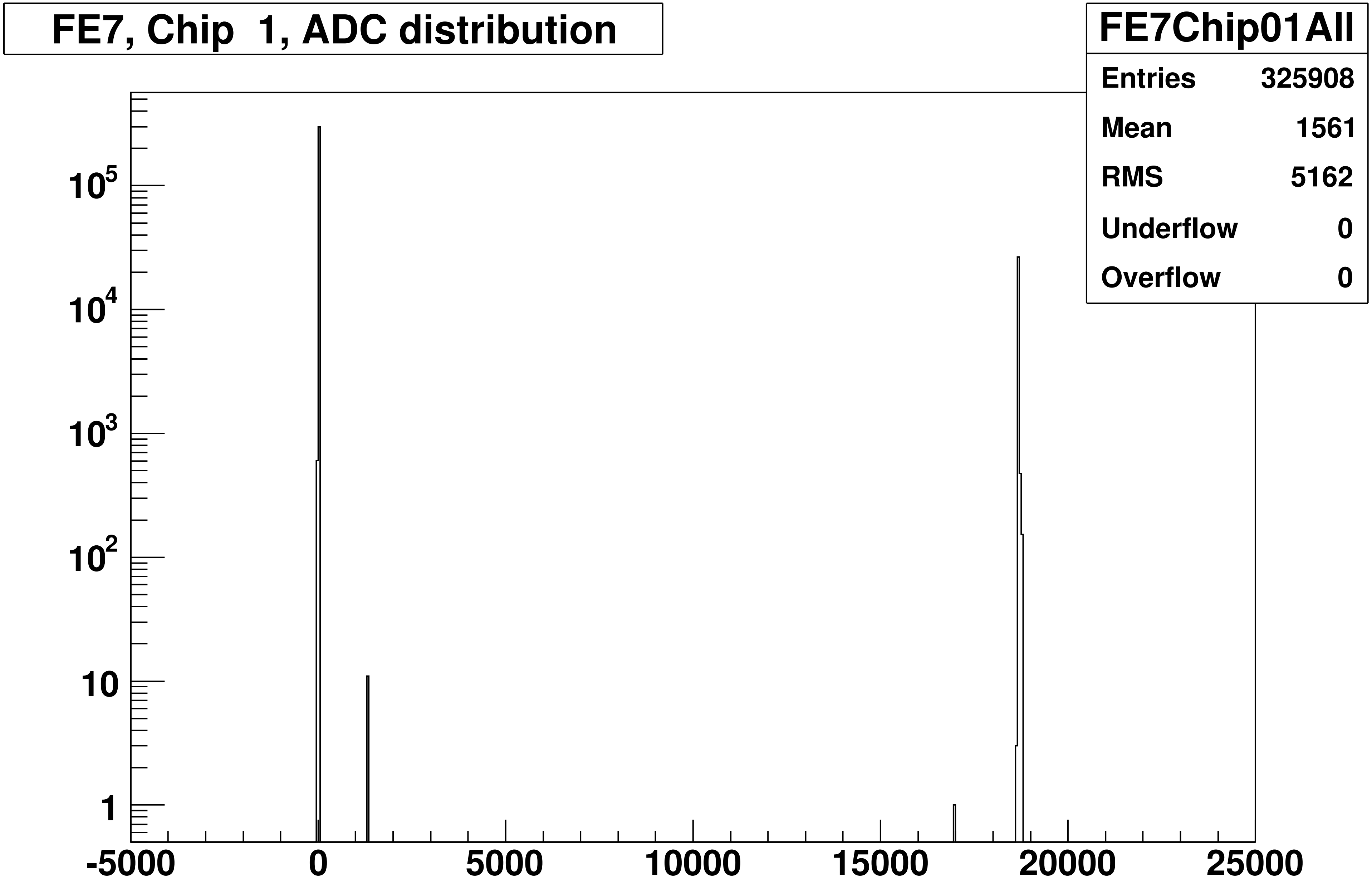
-100
-50
0
50
100
150
200


FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 325908
Mean 1.861e+04
RMS 20.35
Underflow 2.977e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 325908
Mean 1591
RMS 5179
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 325908
Mean 27.72
RMS 1.315
Underflow 0
Overflow 2.722e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 325908
Mean 1.875e+04
RMS 12.85
Underflow 2.987e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 325908
Mean 1561
RMS 5162
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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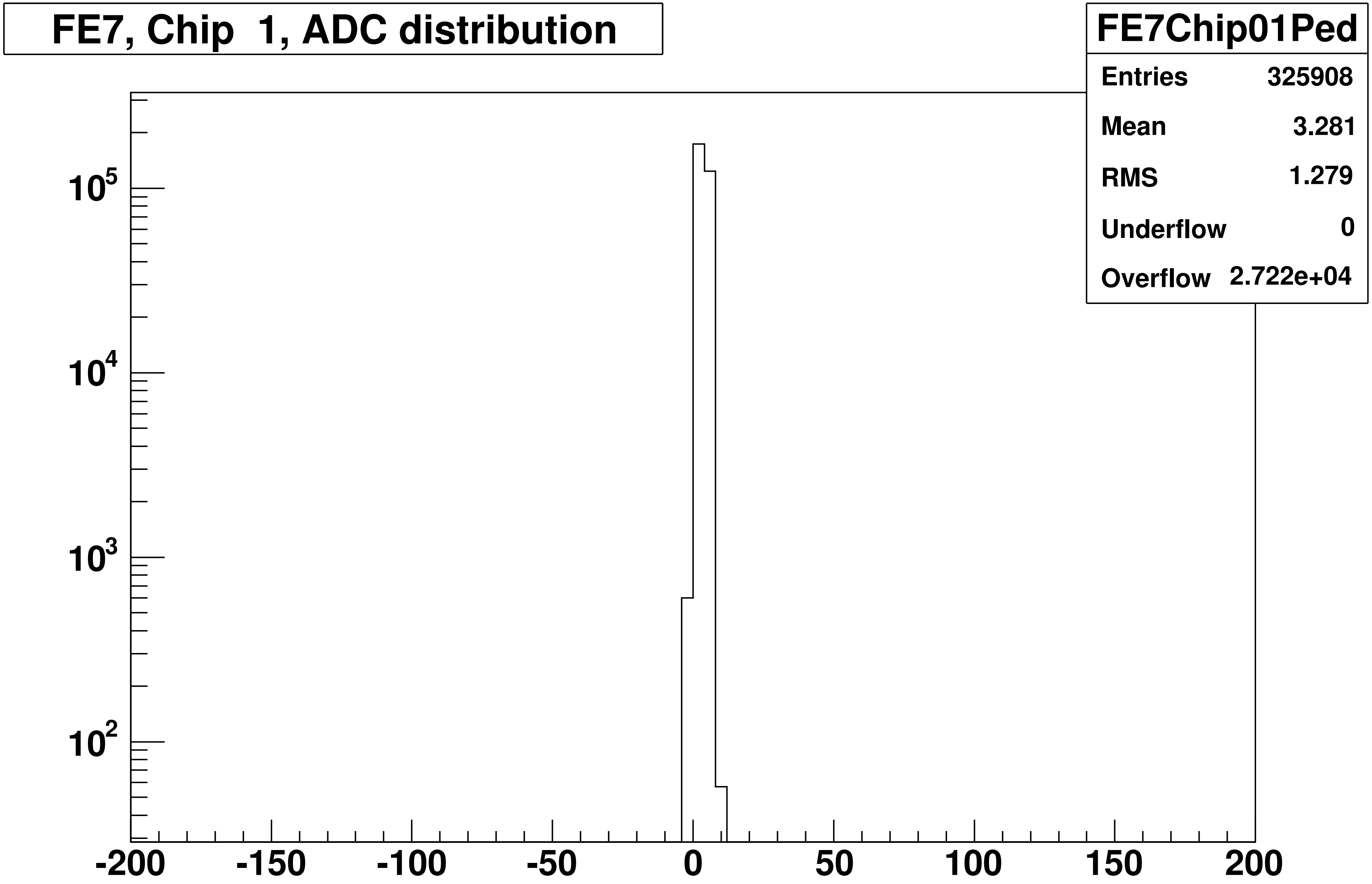
10000
15000
20000
25000



FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 325908
Mean 3.281
RMS 1.279
Underflow 0
Overflow 2.722e+04
10^5
10^4
10^3
10^2
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-150
-100
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0
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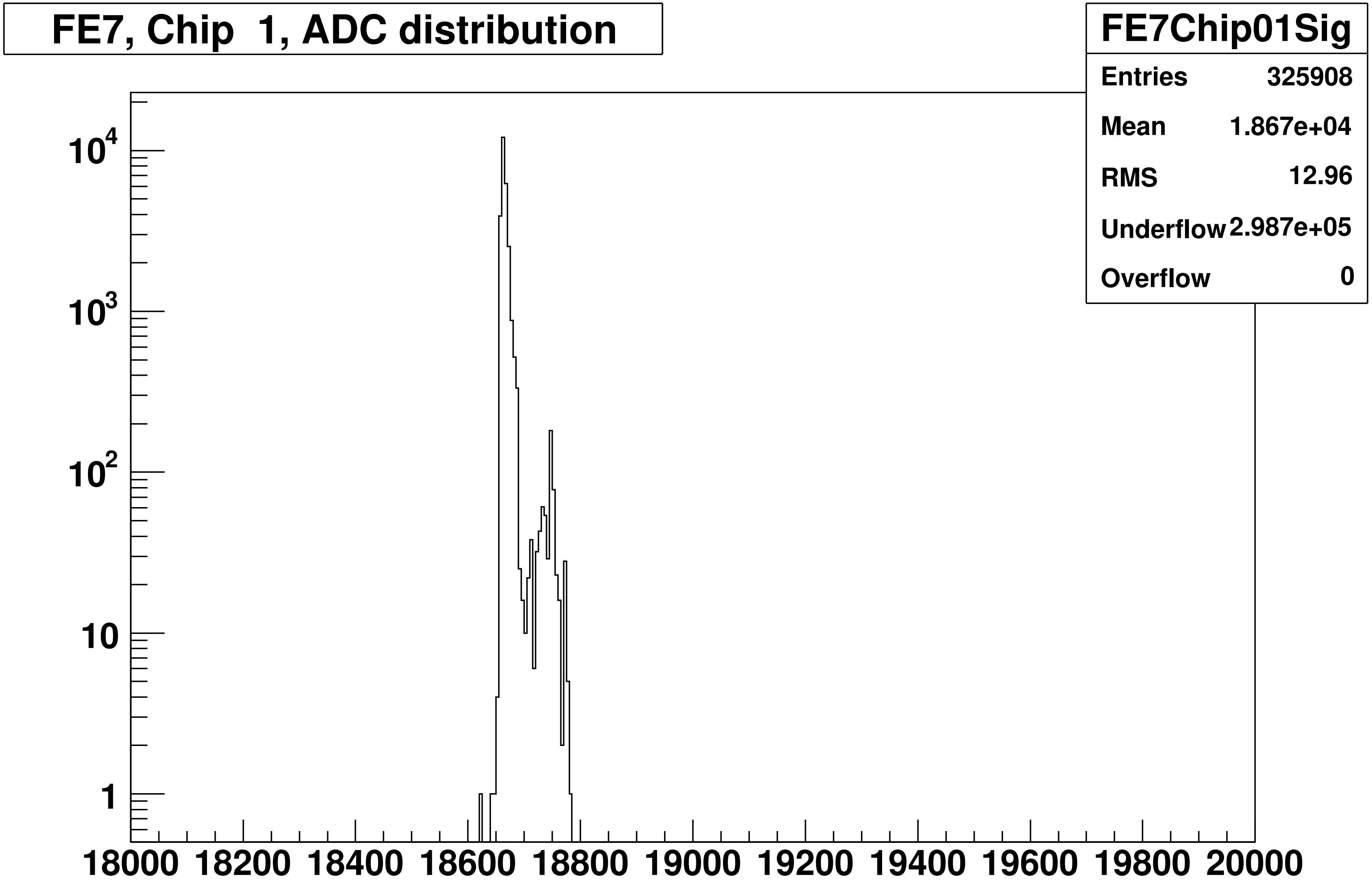
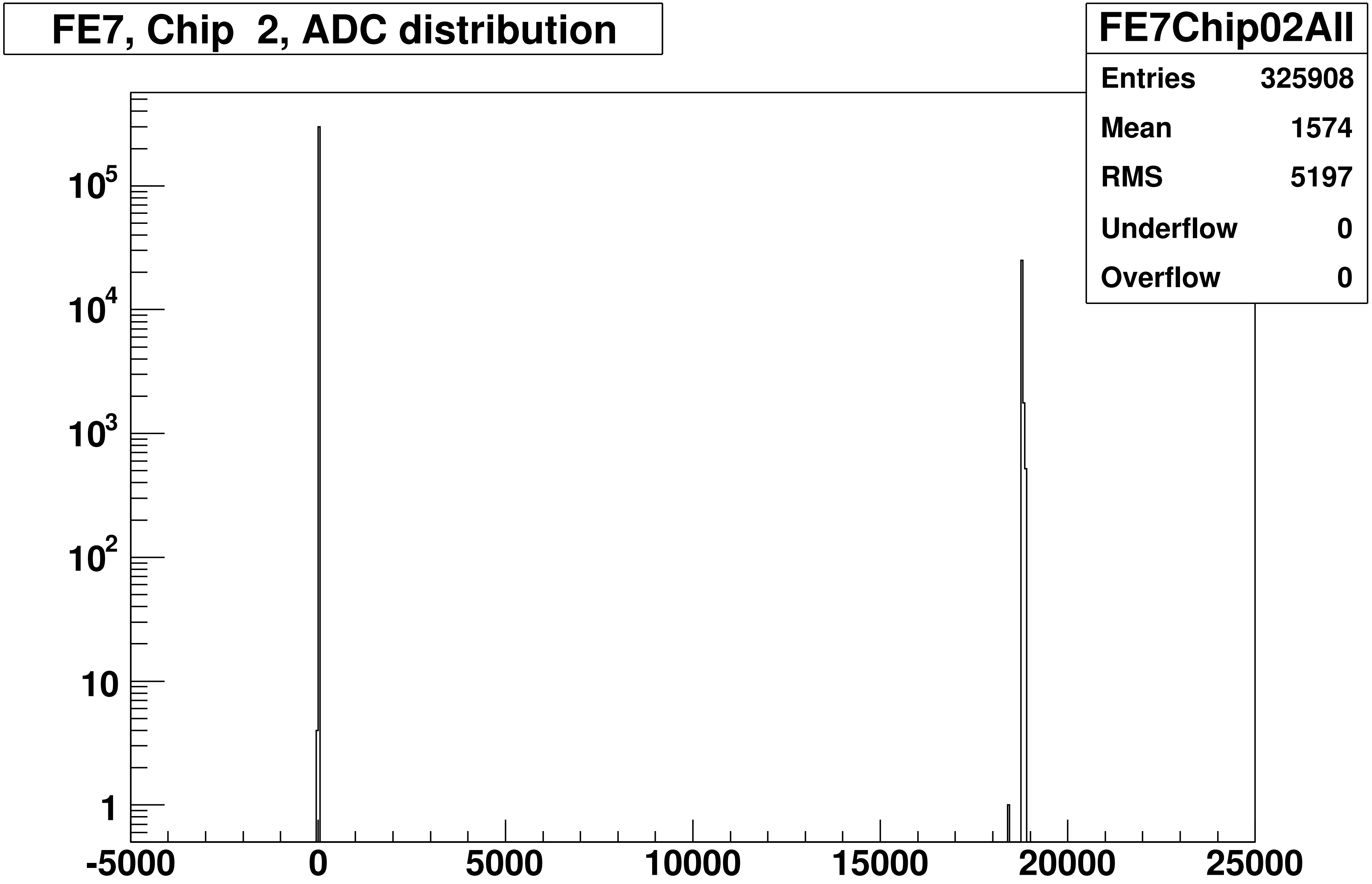
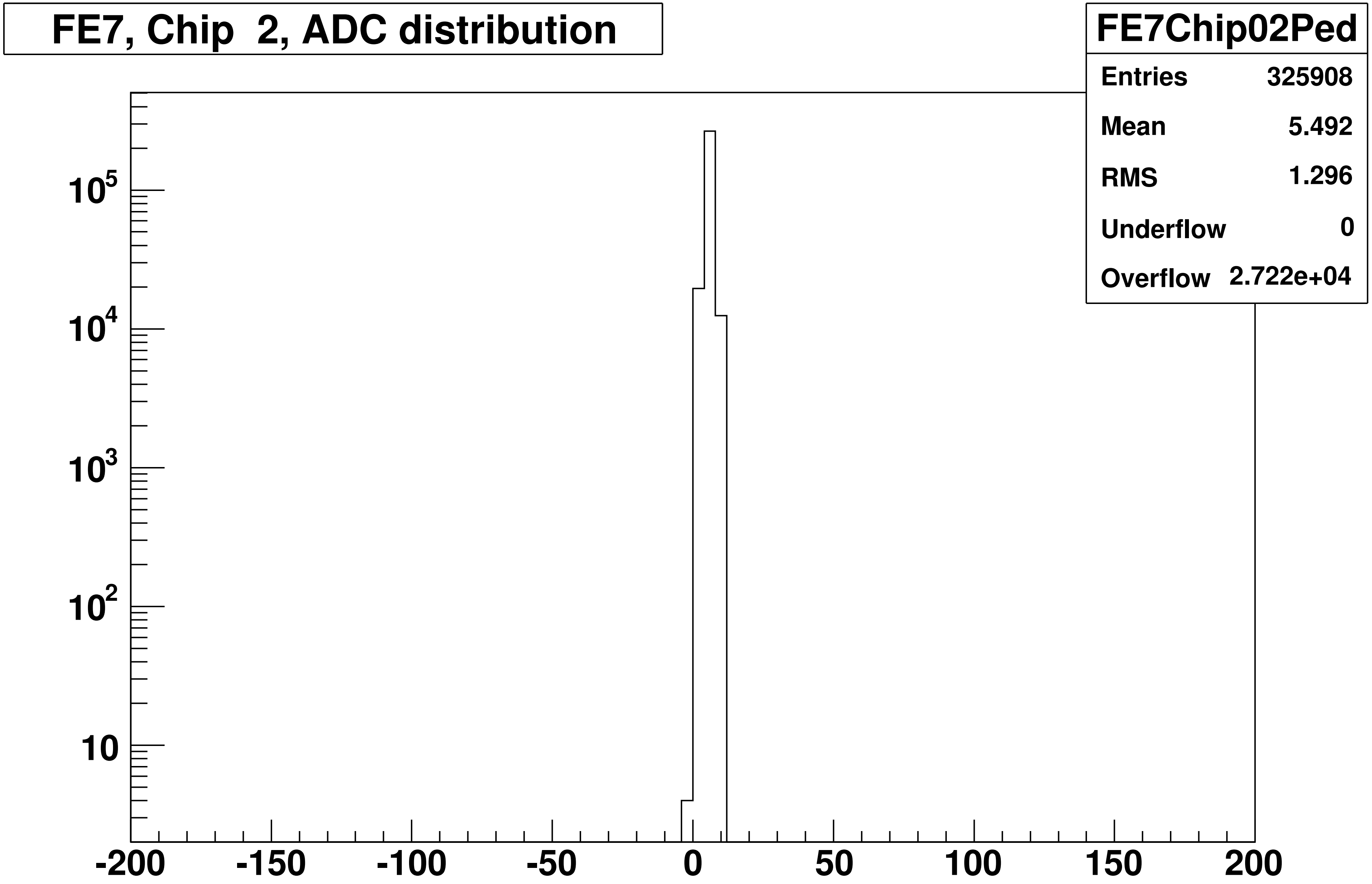
100
150
200

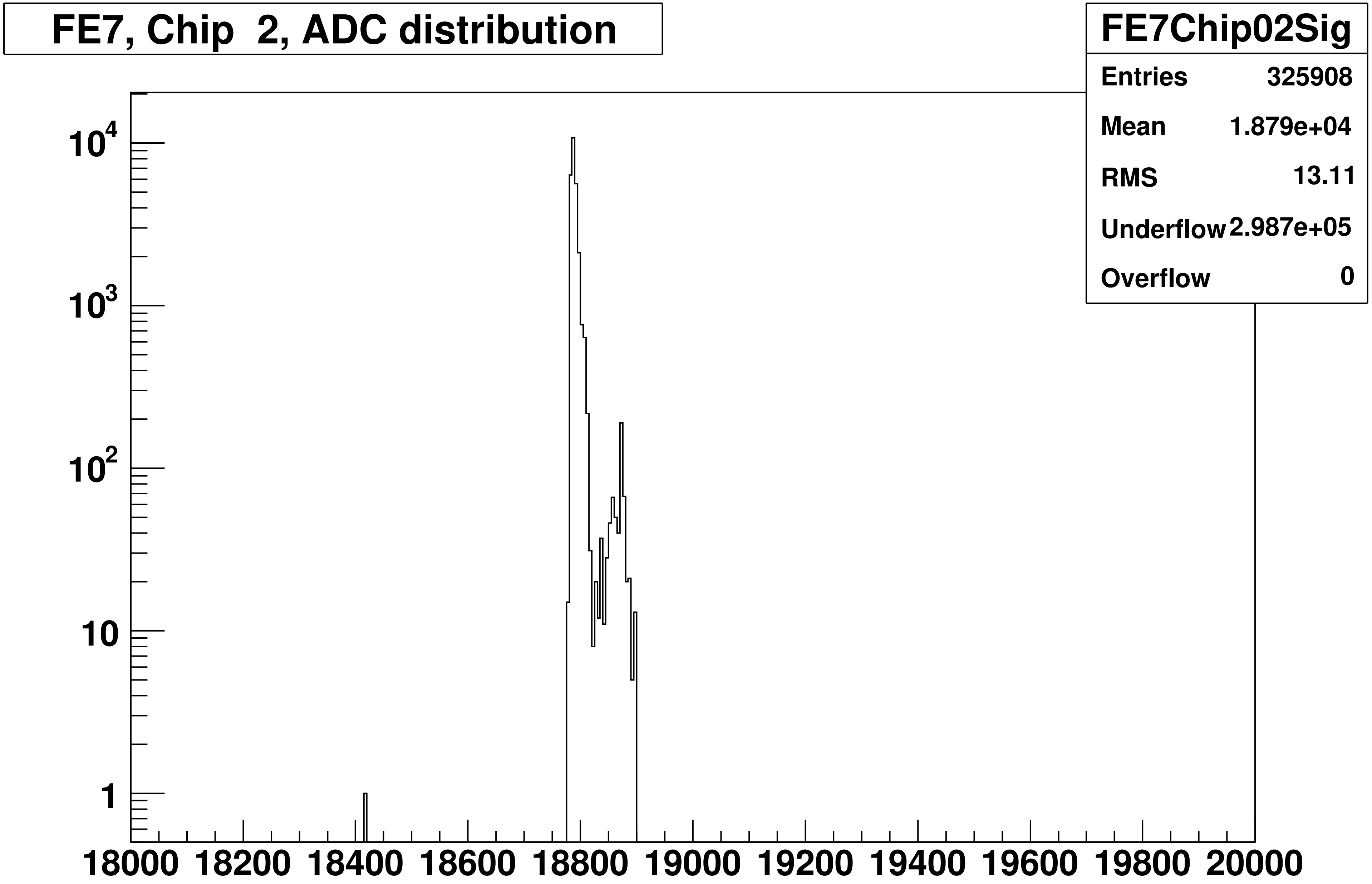


FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 325908
Mean 1.867e+04
RMS 12.96
Underflow 2.987e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 325908
Mean 1574
RMS 5197
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 325908
Mean 5.492
RMS 1.296
Underflow 0
Overflow 2.722e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 325908
Mean 1.879e+04
RMS 13.11
Underflow 2.987e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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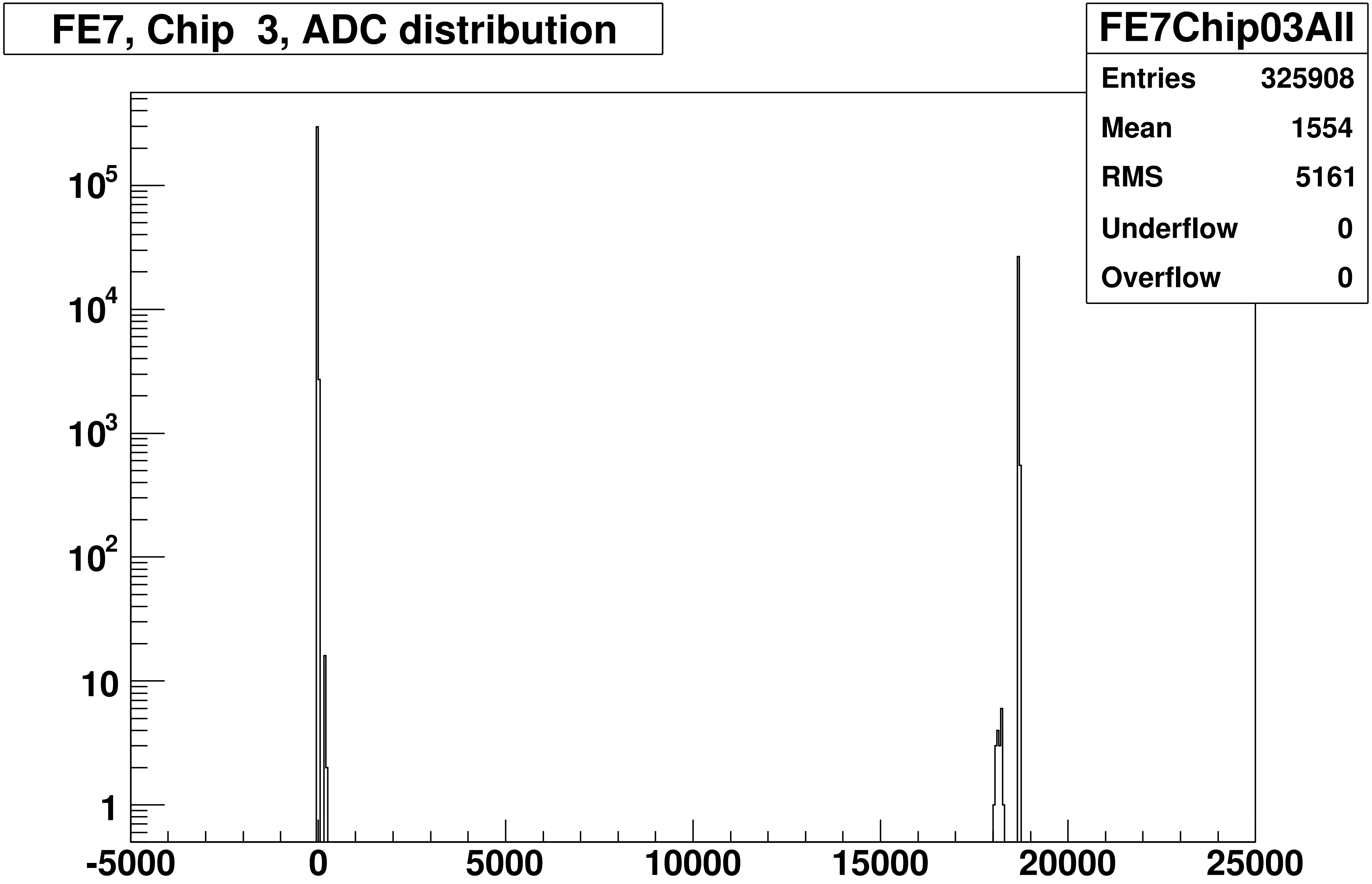
19400
19600
19800
20000



FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 325908
Mean 1554
RMS 5161
Underflow 0
Overflow 0
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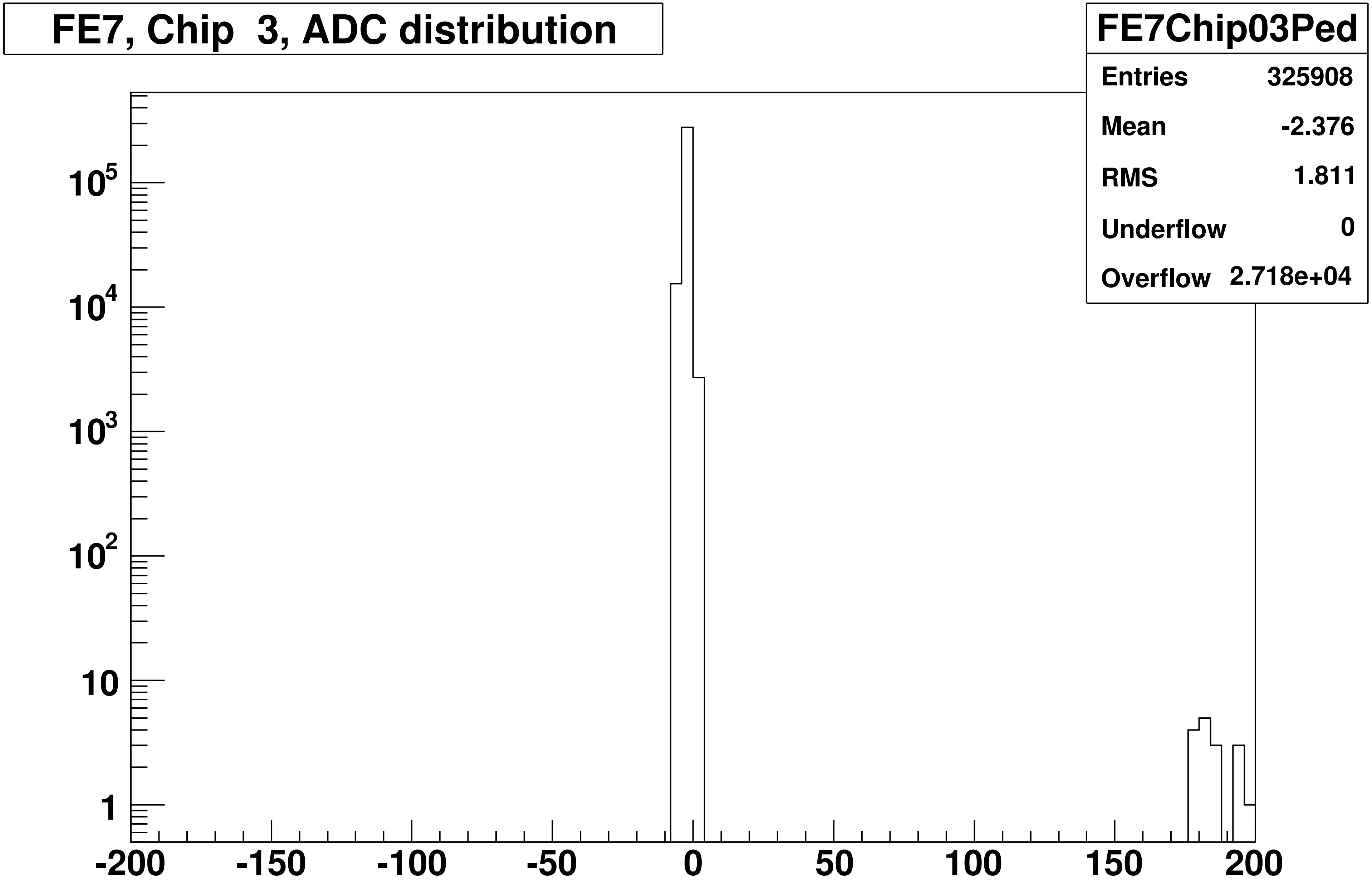
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 325908
Mean -2.376
RMS 1.811
Underflow 0
Overflow 2.718e+04
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10^4
10^3
10^2
10
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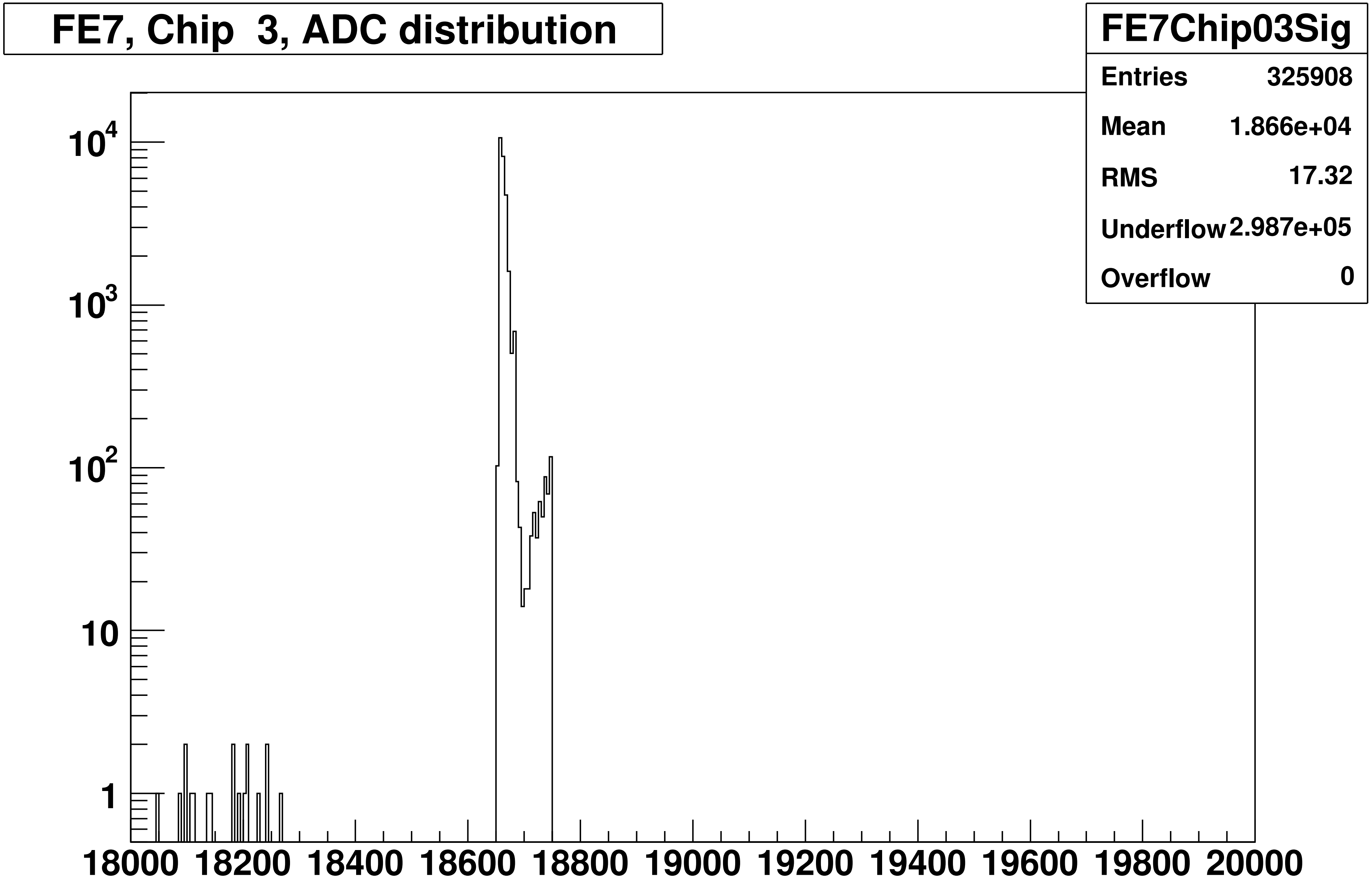
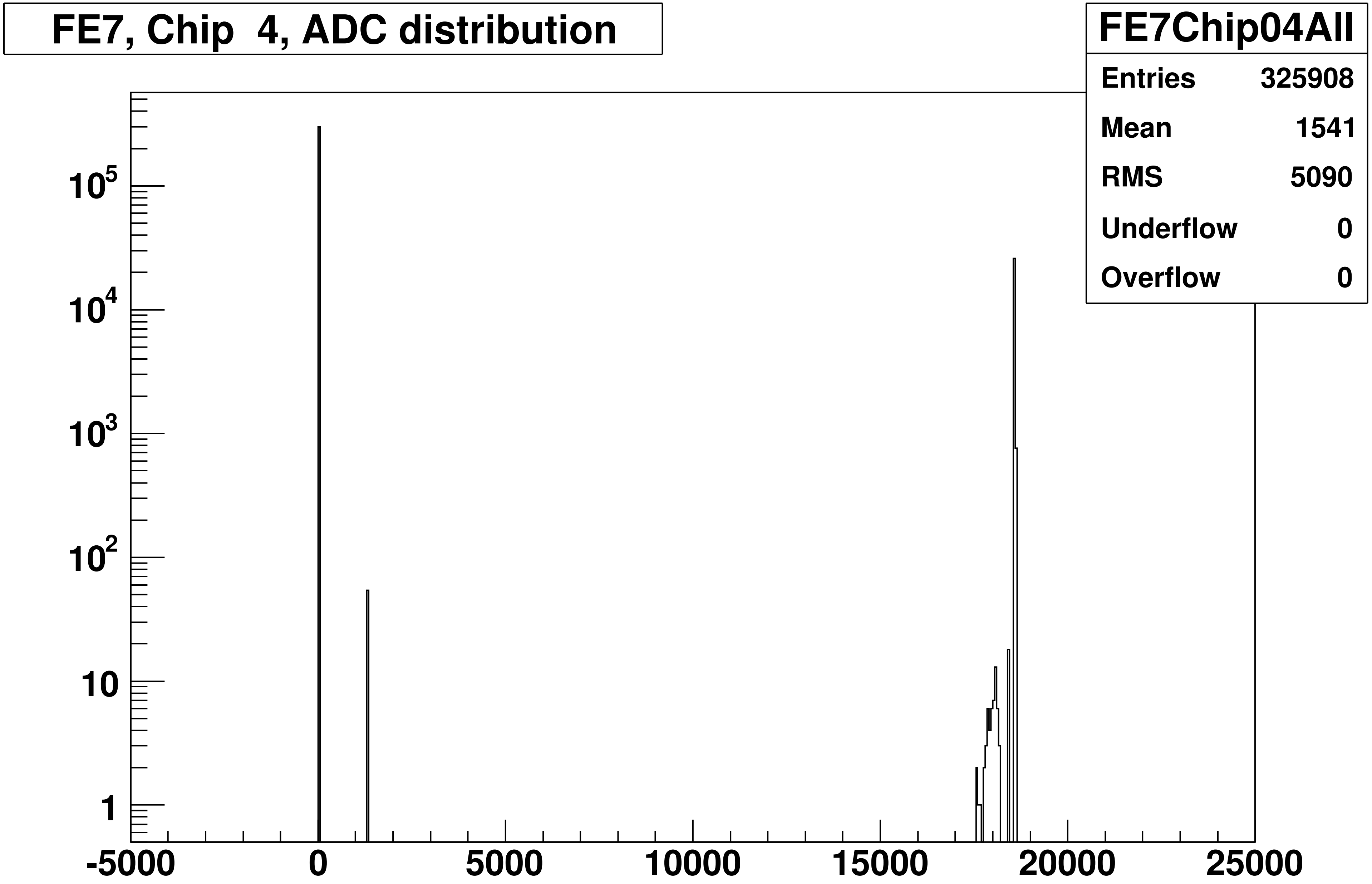
50
100
150
200



FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 325908
Mean 1.866e+04
RMS 17.32
Underflow 2.987e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 325908
Mean 1541
RMS 5090
Underflow 0
Overflow 0
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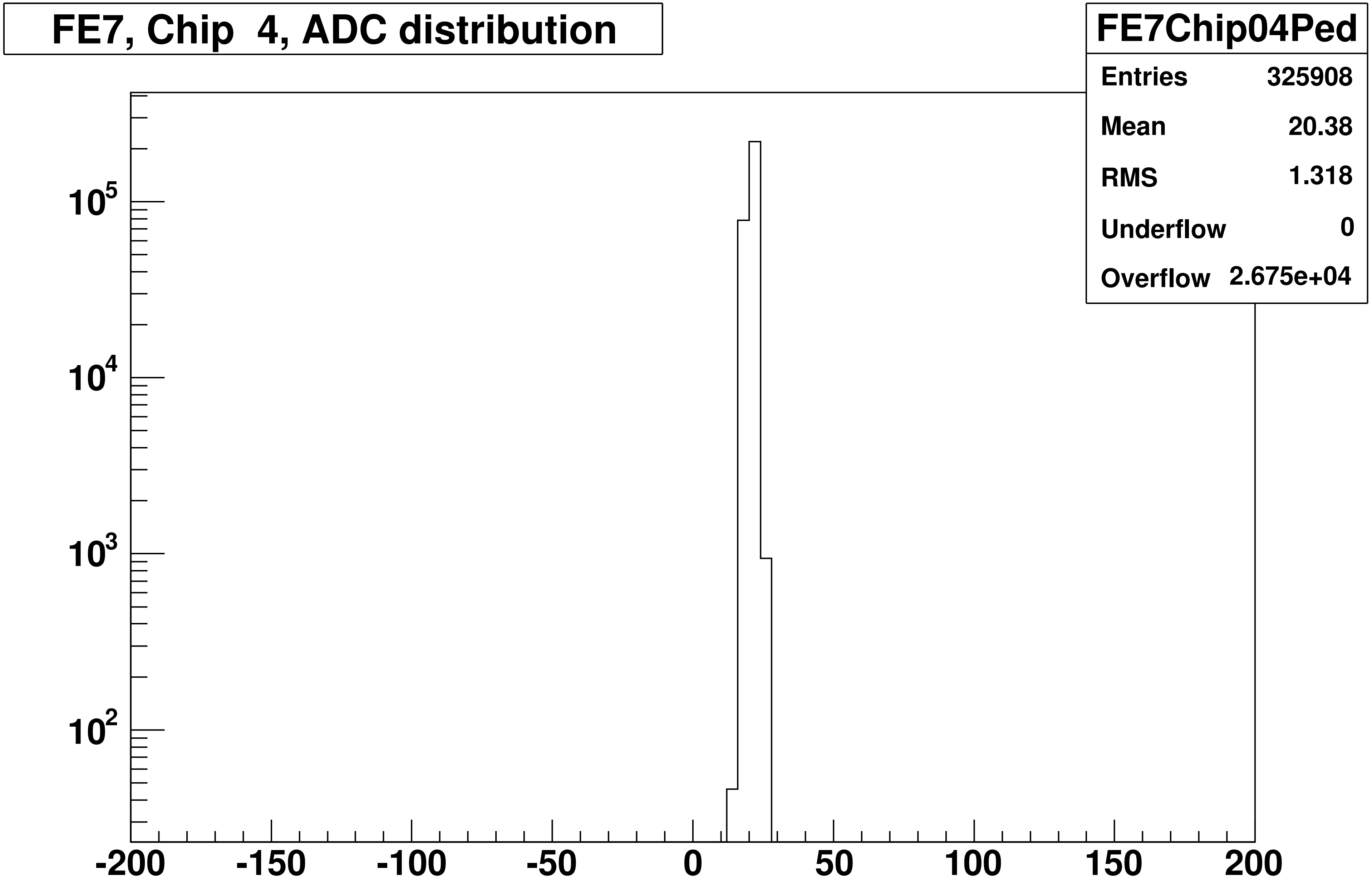
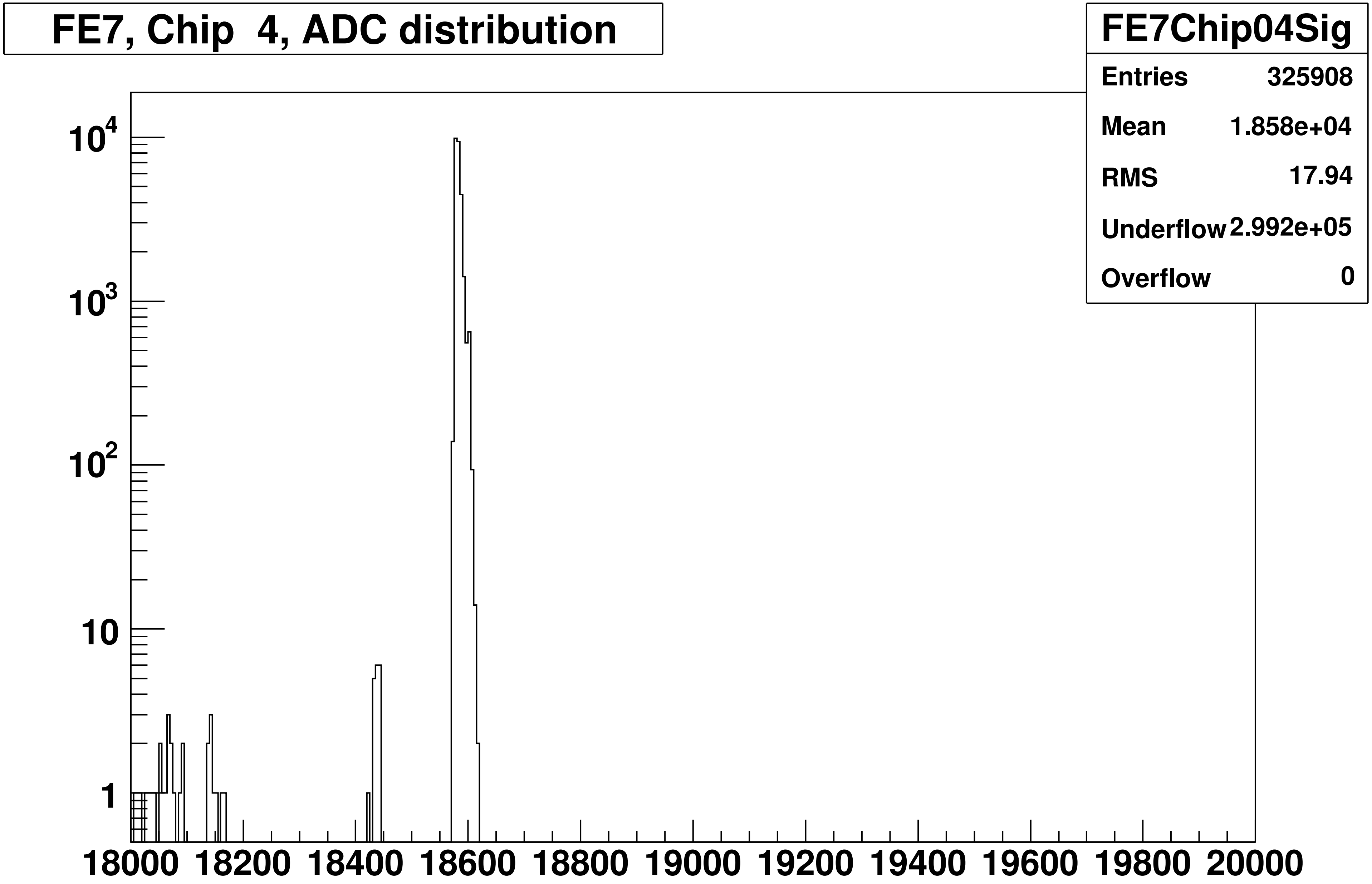
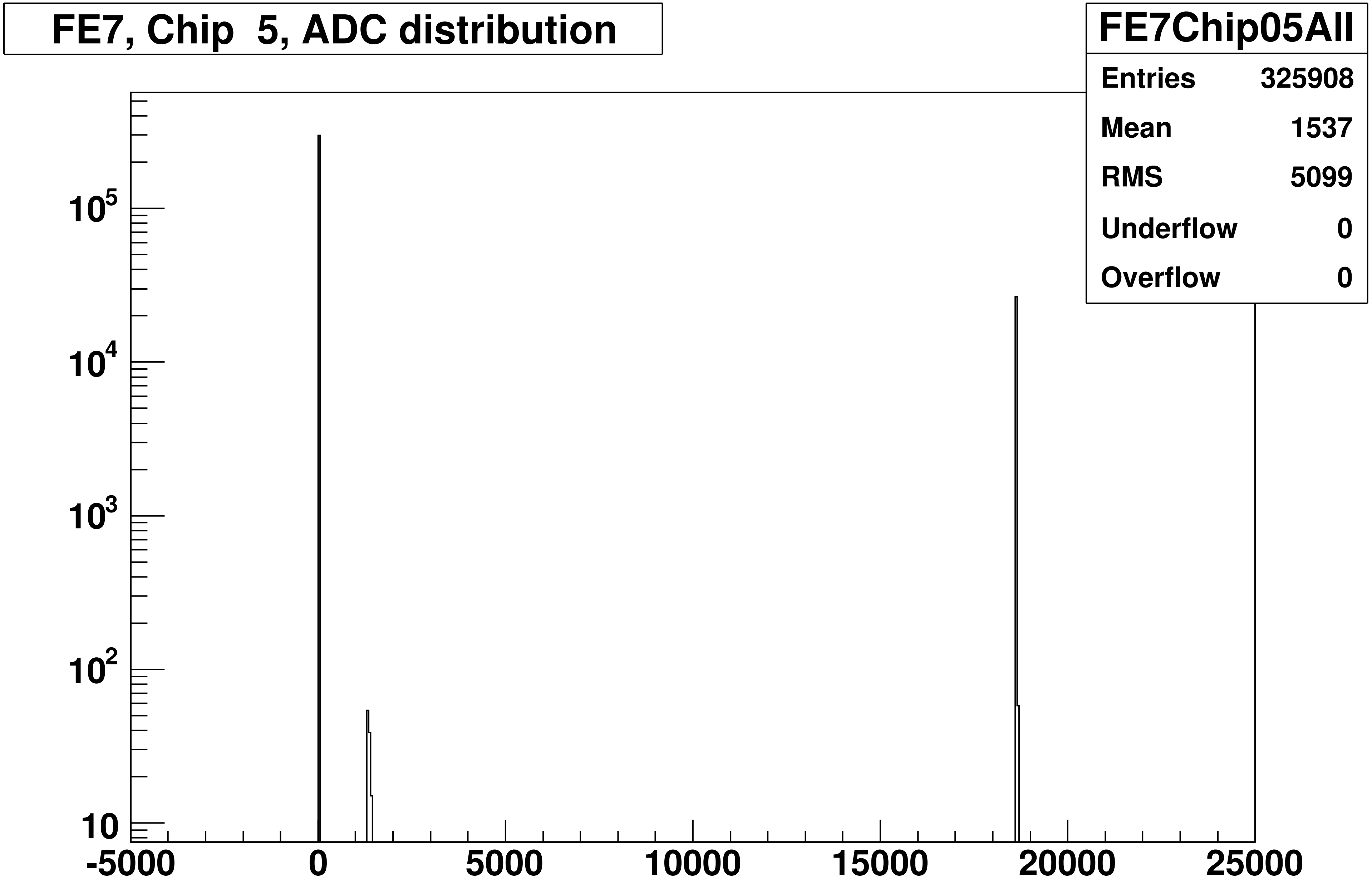
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

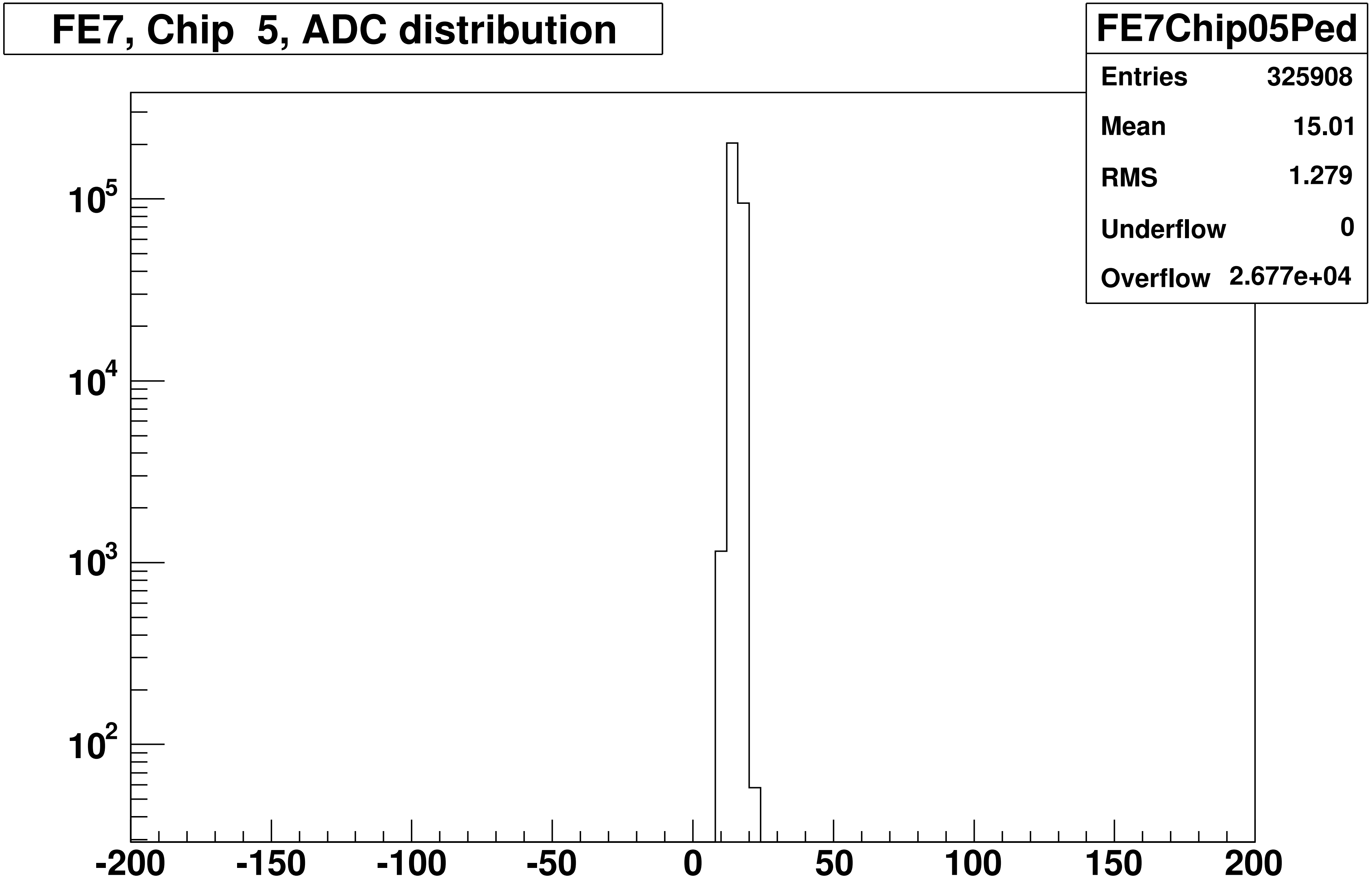


FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 325908
Mean 20.38
RMS 1.318
Underflow 0
Overflow 2.675e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

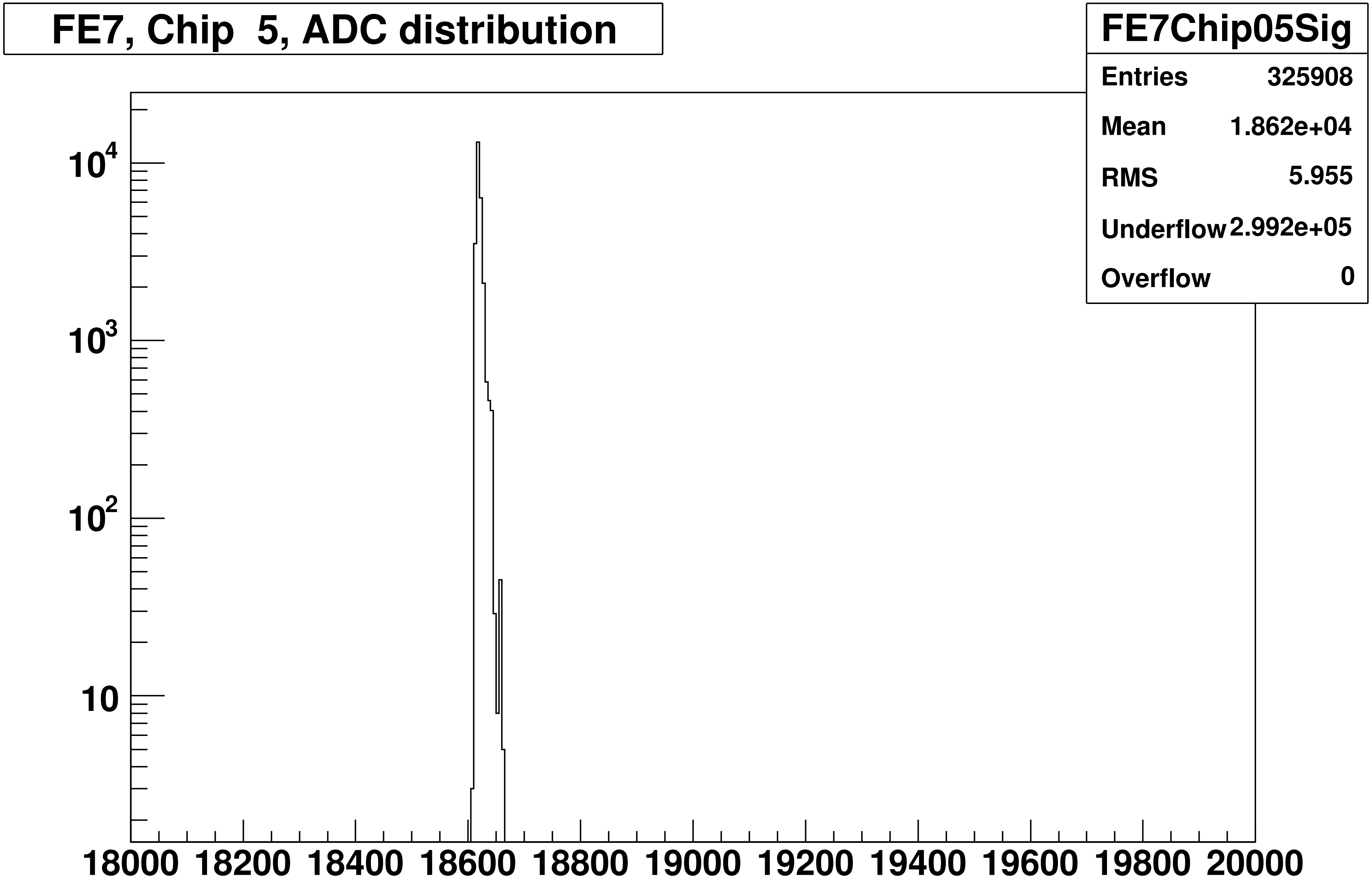

FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 325908
Mean 1.858e+04
RMS 17.94
Underflow 2.992e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



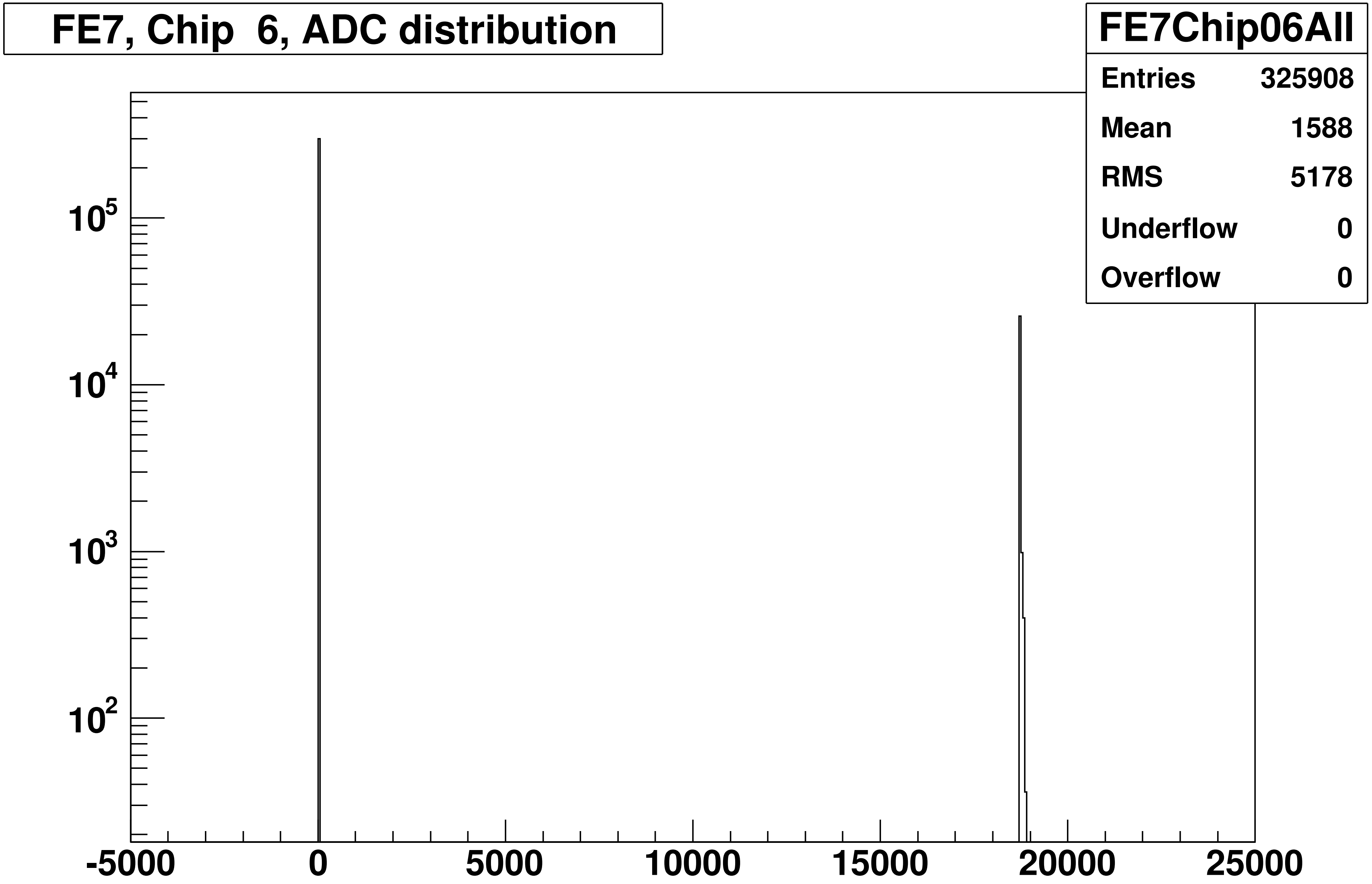
FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 325908
Mean 1537
RMS 5099
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



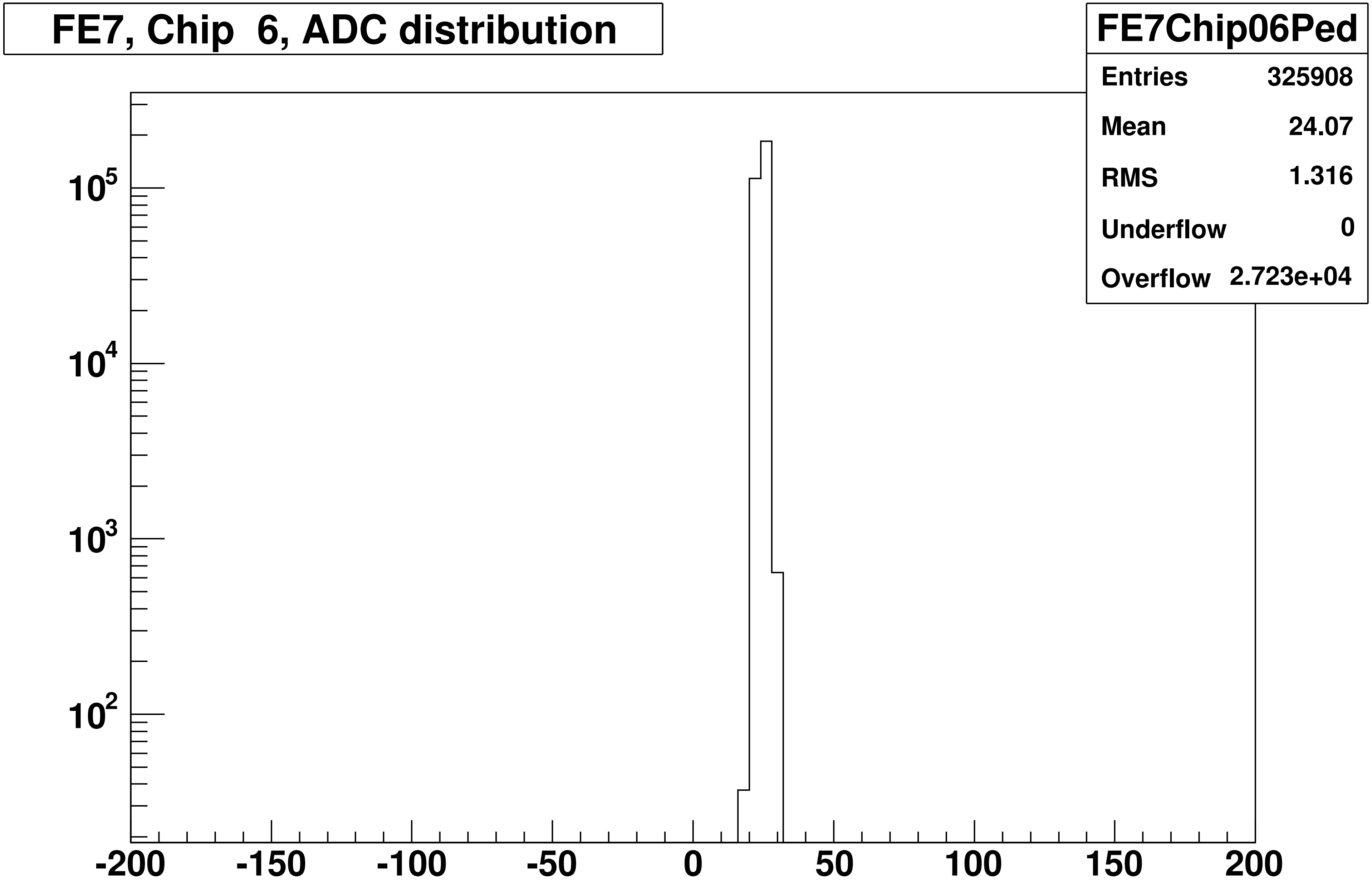
FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 325908
Mean 15.01
RMS 1.279
Underflow 0
Overflow 2.677e+04
10^5
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200



FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 325908
Mean 1.862e+04
RMS 5.955
Underflow 2.992e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 325908
Mean 1588
RMS 5178
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 325908
Mean 24.07
RMS 1.316
Underflow 0
Overflow 2.723e+04
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10^4
10^3
10^2
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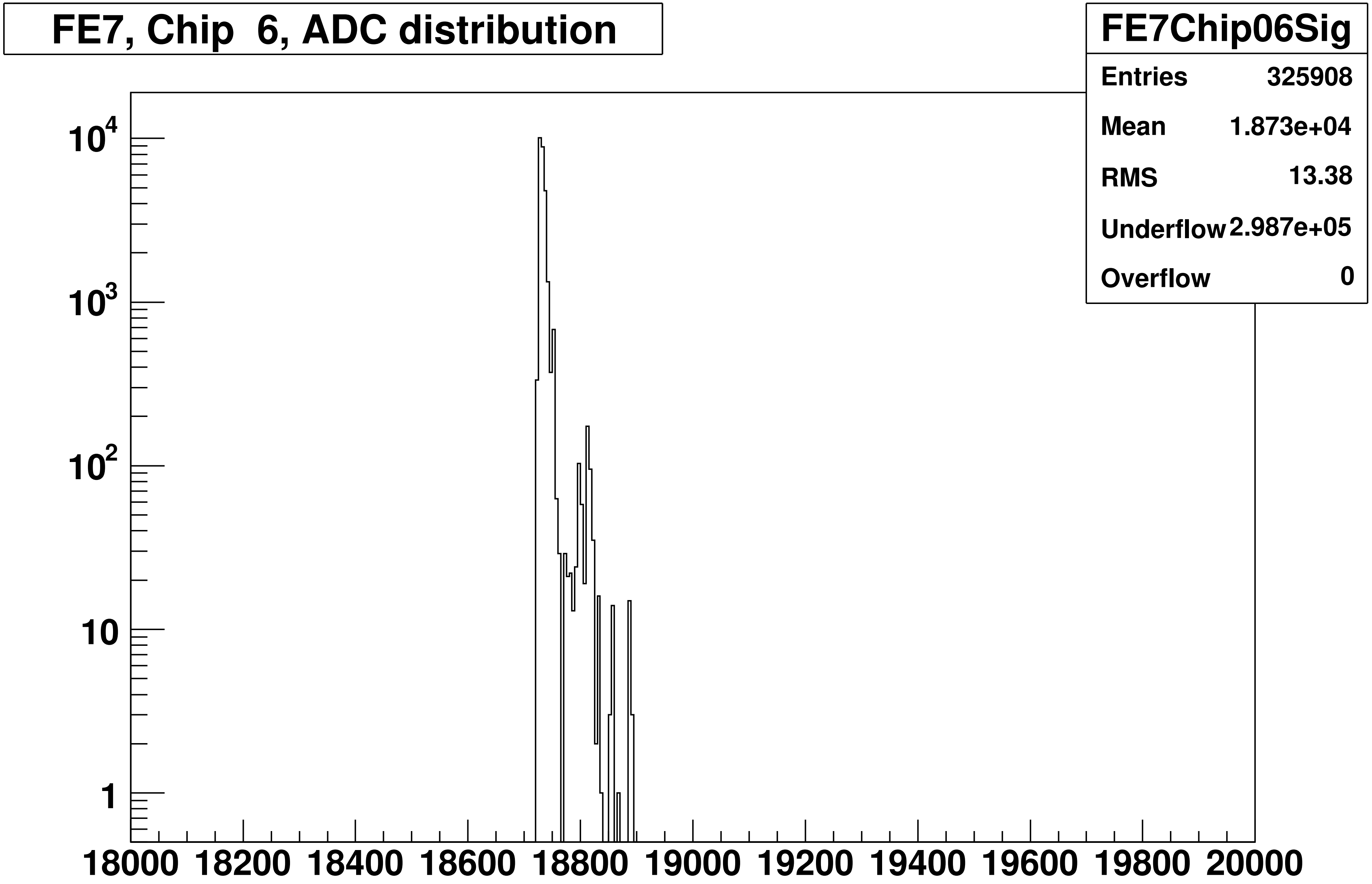
-100
-50
0
50
100
150
200


FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 325908
Mean 1.873e+04
RMS 13.38
Underflow 2.987e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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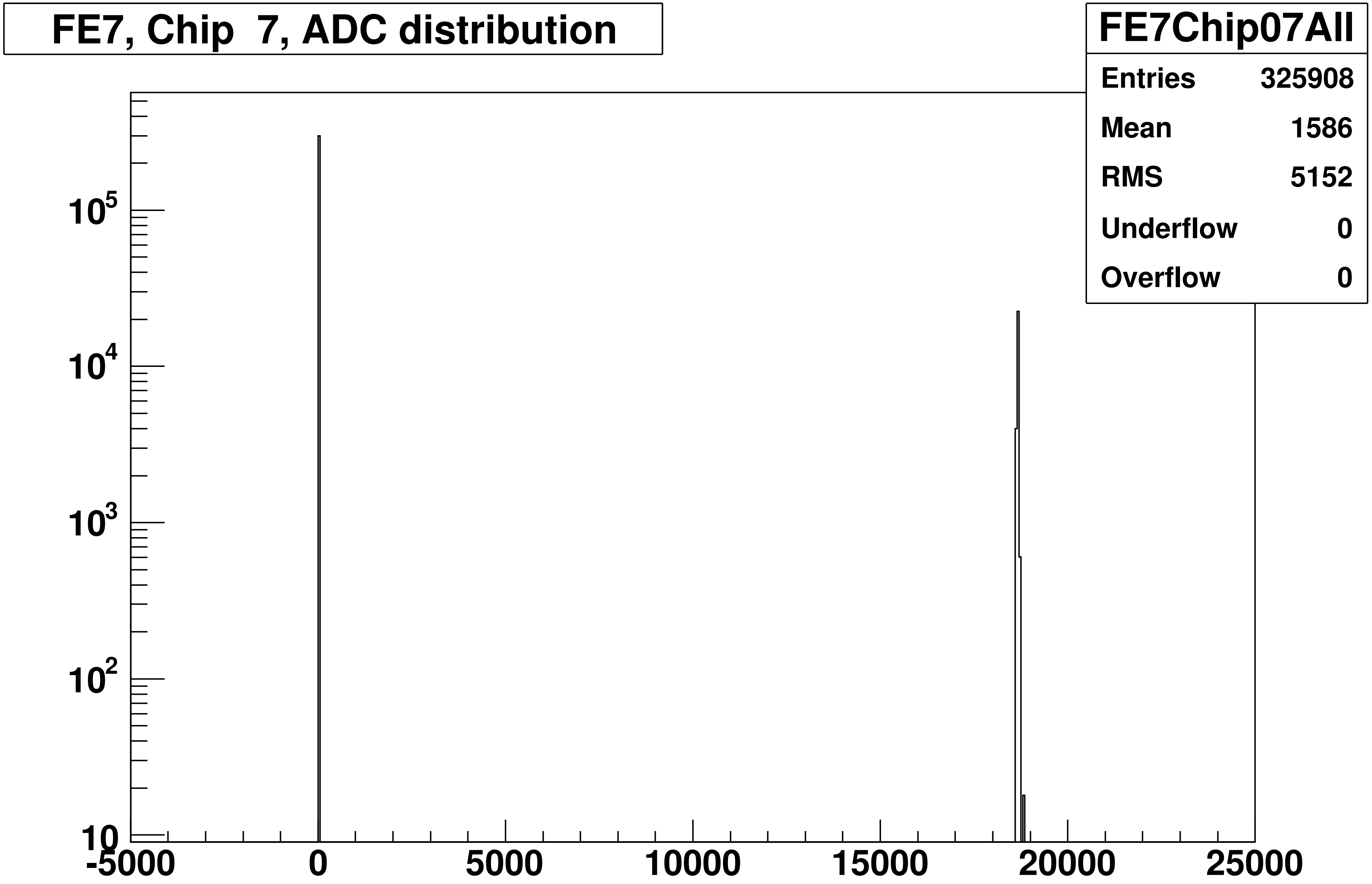
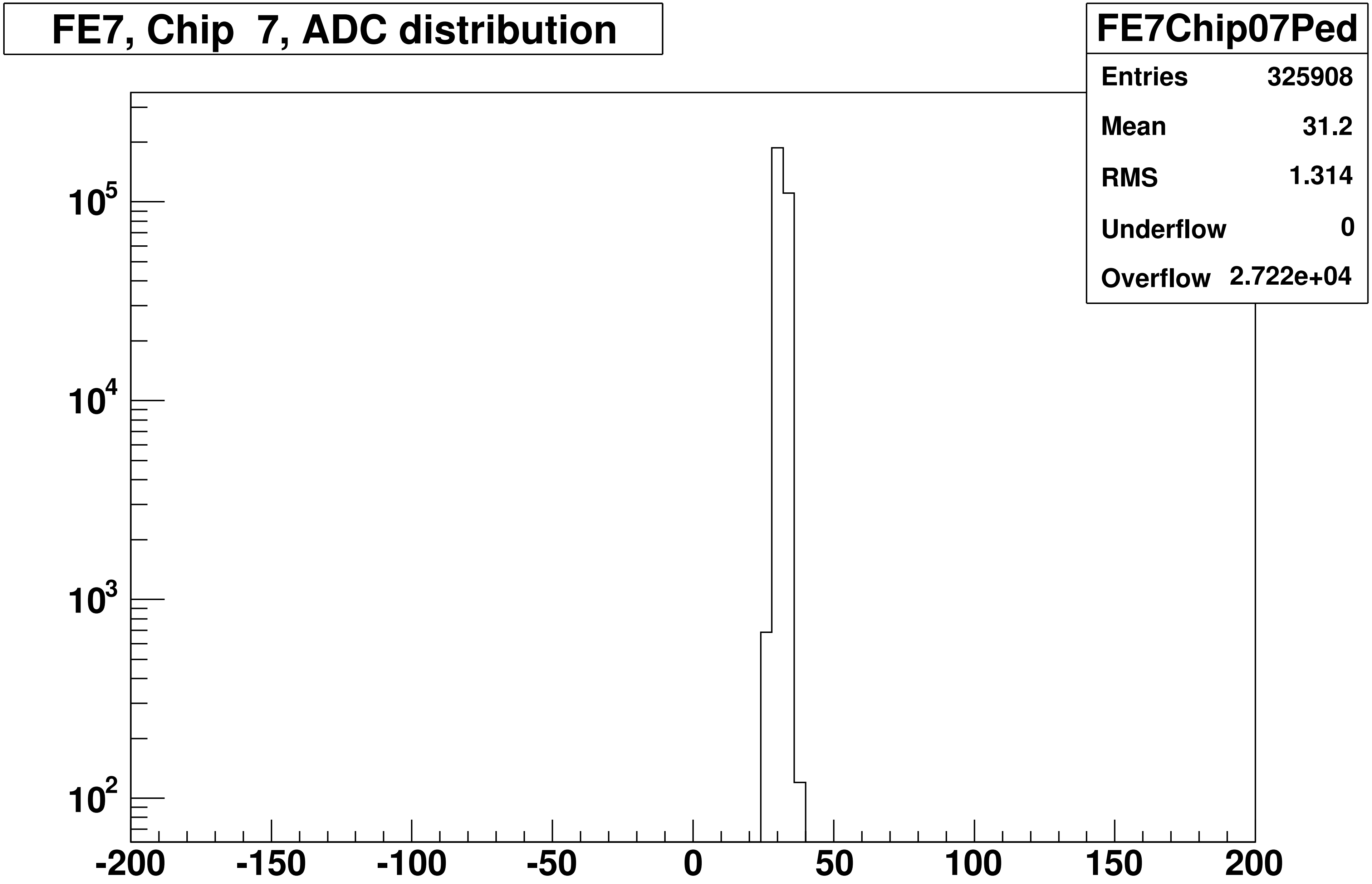
19600
19800
20000



FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 325908
Mean 1586
RMS 5152
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 325908
Mean 31.2
RMS 1.314
Underflow 0
Overflow 2.722e+04
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10^4
10^3
10^2
-200
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-100
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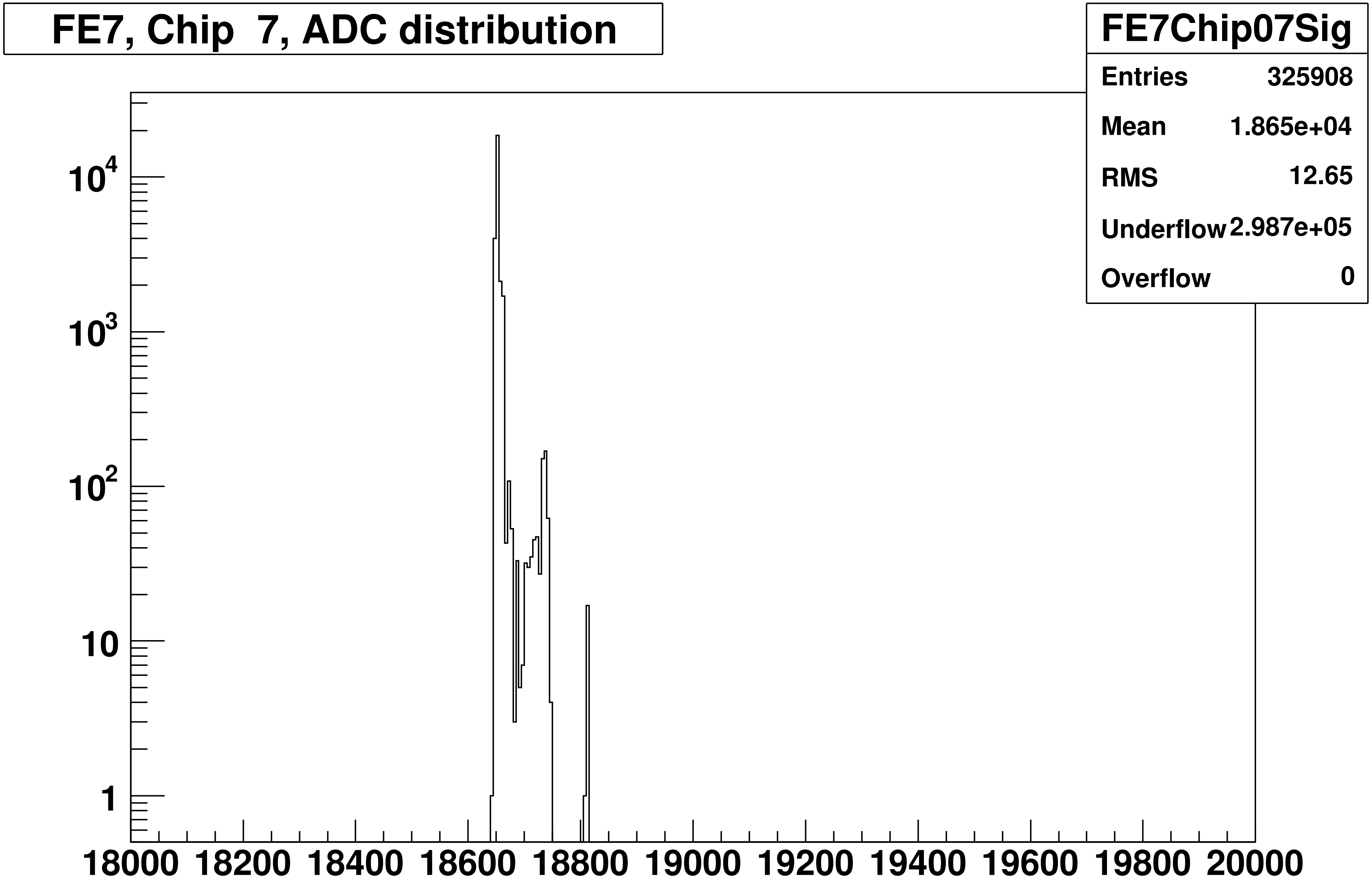
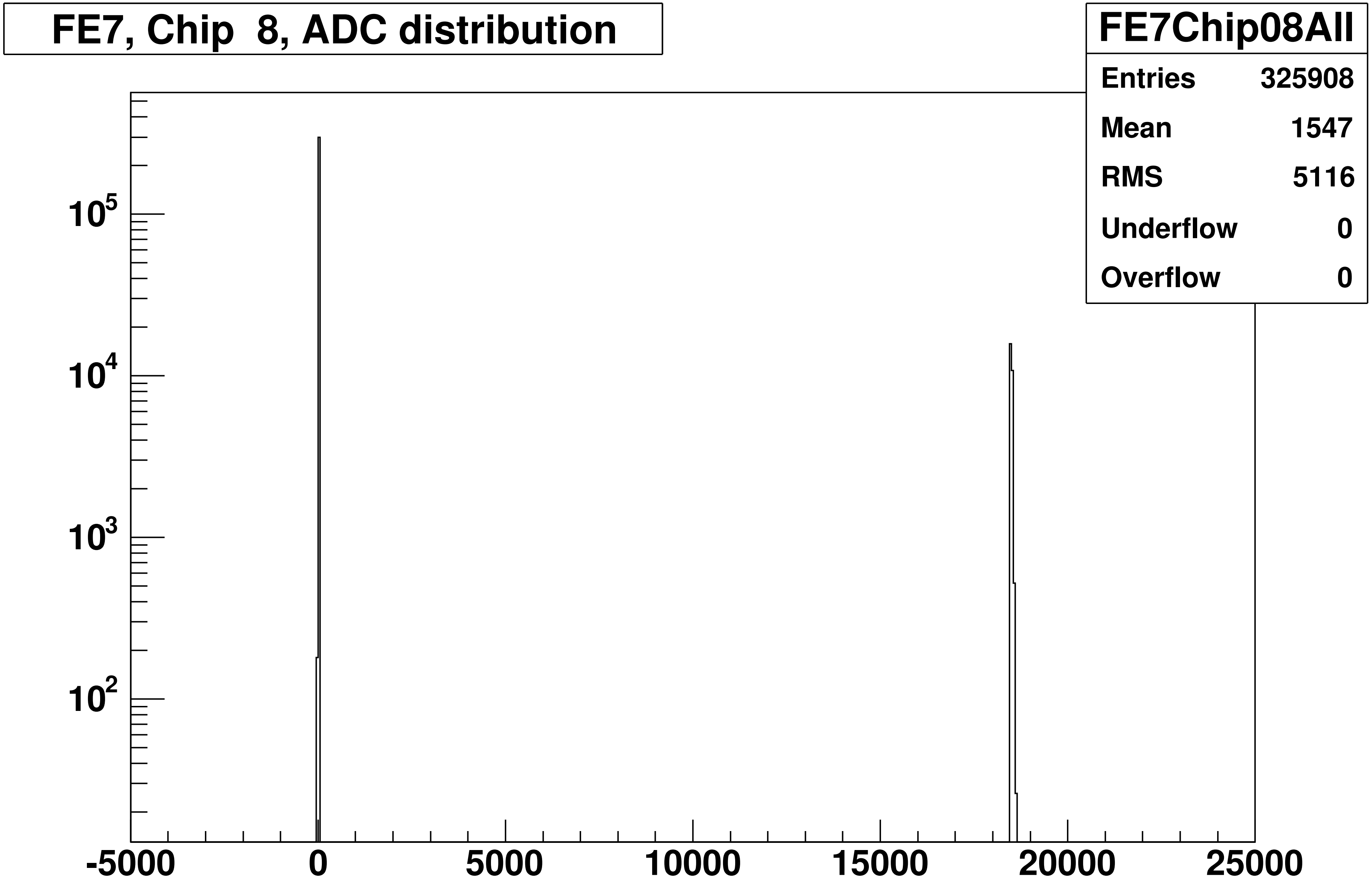
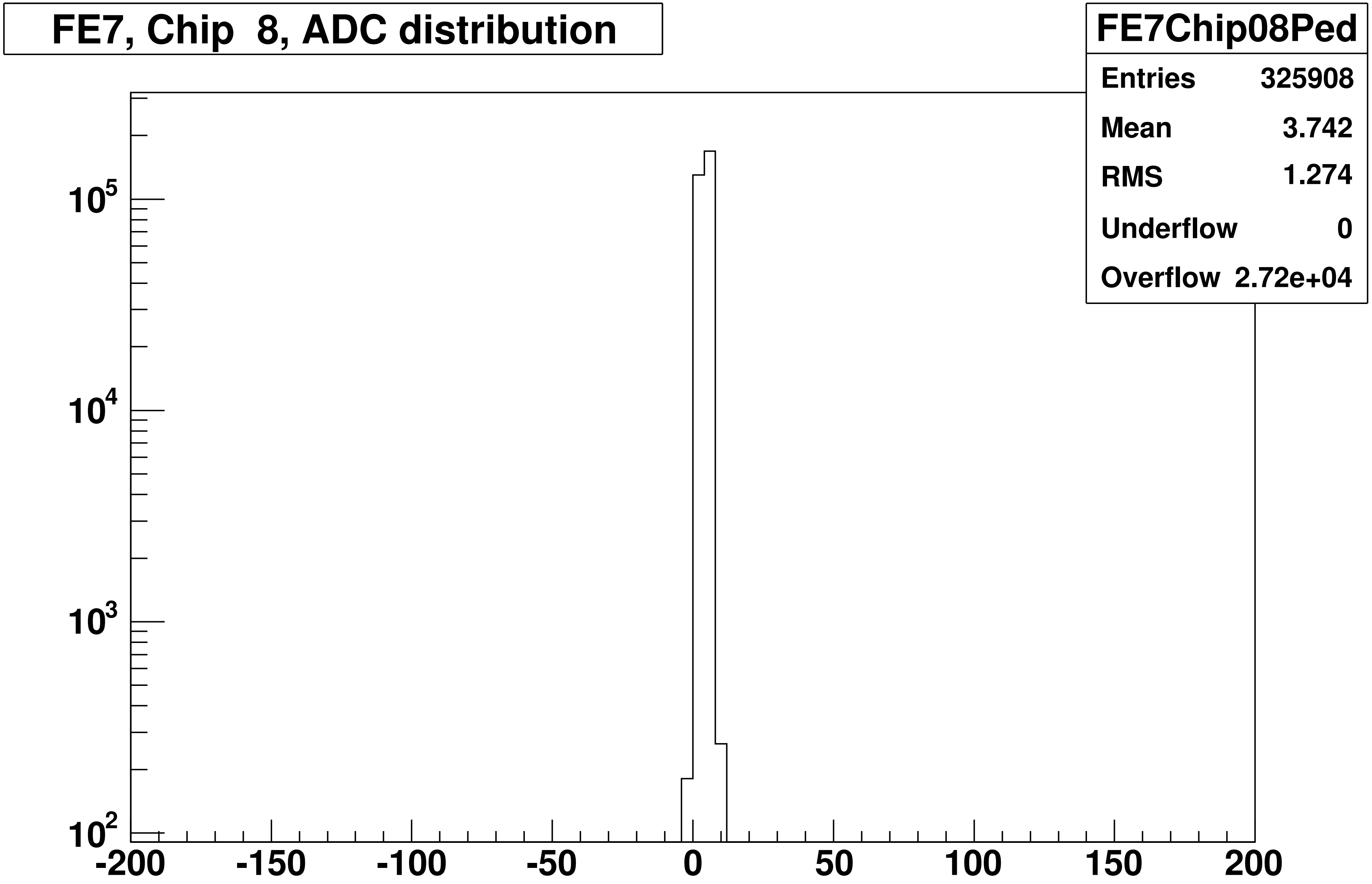
0
50
100
150
200



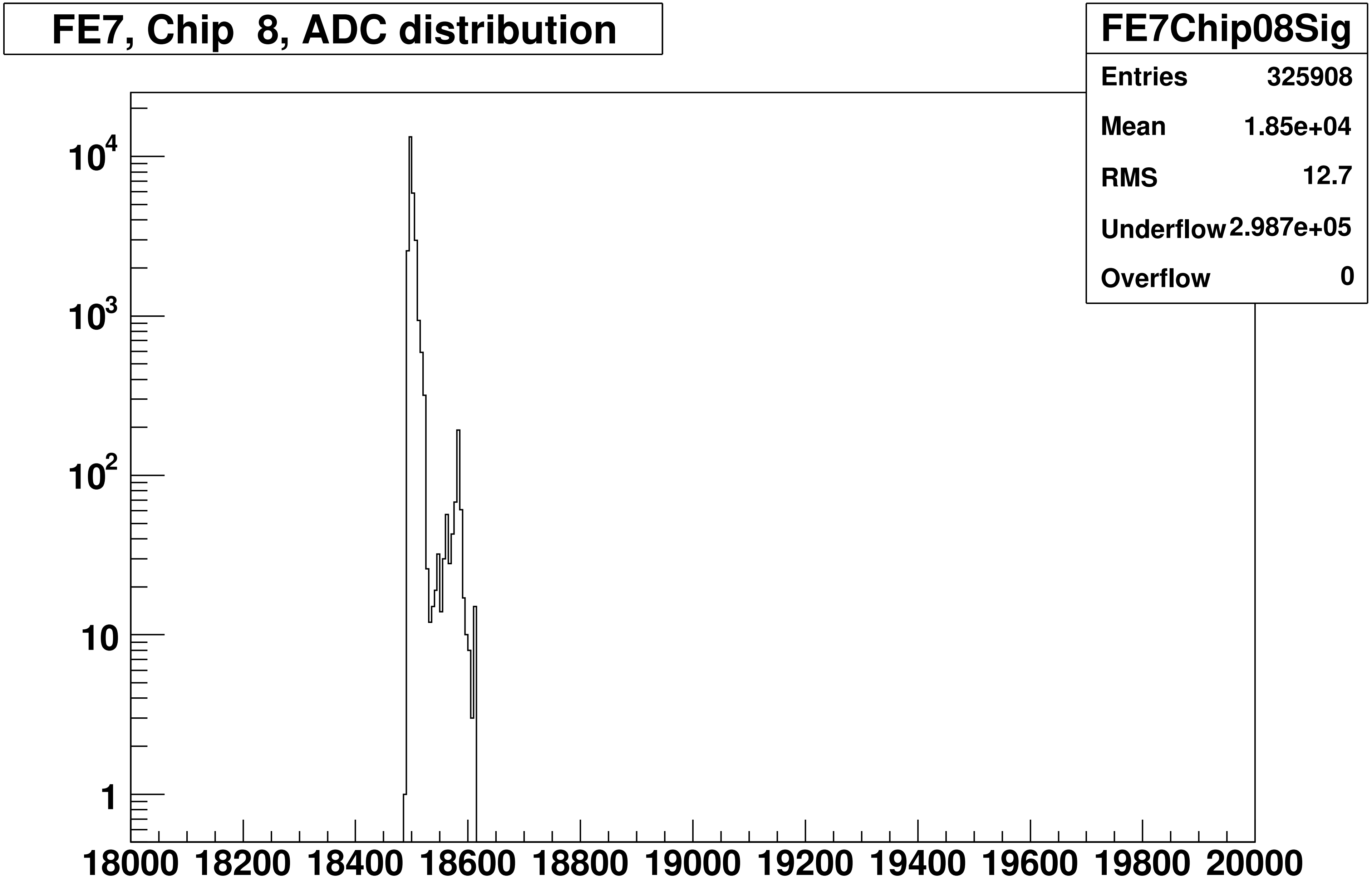
FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 325908
Mean 1.865e+04
RMS 12.65
Underflow 2.987e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



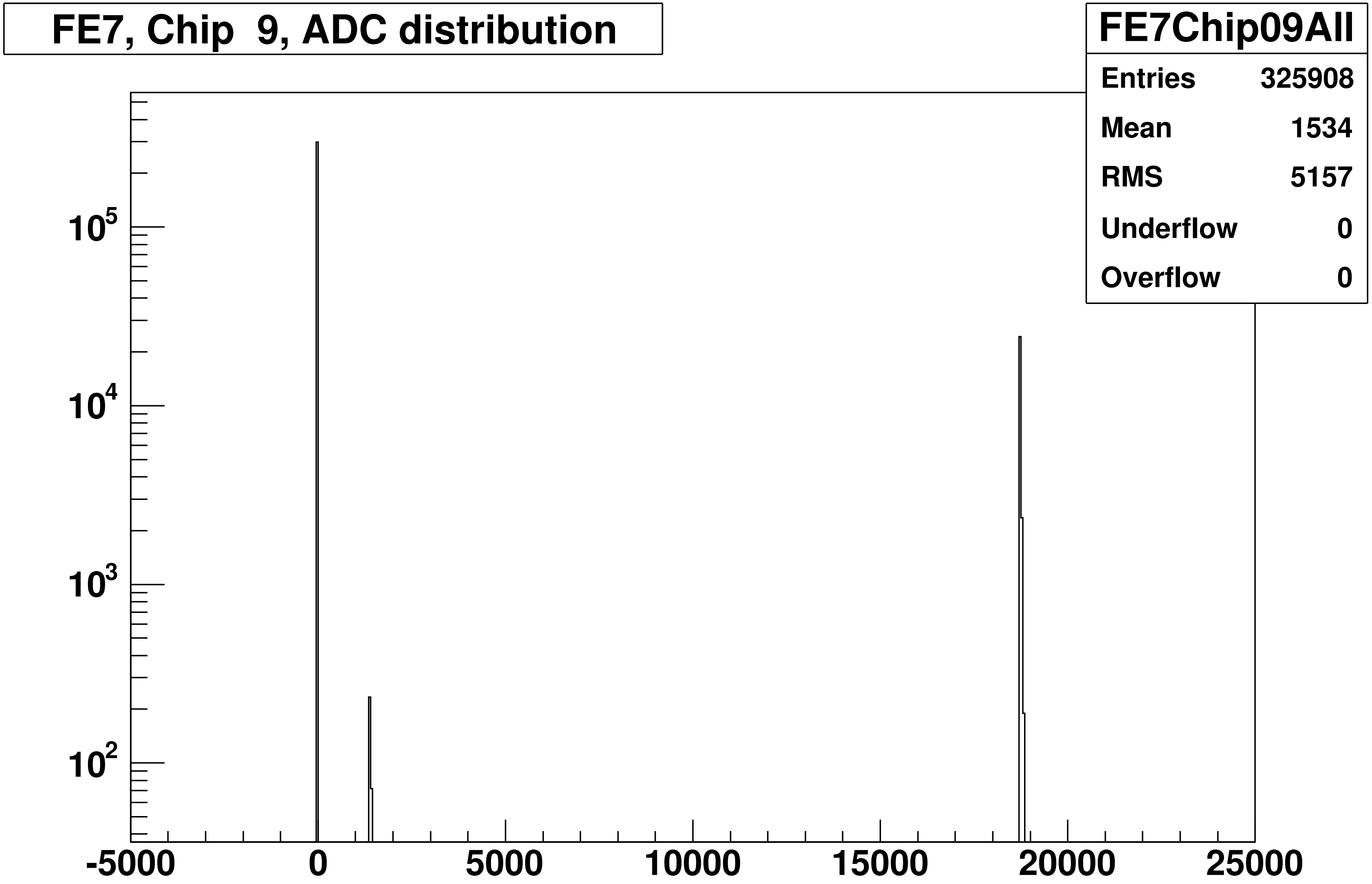
FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 325908
Mean 1547
RMS 5116
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



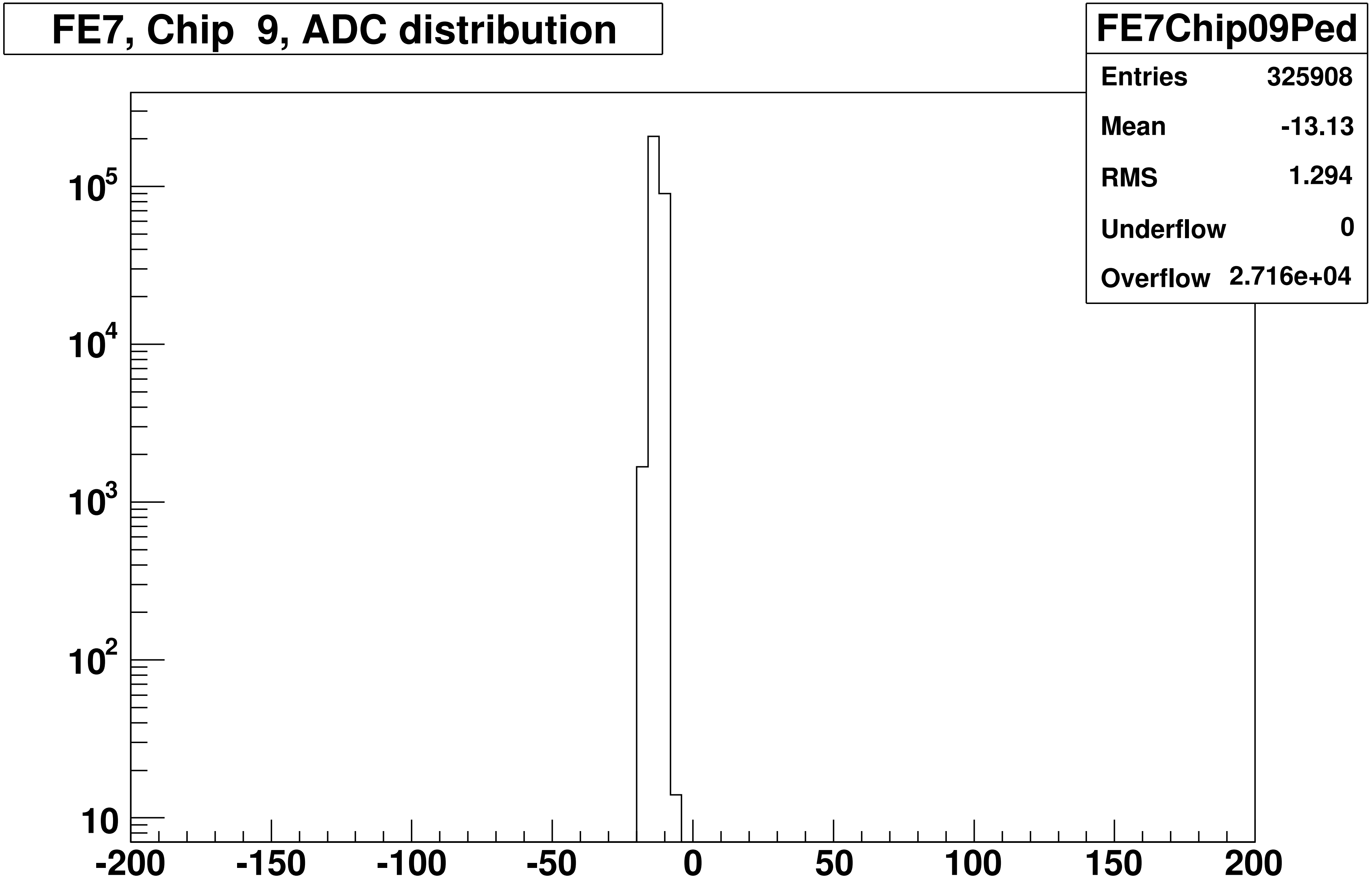
FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 325908
Mean 3.742
RMS 1.274
Underflow 0
Overflow 2.72e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200



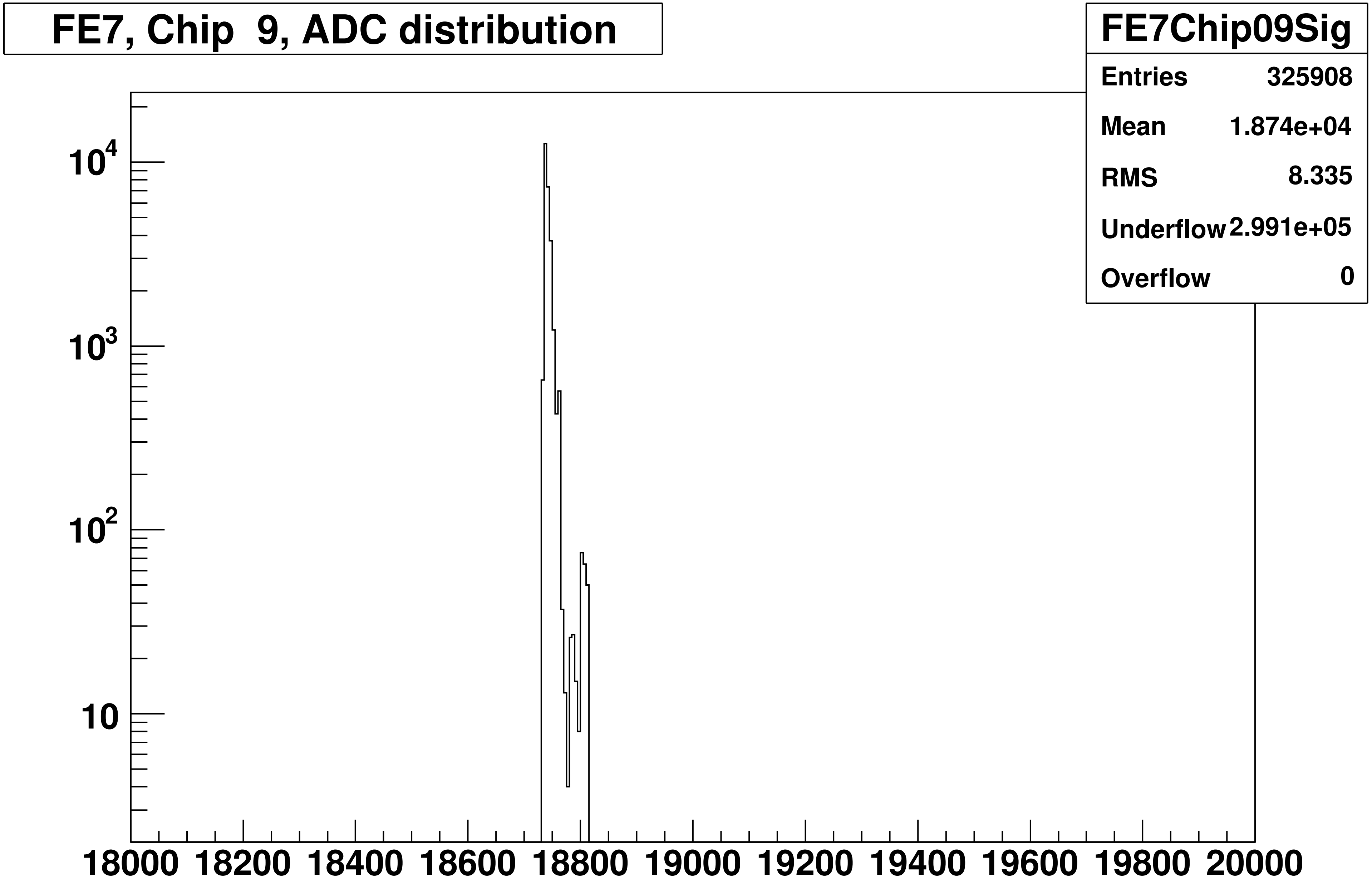
FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 325908
Mean 1.85e+04
RMS 12.7
Underflow 2.987e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



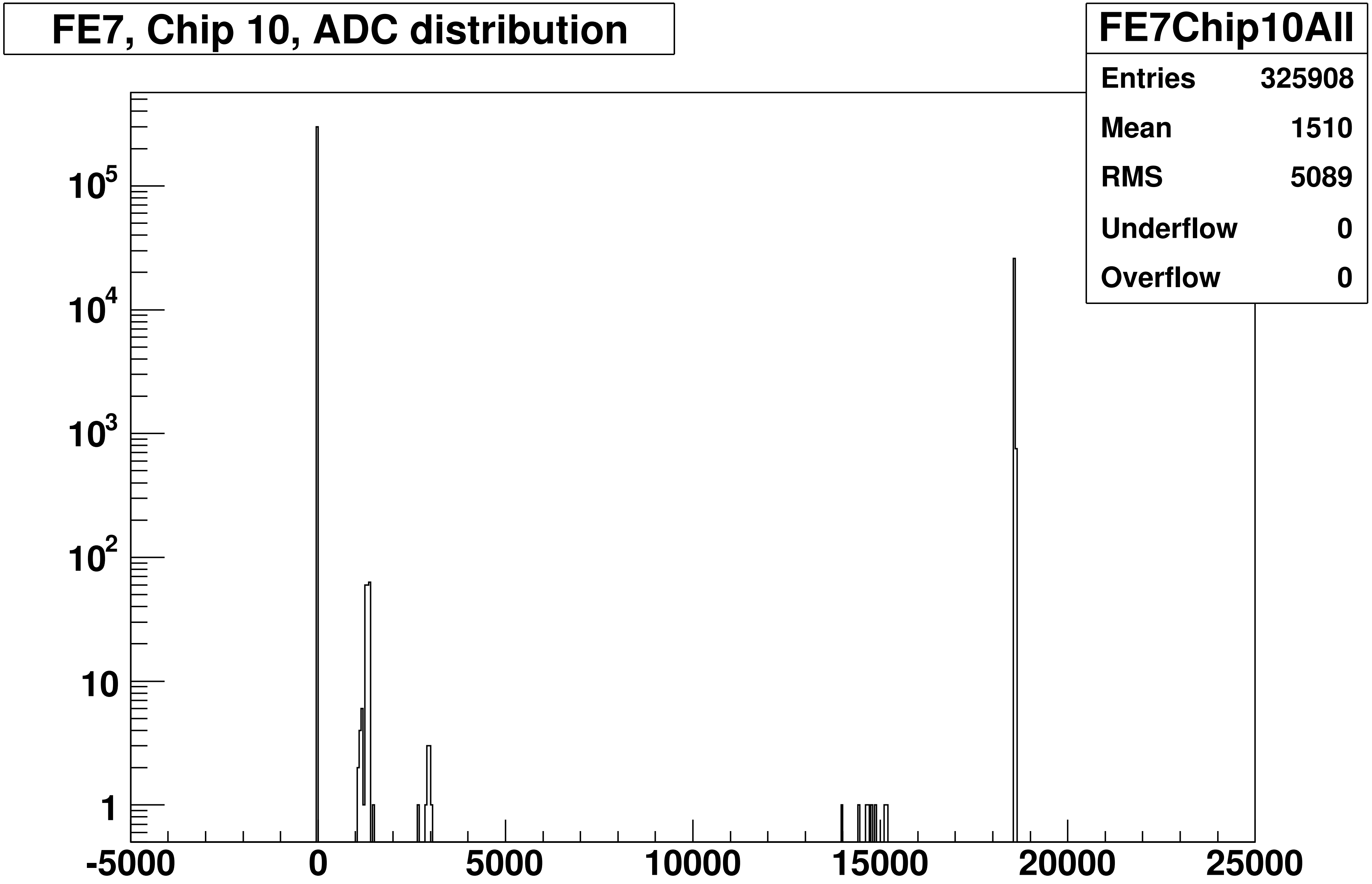
FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 325908
Mean 1534
RMS 5157
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

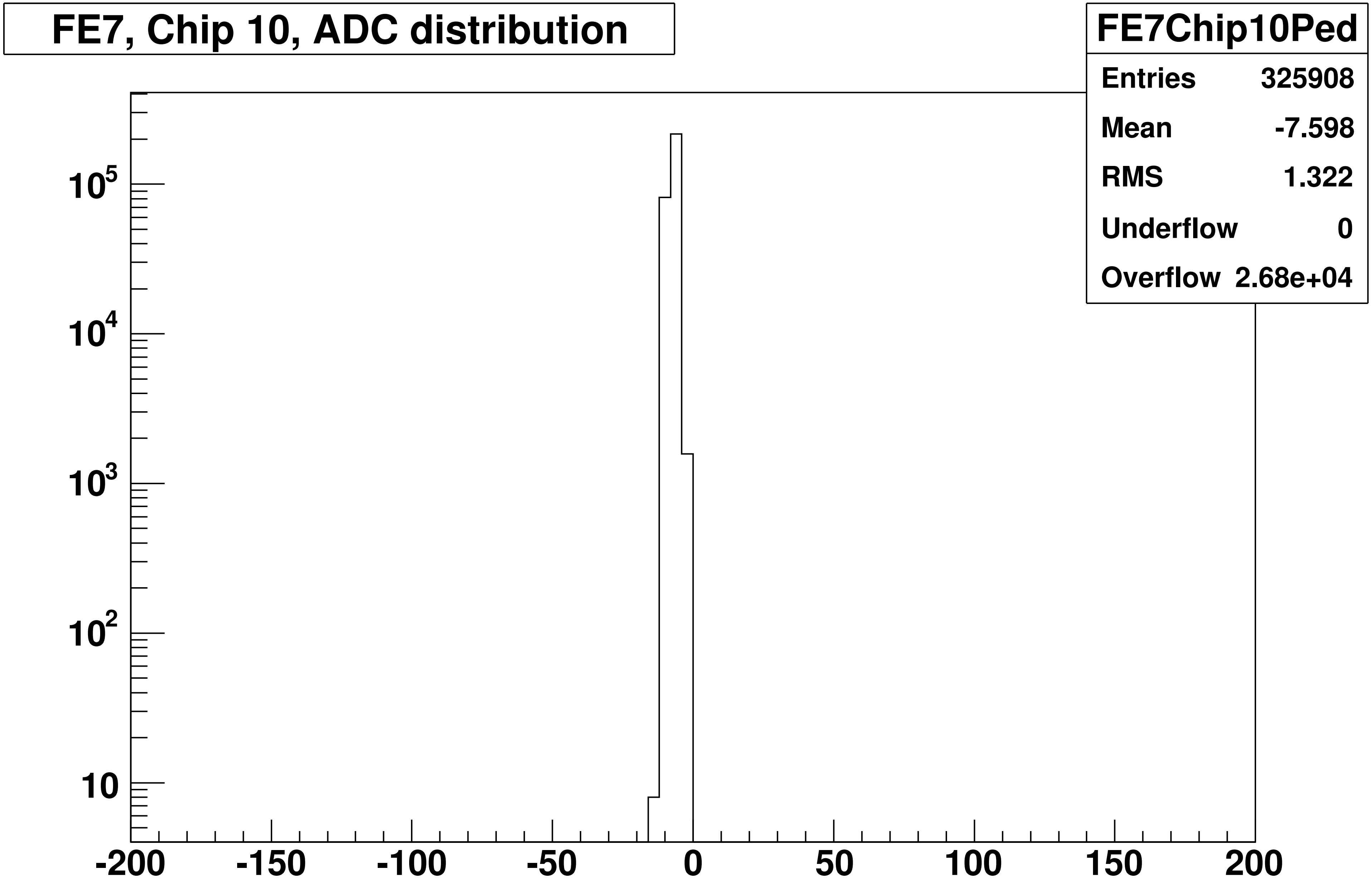


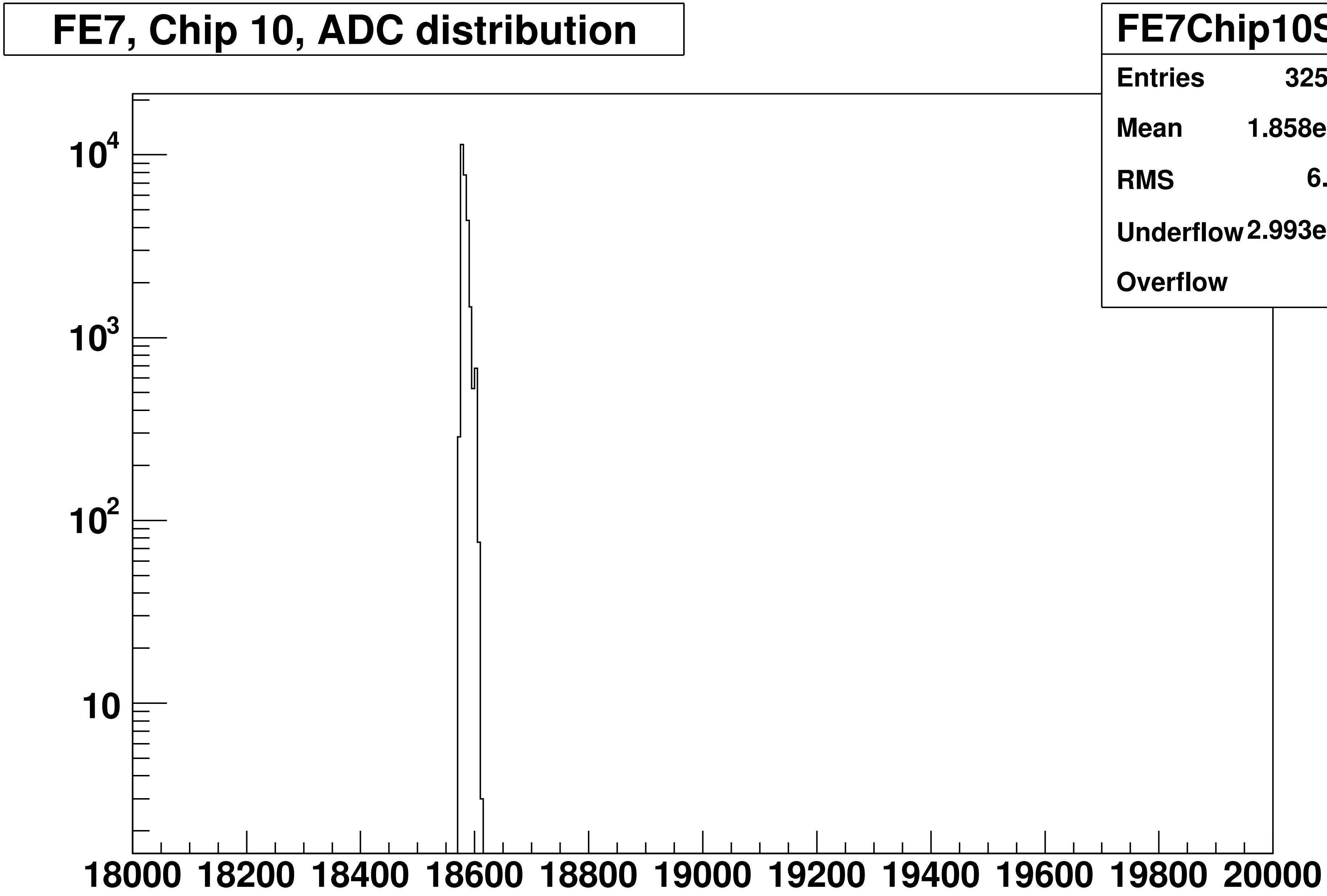
FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 325908
Mean -13.13
RMS 1.294
Underflow 0
Overflow 2.716e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



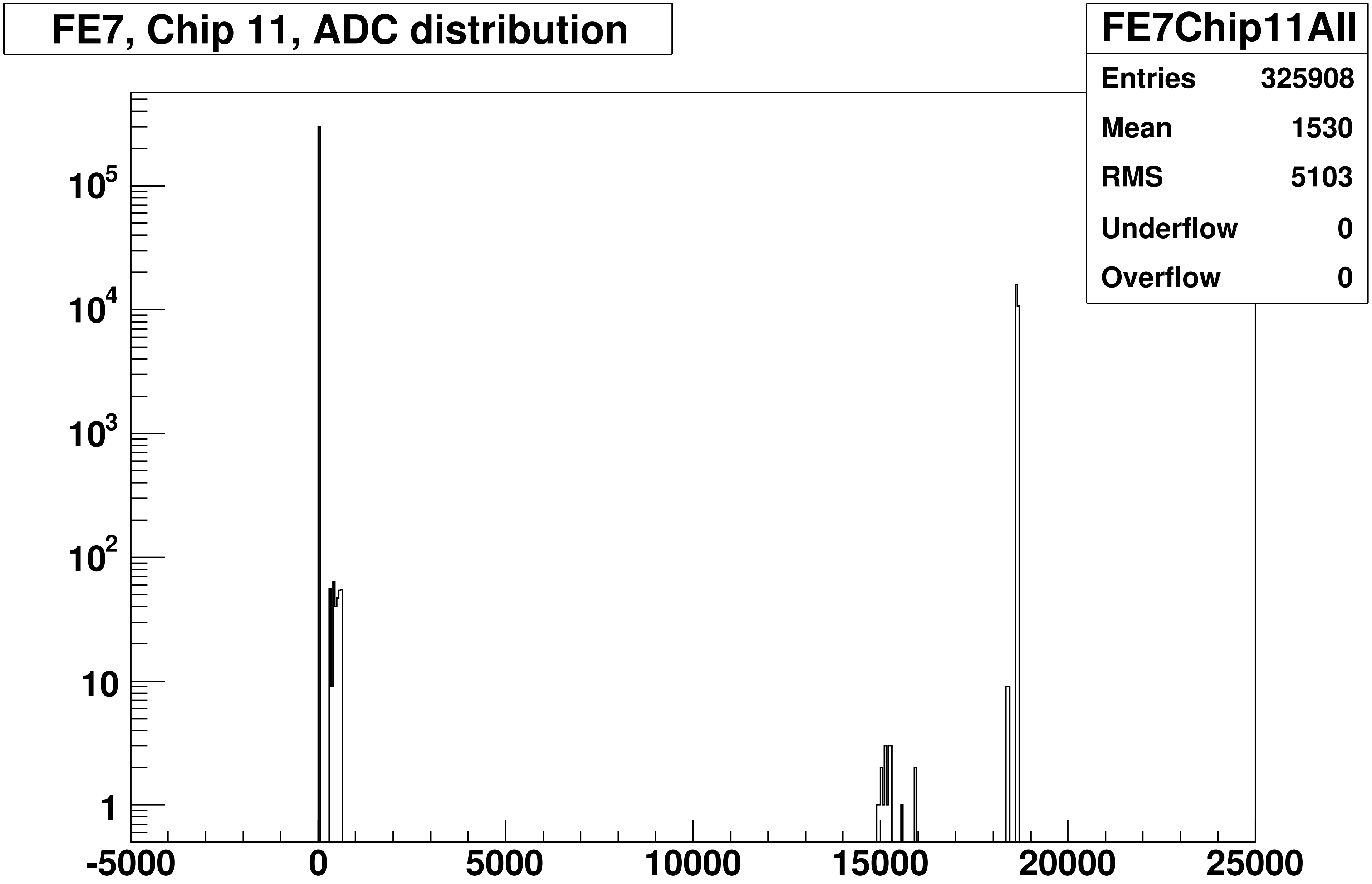
FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 325908
Mean 1.874e+04
RMS 8.335
Underflow 2.991e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

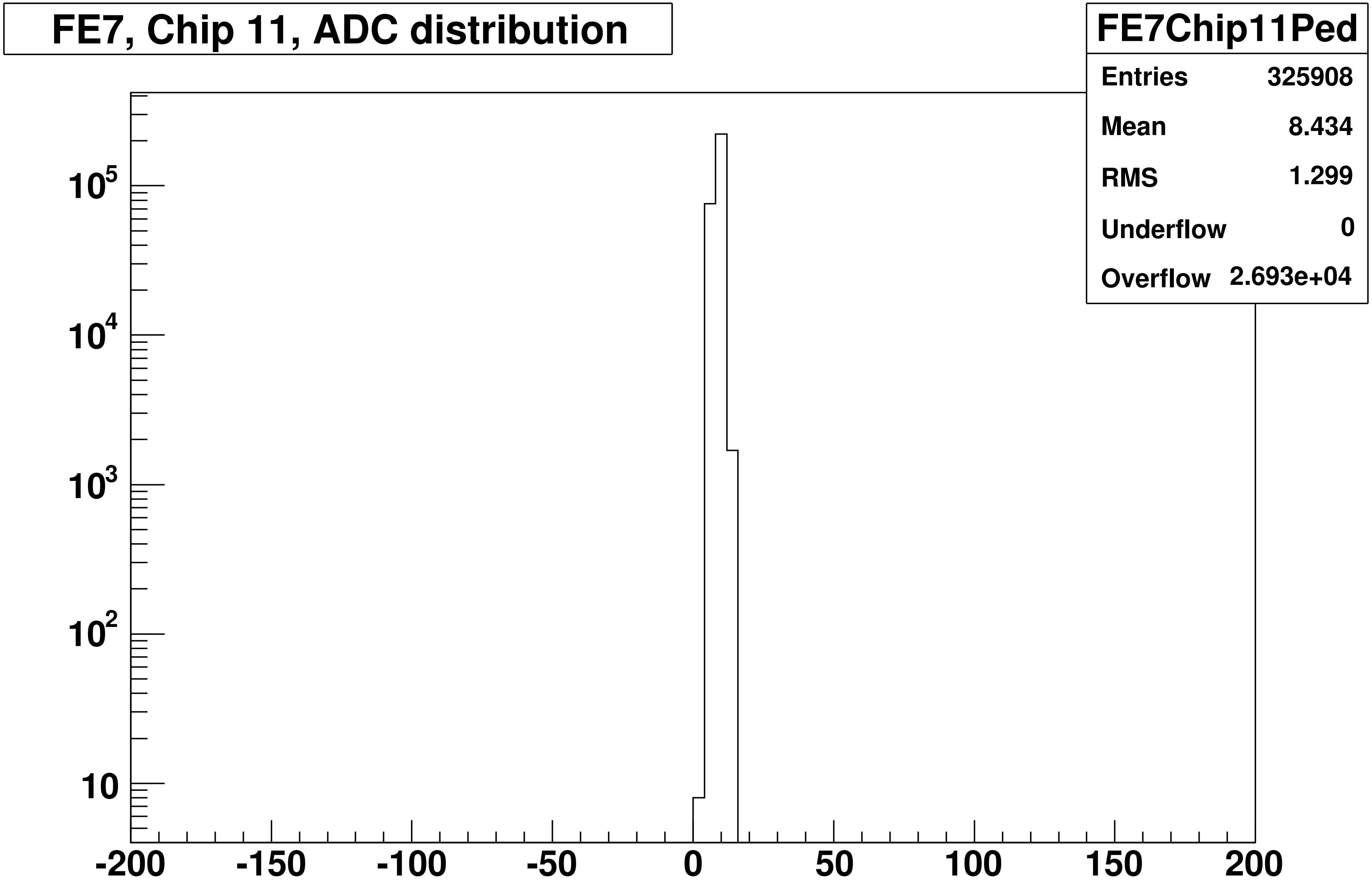

FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 325908
Mean 1510
RMS 5089
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 325908
Mean -7.598
RMS 1.322
Underflow 0
Overflow 2.68e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

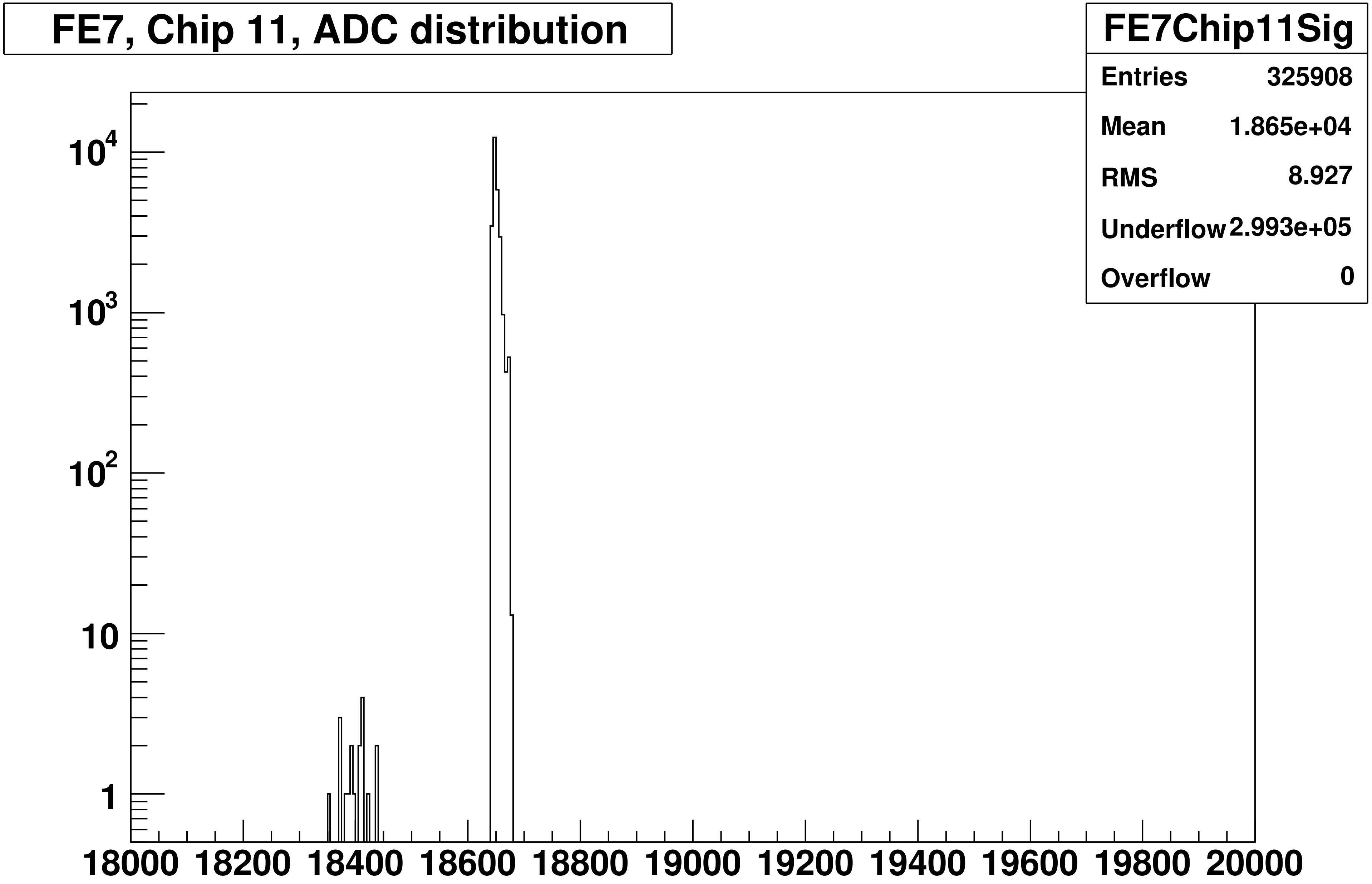


FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 325908
Mean 1.858e+04
RMS 6.027
Underflow 2.993e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

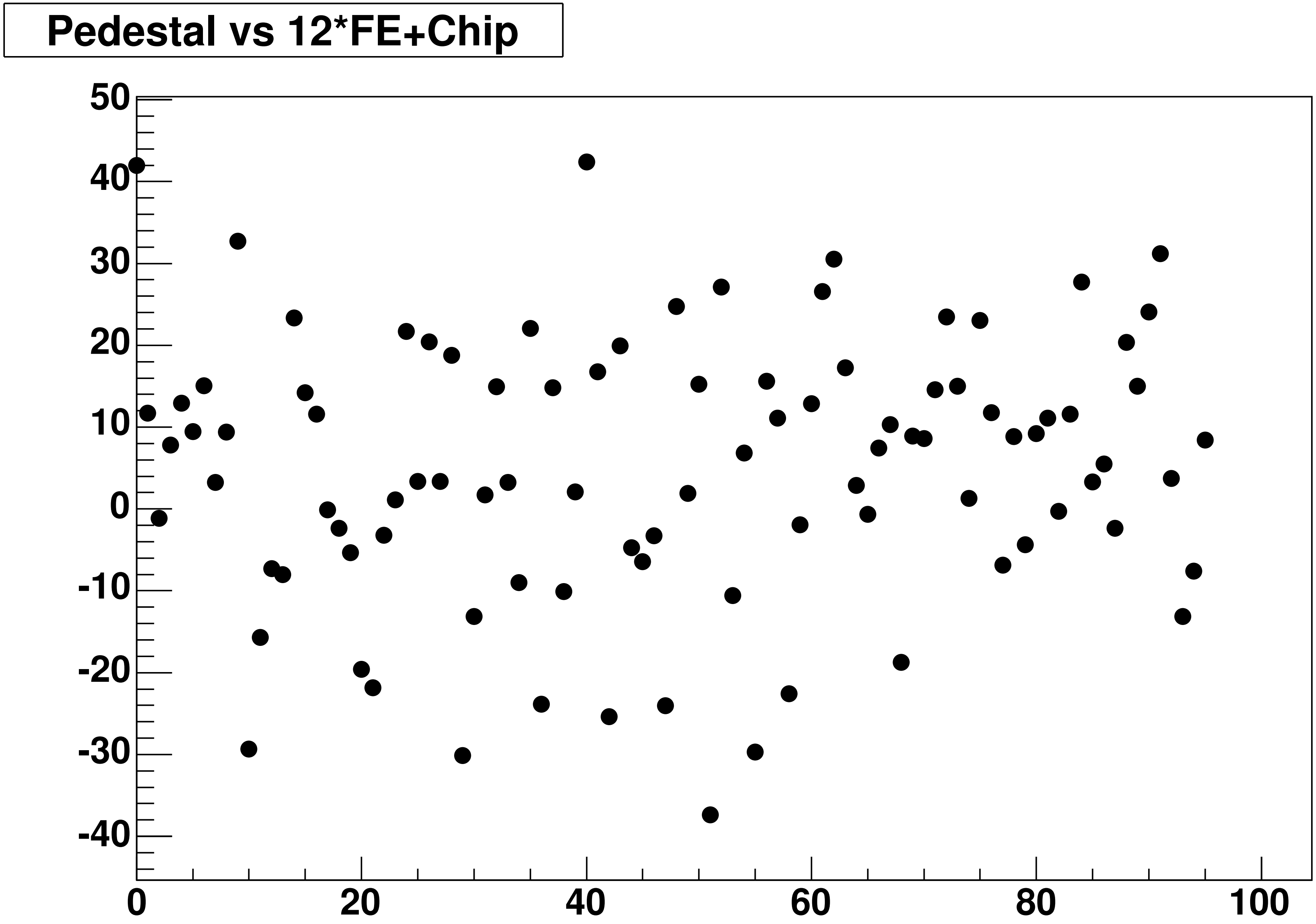

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 325908
Mean 1530
RMS 5103
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

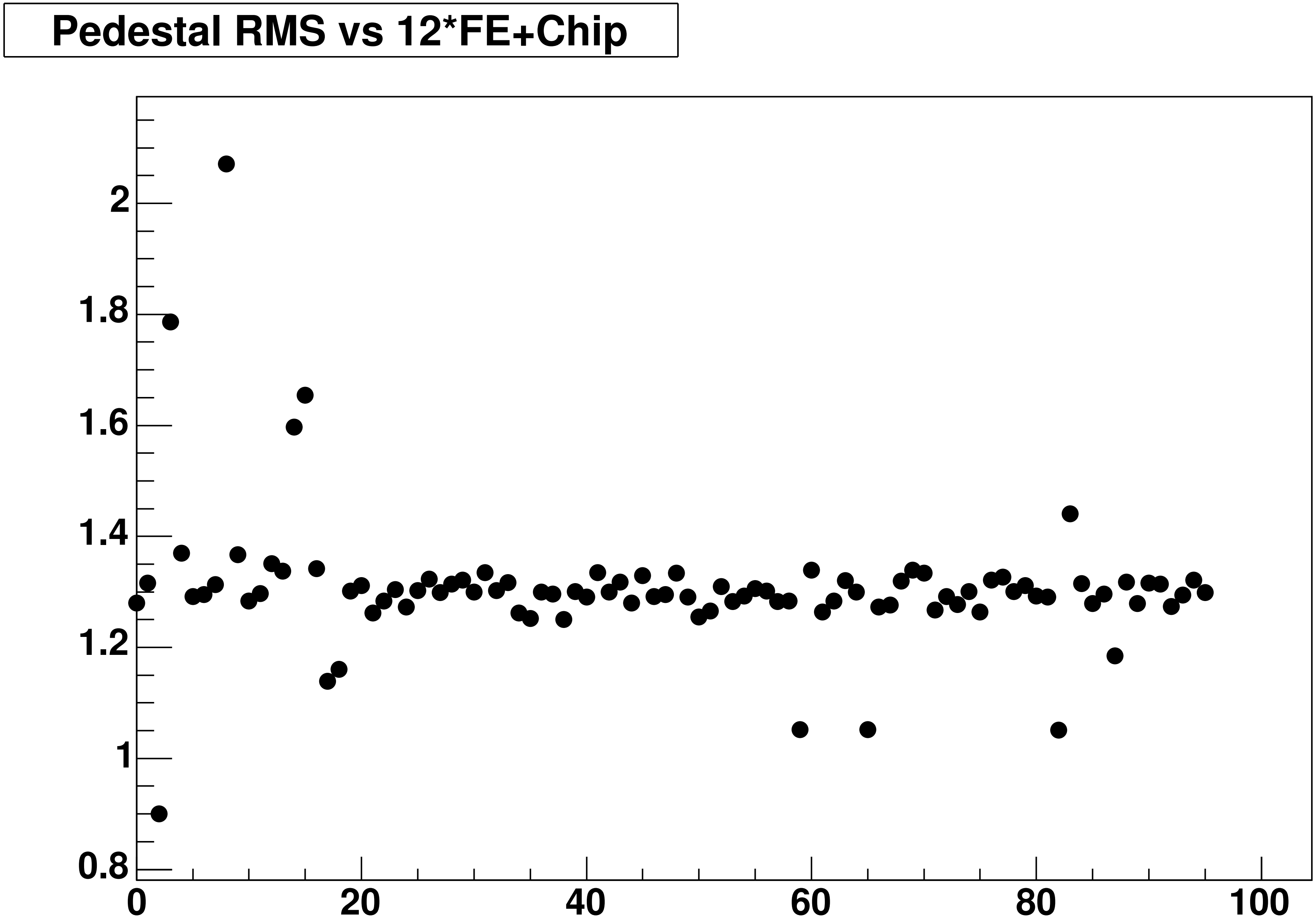


FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 325908
Mean 8.434
RMS 1.299
Underflow 0
Overflow 2.693e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

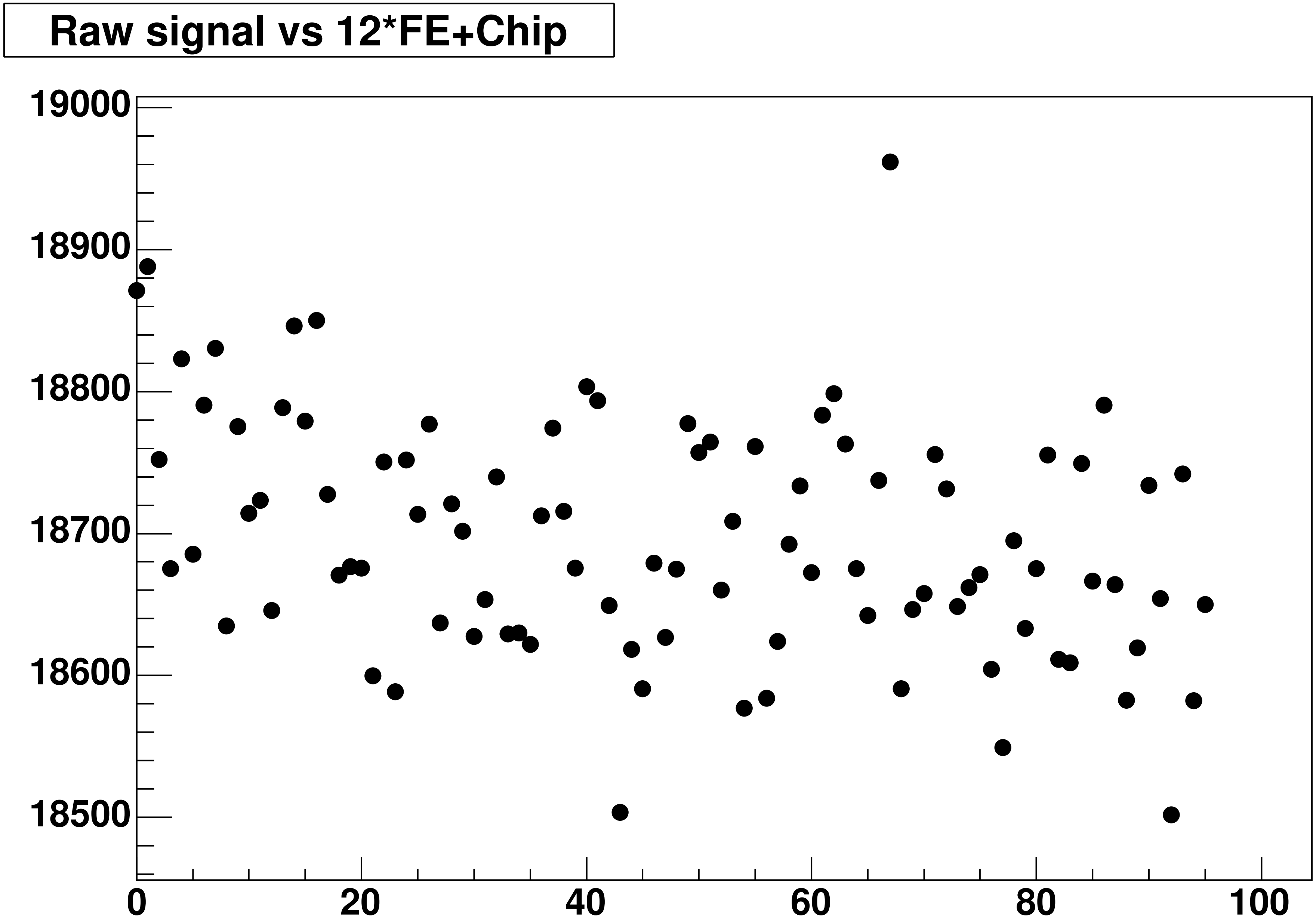


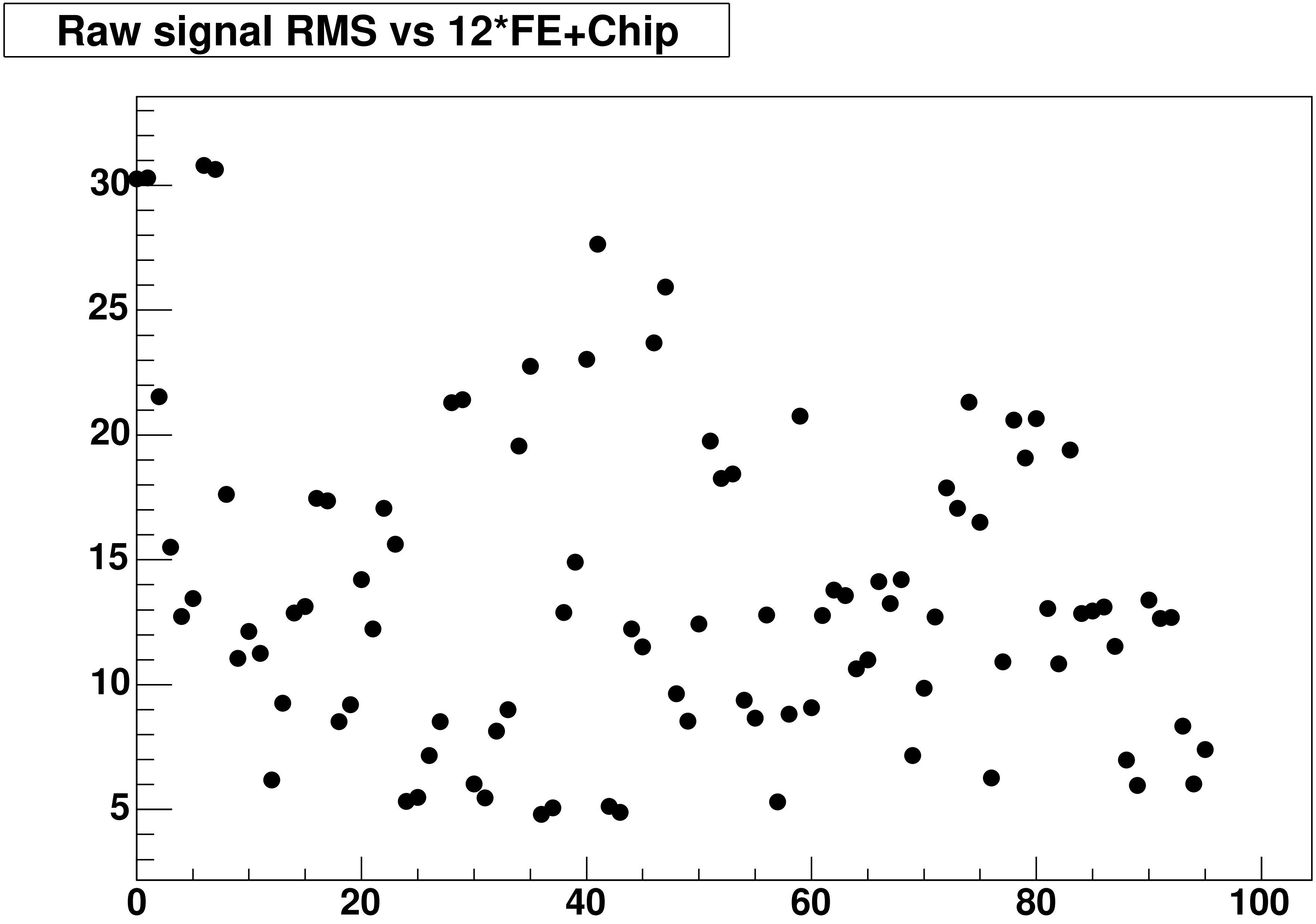
FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 325908
Mean 1.865e+04
RMS 8.927
Underflow 2.993e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


Pedestal vs 12*FE+Chip
50
40
30
20
10
0
-10
-20
-30
-40
0
20
40
60
80
100

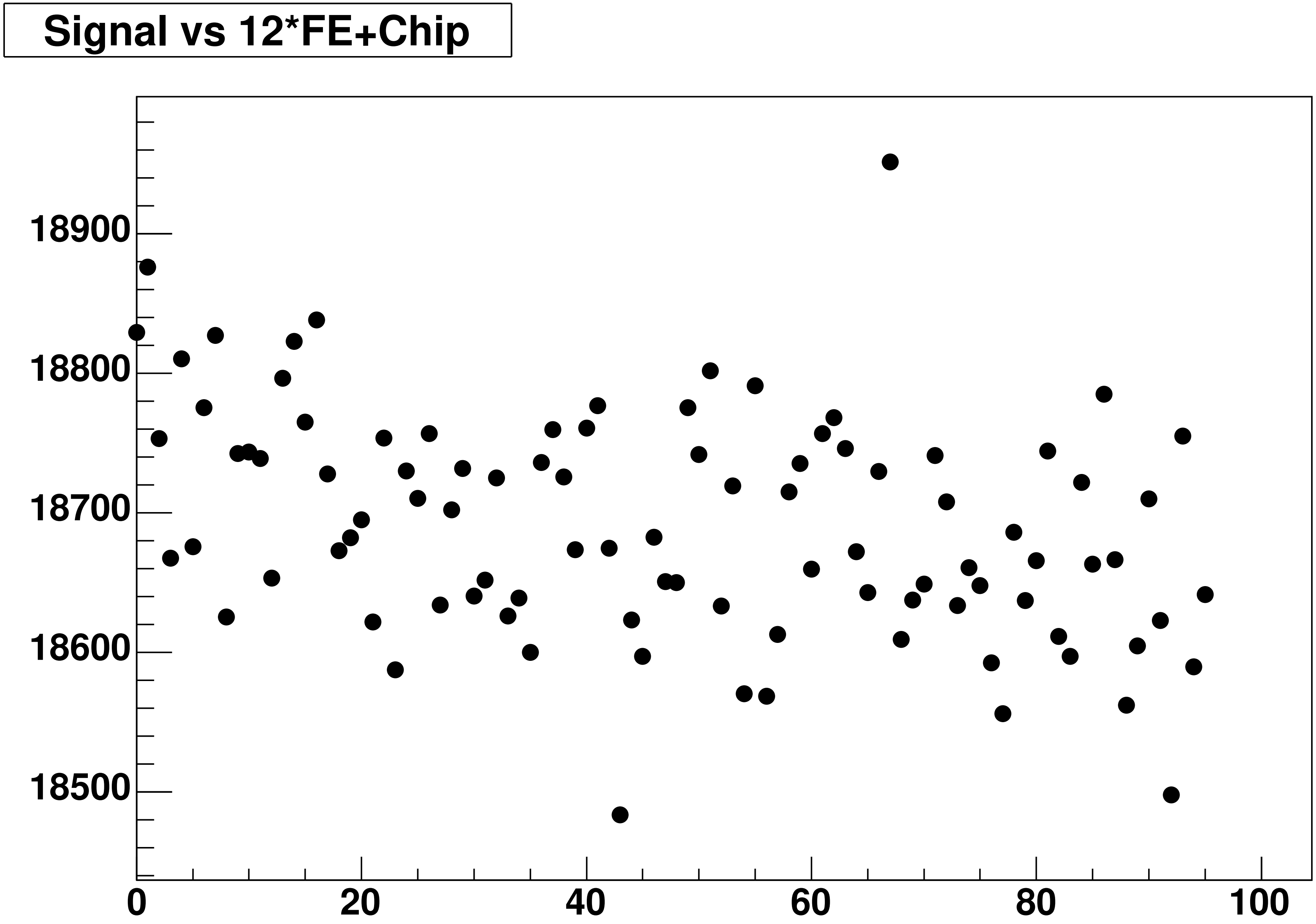


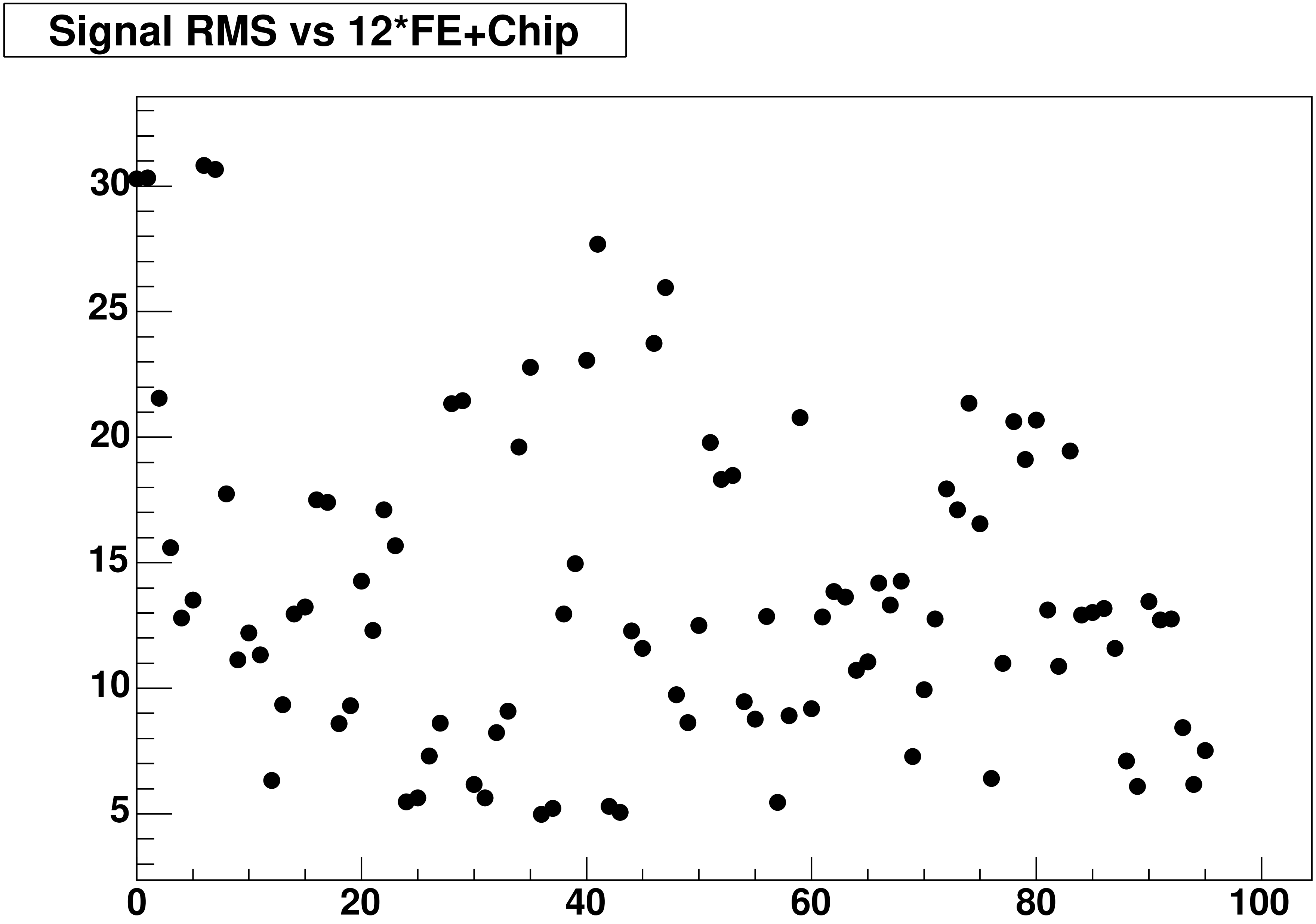
Pedestal RMS vs 12*FE+Chip
0.8
1
1.2
1.4
1.6
1.8
2
0
20
40
60
80
100


Raw signal vs 12*FE+Chip
19000
18900
18800
18700
18600
18500
0
20
40
60
80
100



Raw signal RMS vs 12*FE+Chip
30
25
20
15
10
5
0
20
40
60
80
100


Signal vs 12*FE+Chip
18900
18800
18700
18600
18500
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
30
25
20
15
10
5
0
20
40
60
80
100