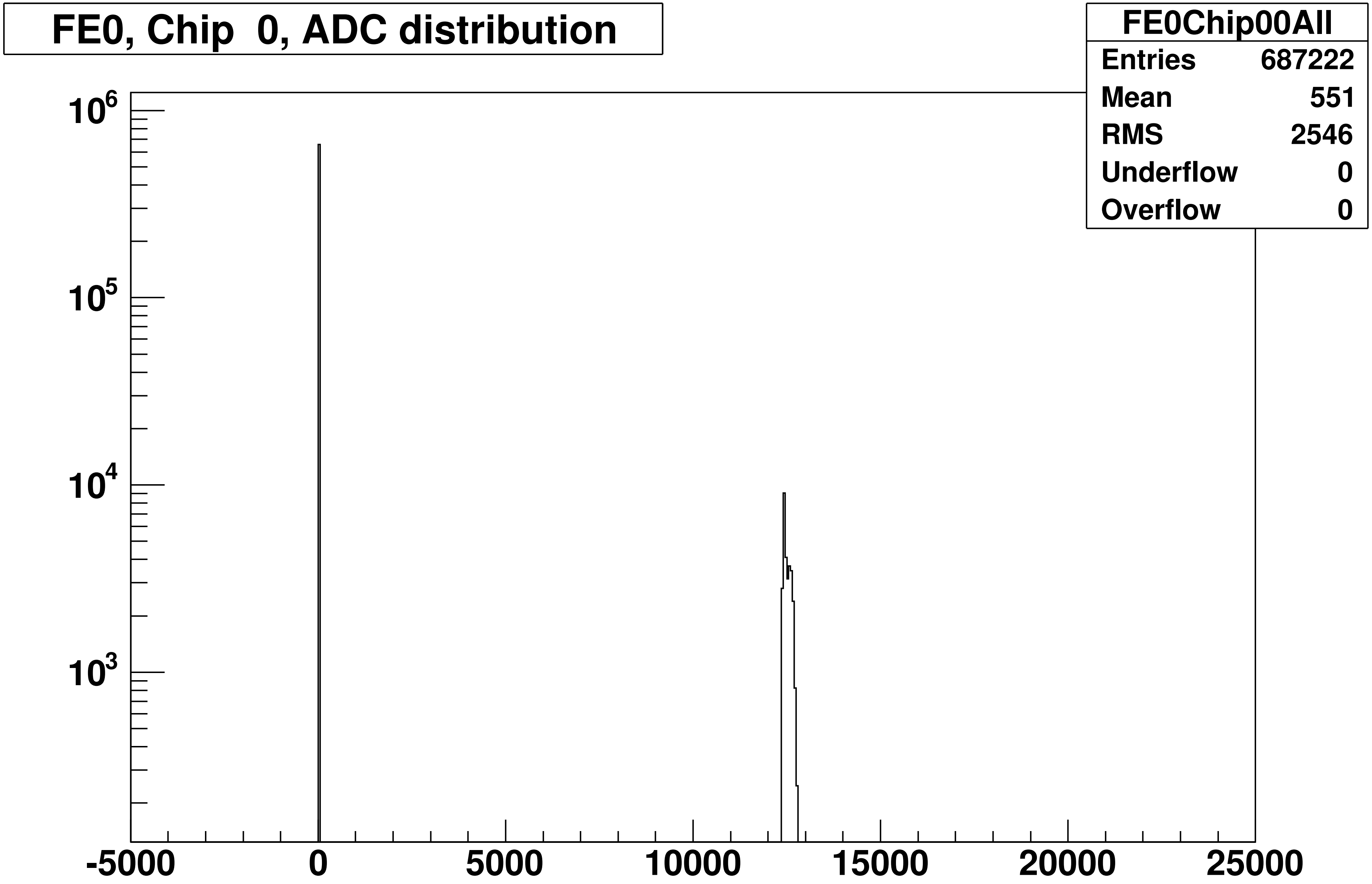
FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 687222
Mean 551
RMS 2546
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
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10000
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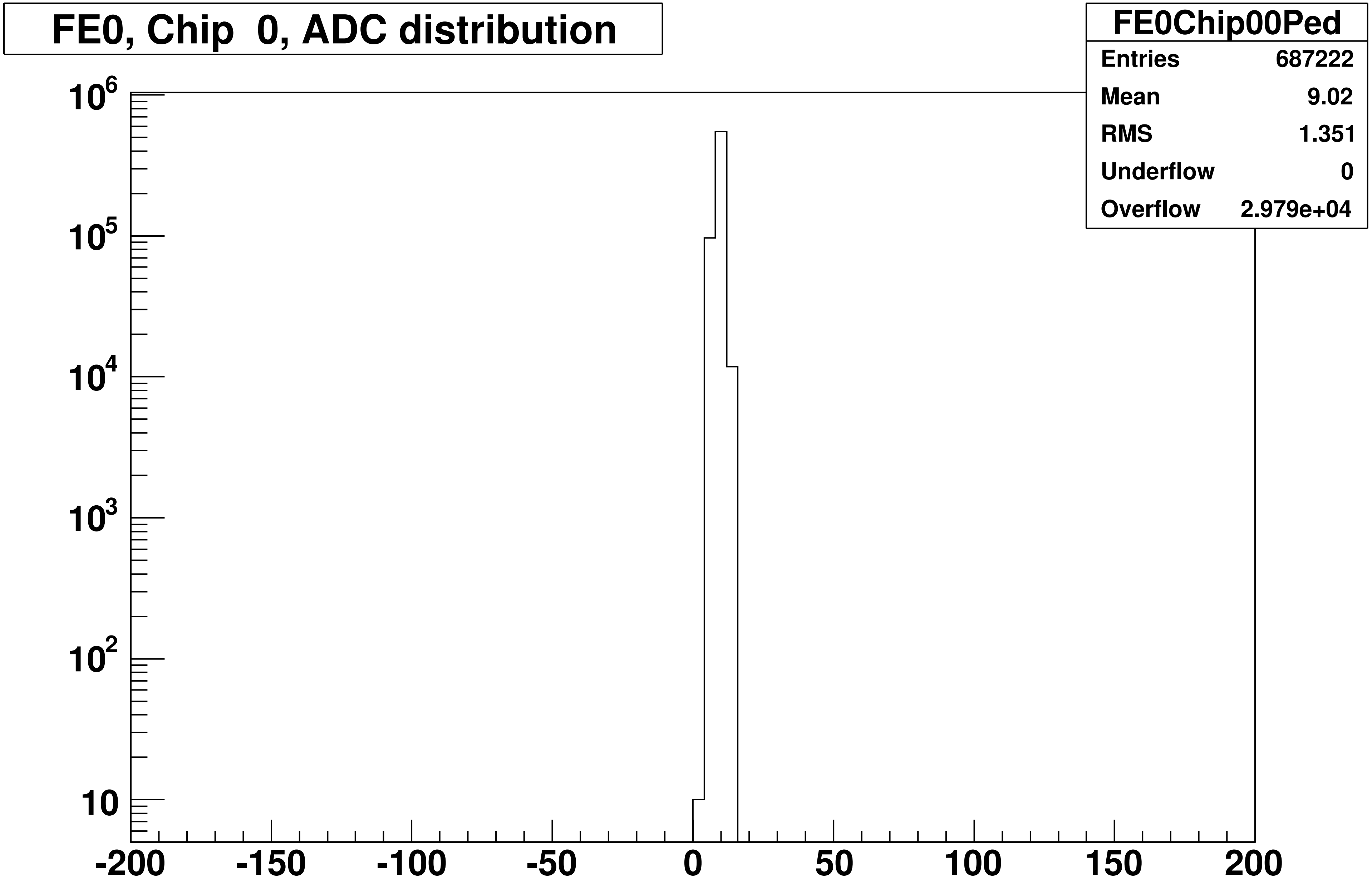
20000
25000


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 687222
Mean 9.02
RMS 1.351
Underflow 0
Overflow 2.979e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
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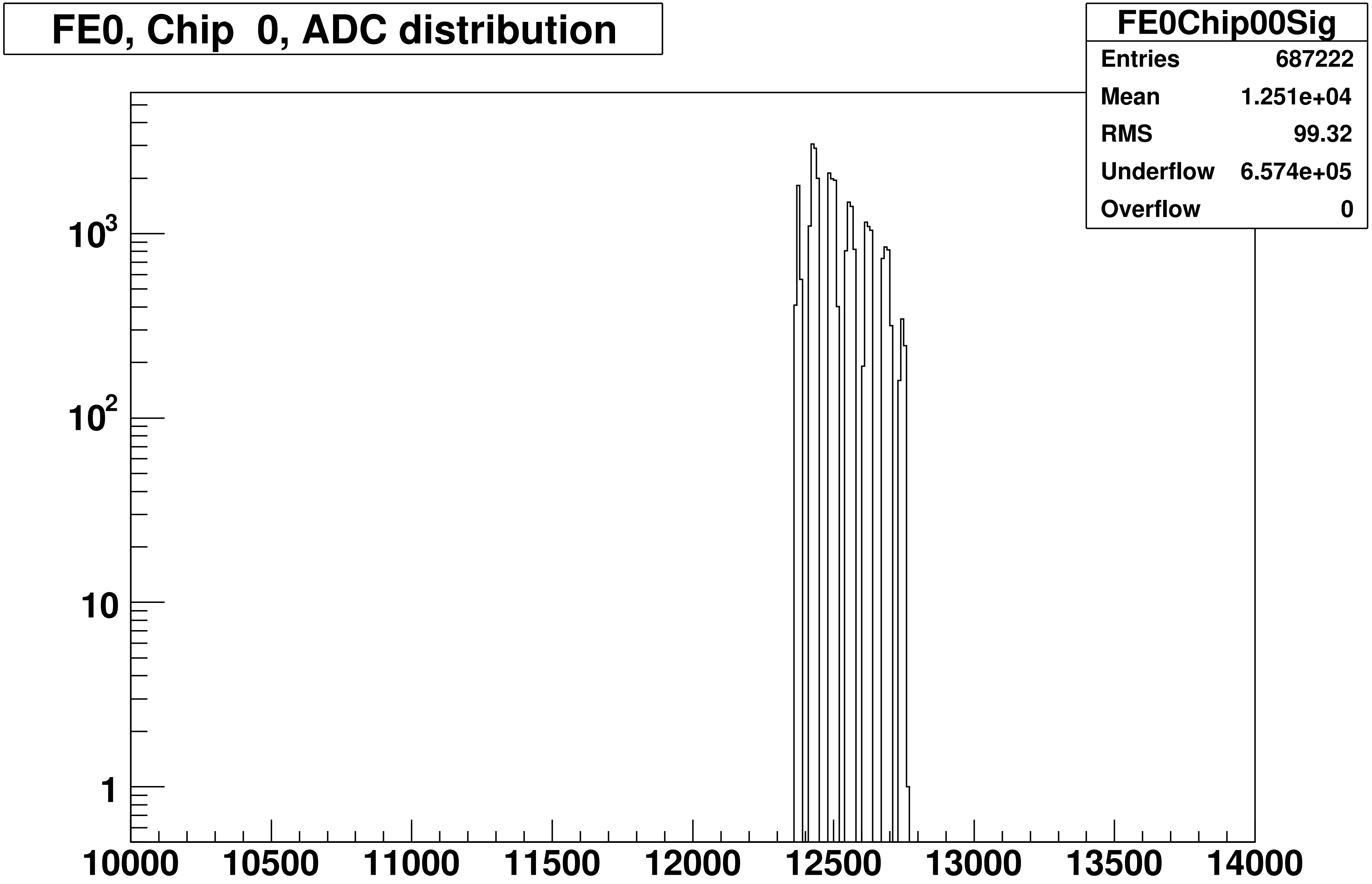
50
100
150
200


FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 687222
Mean 1.251e+04
RMS 99.32
Underflow 6.574e+05
Overflow 0
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10^2
10
1
10000
10500
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11500
12000
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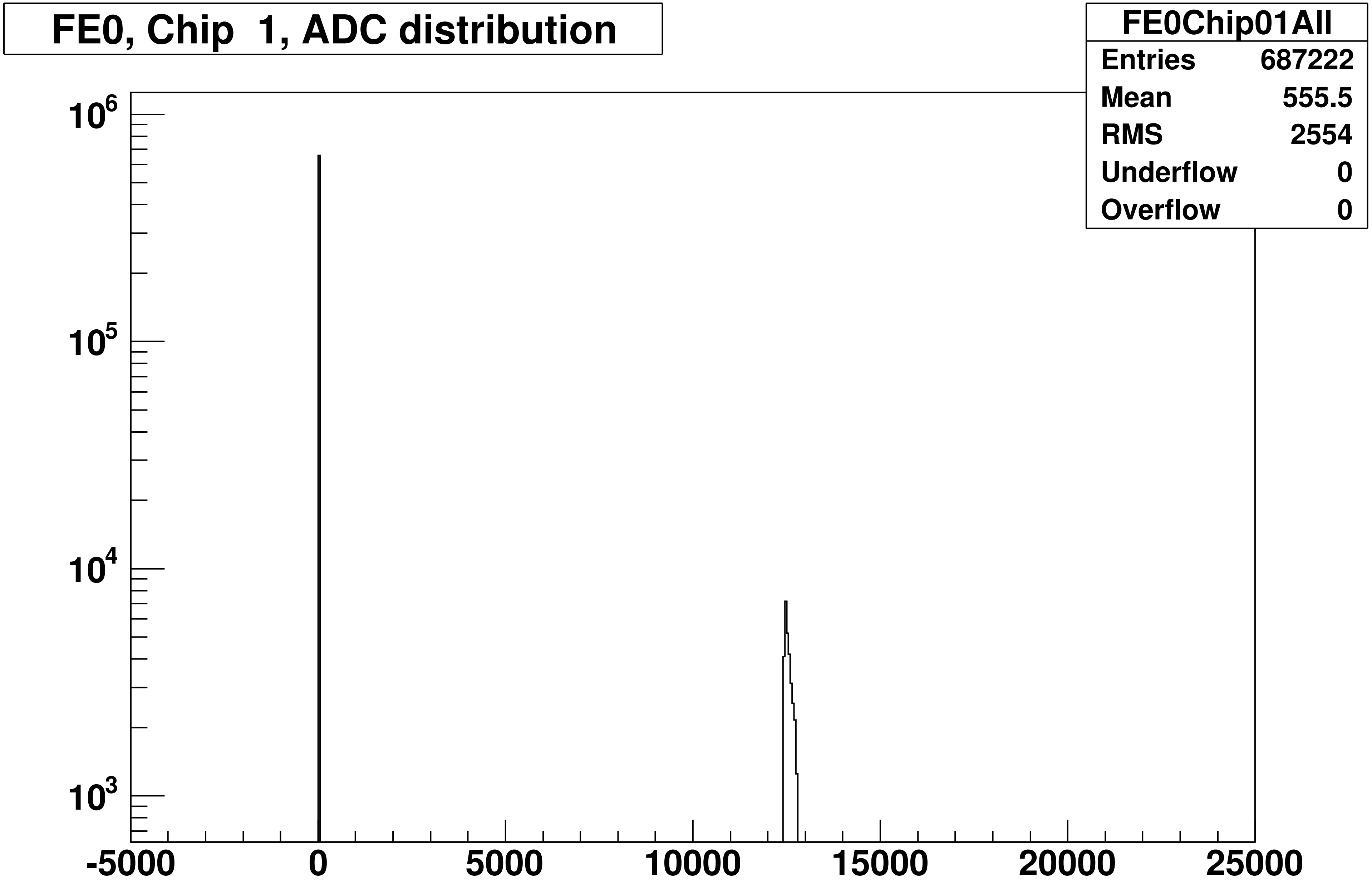
FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 687222
Mean 555.5
RMS 2554
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
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0
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10000
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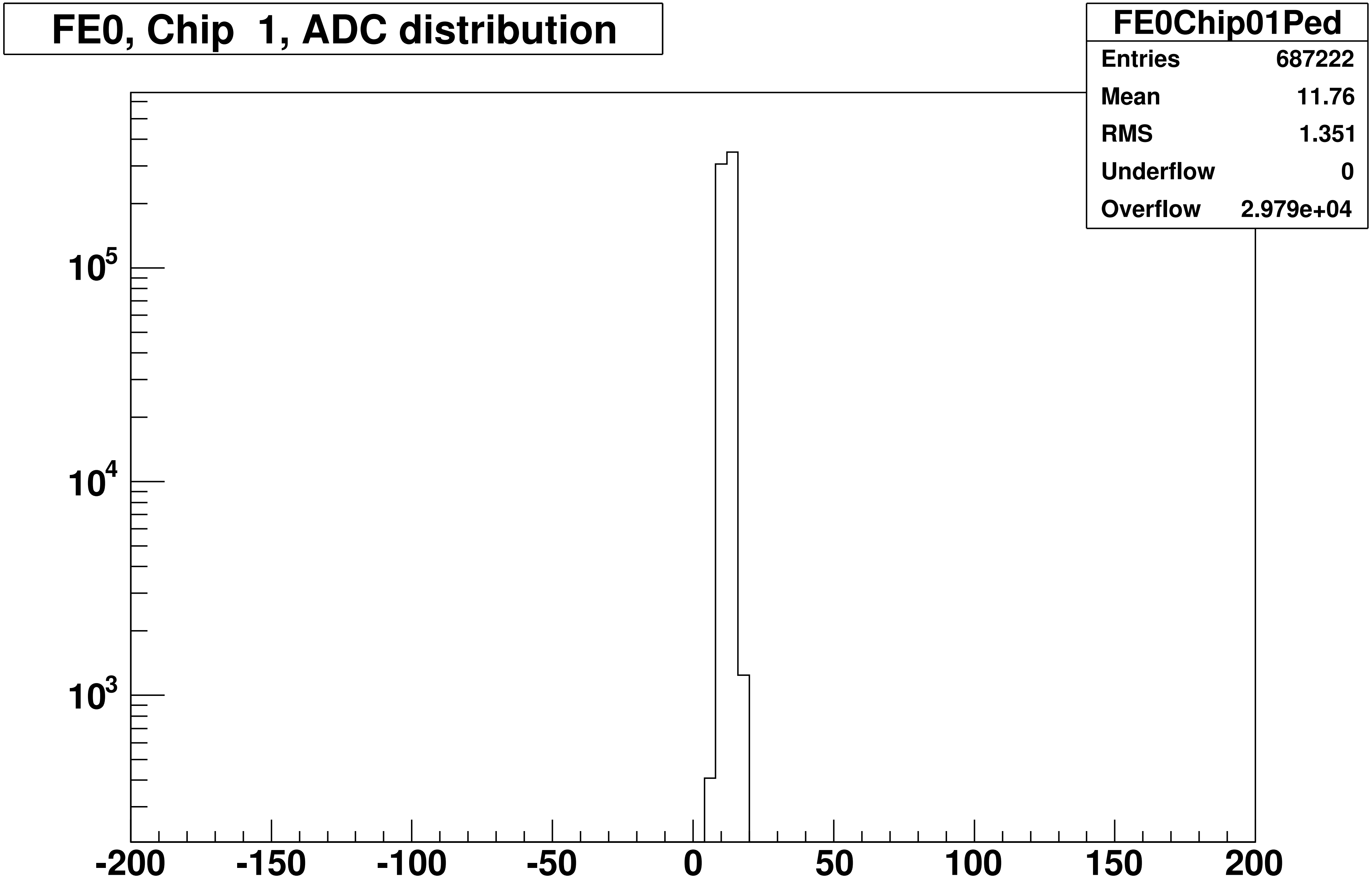

FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 687222
Mean 11.76
RMS 1.351
Underflow 0
Overflow 2.979e+04
10^5
10^4
10^3
-200
-150
-100
-50
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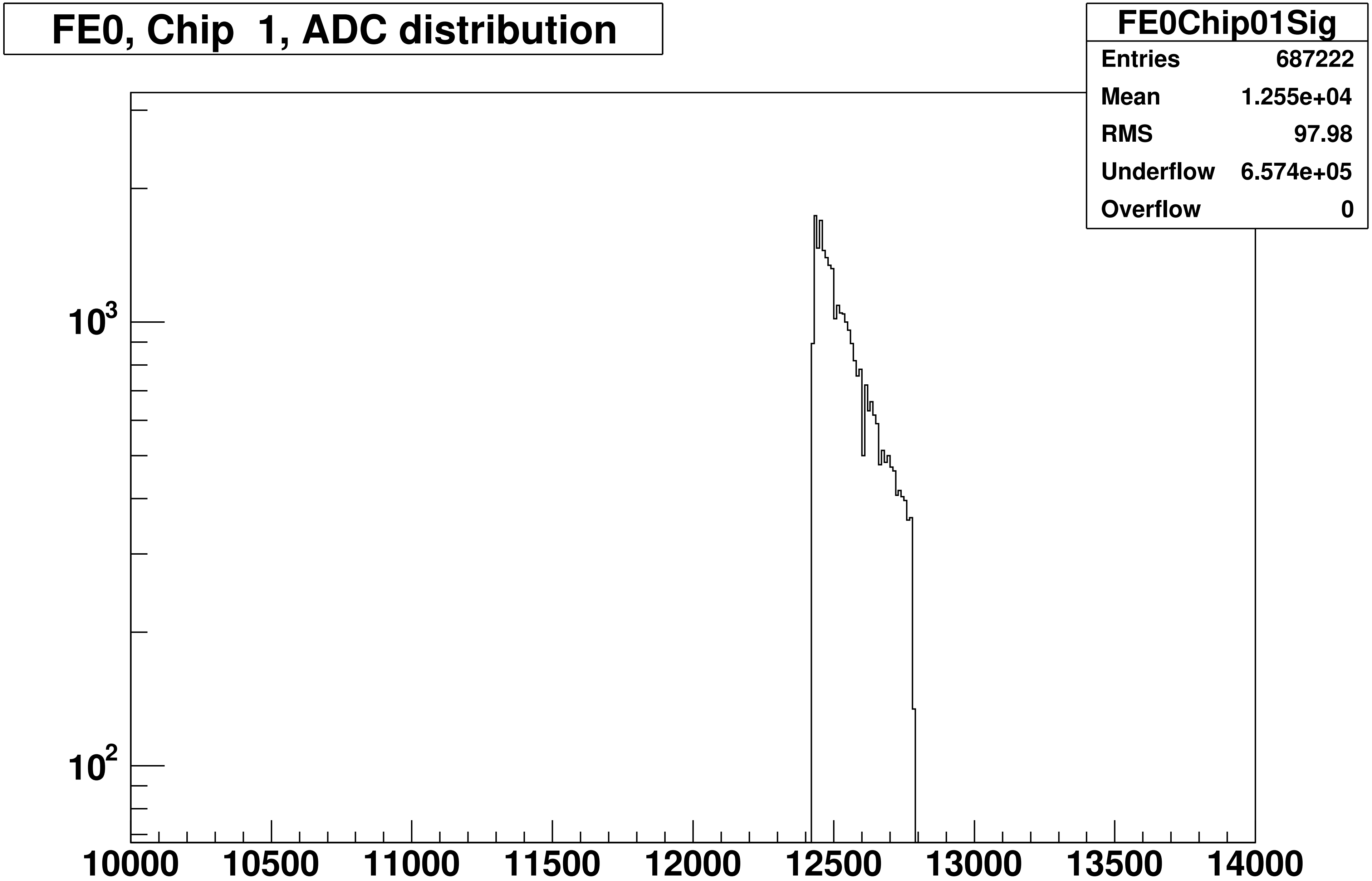
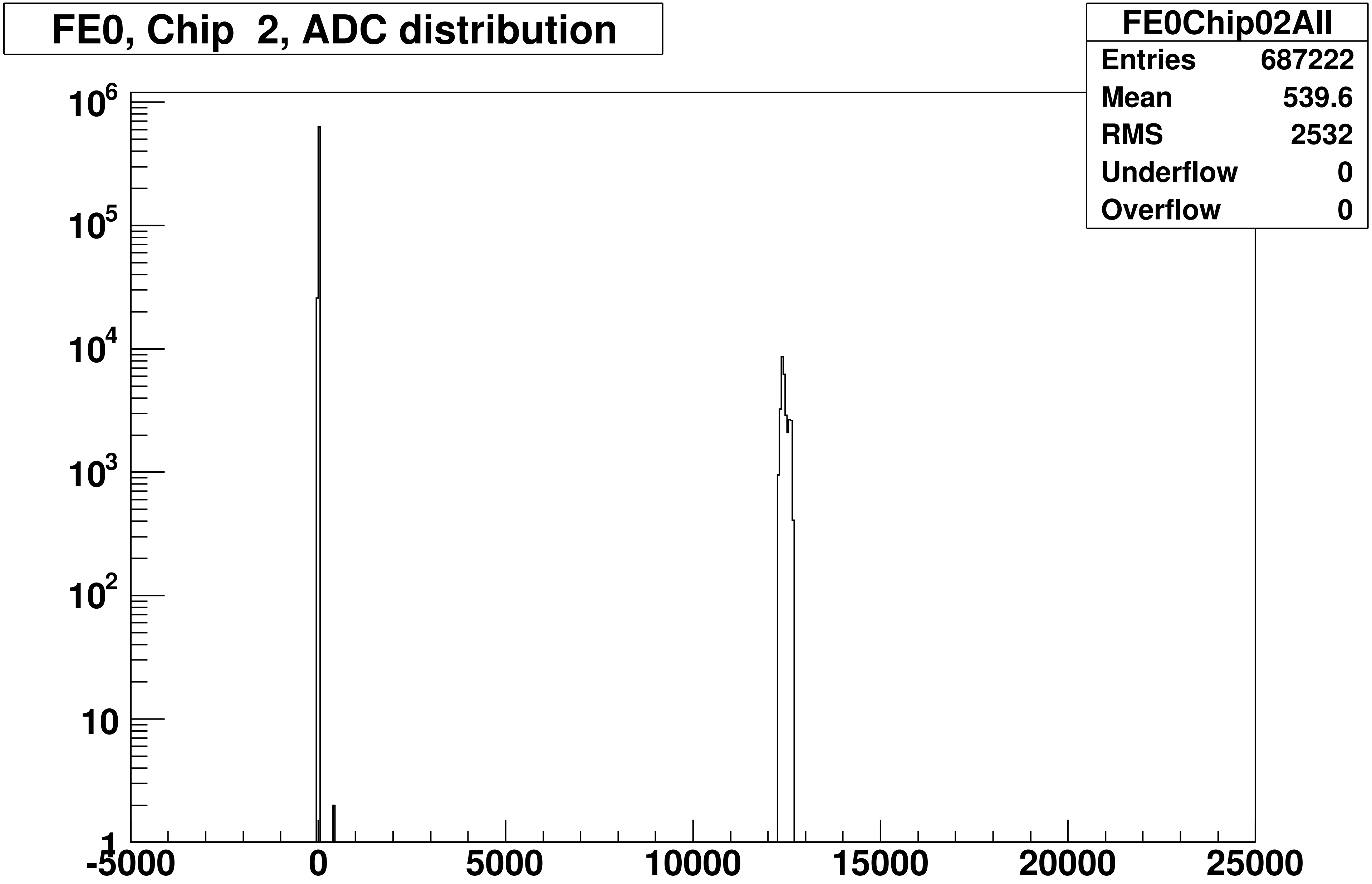


FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 687222
Mean 1.255e+04
RMS 97.98
Underflow 6.574e+05
Overflow 0
10^3
10^2
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 687222
Mean 539.6
RMS 2532
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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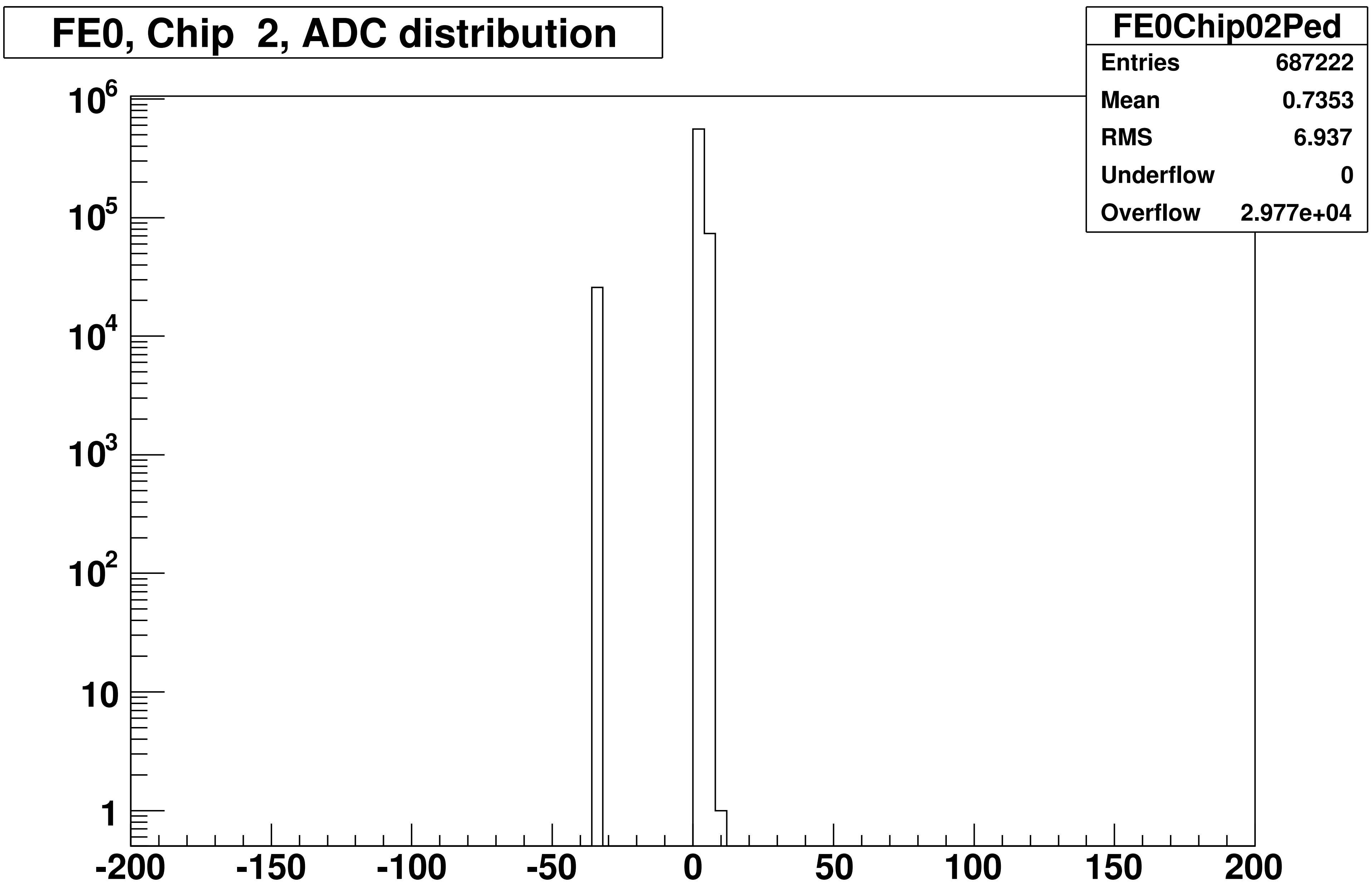
20000
25000


FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 687222
Mean 0.7353
RMS 6.937
Underflow 0
Overflow 2.977e+04
10^6
10^5
10^4
10^3
10^2
10
1
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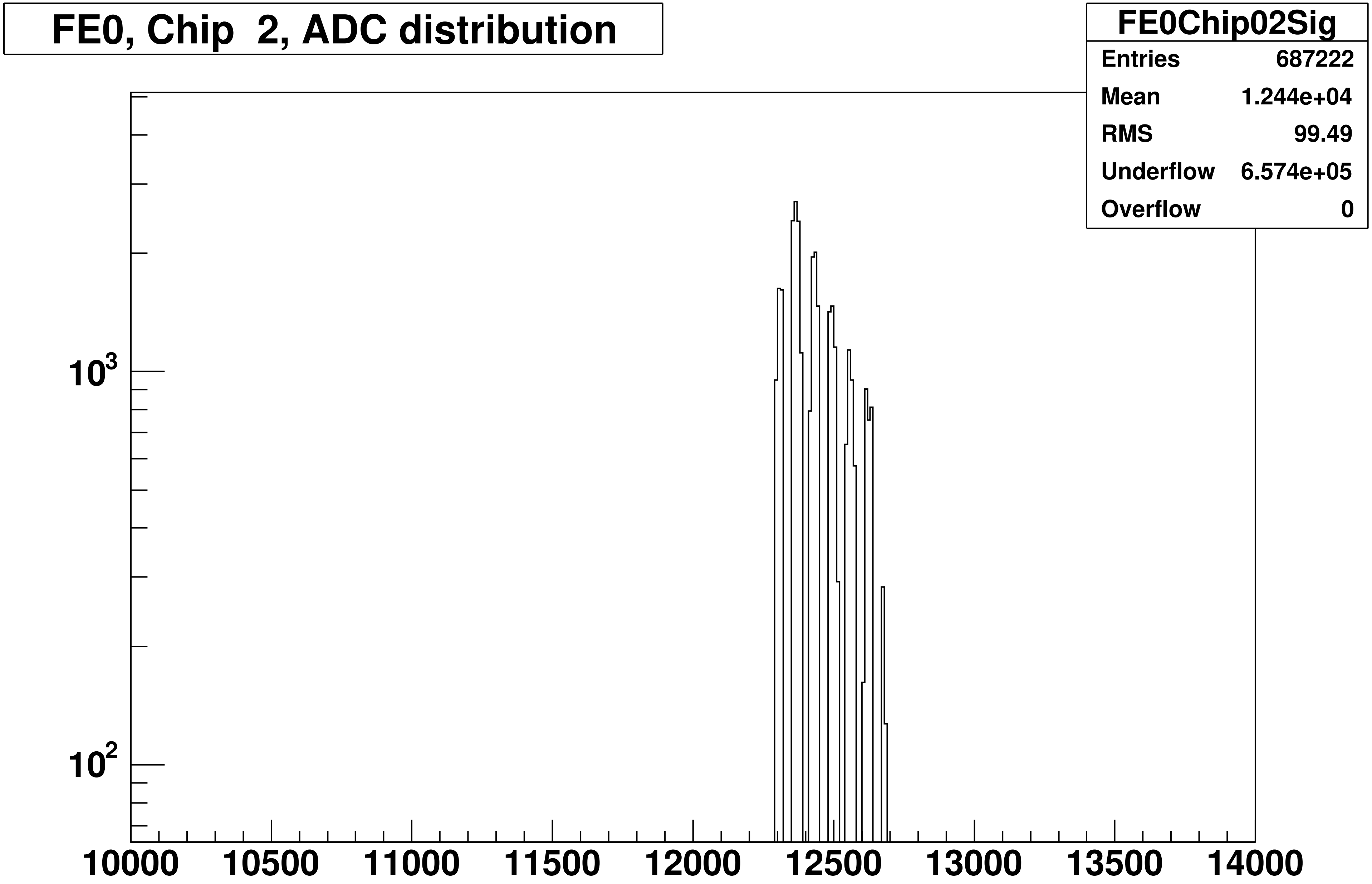
-100
-50
0
50
100
150
200


FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 687222
Mean 1.244e+04
RMS 99.49
Underflow 6.574e+05
Overflow 0
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10^2
10000
10500
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11500
12000
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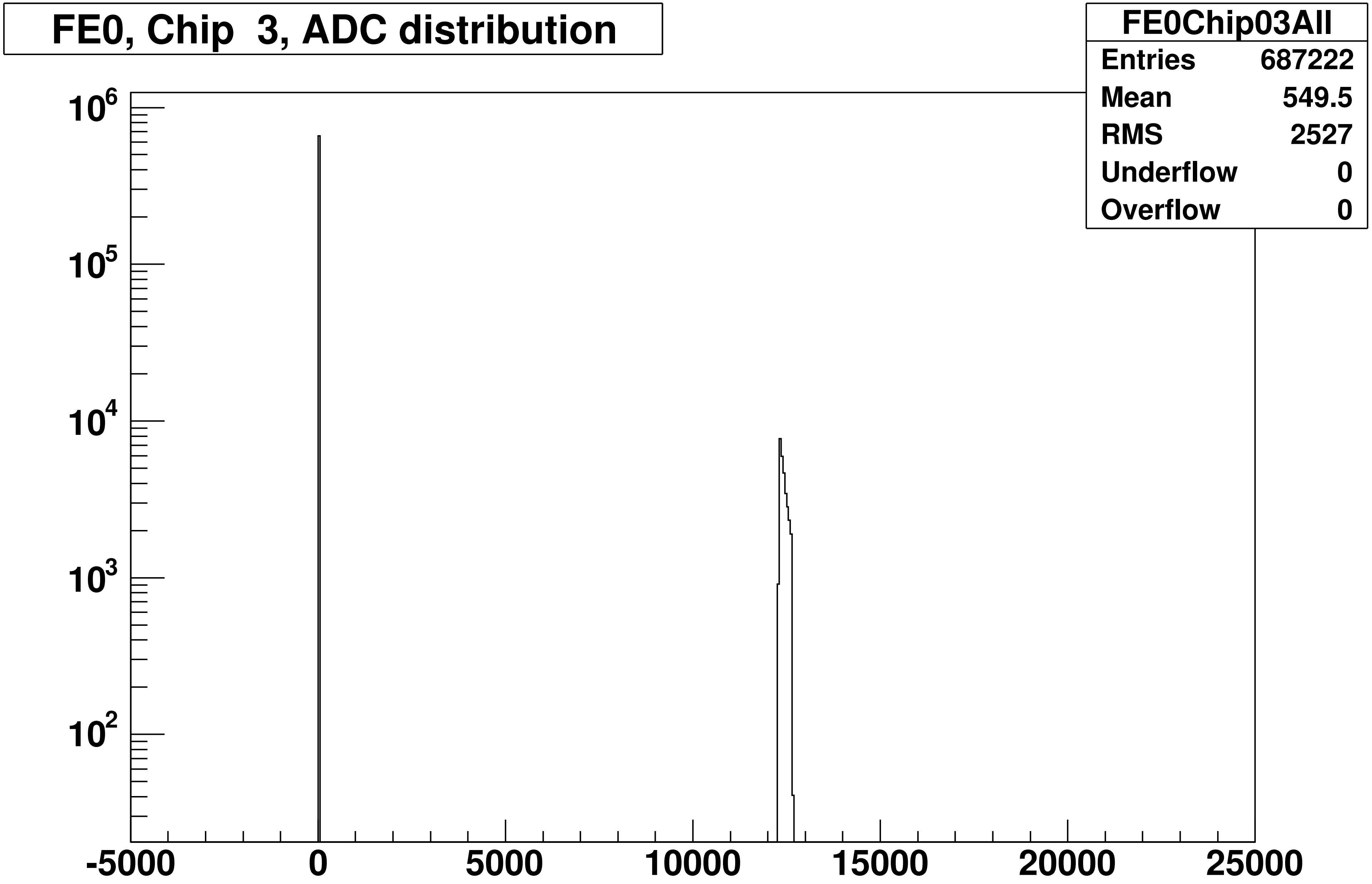
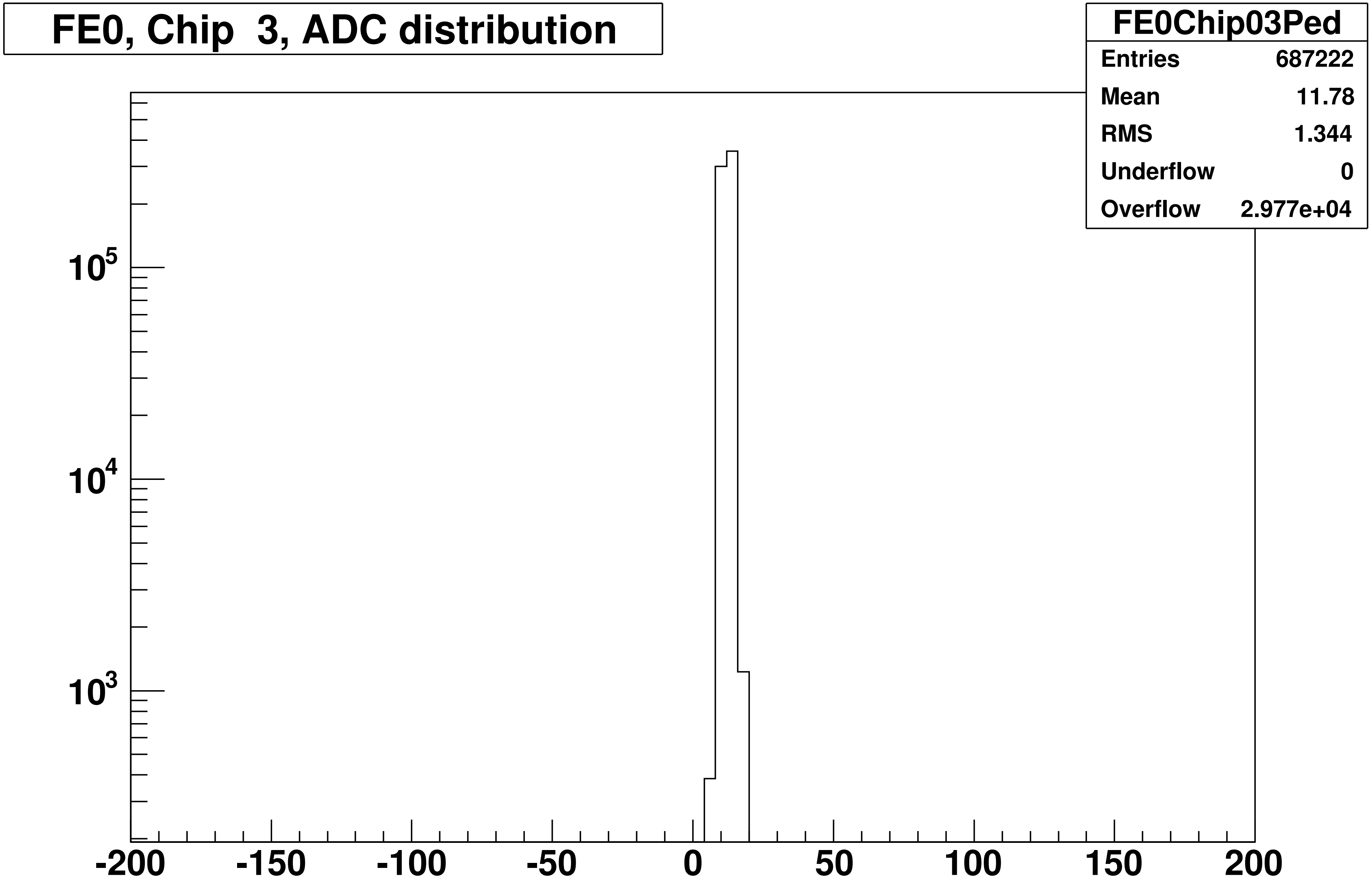
13000
13500
14000



FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 687222
Mean 549.5
RMS 2527
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 687222
Mean 11.78
RMS 1.344
Underflow 0
Overflow 2.977e+04
10^3
10^4
10^5
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-150
-100
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0
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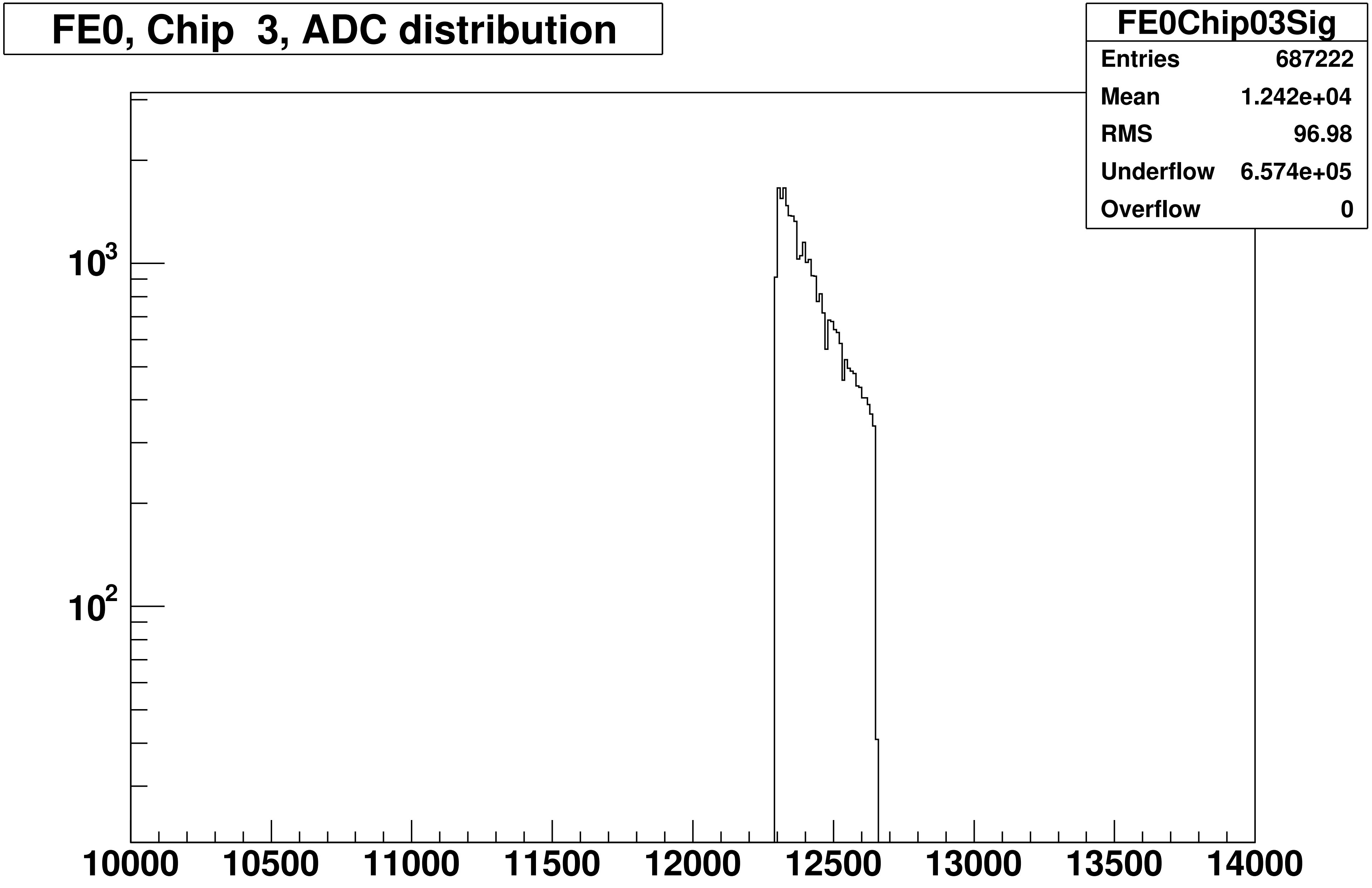
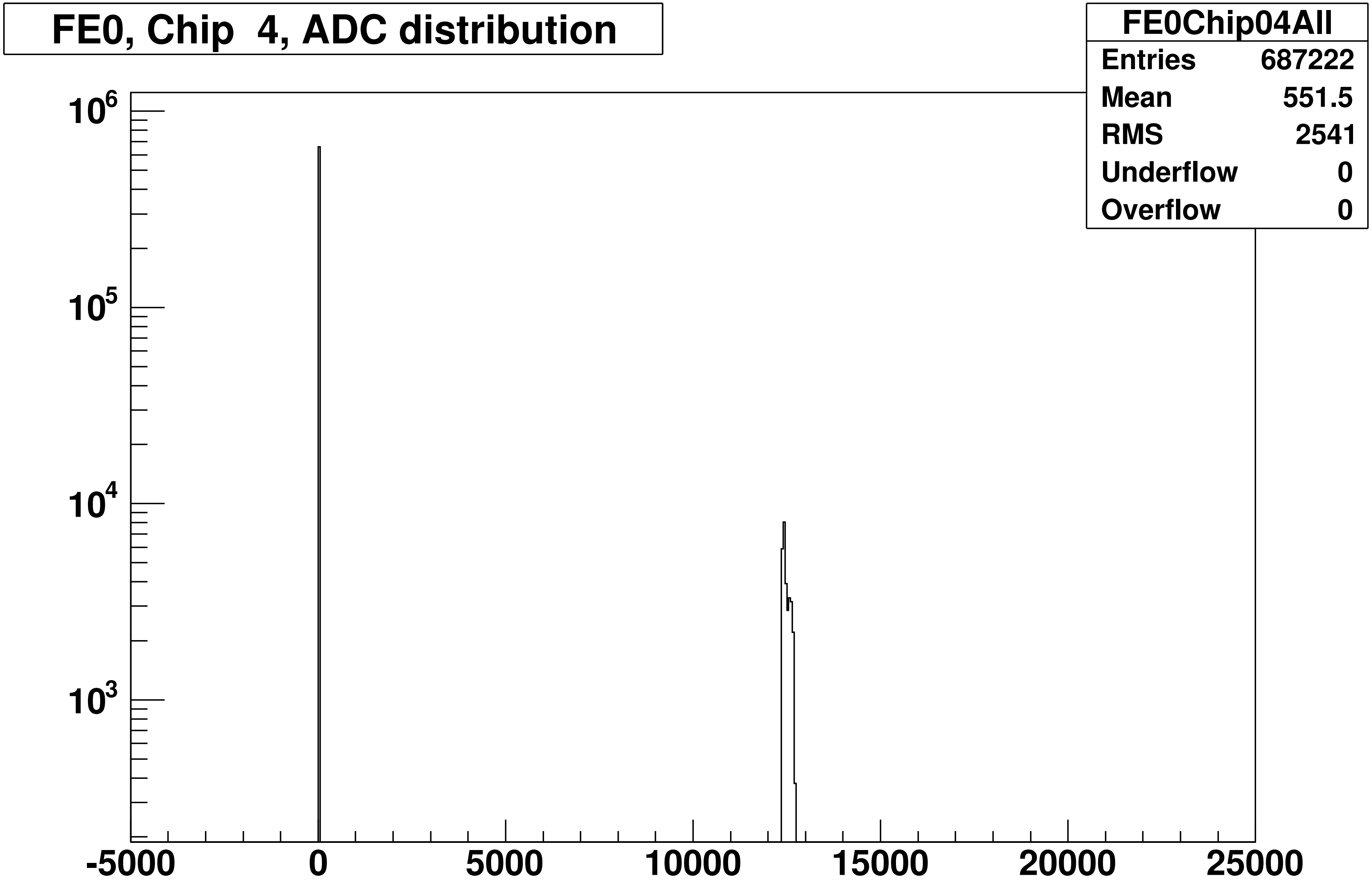
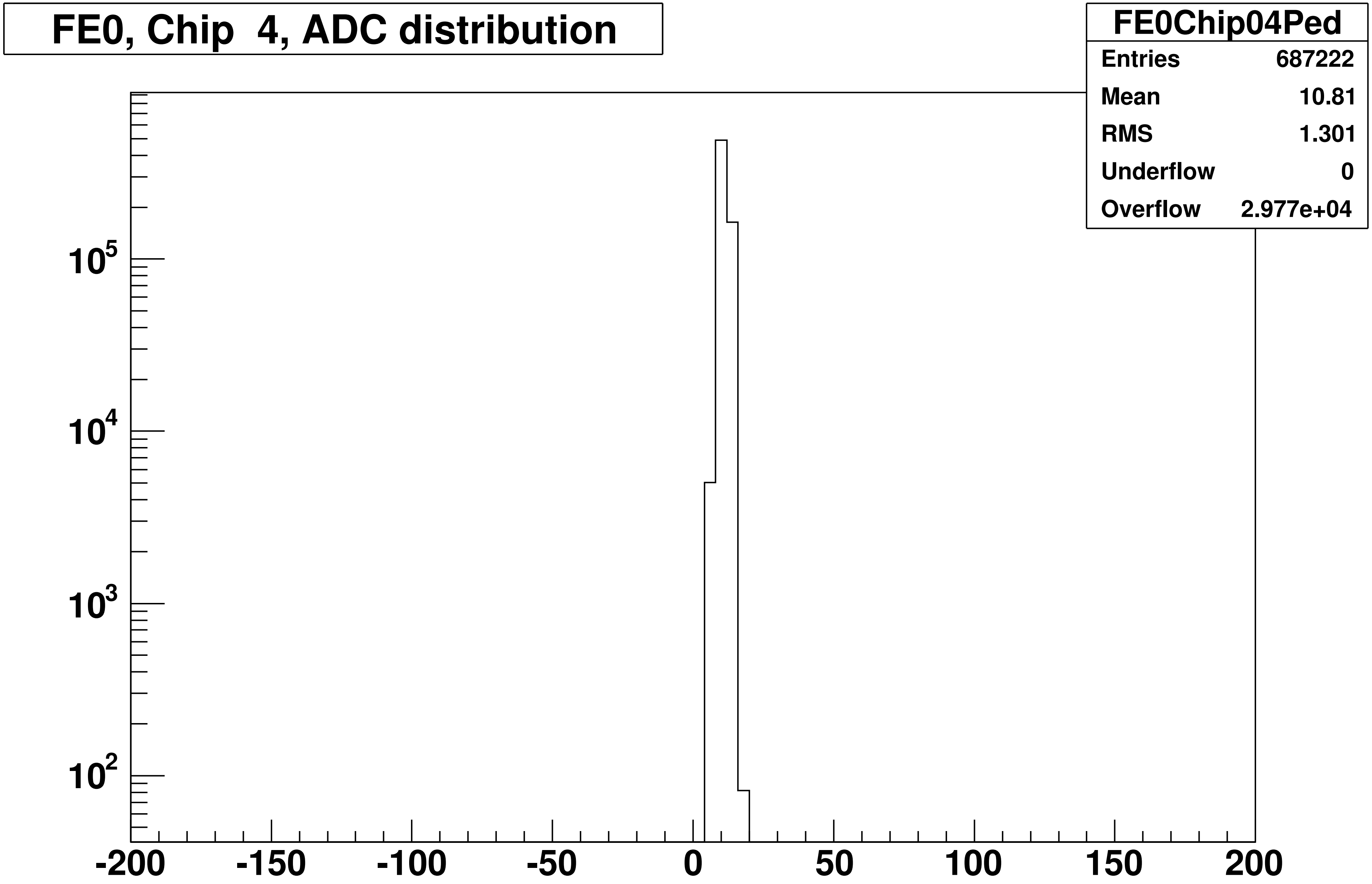
100
150
200



FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 687222
Mean 1.242e+04
RMS 96.98
Underflow 6.574e+05
Overflow 0
10^3
10^2
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 687222
Mean 551.5
RMS 2541
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 687222
Mean 10.81
RMS 1.301
Underflow 0
Overflow 2.977e+04
10^5
10^4
10^3
10^2
-200
-150
-100
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0
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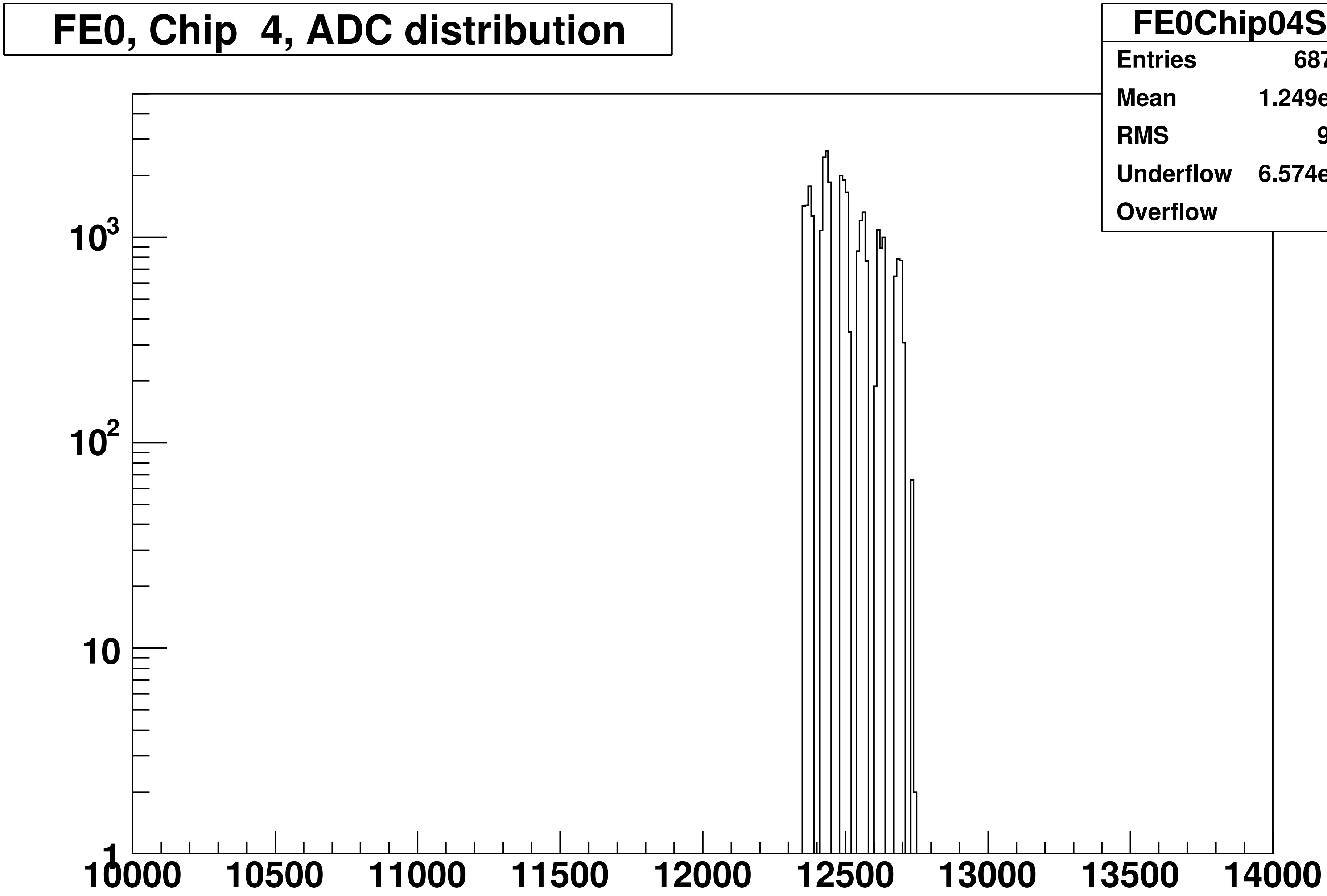
100
150
200


FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 687222
Mean 1.249e+04
RMS 99.51
Underflow 6.574e+05
Overflow 0
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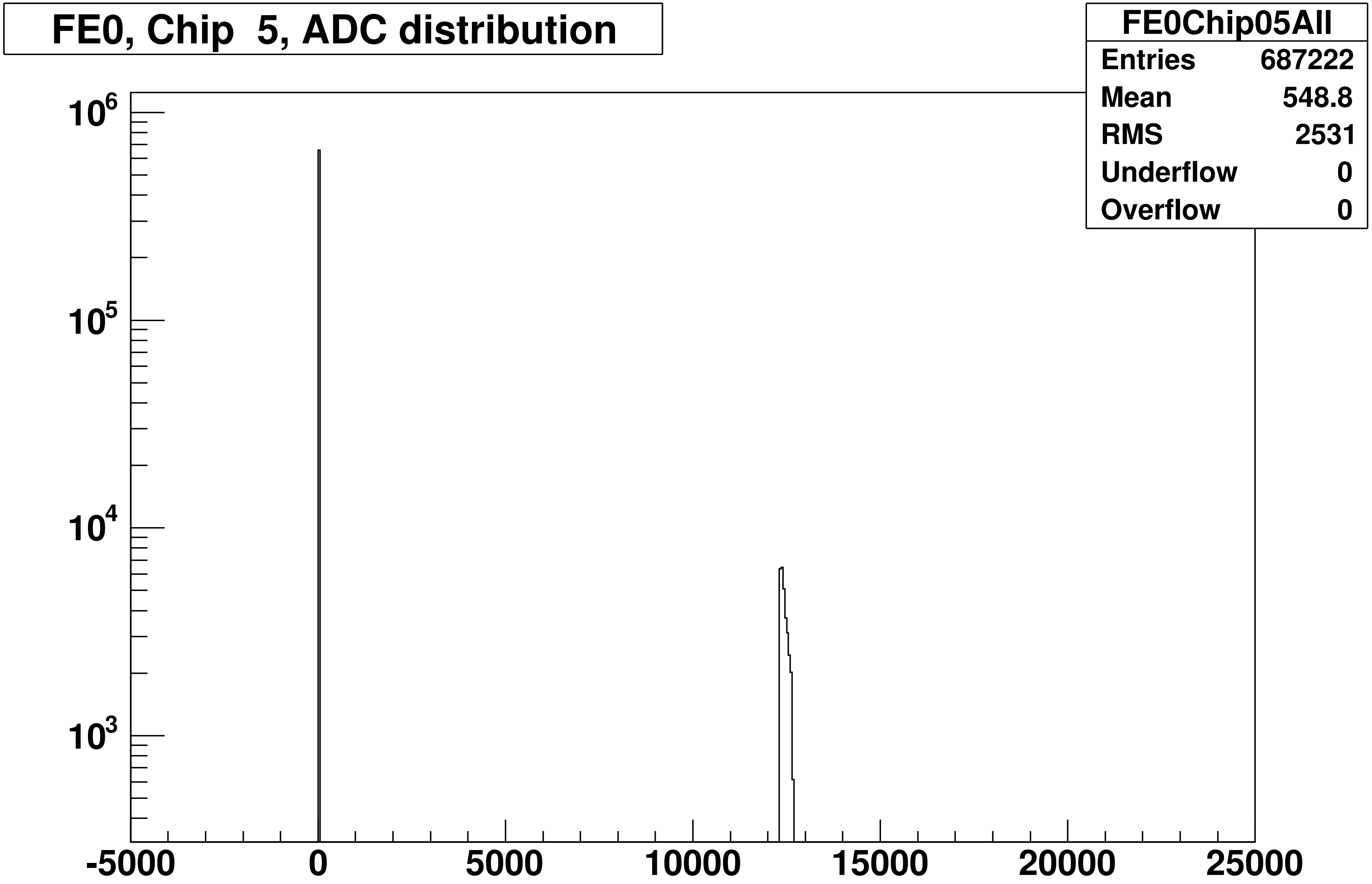
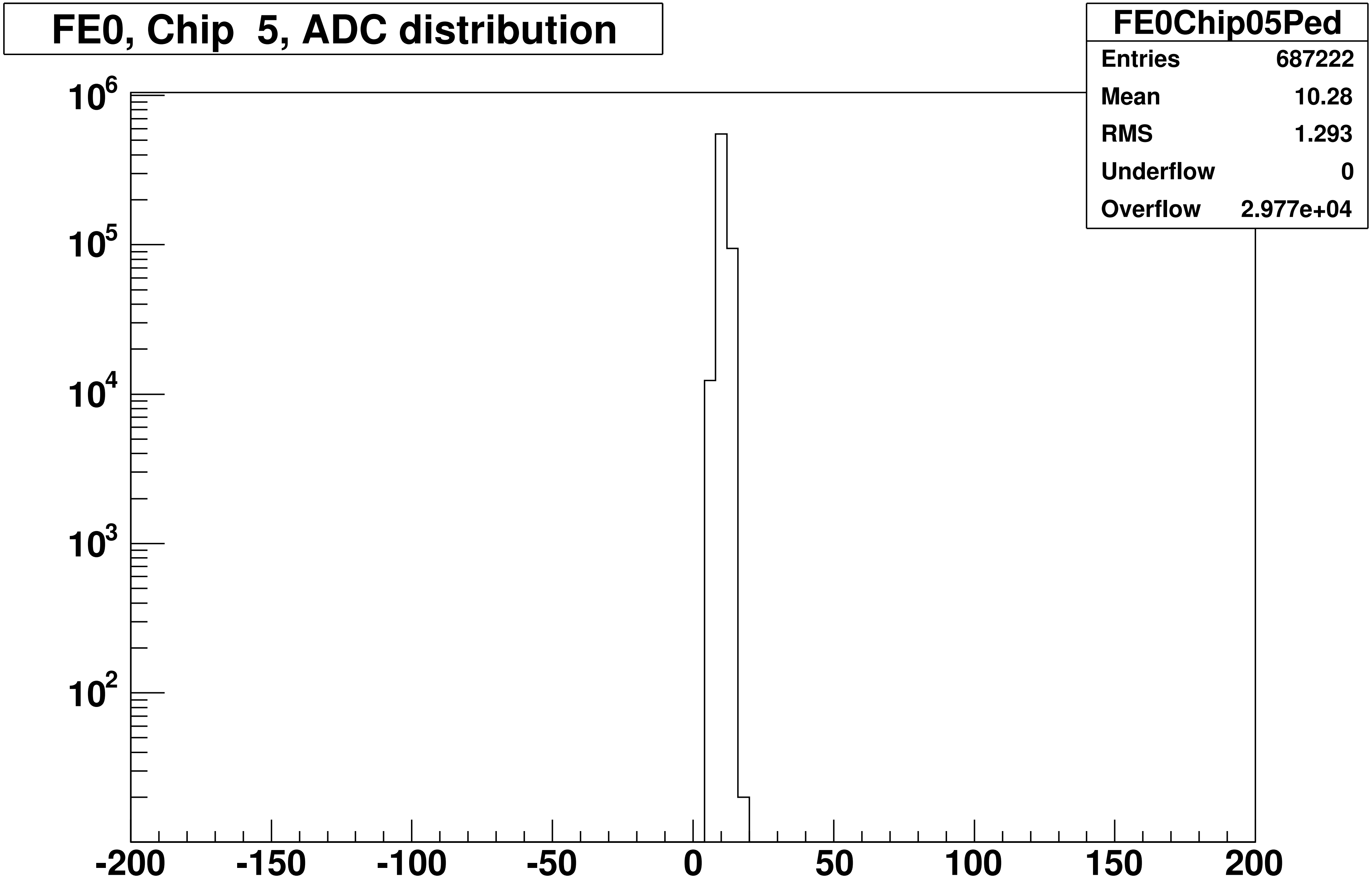
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000



FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 687222
Mean 548.8
RMS 2531
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 687222
Mean 10.28
RMS 1.293
Underflow 0
Overflow 2.977e+04
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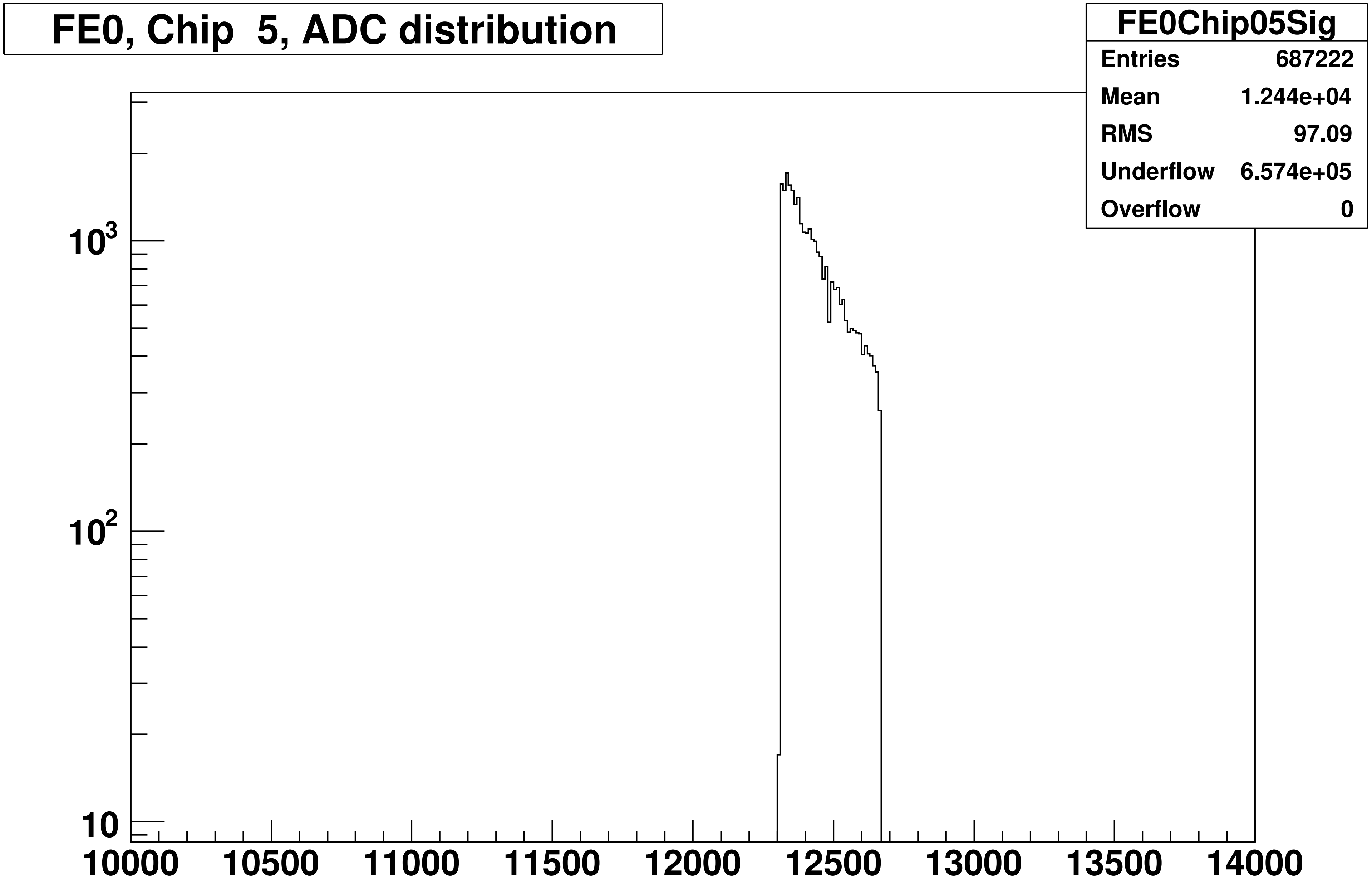
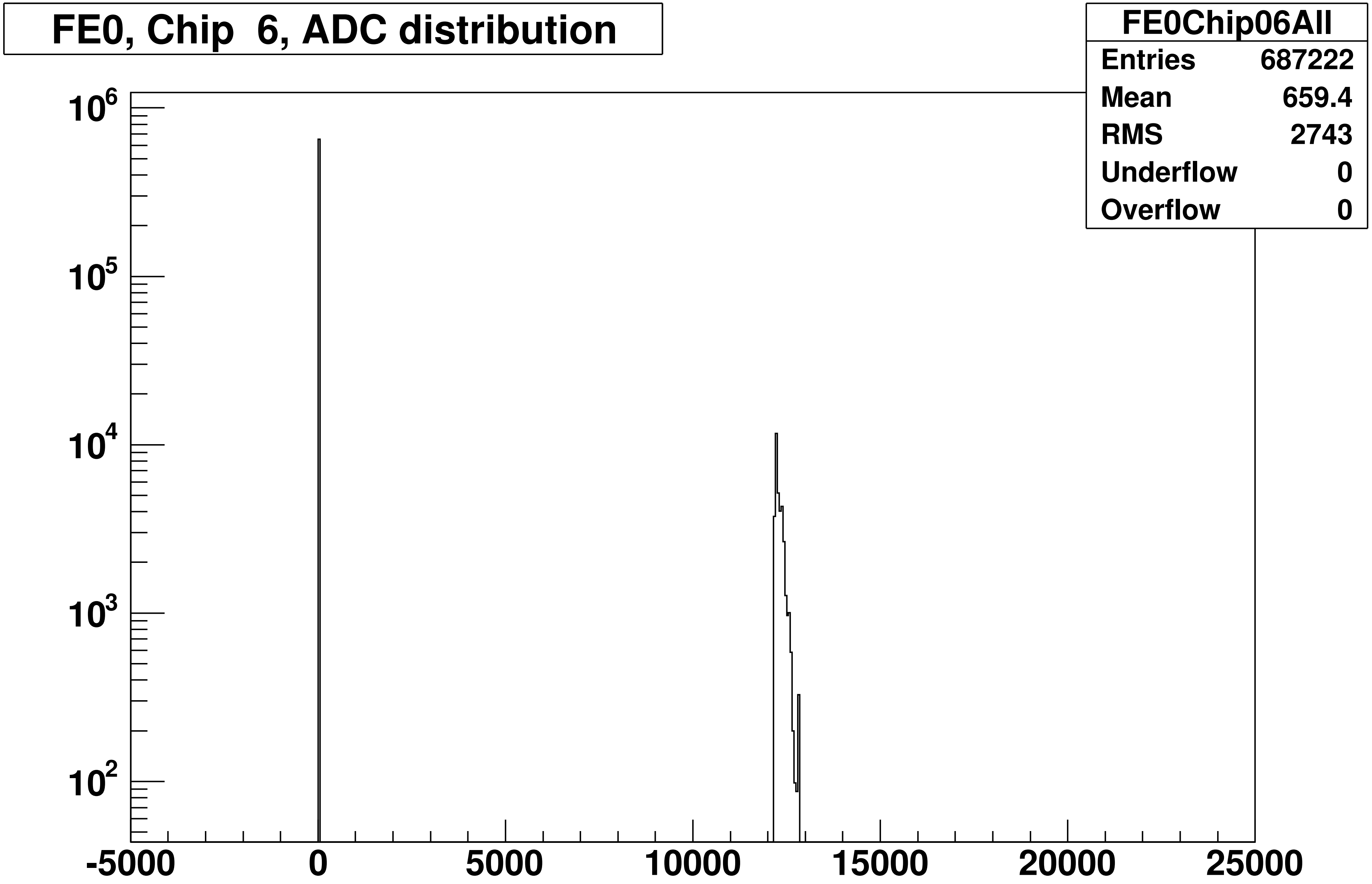
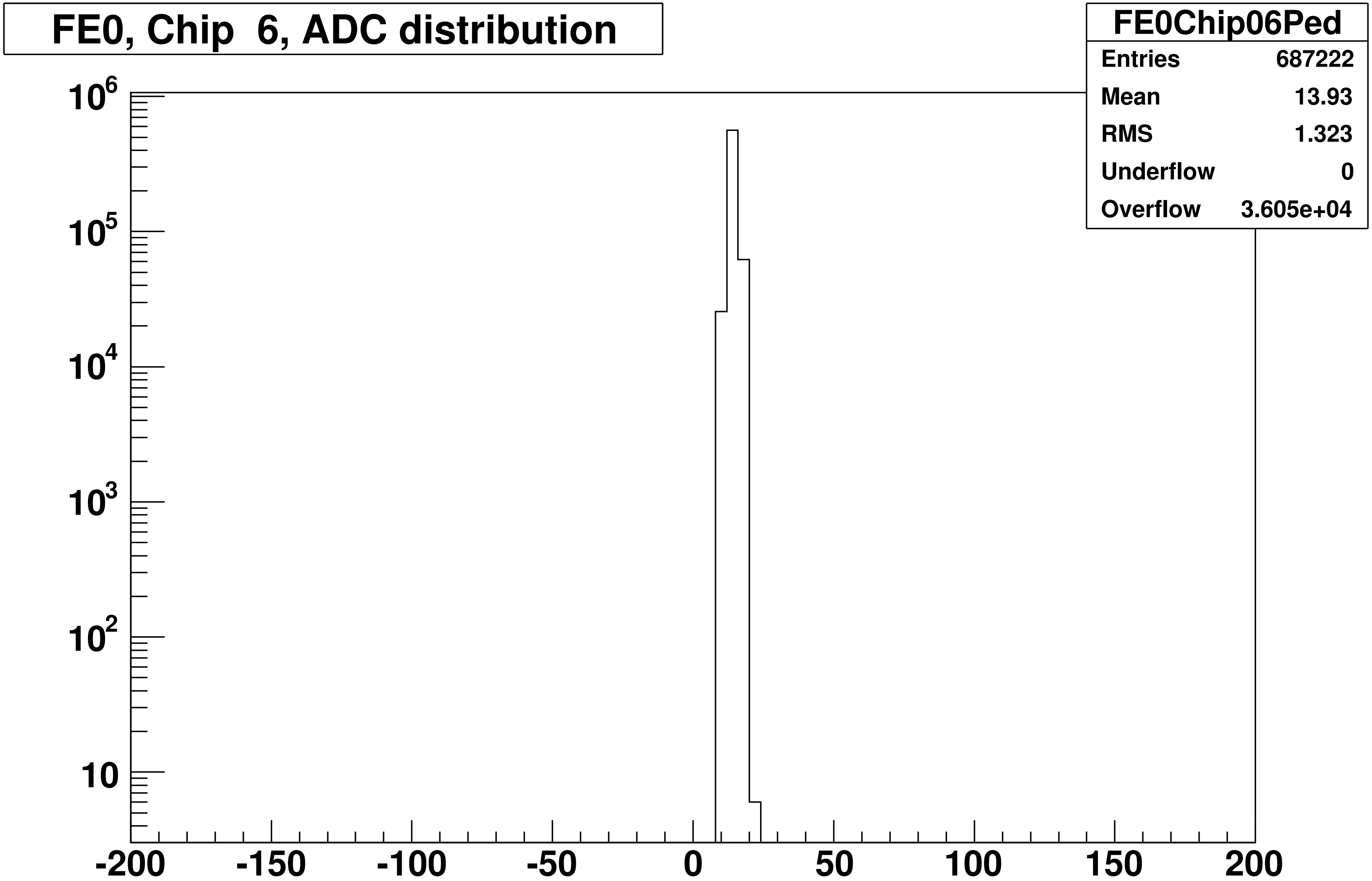
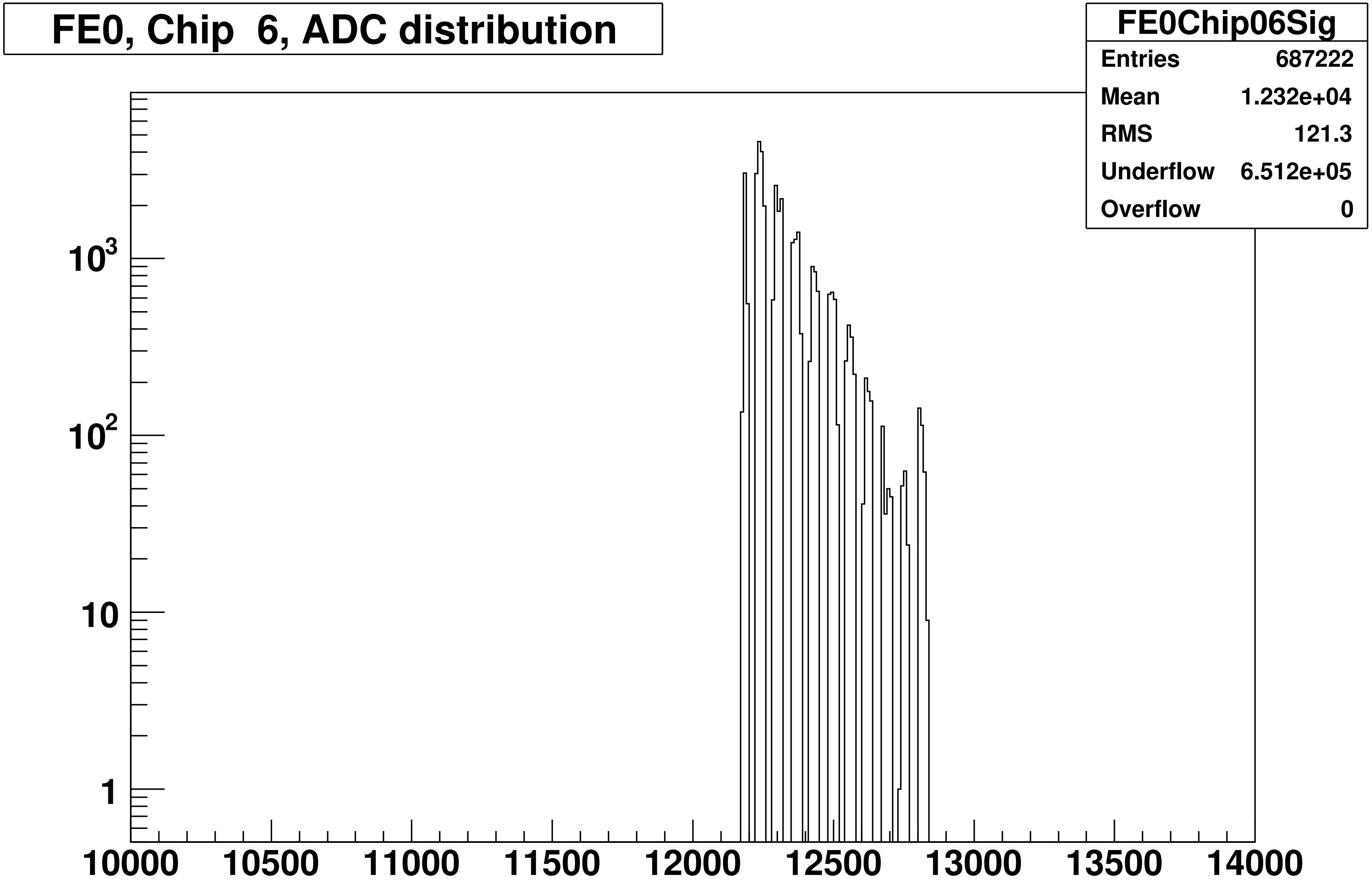
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 687222
Mean 1.244e+04
RMS 97.09
Underflow 6.574e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 687222
Mean 659.4
RMS 2743
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 687222
Mean 13.93
RMS 1.323
Underflow 0
Overflow 3.605e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 687222
Mean 1.232e+04
RMS 121.3
Underflow 6.512e+05
Overflow 0
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10^2
10
1
10000
10500
11000
11500
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12500
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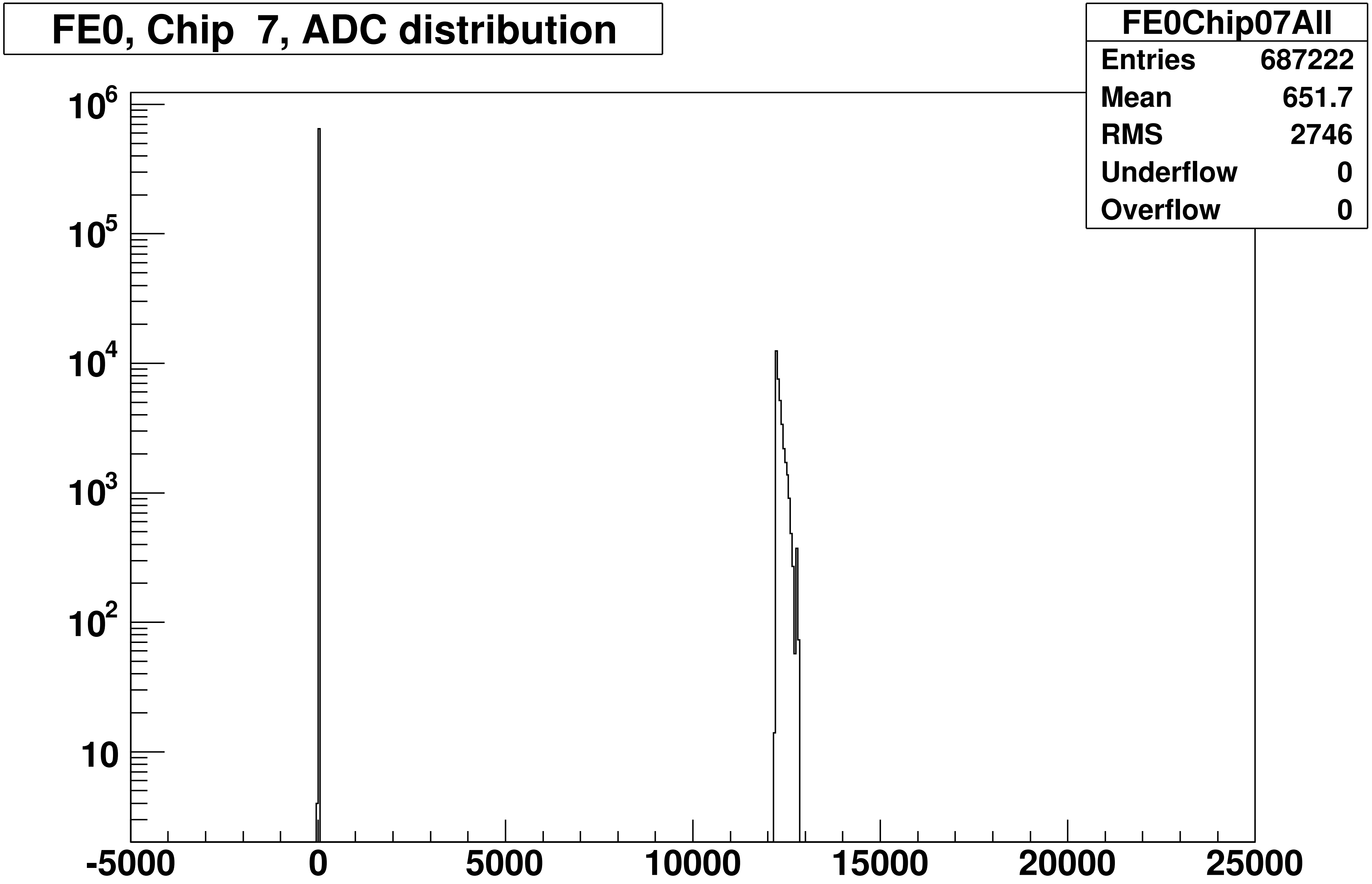
FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 687222
Mean 651.7
RMS 2746
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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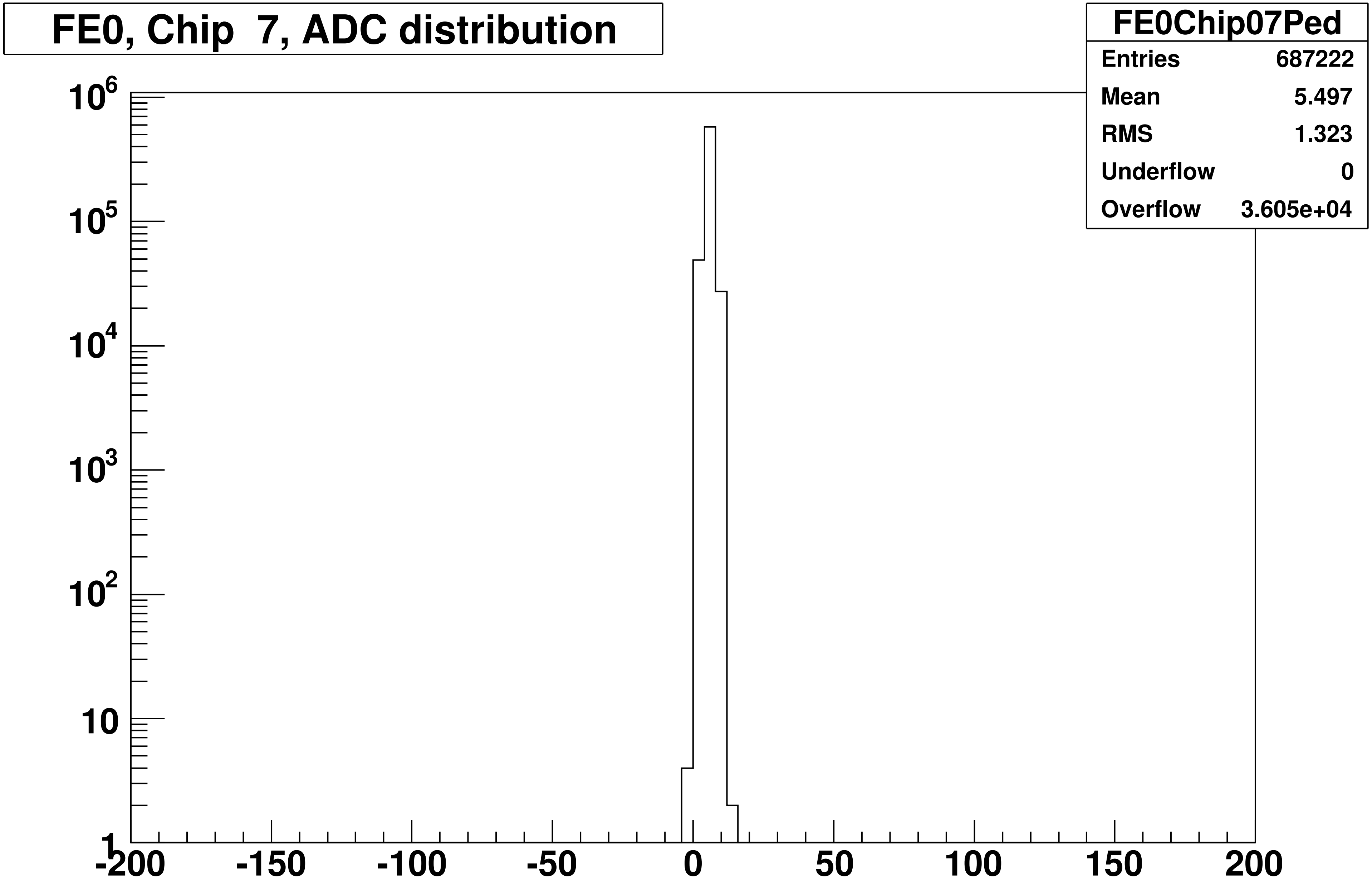
20000
25000


FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 687222
Mean 5.497
RMS 1.323
Underflow 0
Overflow 3.605e+04
10^6
10^5
10^4
10^3
10^2
10
1
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-100
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0
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150
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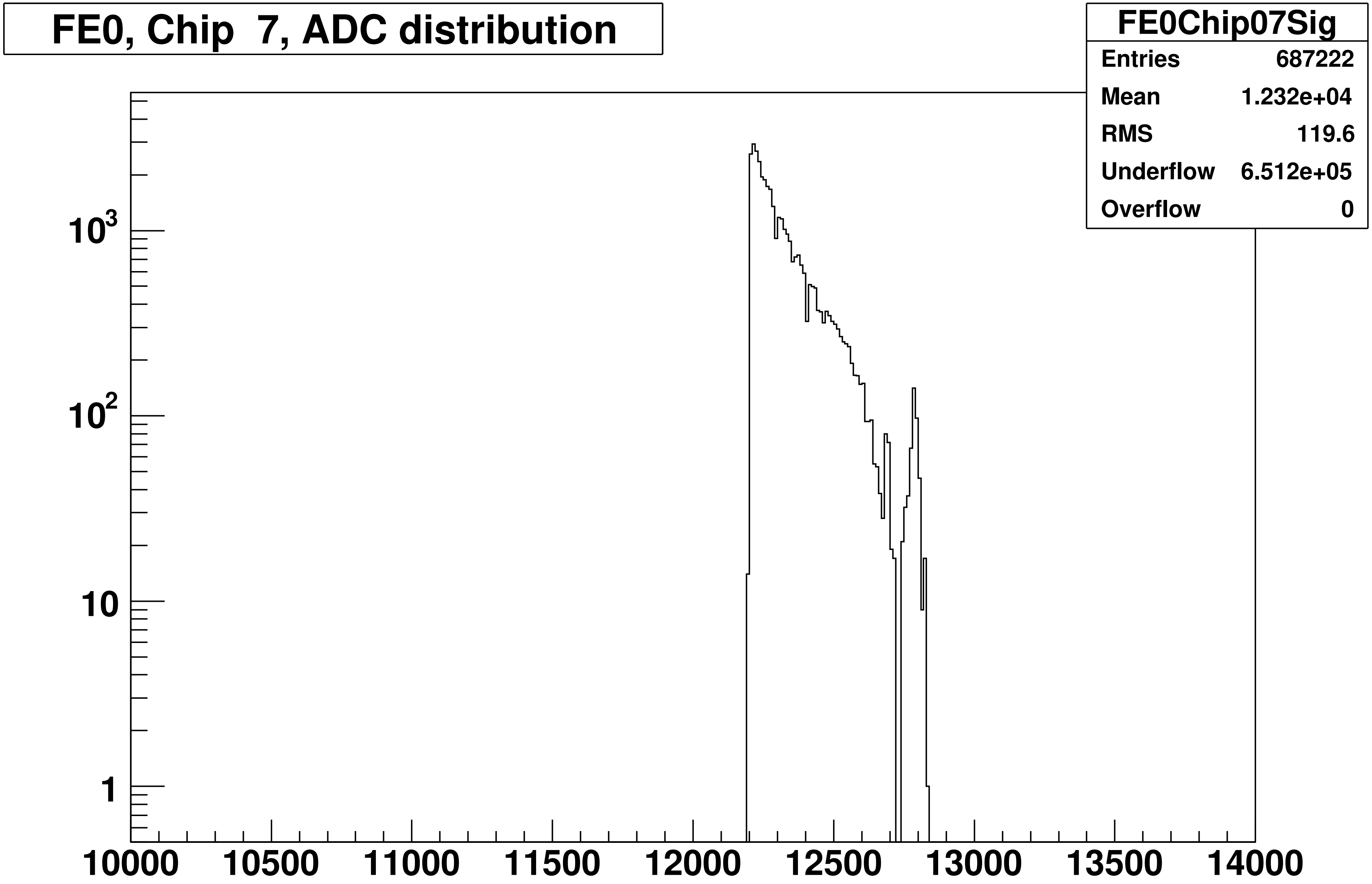

FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 687222
Mean 1.232e+04
RMS 119.6
Underflow 6.512e+05
Overflow 0
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10^2
10
1
10000
10500
11000
11500
12000
12500
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13500
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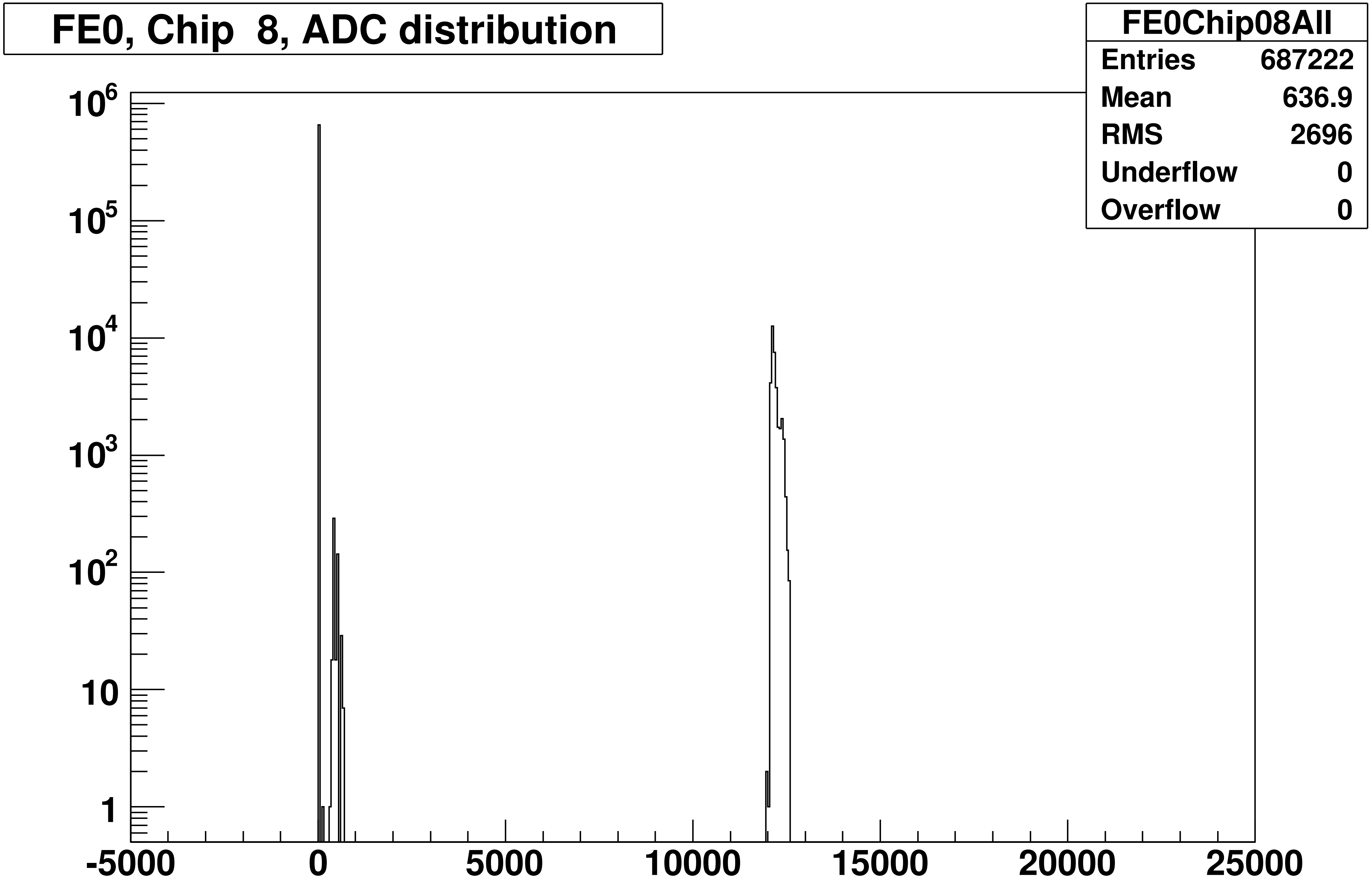

FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 687222
Mean 636.9
RMS 2696
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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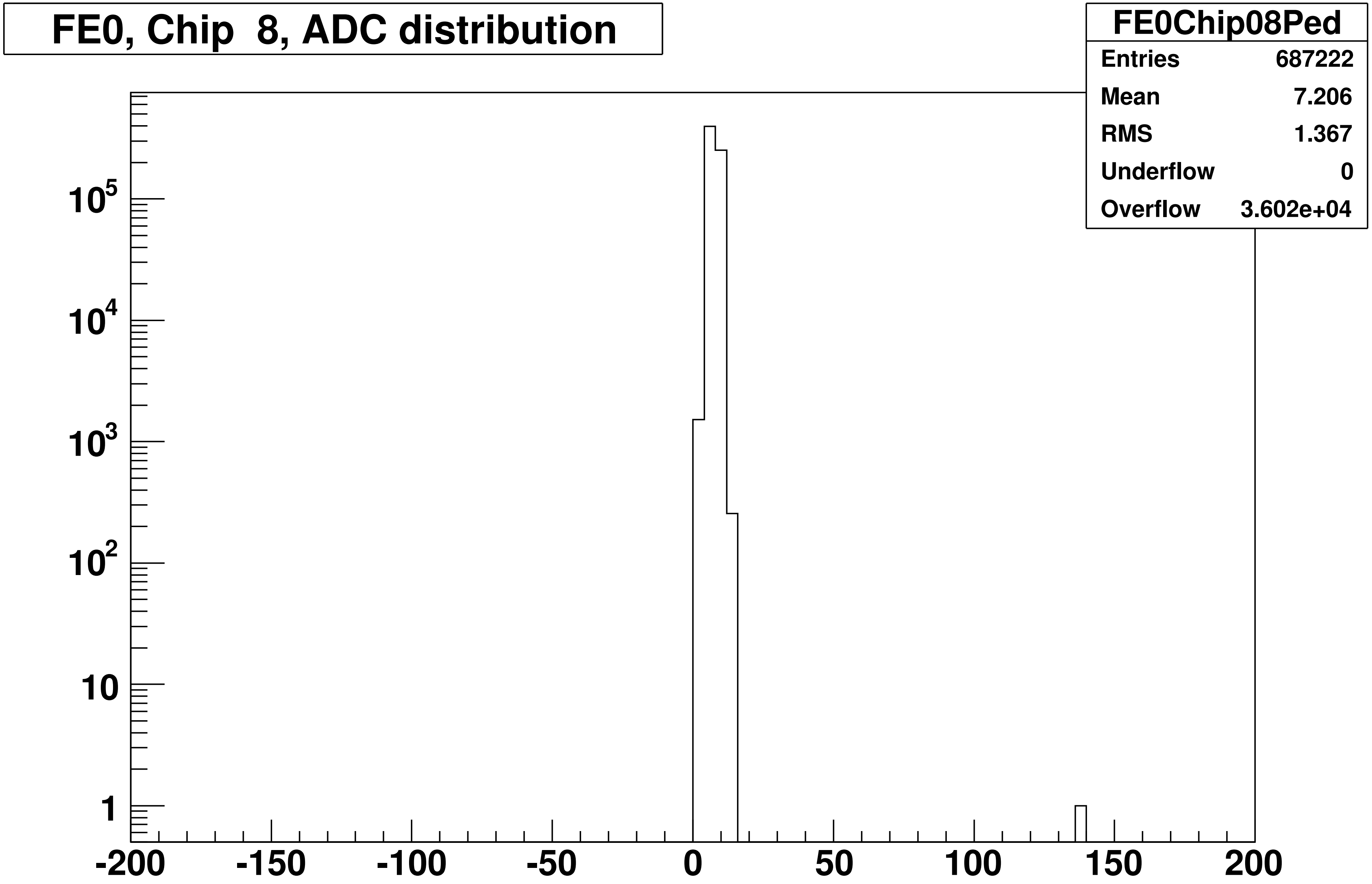
10000
15000
20000
25000


FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 687222
Mean 7.206
RMS 1.367
Underflow 0
Overflow 3.602e+04
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10^4
10^3
10^2
10
1
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-100
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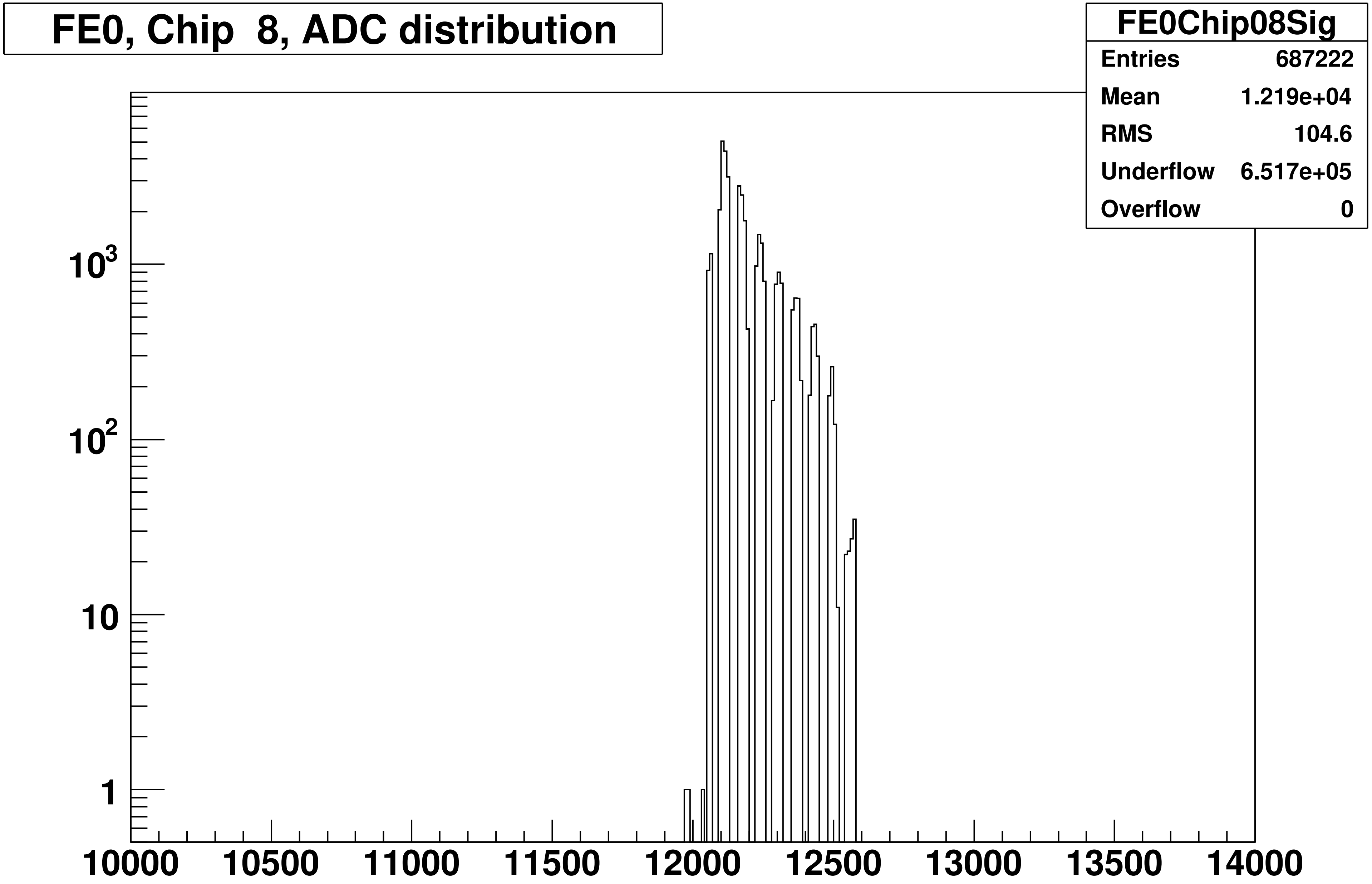
150
200



FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 687222
Mean 1.219e+04
RMS 104.6
Underflow 6.517e+05
Overflow 0
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10^2
10
1
10000
10500
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11500
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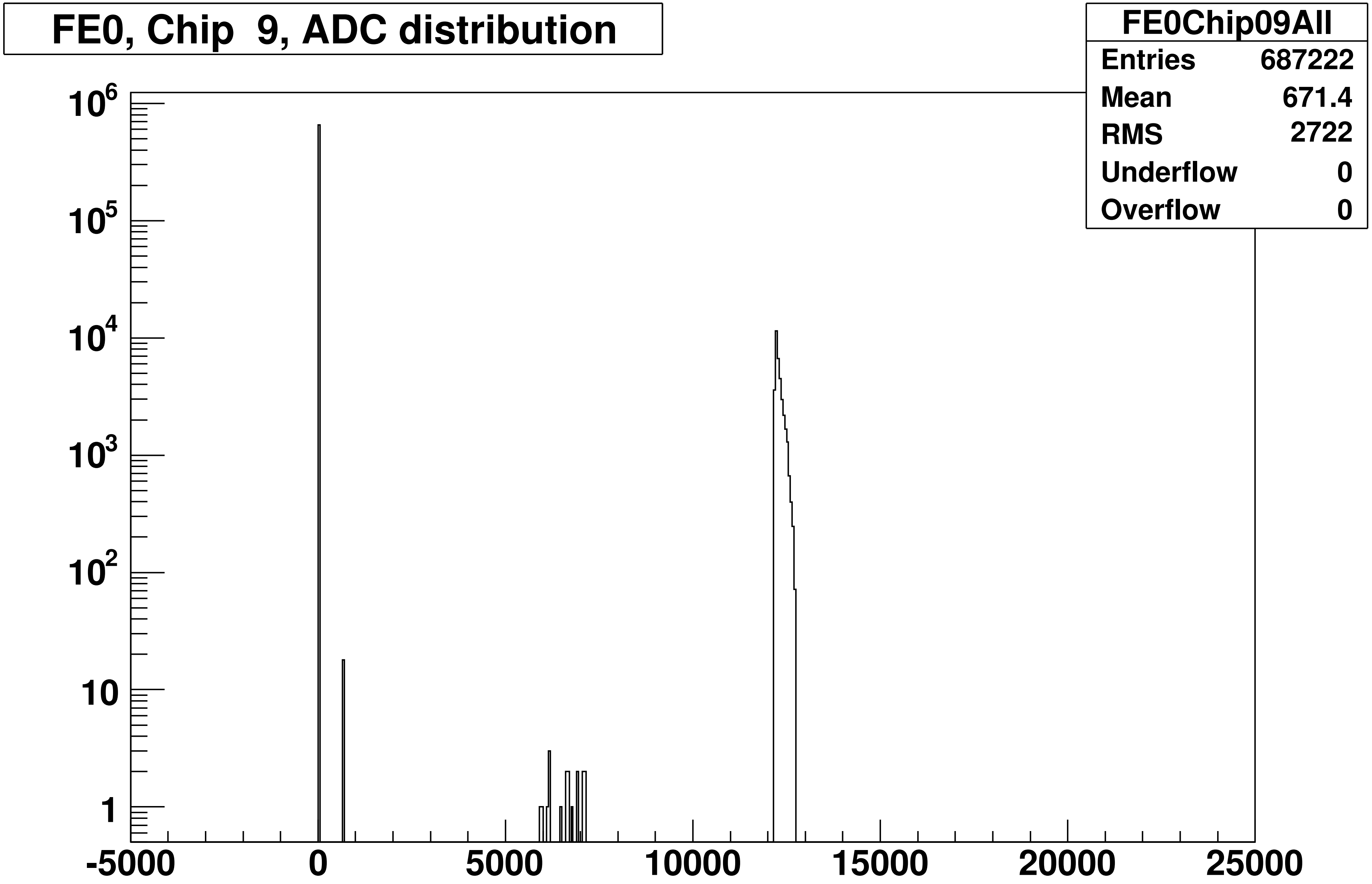
FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 687222
Mean 671.4
RMS 2722
Underflow 0
Overflow 0
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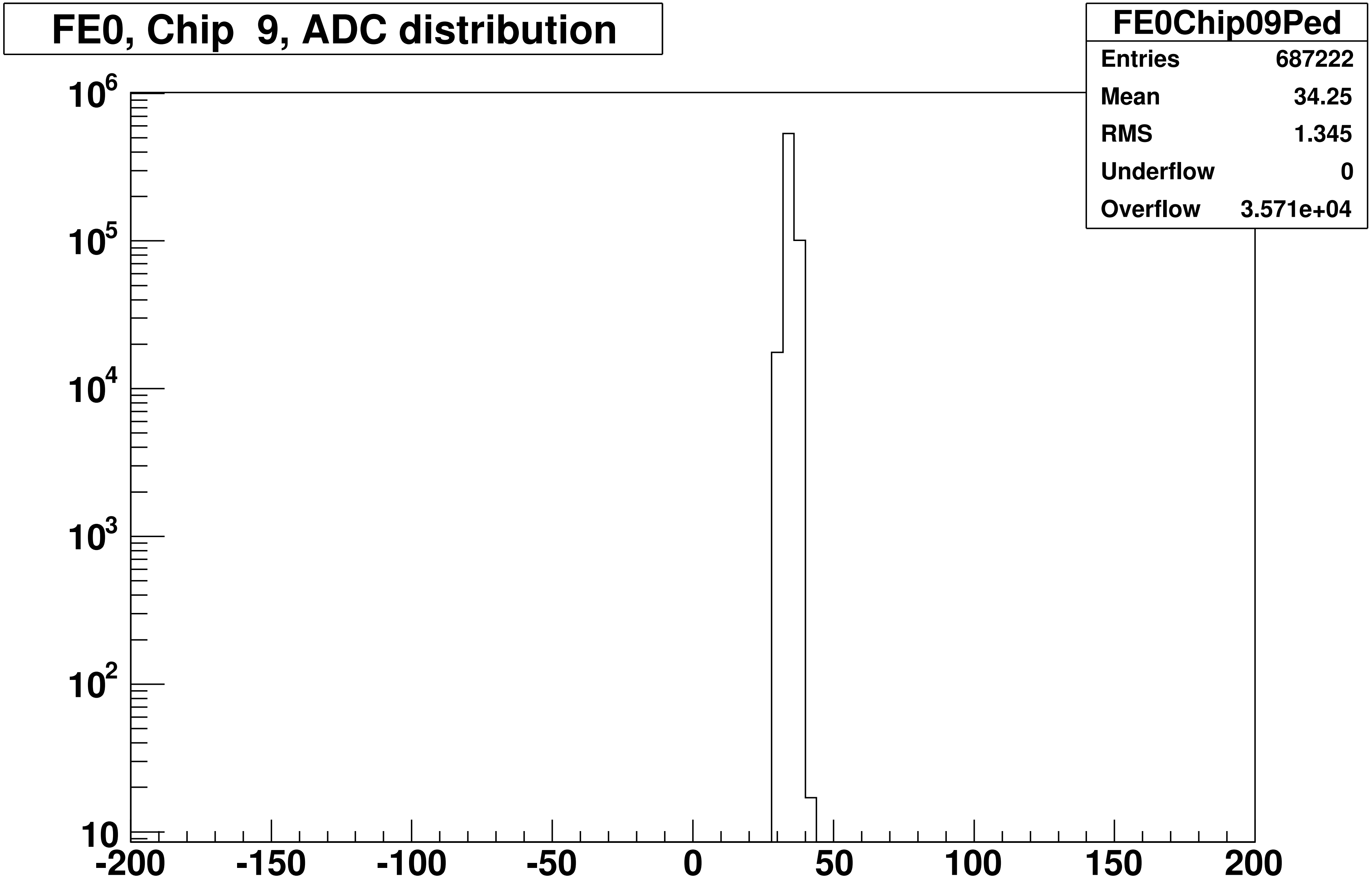
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 687222
Mean 34.25
RMS 1.345
Underflow 0
Overflow 3.571e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
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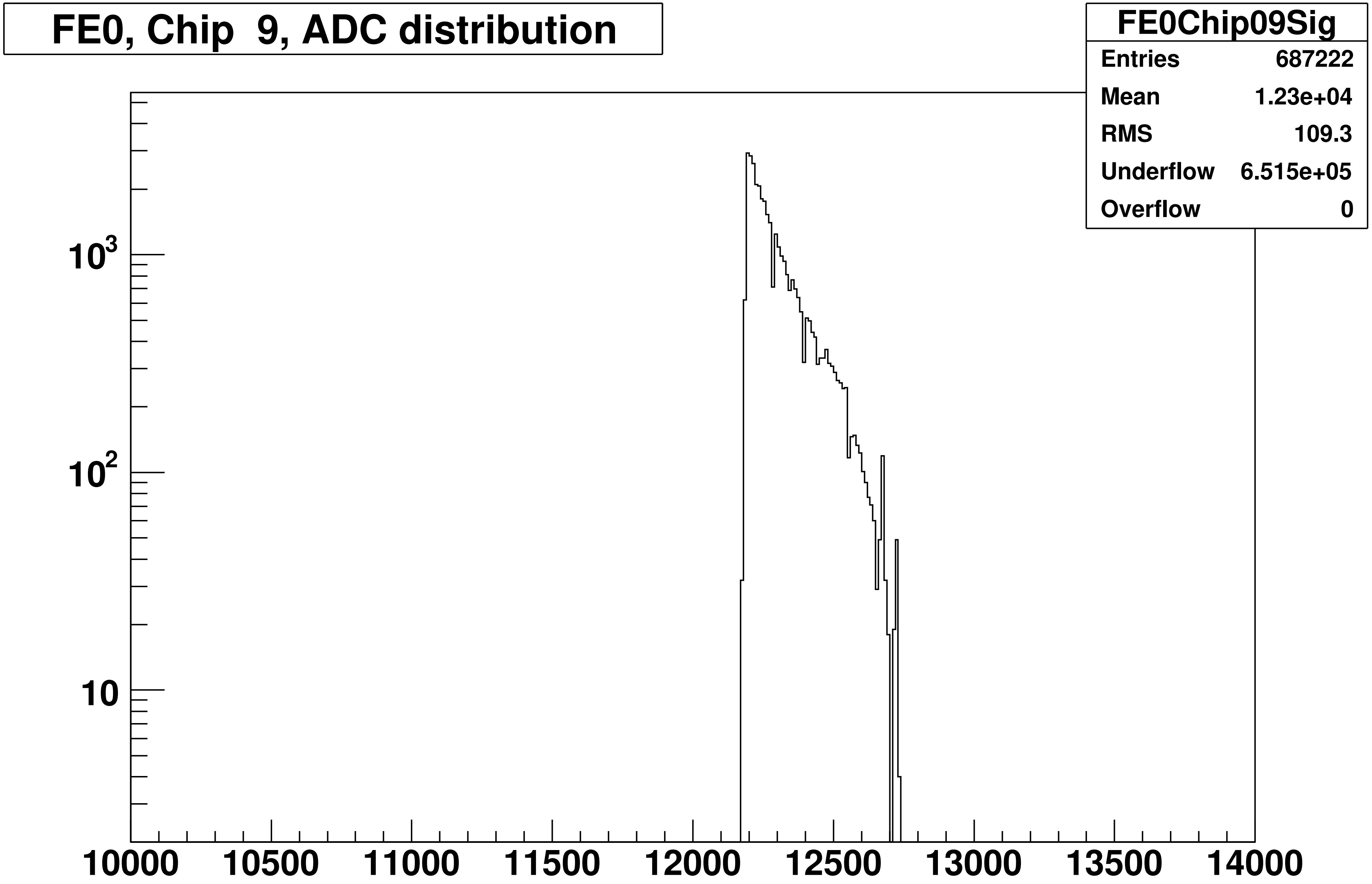
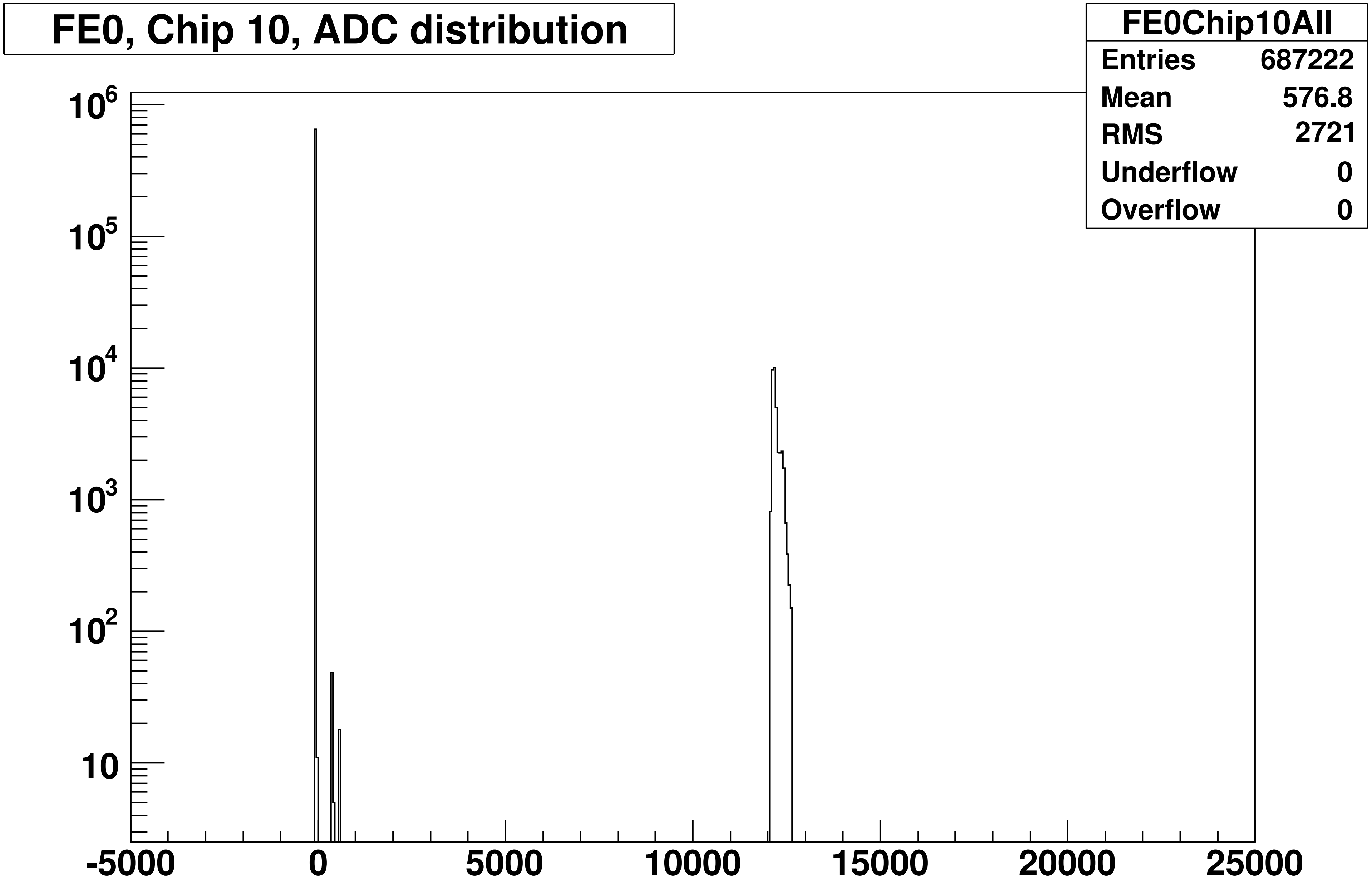
50
100
150
200



FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 687222
Mean 1.23e+04
RMS 109.3
Underflow 6.515e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 687222
Mean 576.8
RMS 2721
Underflow 0
Overflow 0
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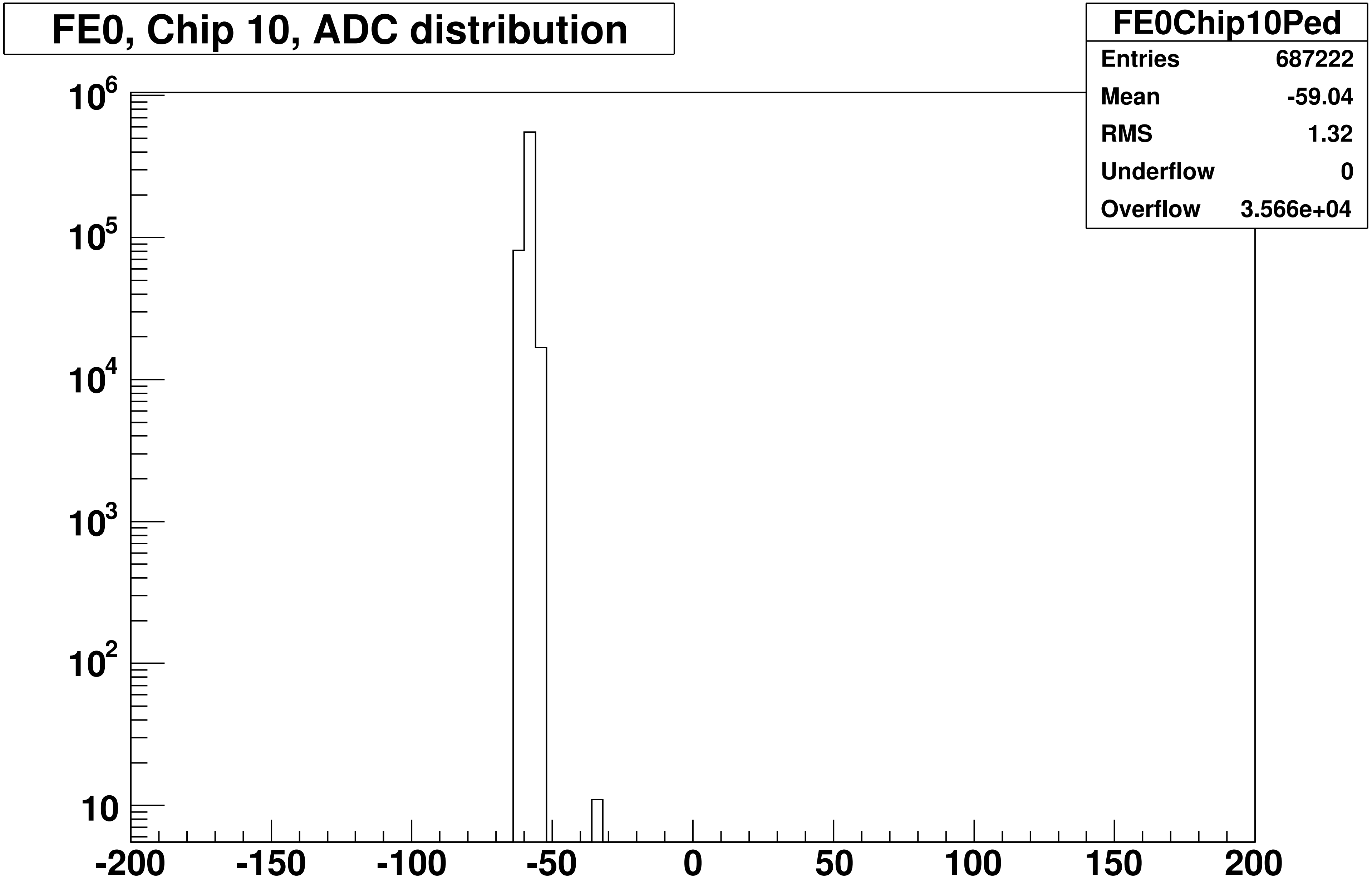
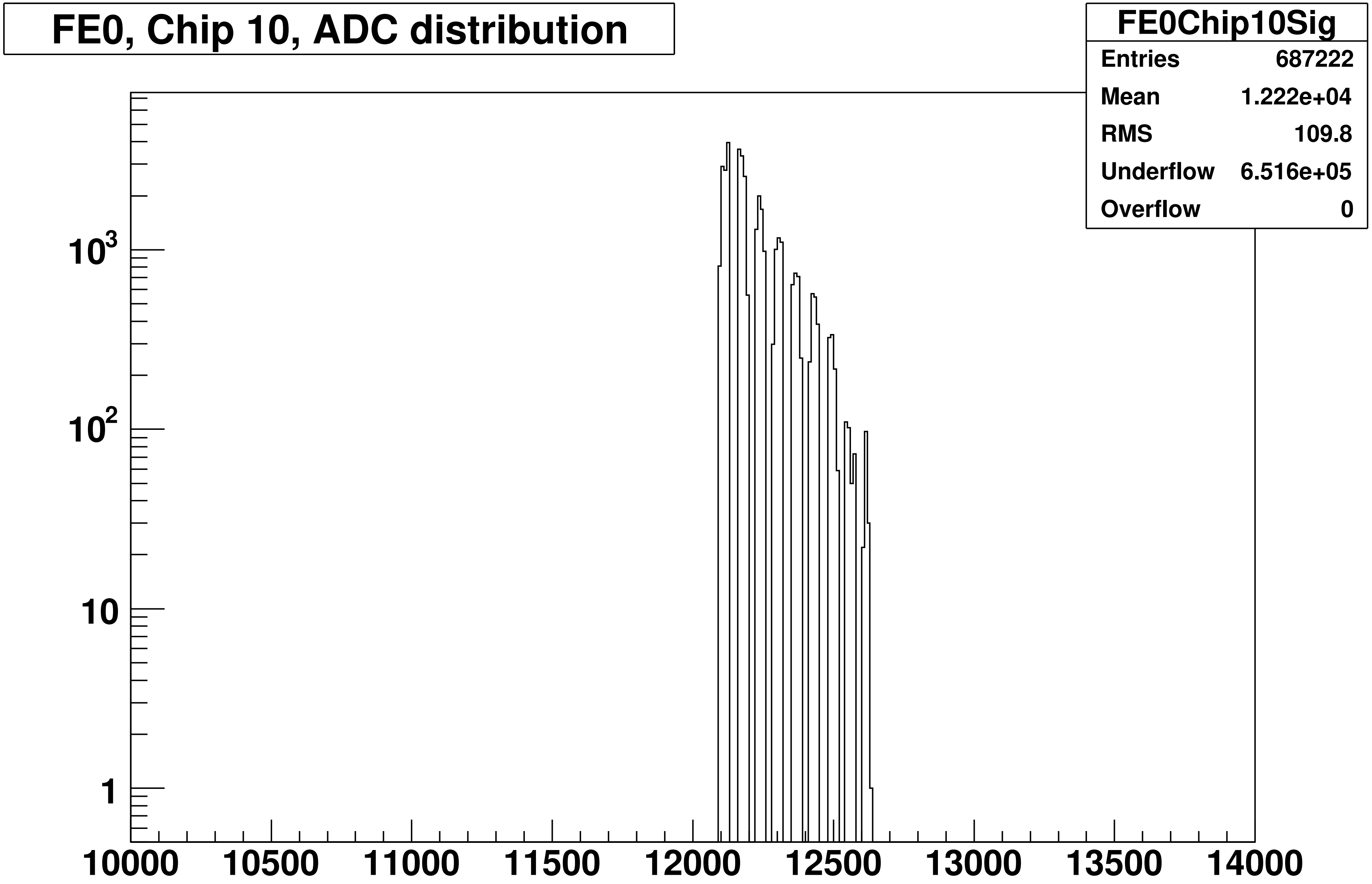
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 687222
Mean -59.04
RMS 1.32
Underflow 0
Overflow 3.566e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 687222
Mean 1.222e+04
RMS 109.8
Underflow 6.516e+05
Overflow 0
10^3
10^2
10
1
10000
10500
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11500
12000
12500
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13500
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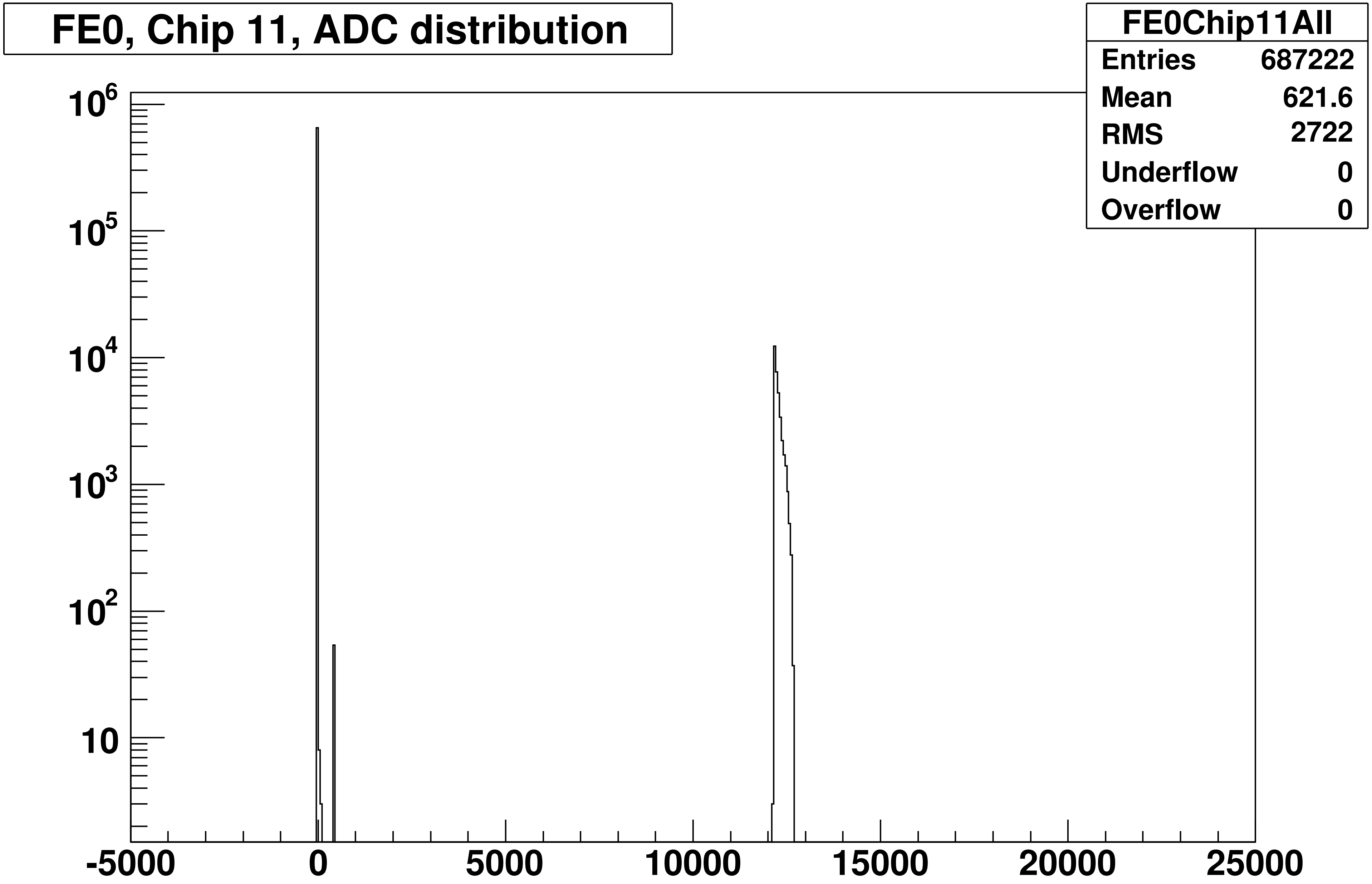
FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 687222
Mean 621.6
RMS 2722
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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10000
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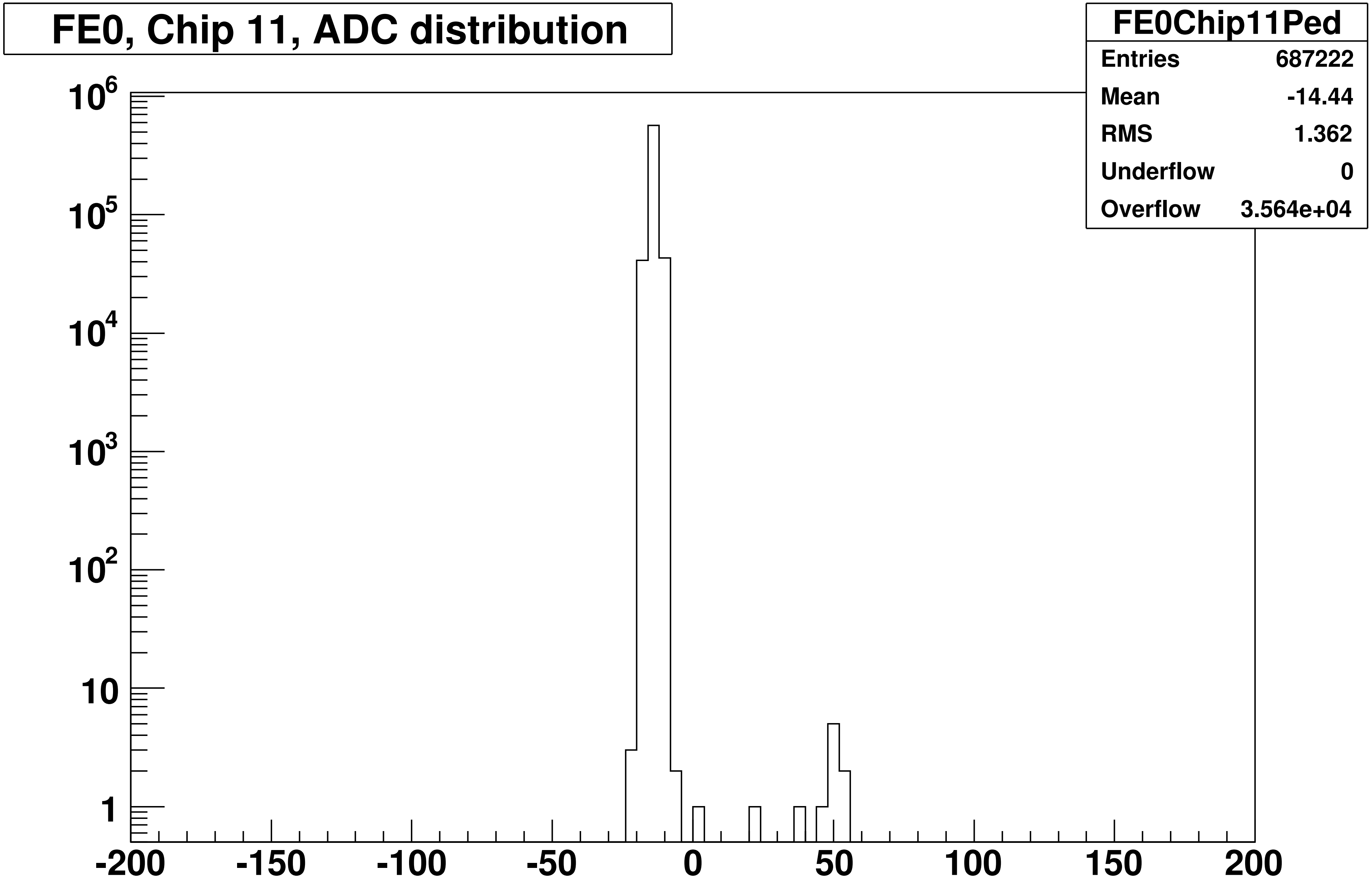
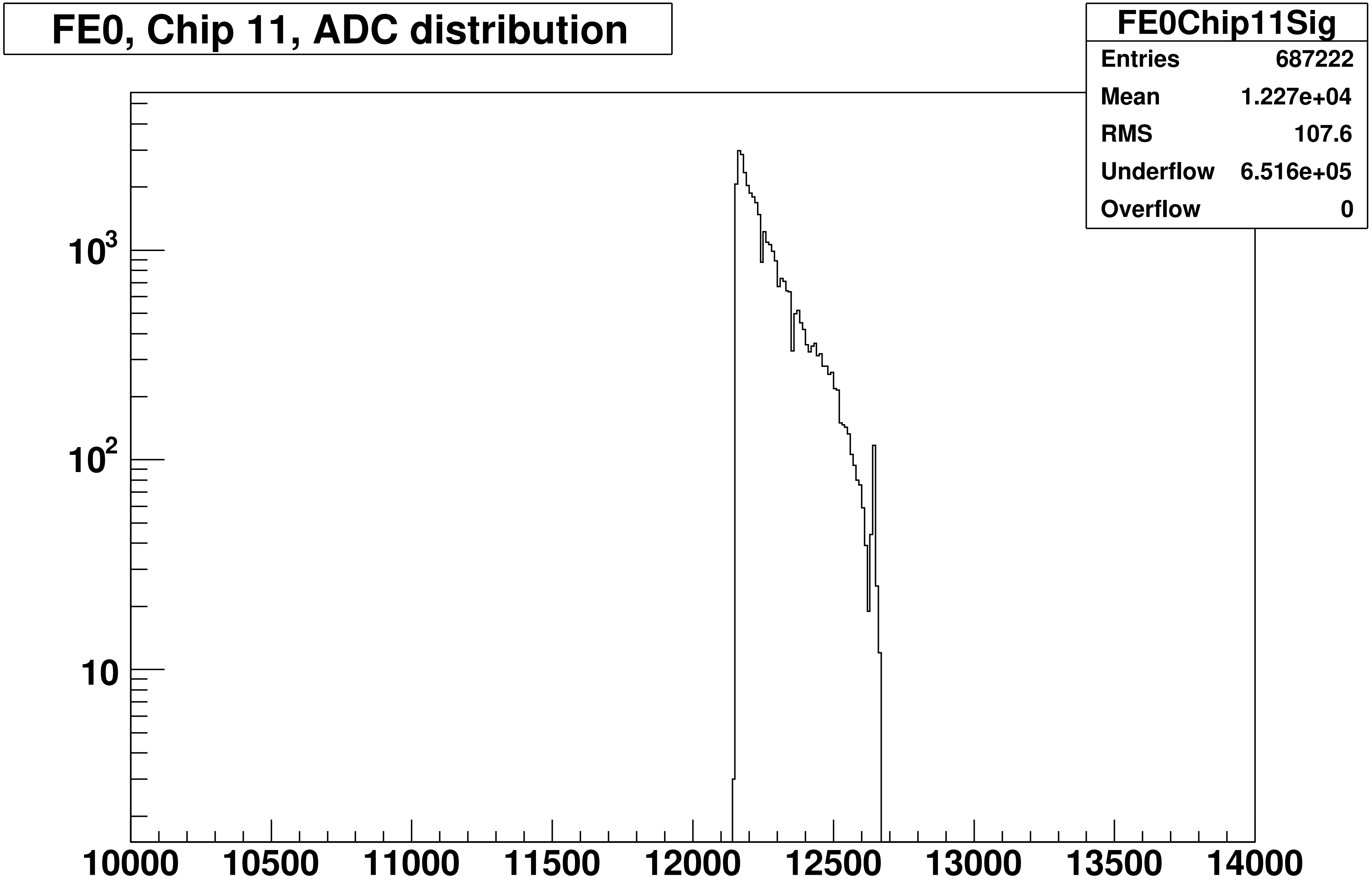
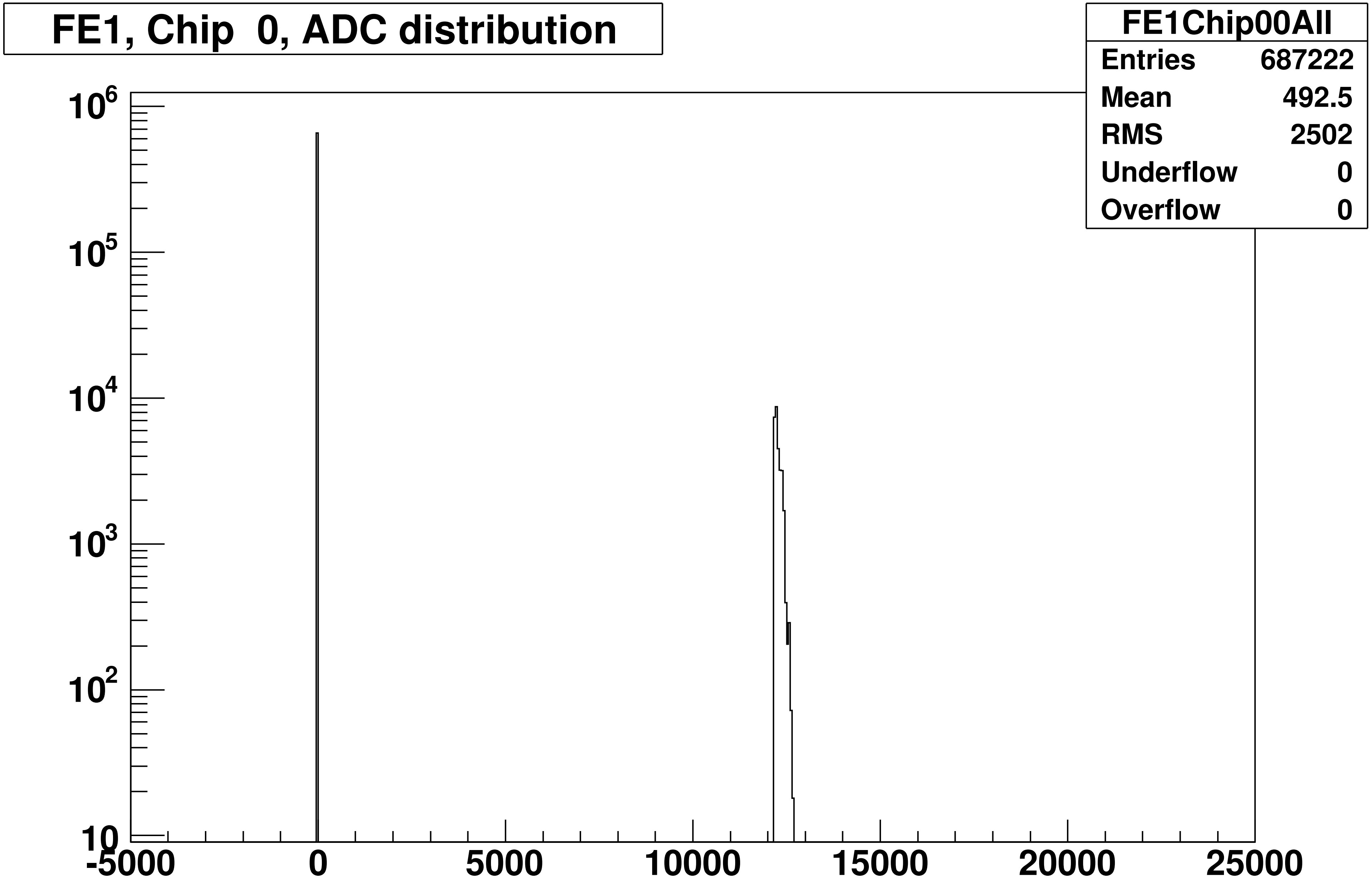
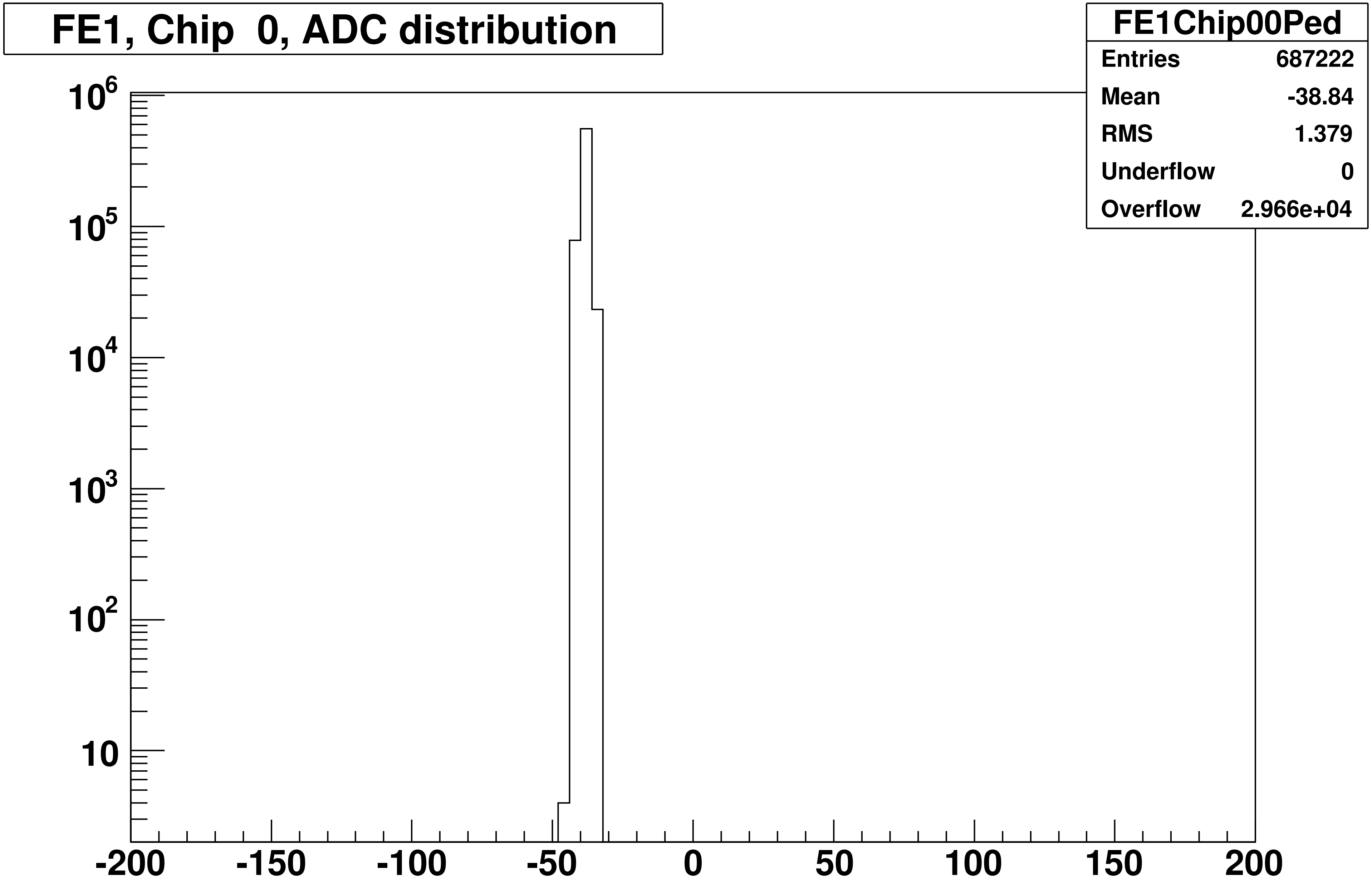
25000


FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 687222
Mean -14.44
RMS 1.362
Underflow 0
Overflow 3.564e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 687222
Mean 1.227e+04
RMS 107.6
Underflow 6.516e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 687222
Mean 492.5
RMS 2502
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 687222
Mean -38.84
RMS 1.379
Underflow 0
Overflow 2.966e+04
10^6
10^5
10^4
10^3
10^2
10
-200
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-100
-50
0
50
100
150
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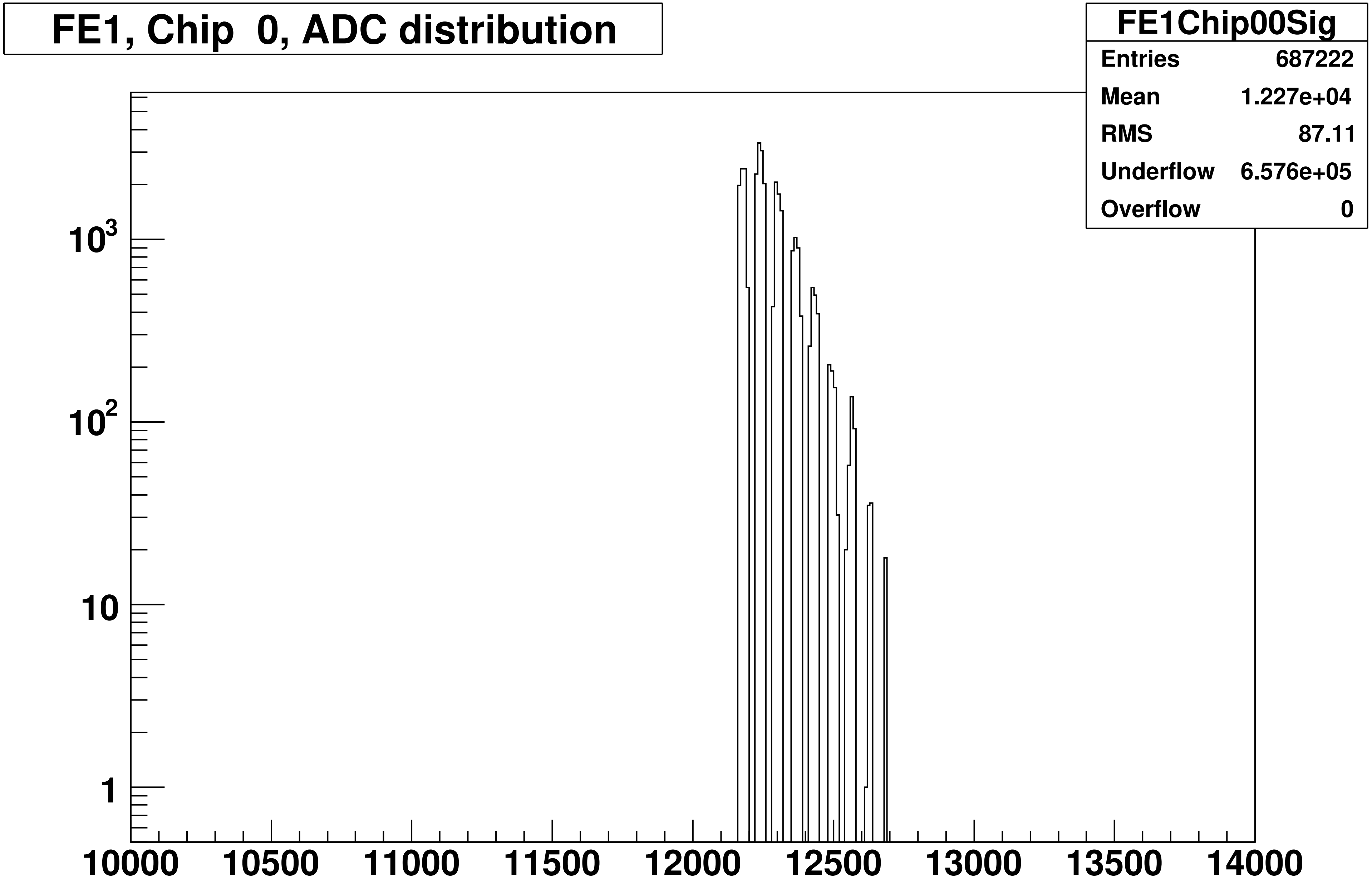
FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 687222
Mean 1.227e+04
RMS 87.11
Underflow 6.576e+05
Overflow 0
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10^2
10
1
10000
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11500
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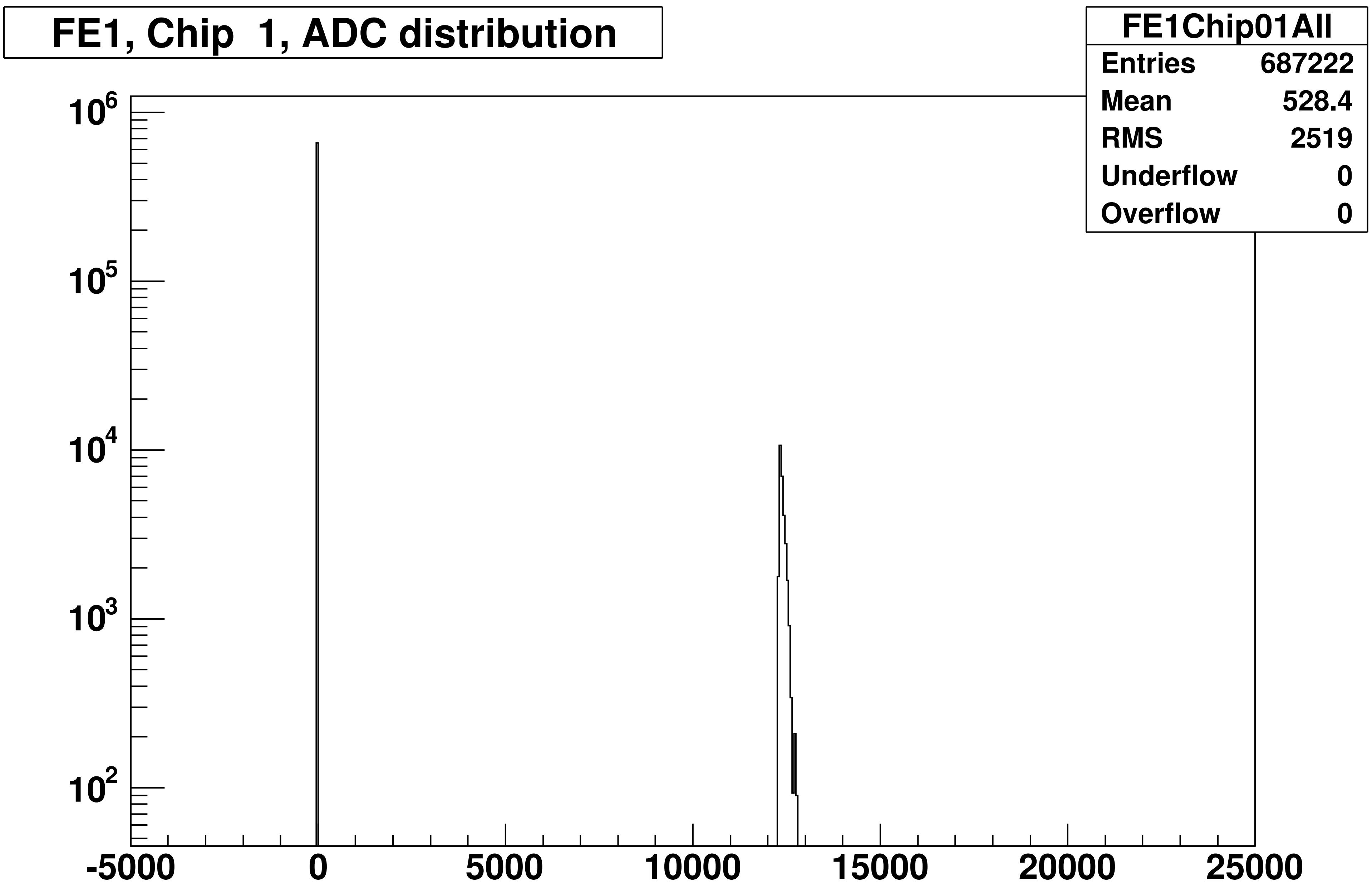
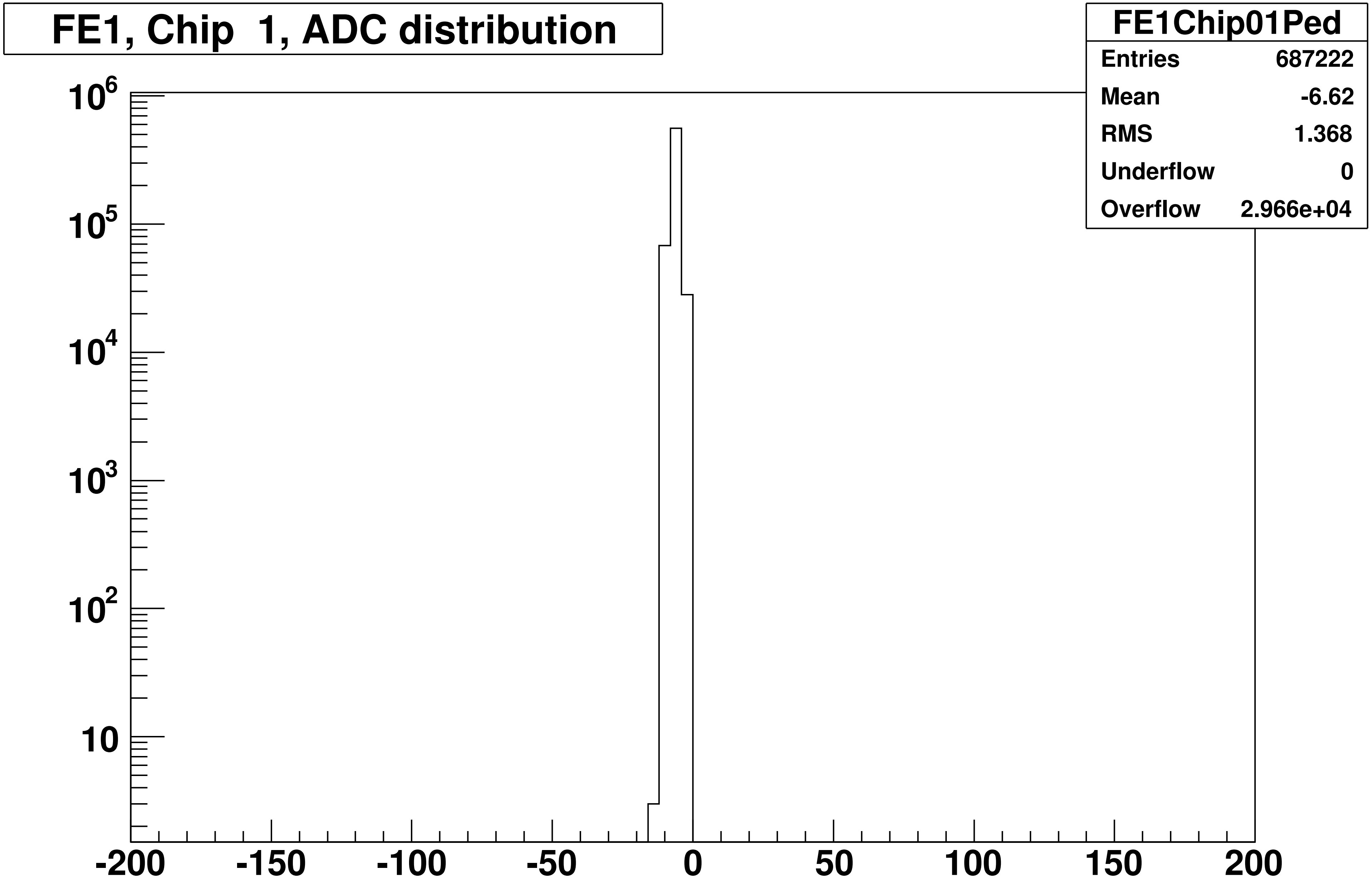

FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 687222
Mean 528.4
RMS 2519
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 687222
Mean -6.62
RMS 1.368
Underflow 0
Overflow 2.966e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
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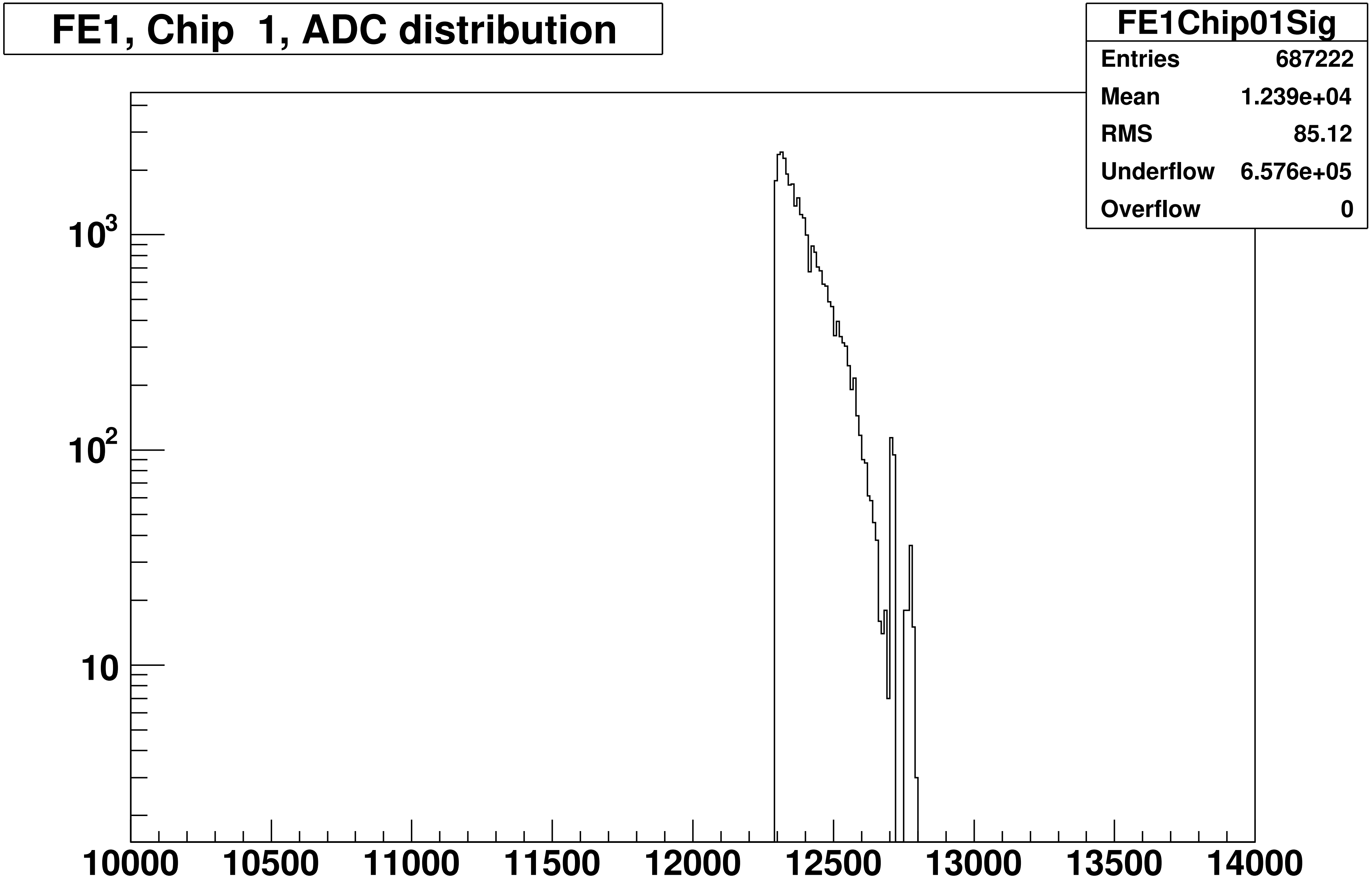
100
150
200


FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 687222
Mean 1.239e+04
RMS 85.12
Underflow 6.576e+05
Overflow 0
10^3
10^2
10
10000
10500
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11500
12000
12500
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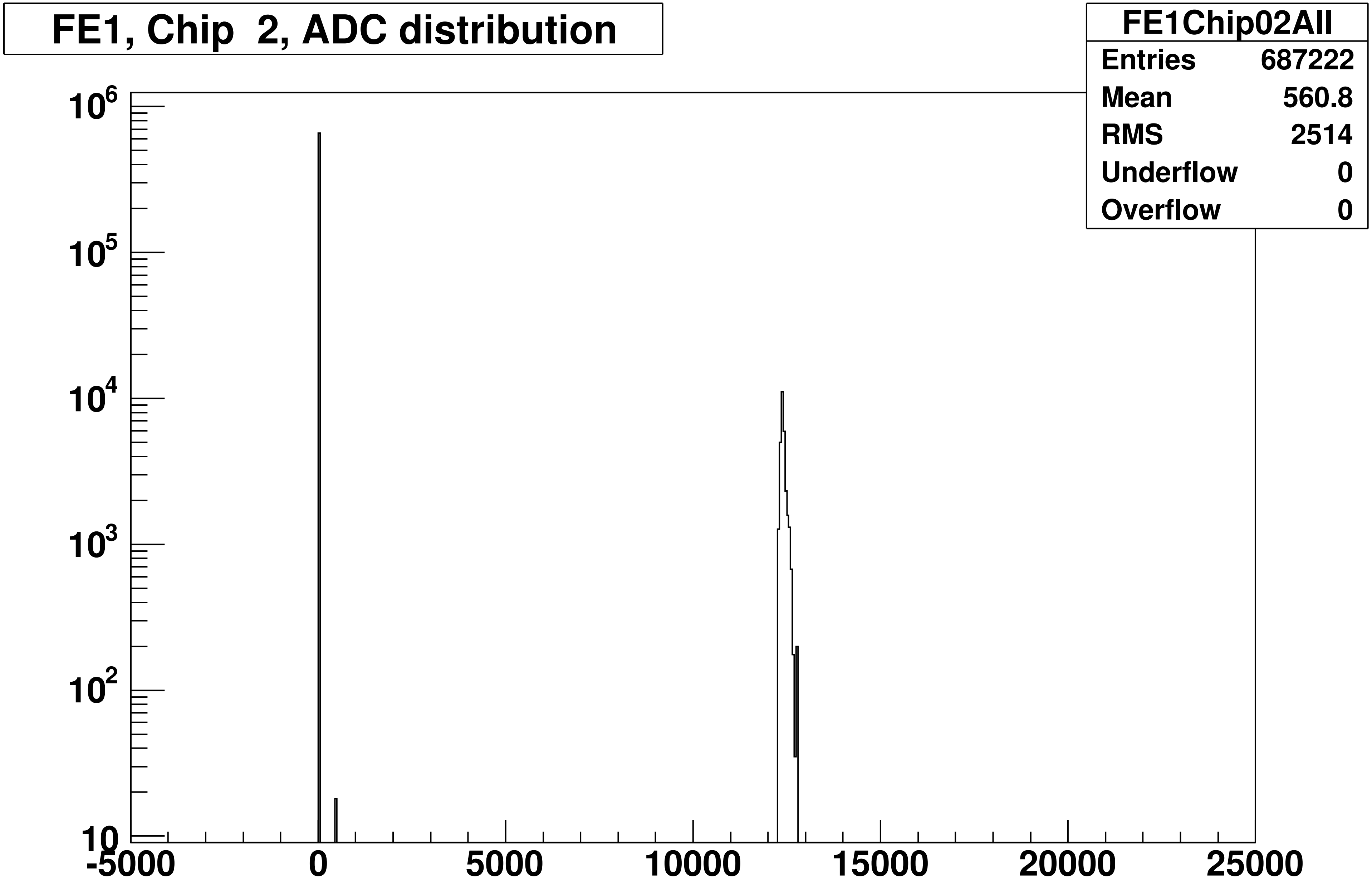
FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 687222
Mean 560.8
RMS 2514
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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10000
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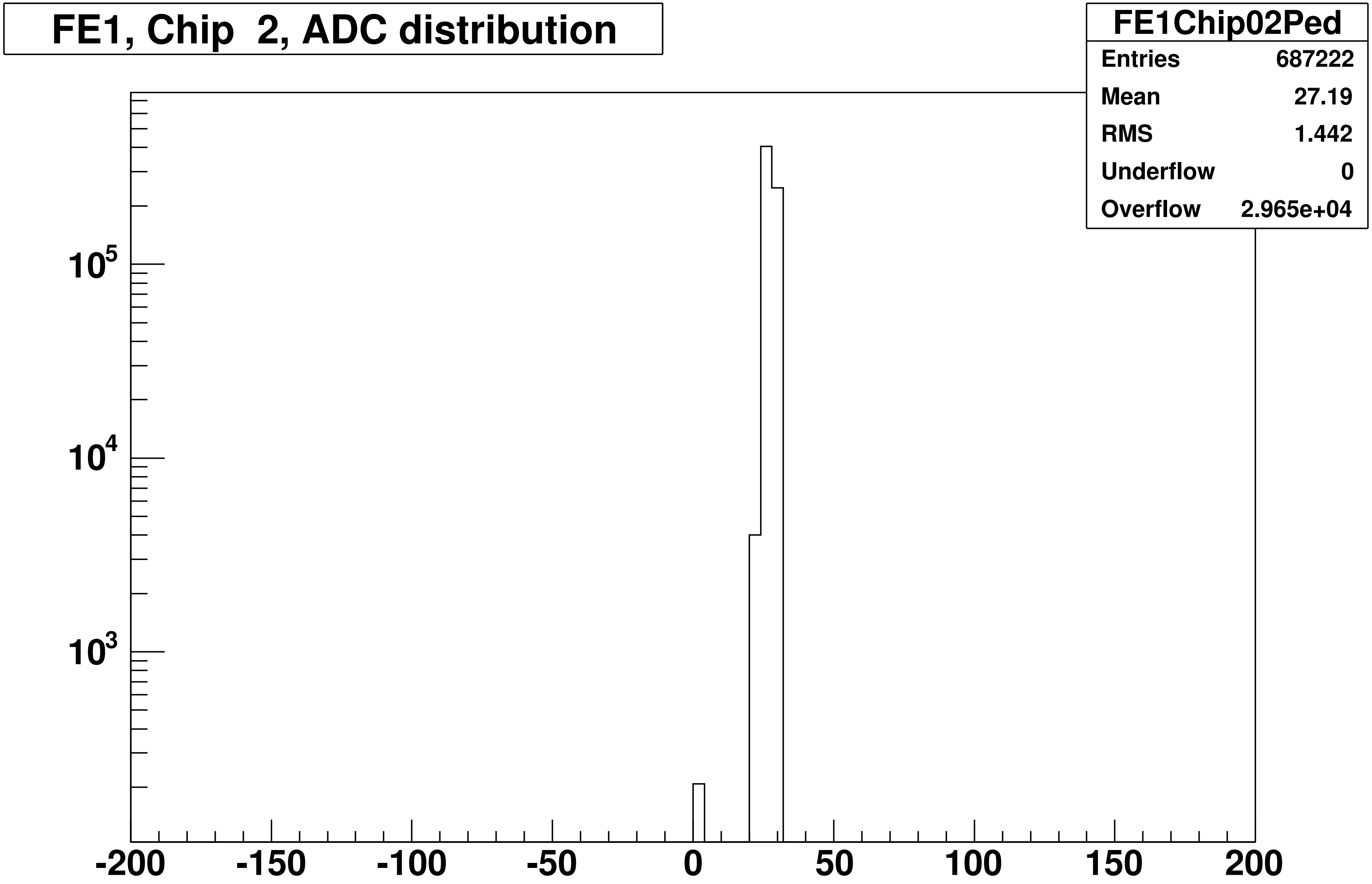
FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 687222
Mean 27.19
RMS 1.442
Underflow 0
Overflow 2.965e+04
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10^4
10^3
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-100
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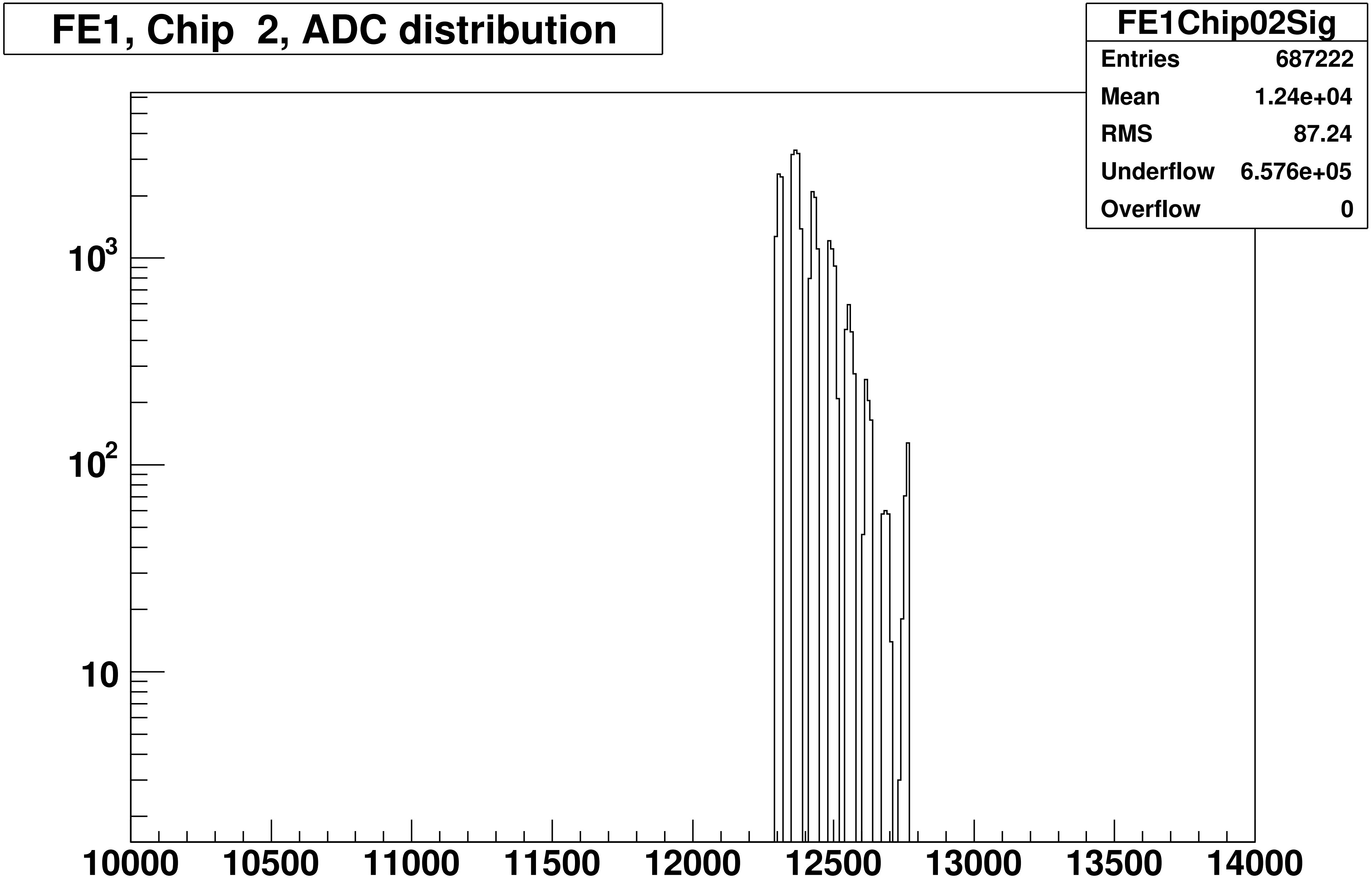
FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 687222
Mean 1.24e+04
RMS 87.24
Underflow 6.576e+05
Overflow 0
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10^2
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10000
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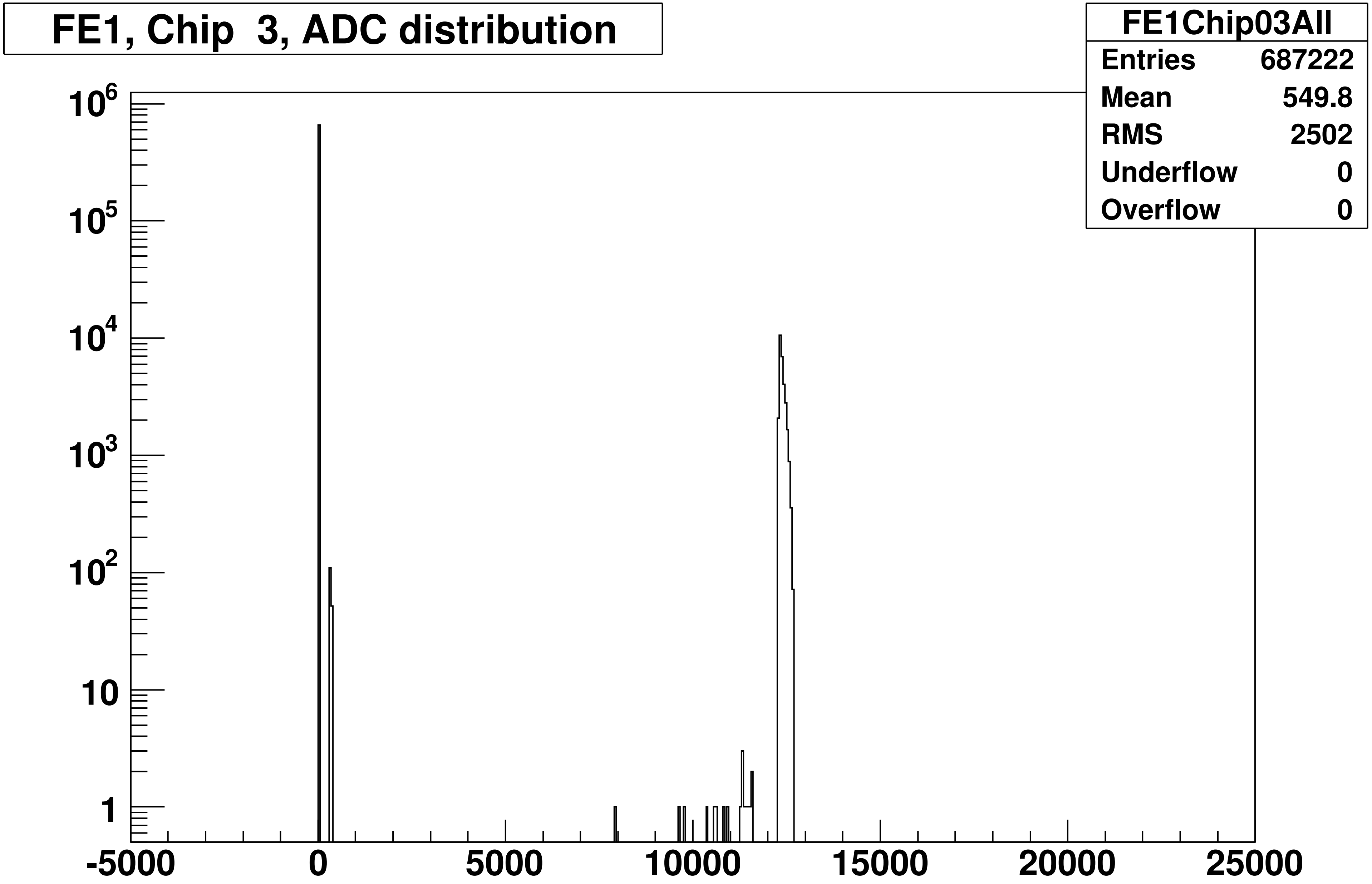
FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 687222
Mean 549.8
RMS 2502
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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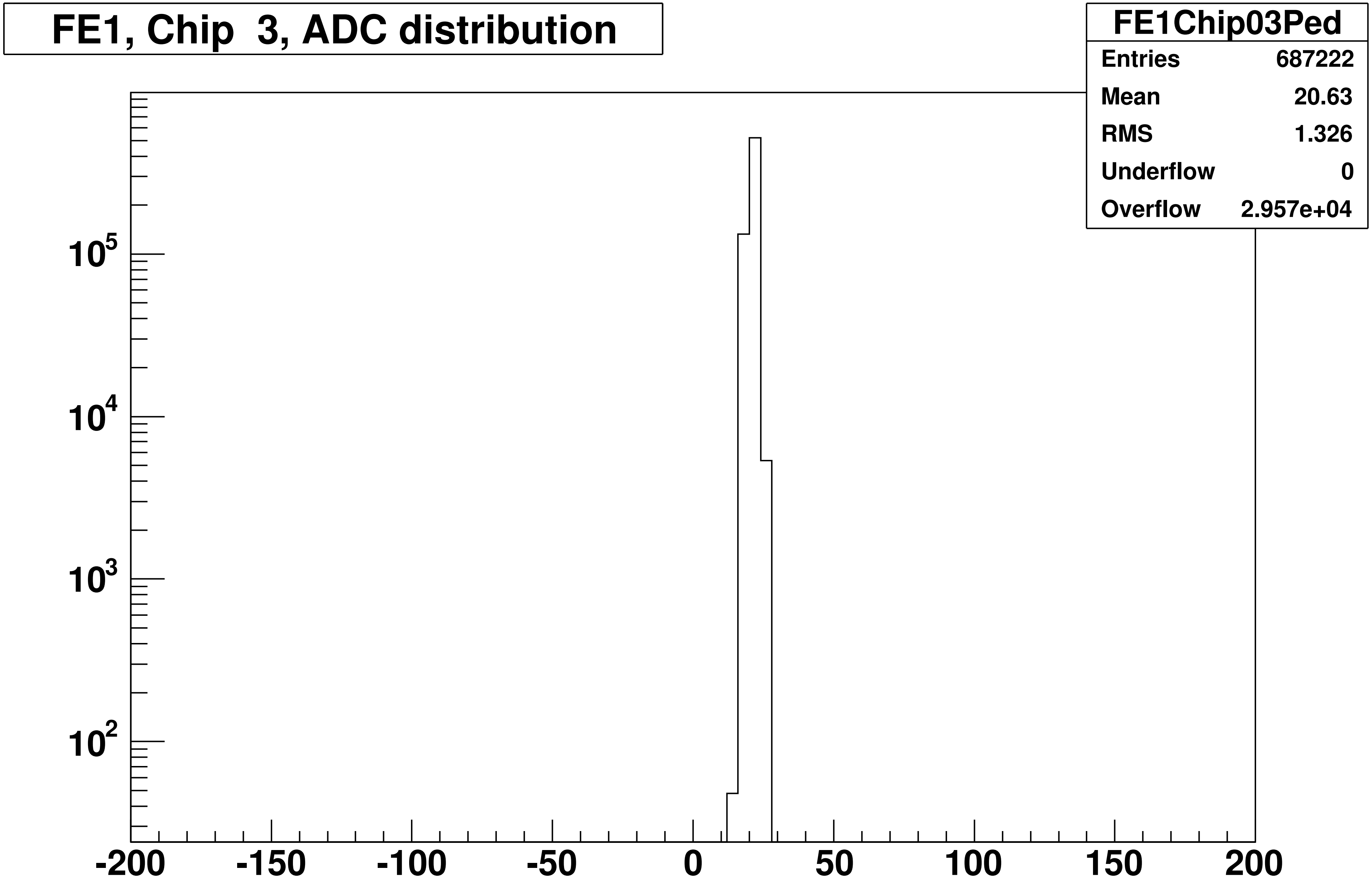
FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 687222
Mean 20.63
RMS 1.326
Underflow 0
Overflow 2.957e+04
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10^4
10^3
10^2
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-100
-50
0
50
100
150
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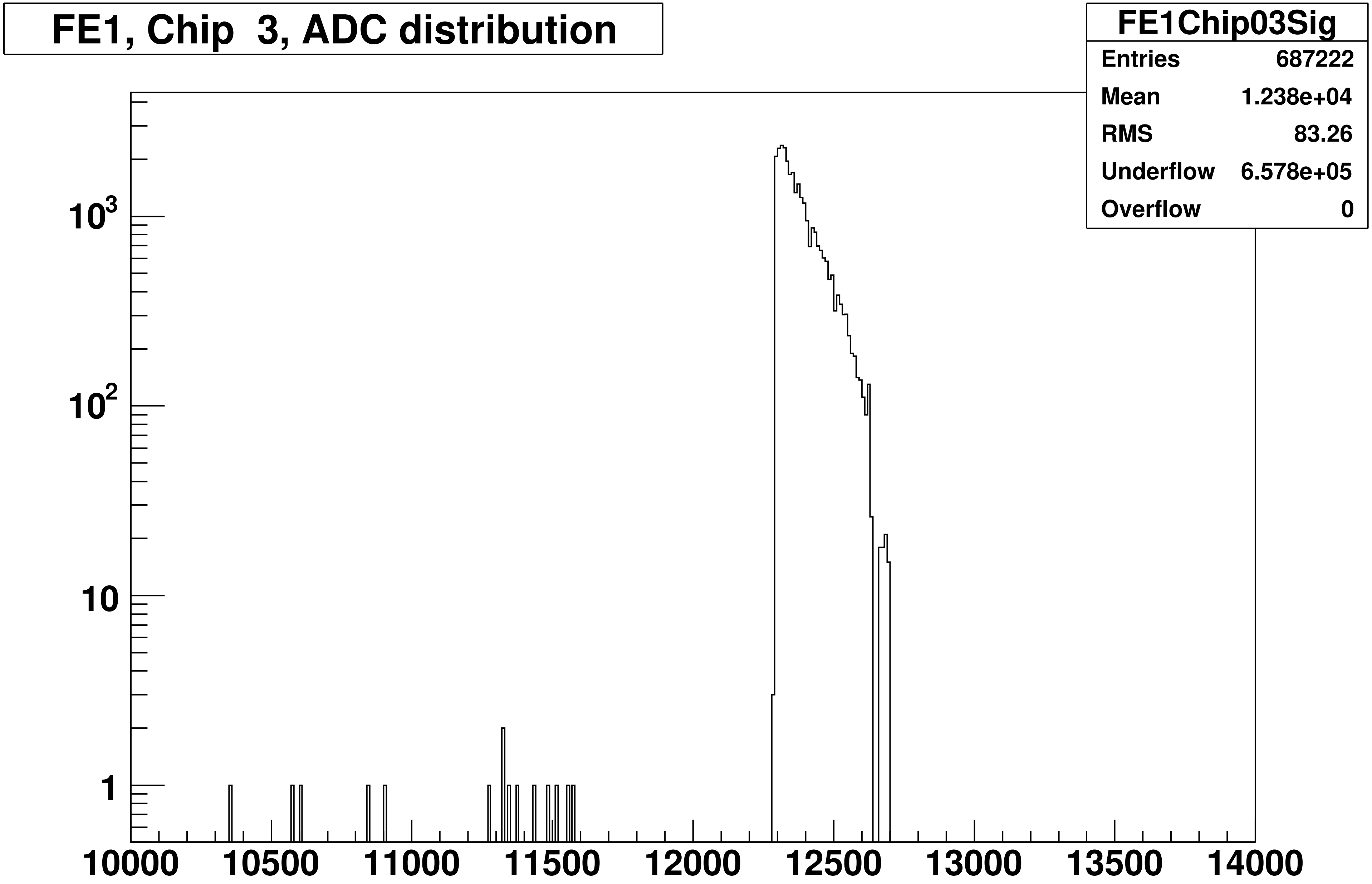
FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 687222
Mean 1.238e+04
RMS 83.26
Underflow 6.578e+05
Overflow 0
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10^2
10
1
10000
10500
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11500
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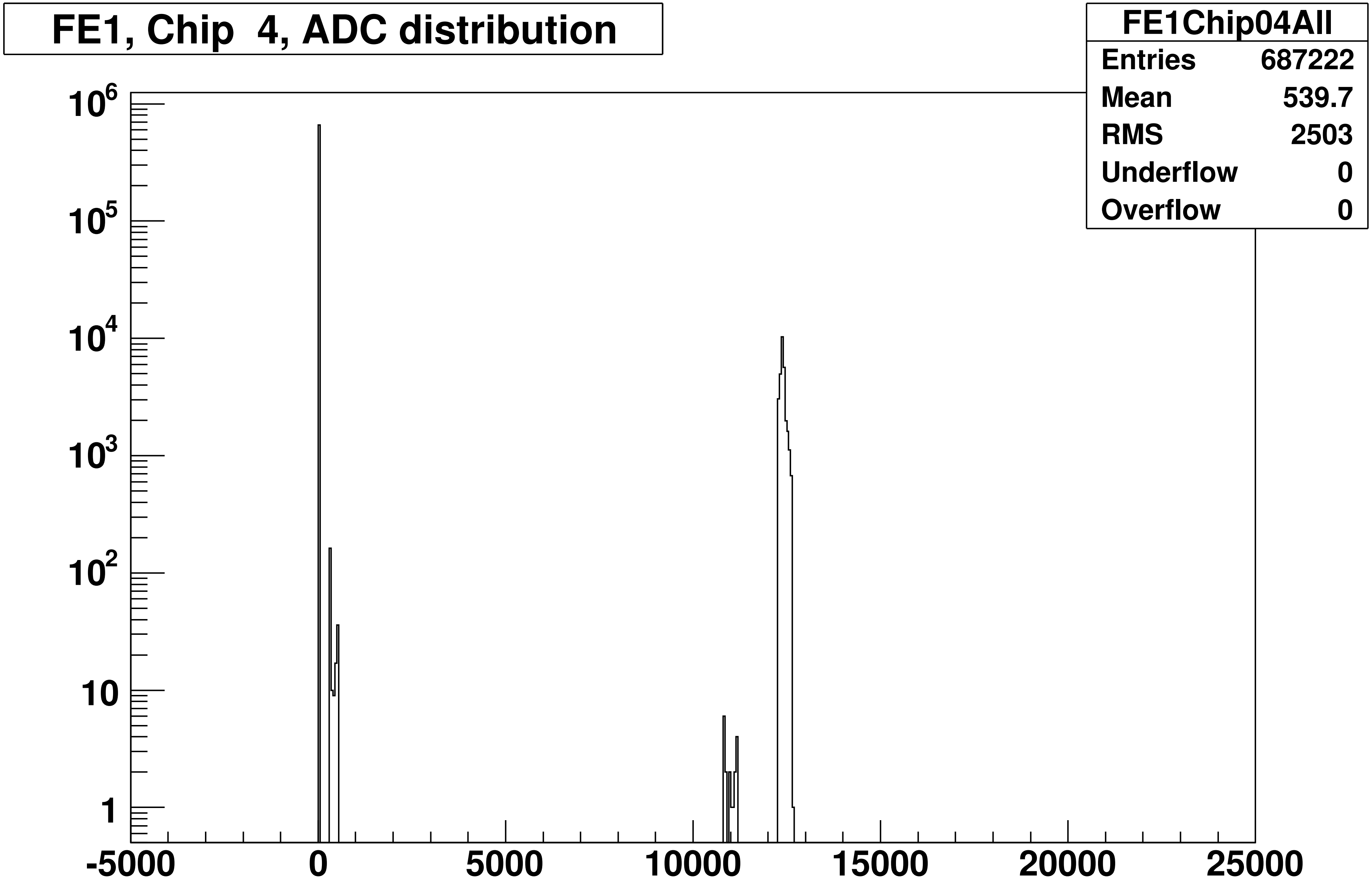

FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 687222
Mean 539.7
RMS 2503
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
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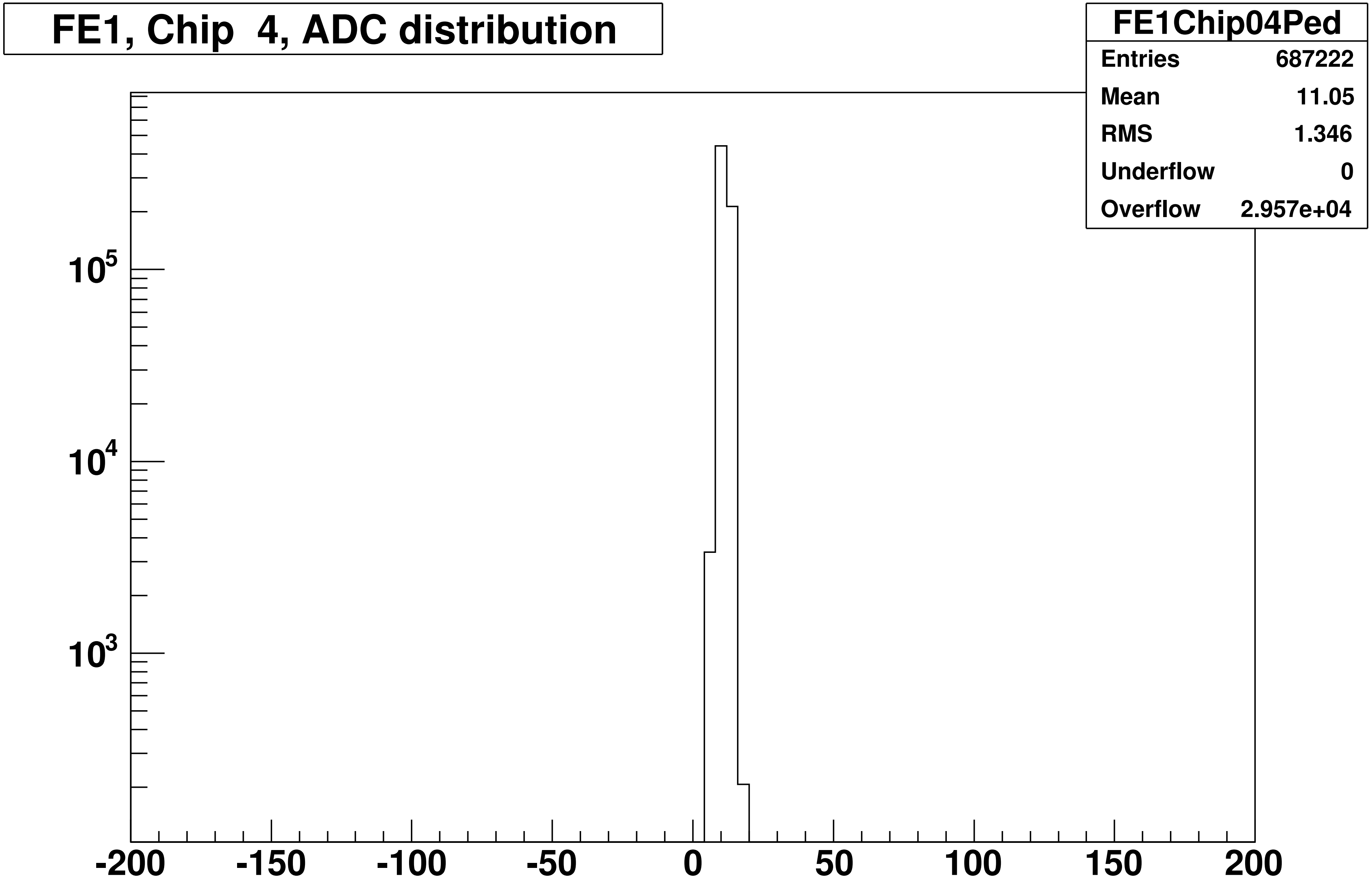
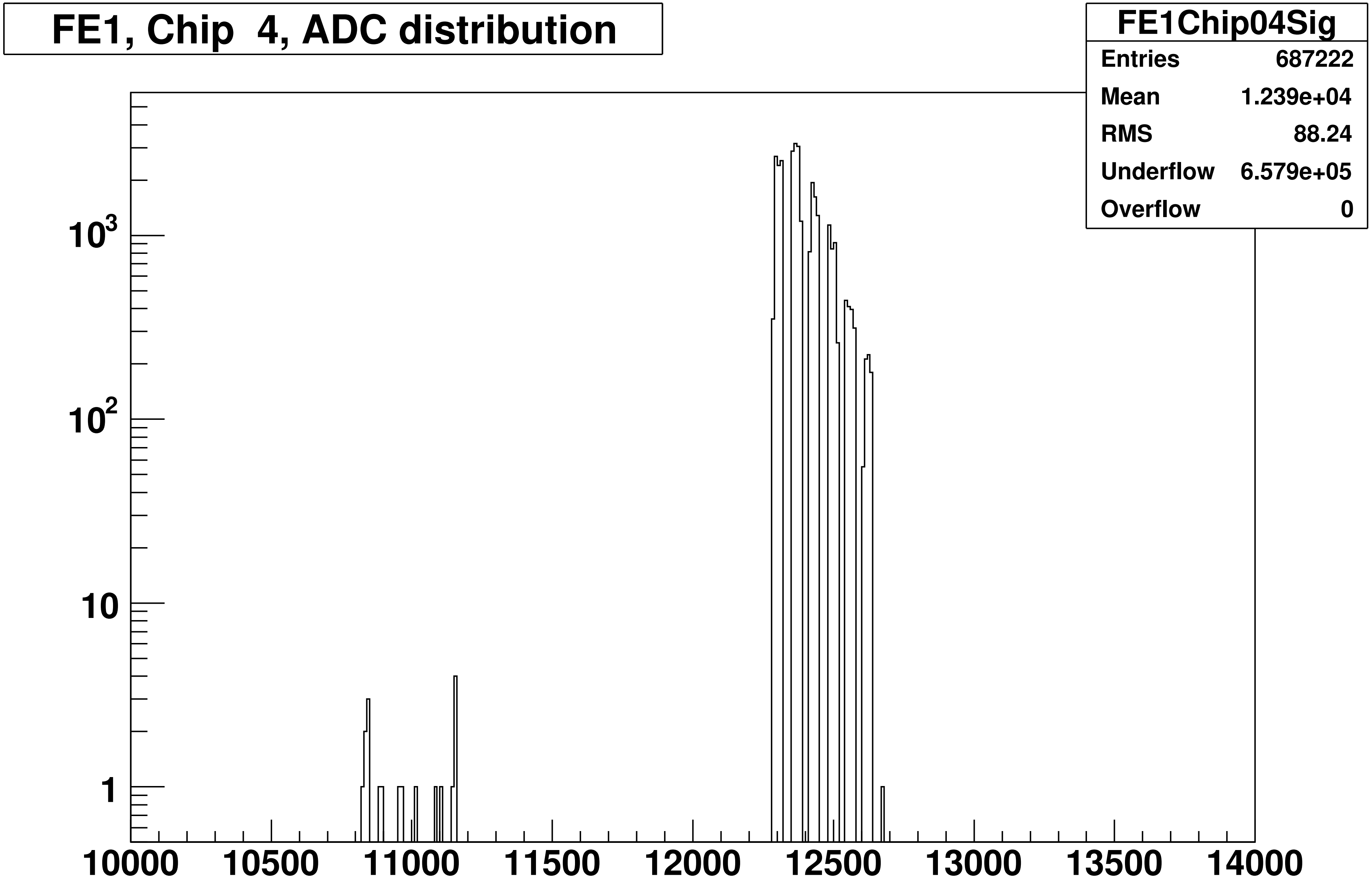


FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 687222
Mean 11.05
RMS 1.346
Underflow 0
Overflow 2.957e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 687222
Mean 1.239e+04
RMS 88.24
Underflow 6.579e+05
Overflow 0
1
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10^2
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13500
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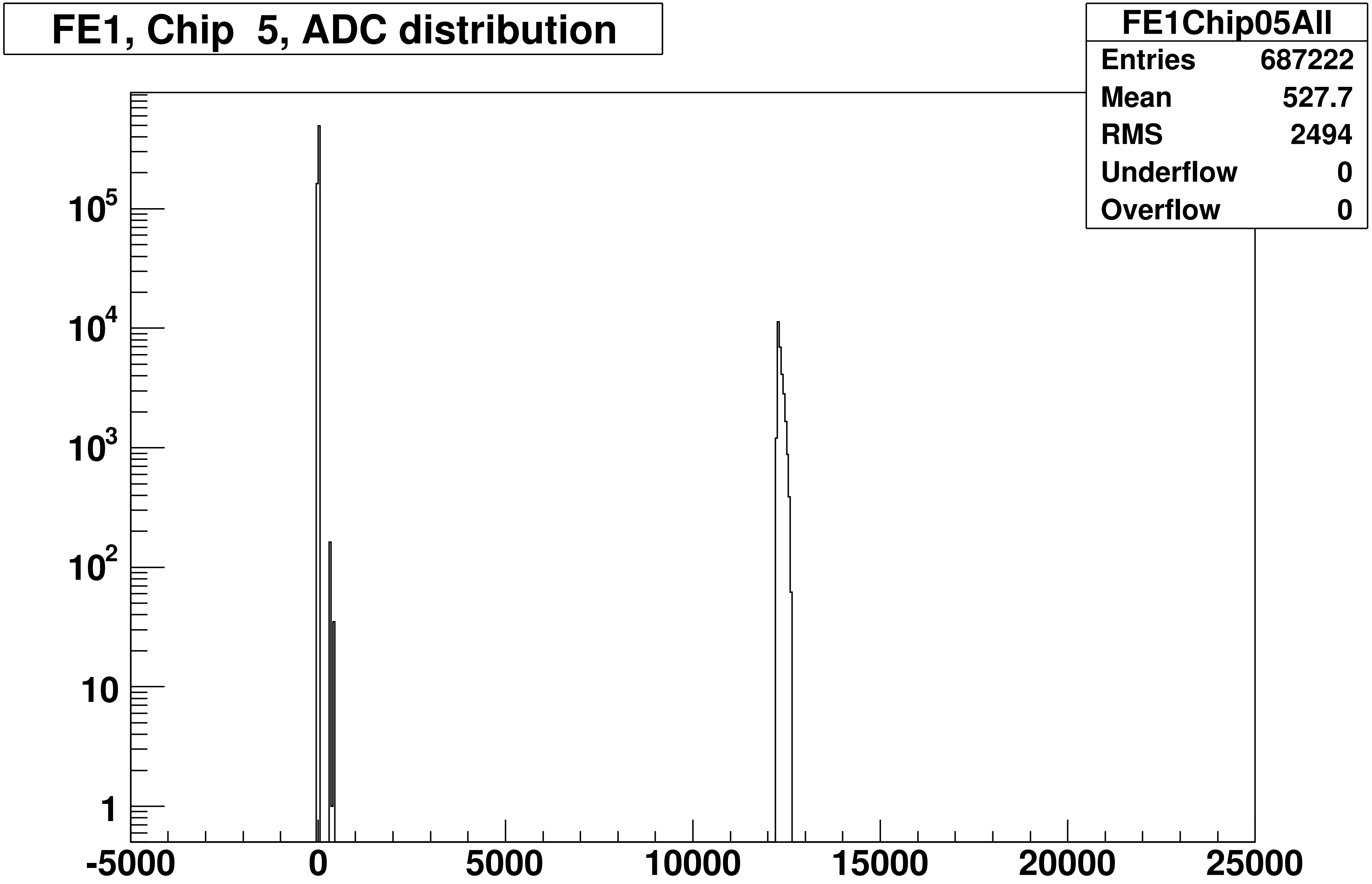

FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 687222
Mean 527.7
RMS 2494
Underflow 0
Overflow 0
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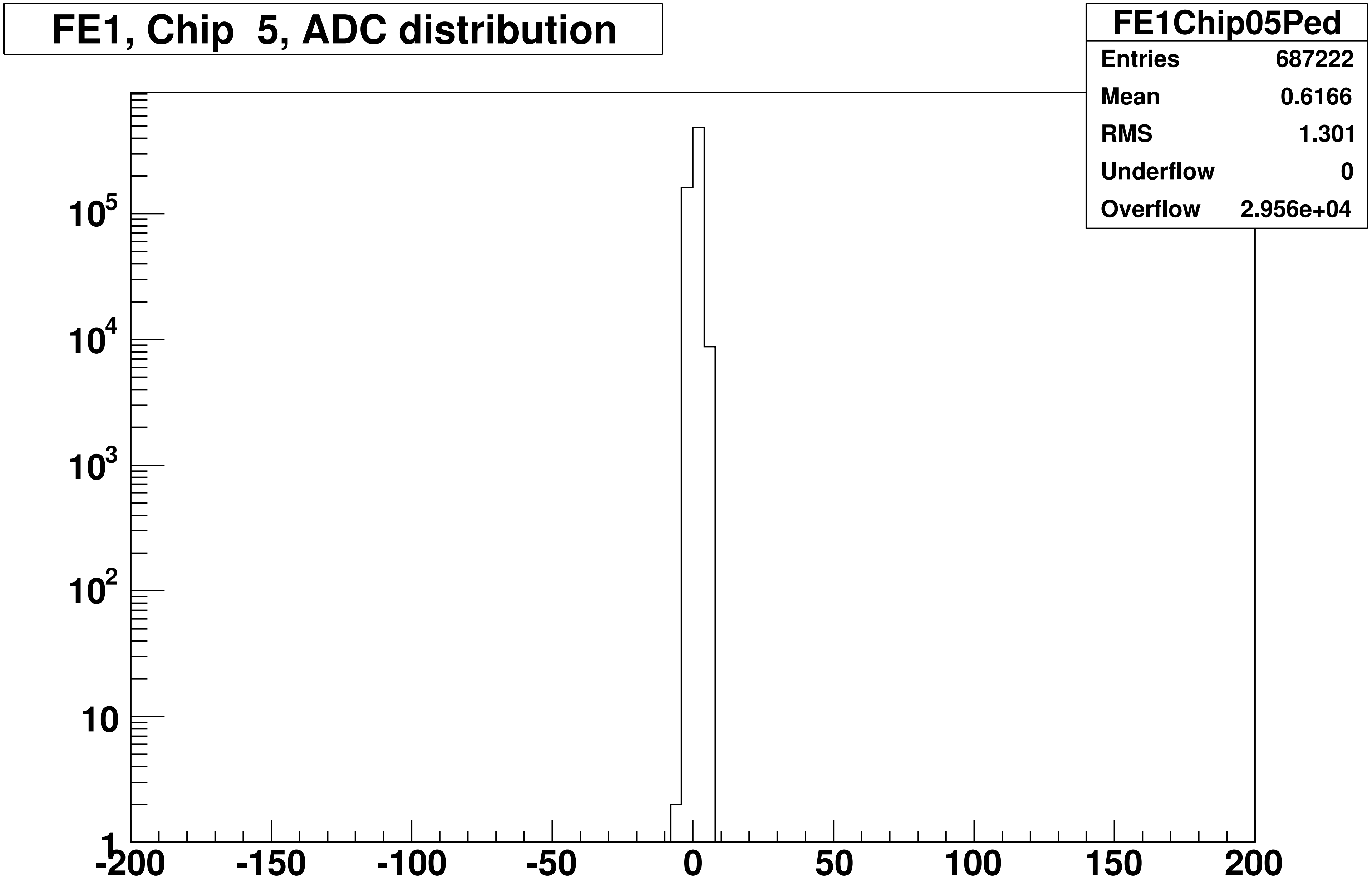
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 687222
Mean 0.6166
RMS 1.301
Underflow 0
Overflow 2.956e+04
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10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
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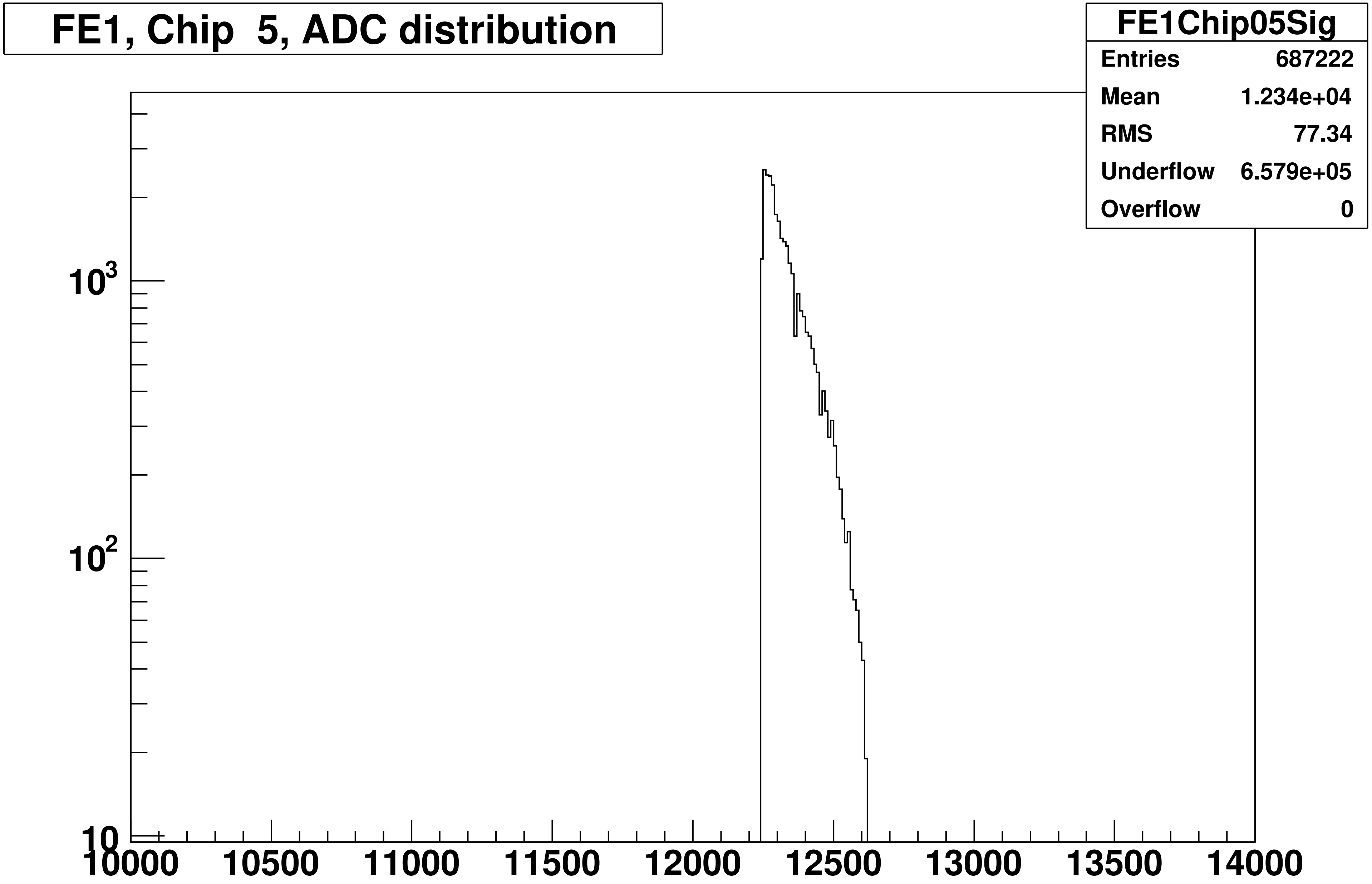
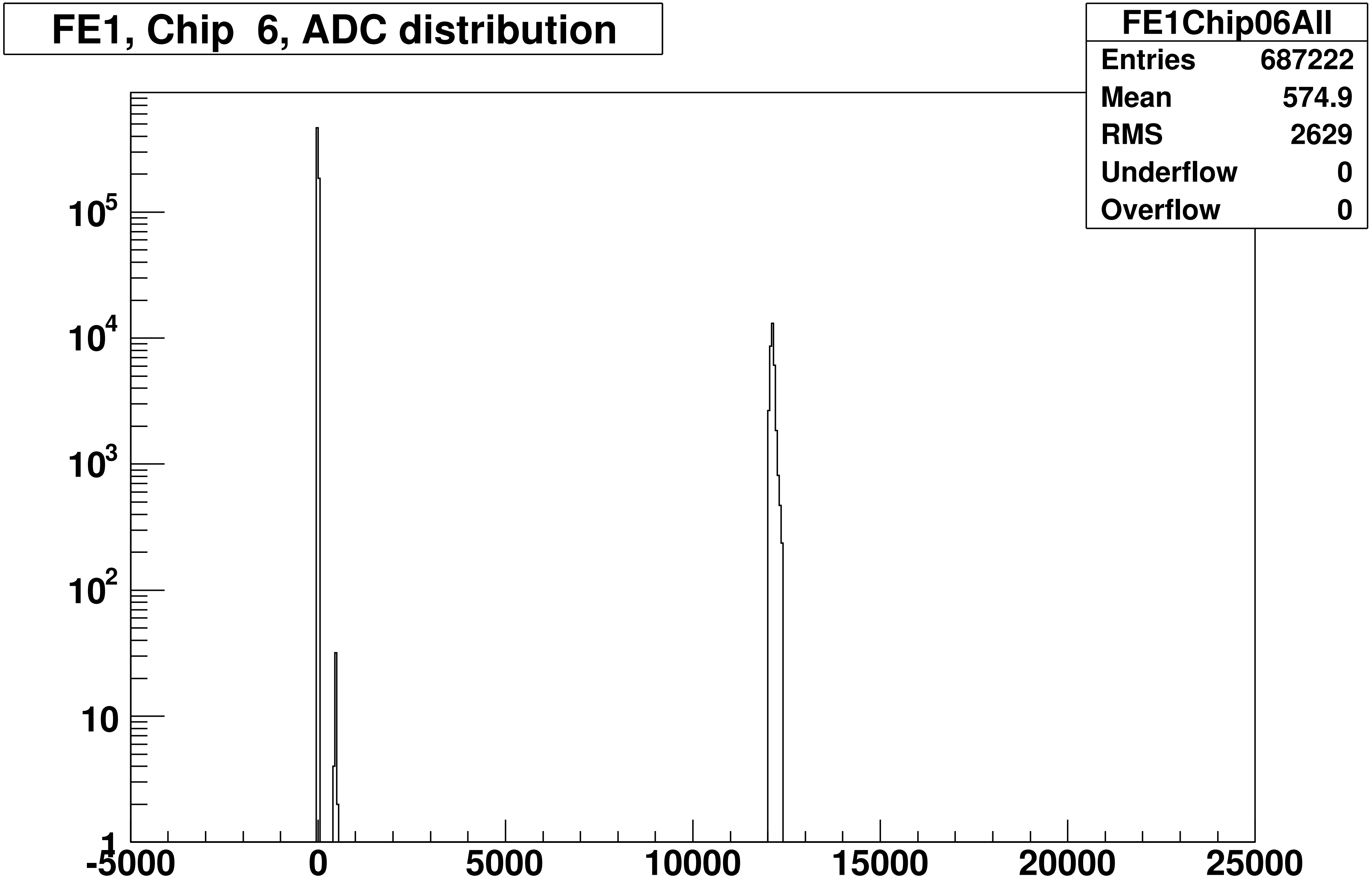
100
150
200


FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 687222
Mean 1.234e+04
RMS 77.34
Underflow 6.579e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000



FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 687222
Mean 574.9
RMS 2629
Underflow 0
Overflow 0
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10^4
10^3
10^2
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1
-5000
0
5000
10000
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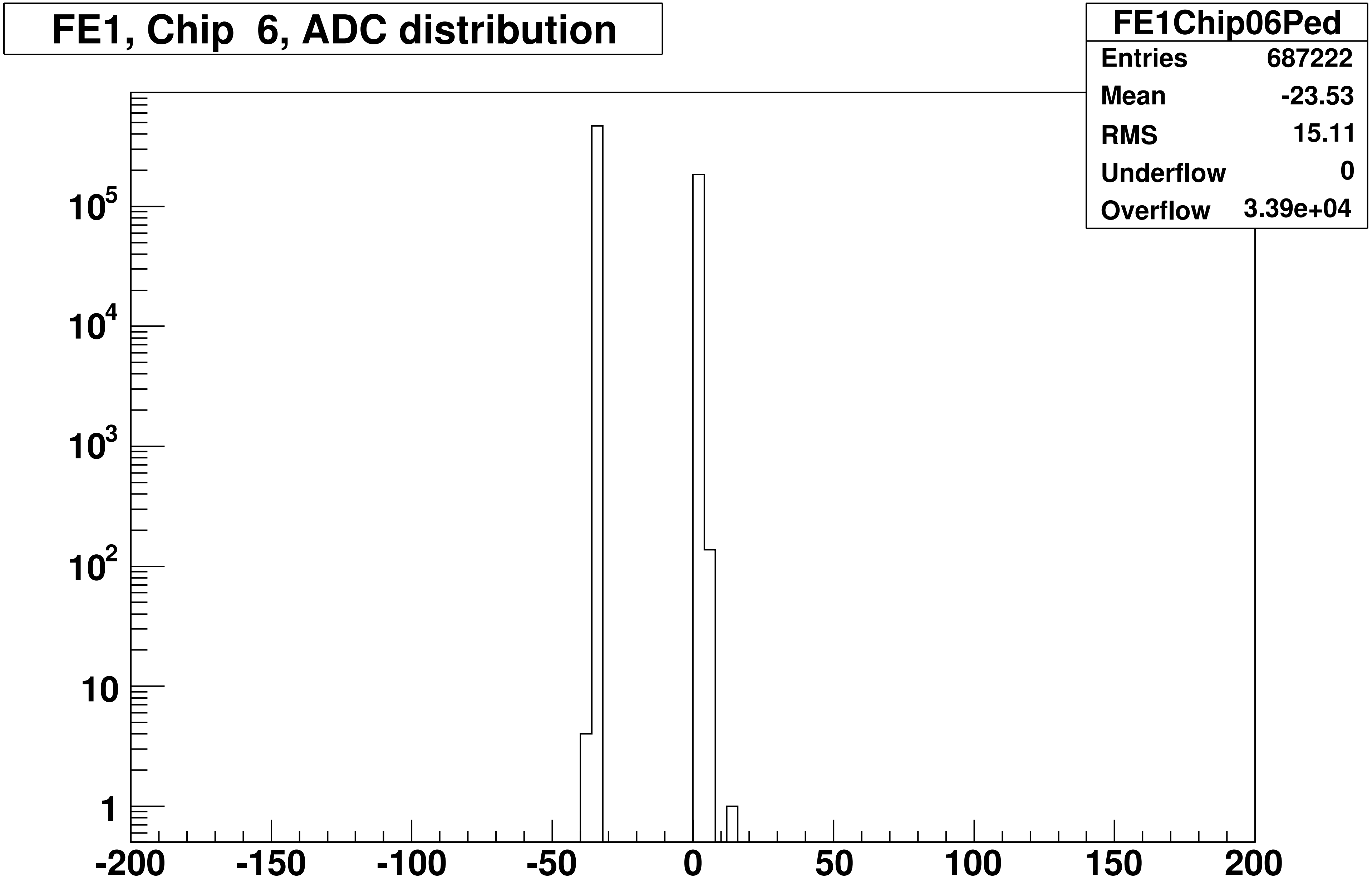
20000
25000



FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 687222
Mean -23.53
RMS 15.11
Underflow 0
Overflow 3.39e+04
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10^4
10^3
10^2
10
1
-200
-150
-100
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0
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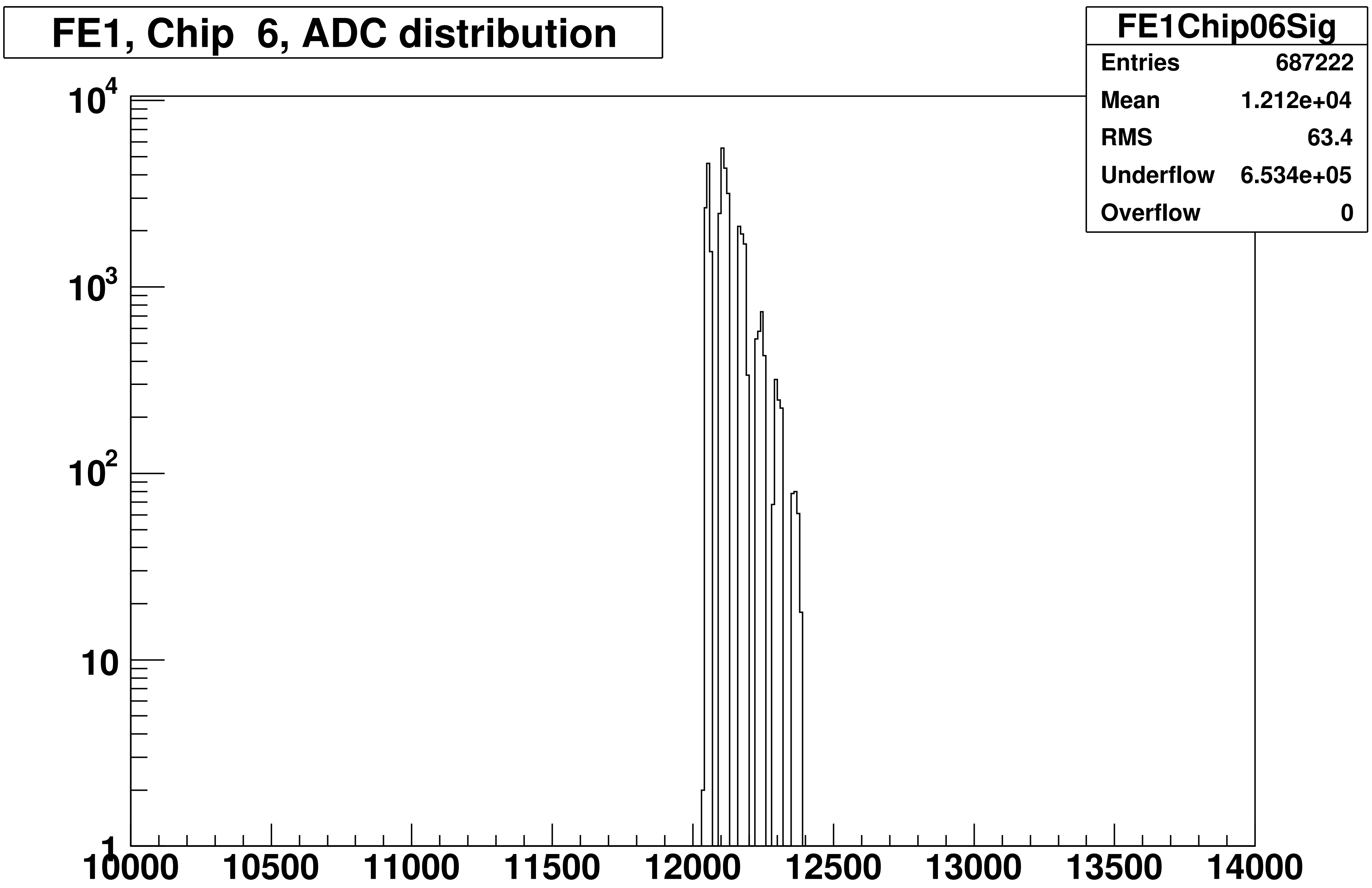
100
150
200



FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 687222
Mean 1.212e+04
RMS 63.4
Underflow 6.534e+05
Overflow 0
10^4
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
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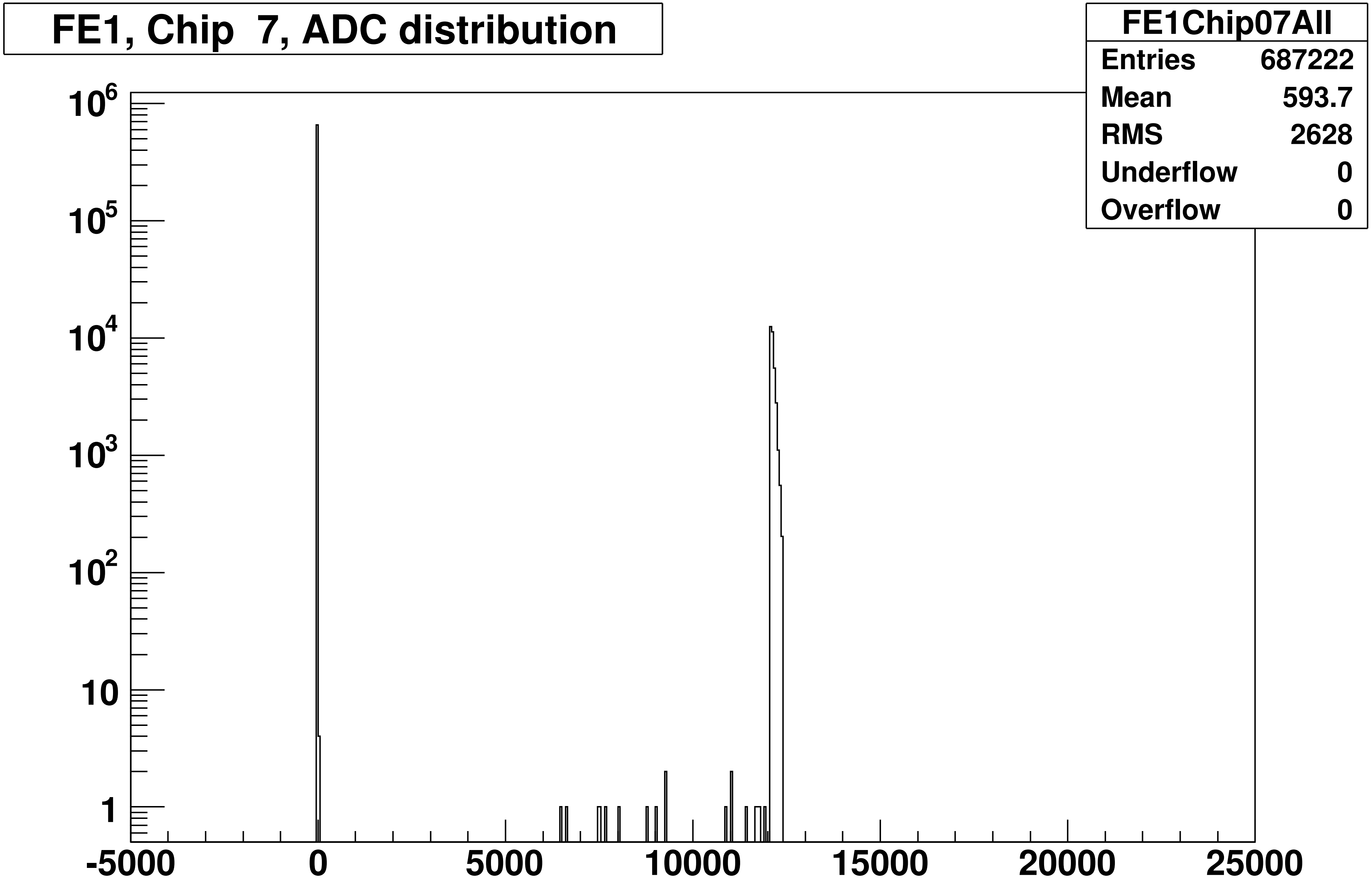
13500
14000


FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 687222
Mean 593.7
RMS 2628
Underflow 0
Overflow 0
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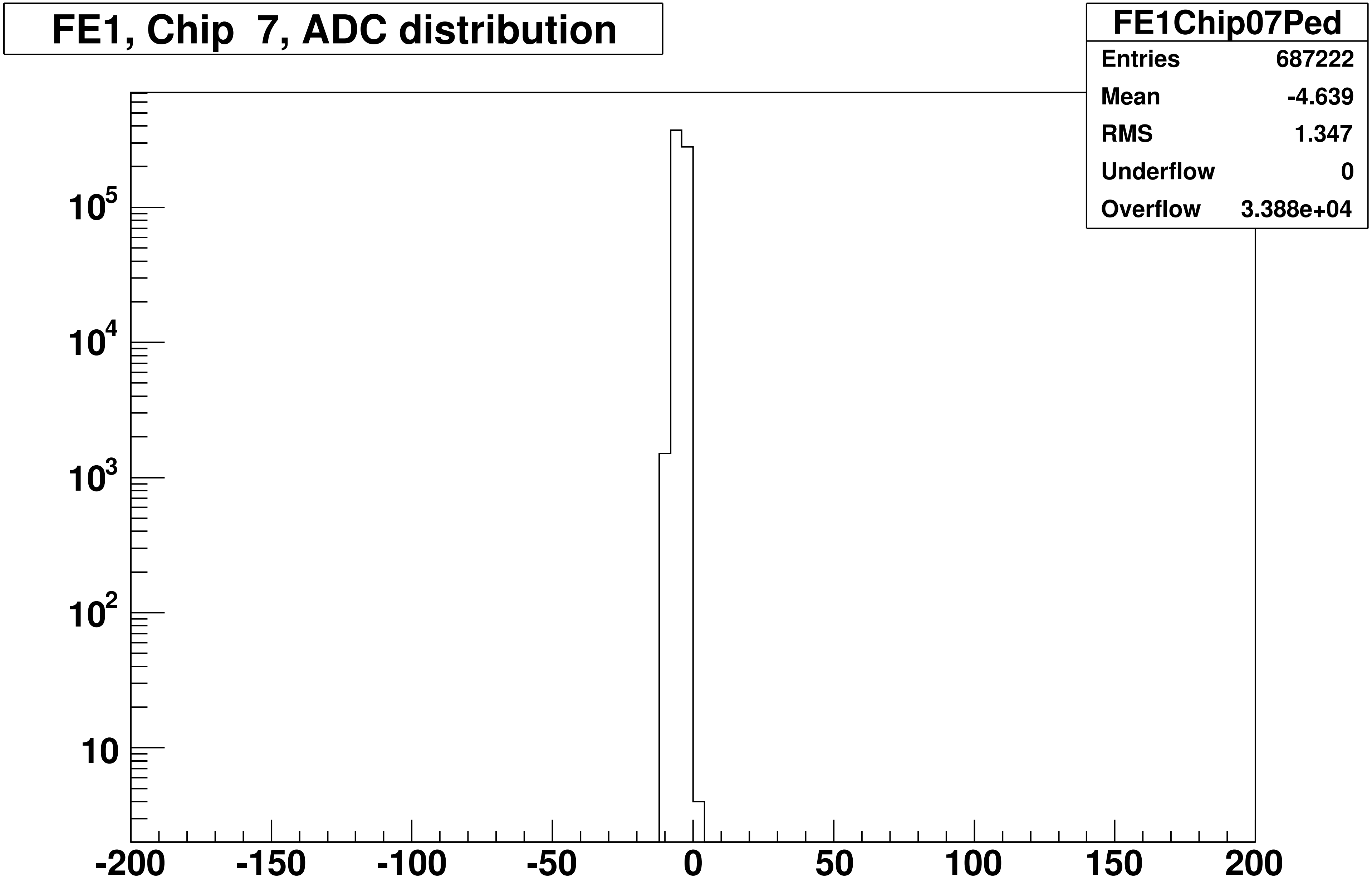
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 687222
Mean -4.639
RMS 1.347
Underflow 0
Overflow 3.388e+04
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10^4
10^3
10^2
10
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-150
-100
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0
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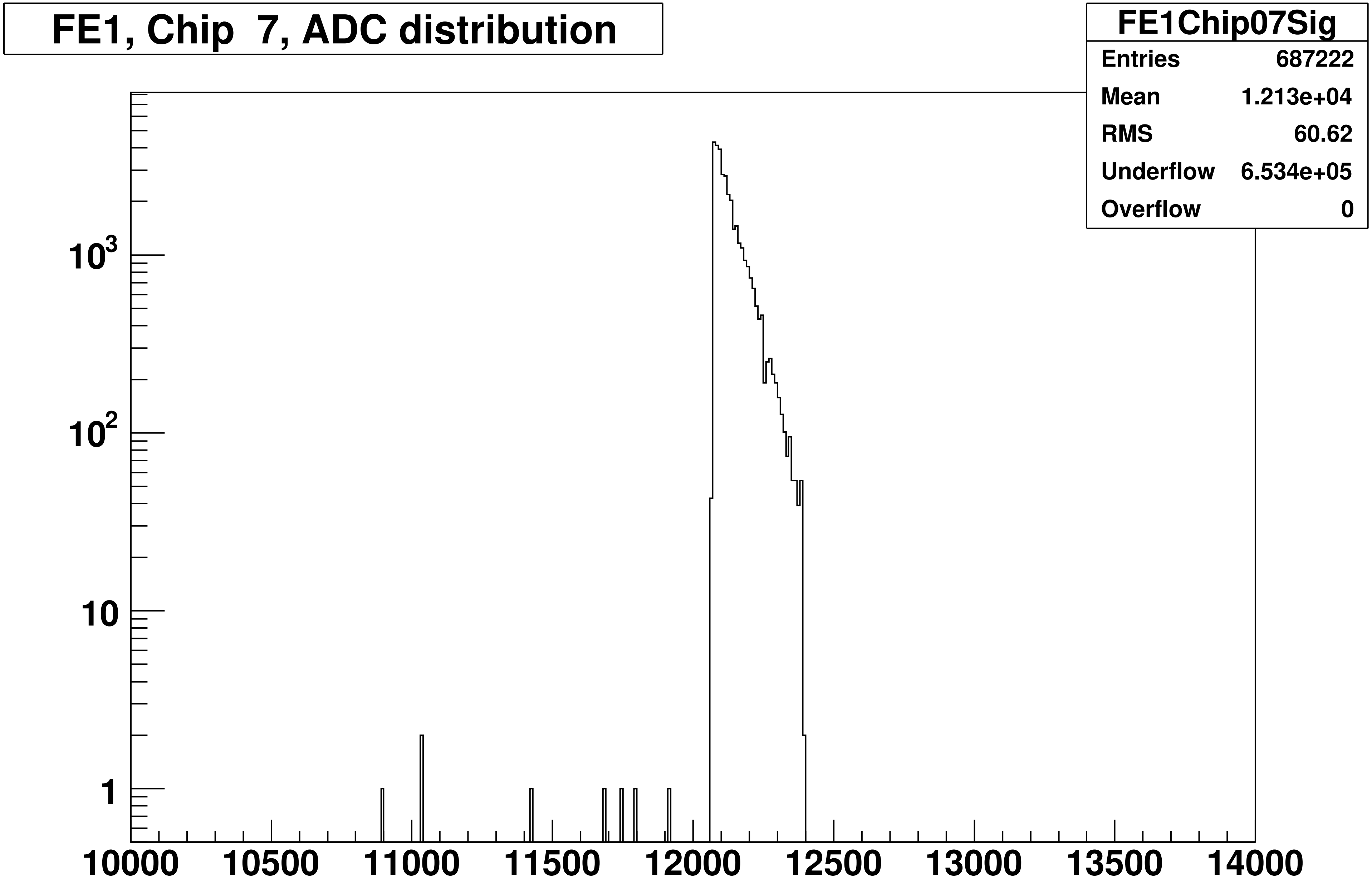
100
150
200


FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 687222
Mean 1.213e+04
RMS 60.62
Underflow 6.534e+05
Overflow 0
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10^2
10
1
10000
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11500
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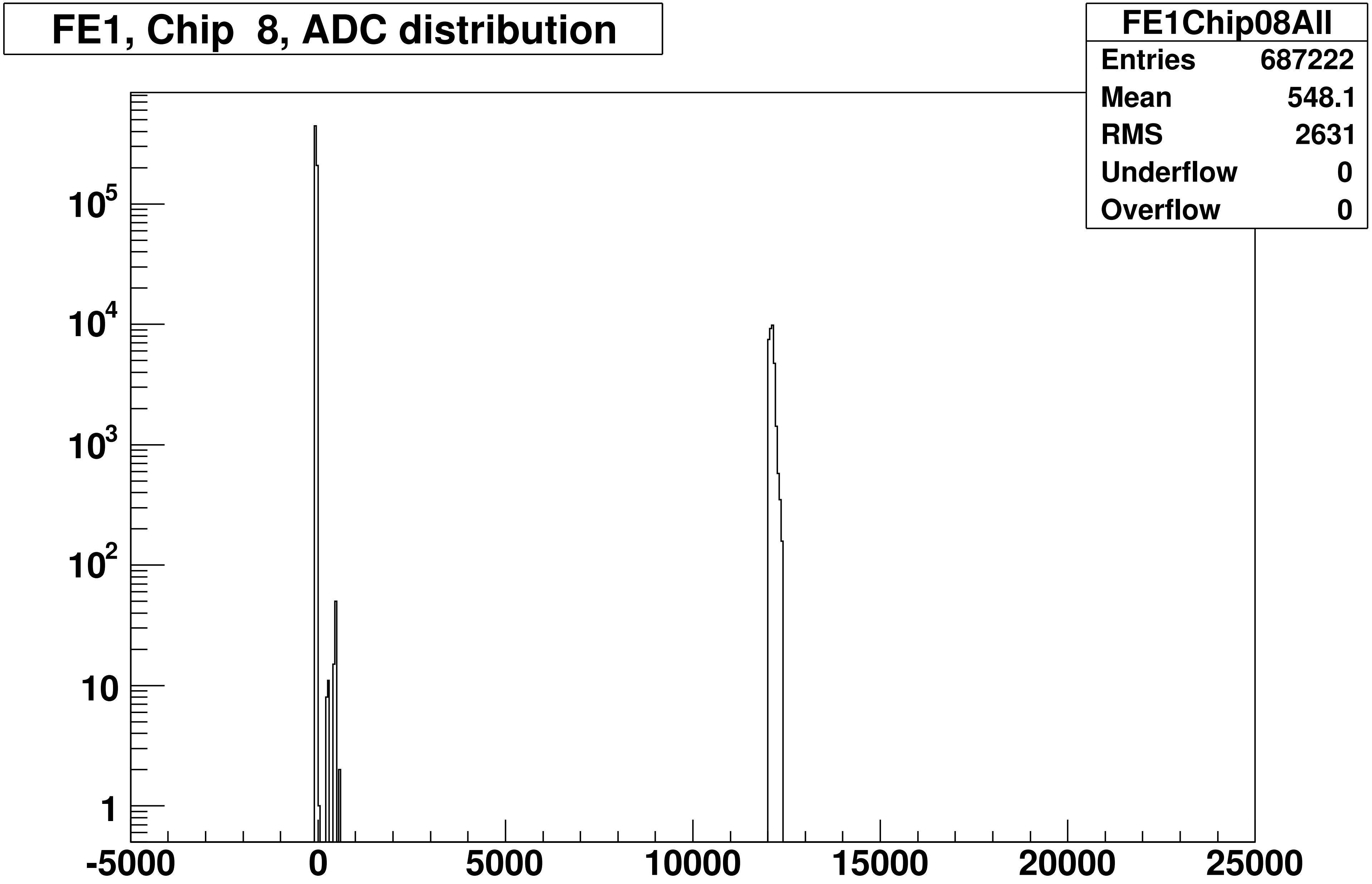

FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 687222
Mean 548.1
RMS 2631
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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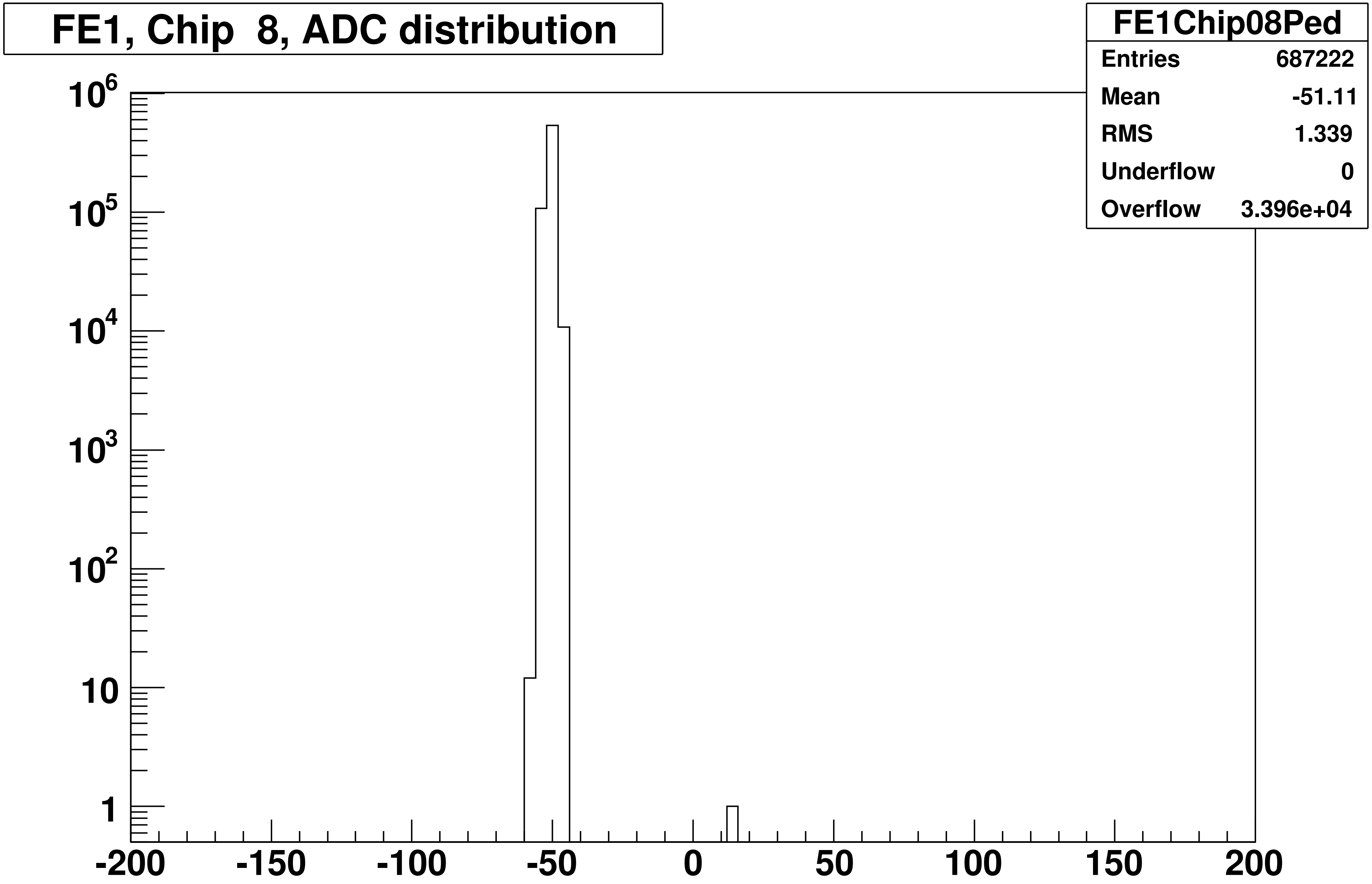
10000
15000
20000
25000



FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 687222
Mean -51.11
RMS 1.339
Underflow 0
Overflow 3.396e+04
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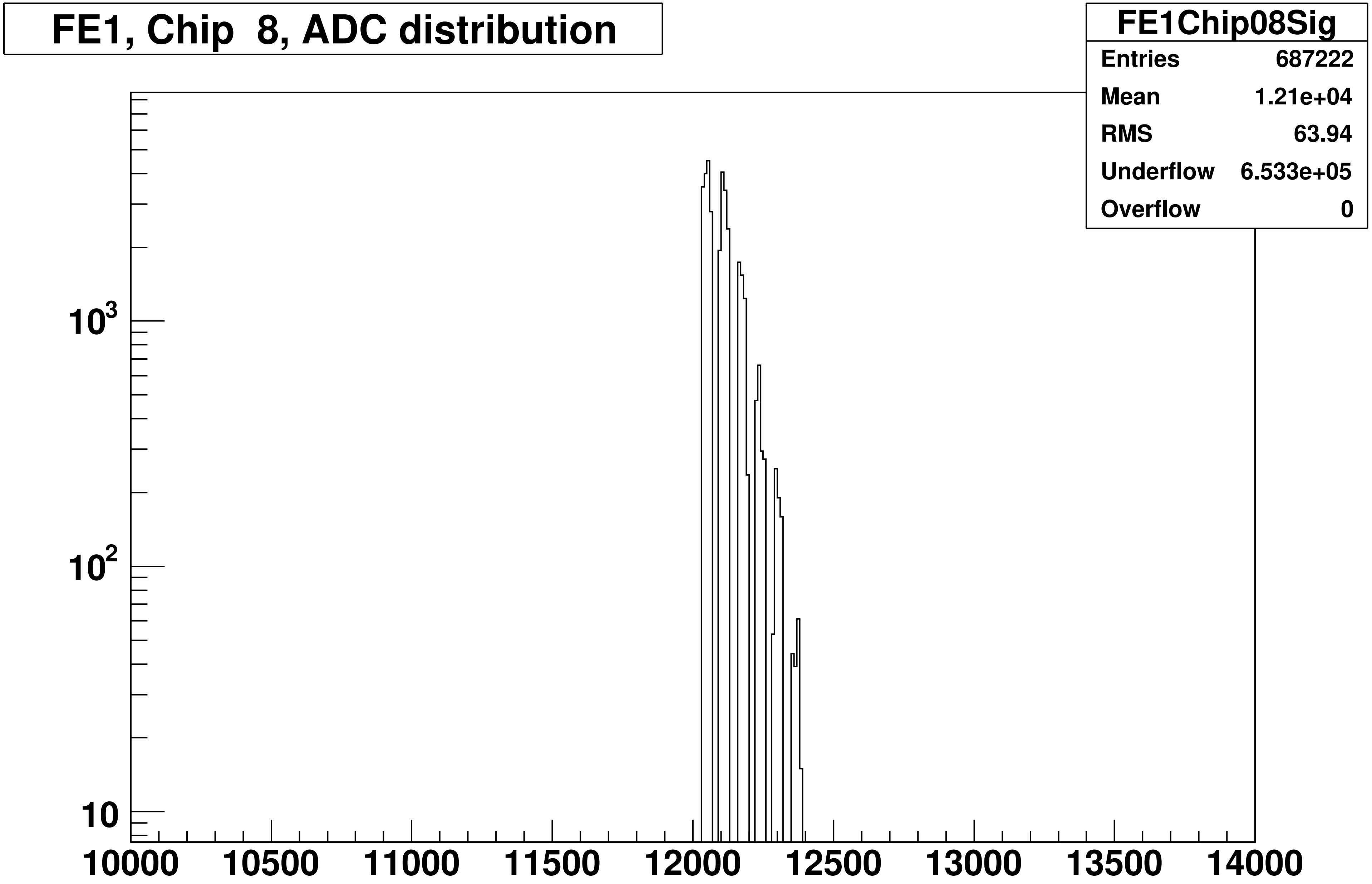
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 687222
Mean 1.21e+04
RMS 63.94
Underflow 6.533e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
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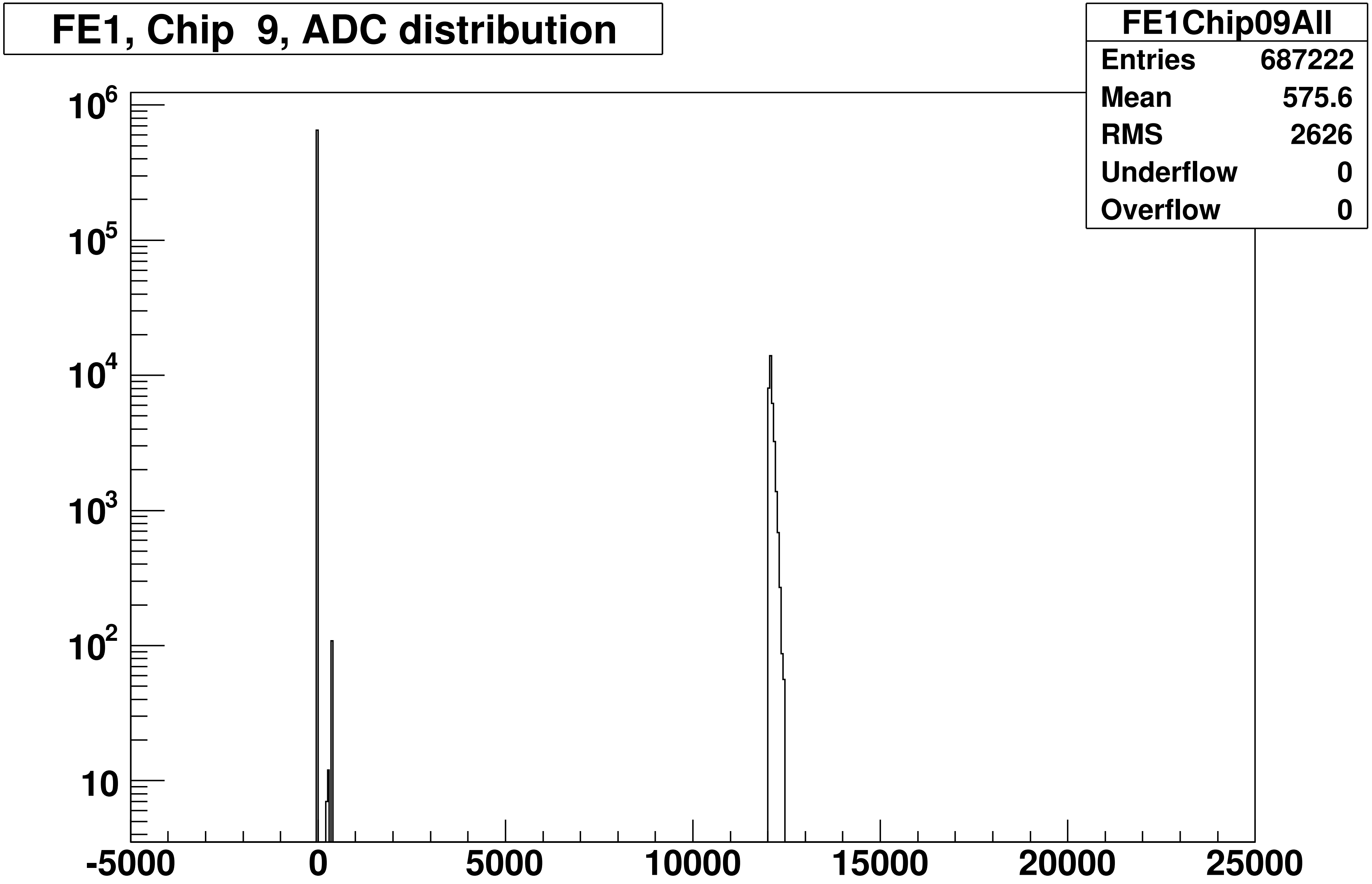
FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 687222
Mean 575.6
RMS 2626
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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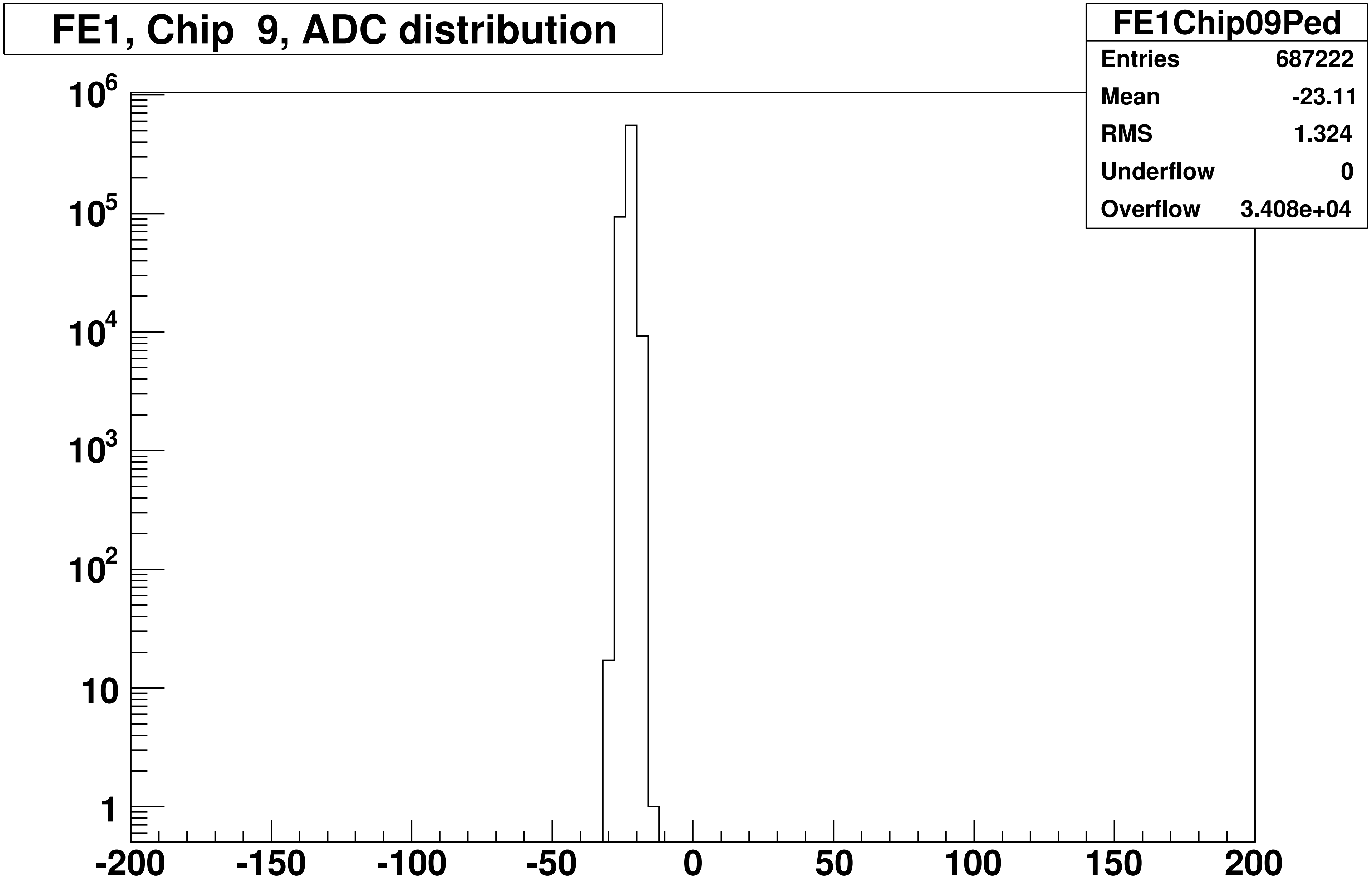
10000
15000
20000
25000


FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 687222
Mean -23.11
RMS 1.324
Underflow 0
Overflow 3.408e+04
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10^5
10^4
10^3
10^2
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1
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-100
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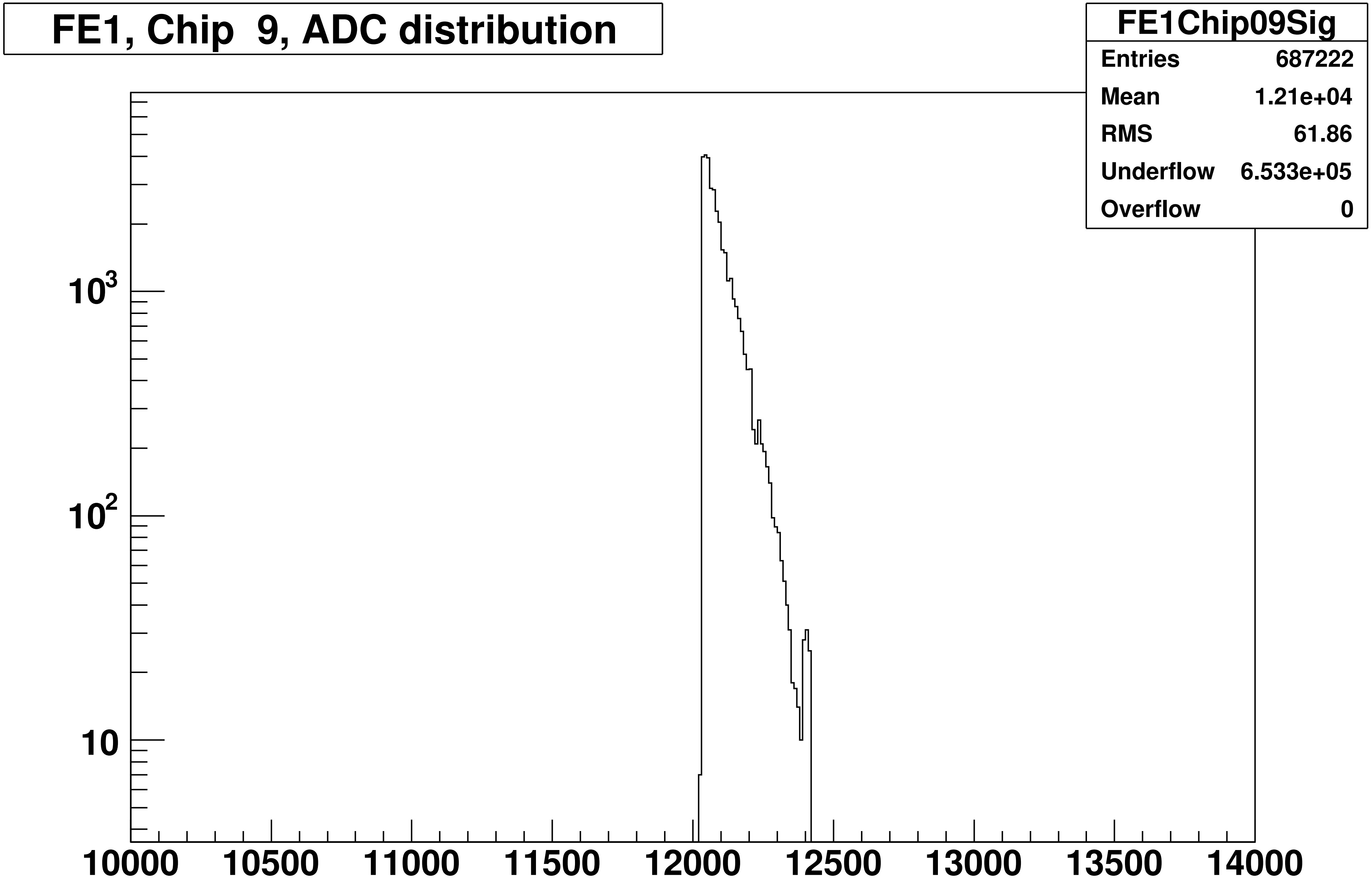
200


FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 687222
Mean 1.21e+04
RMS 61.86
Underflow 6.533e+05
Overflow 0
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10^2
10^3
10000
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11000
11500
12000
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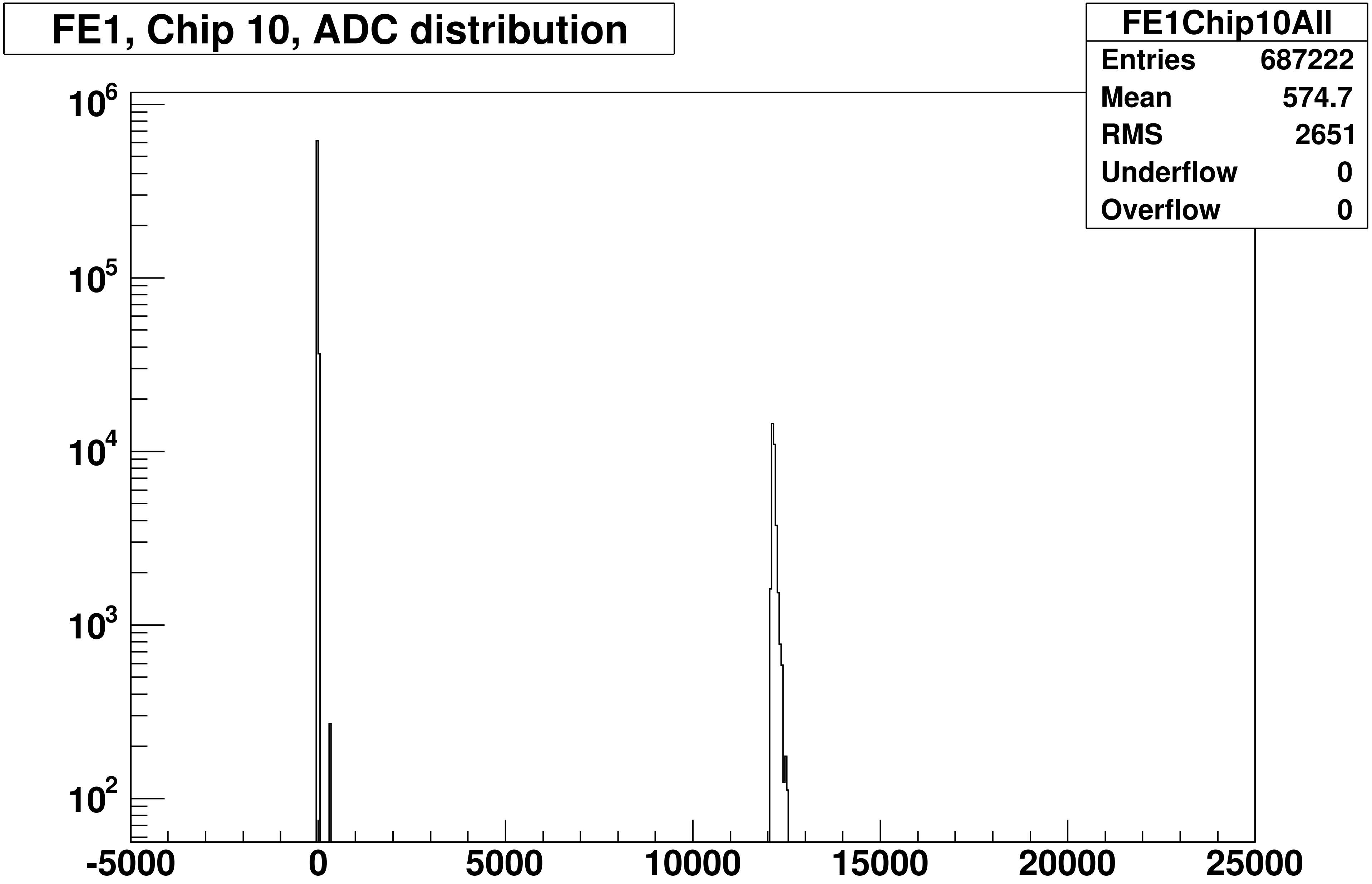
FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 687222
Mean 574.7
RMS 2651
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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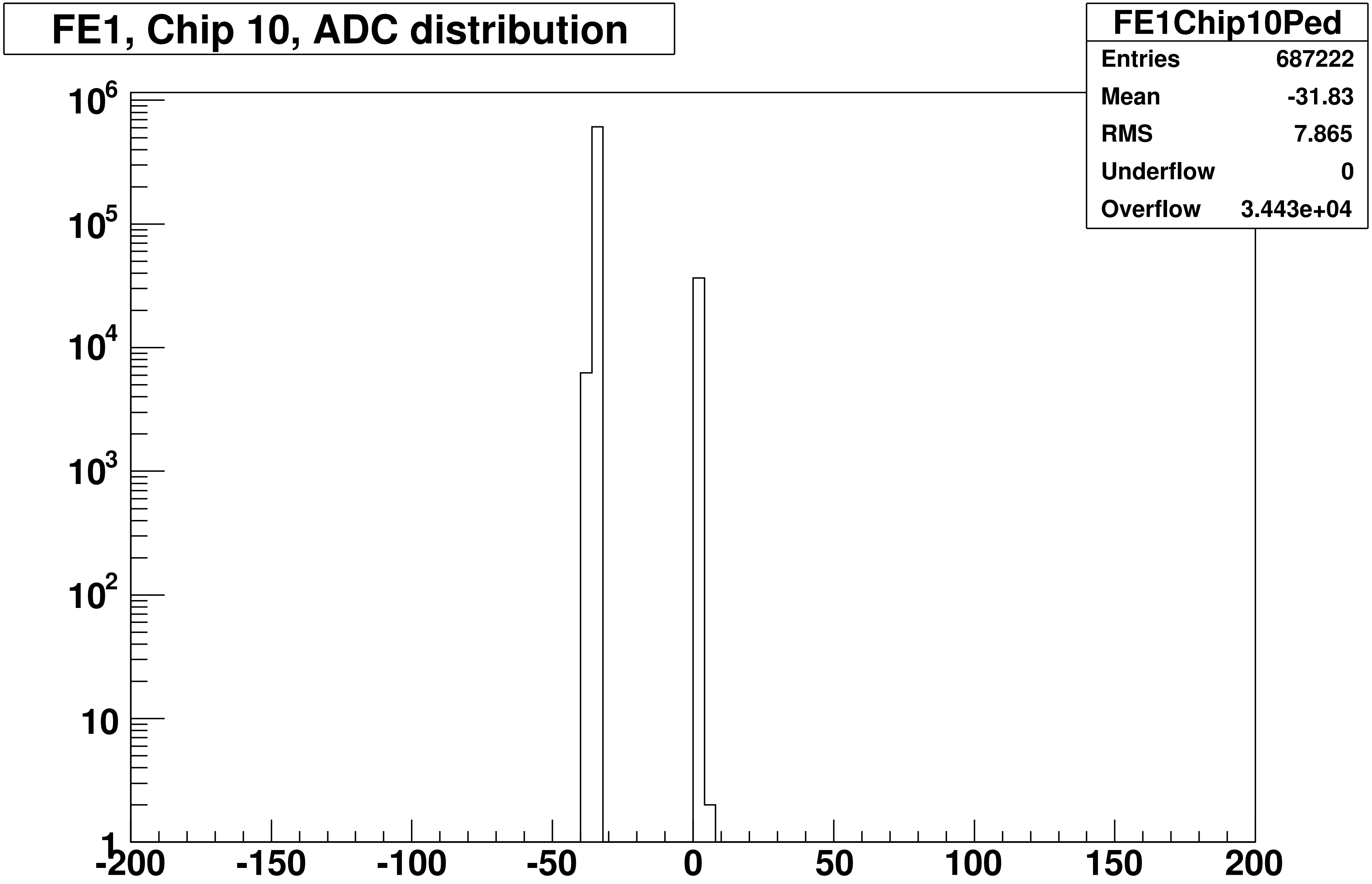
0
5000
10000
15000
20000
25000


FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 687222
Mean -31.83
RMS 7.865
Underflow 0
Overflow 3.443e+04
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10^5
10^4
10^3
10^2
10
1
-200
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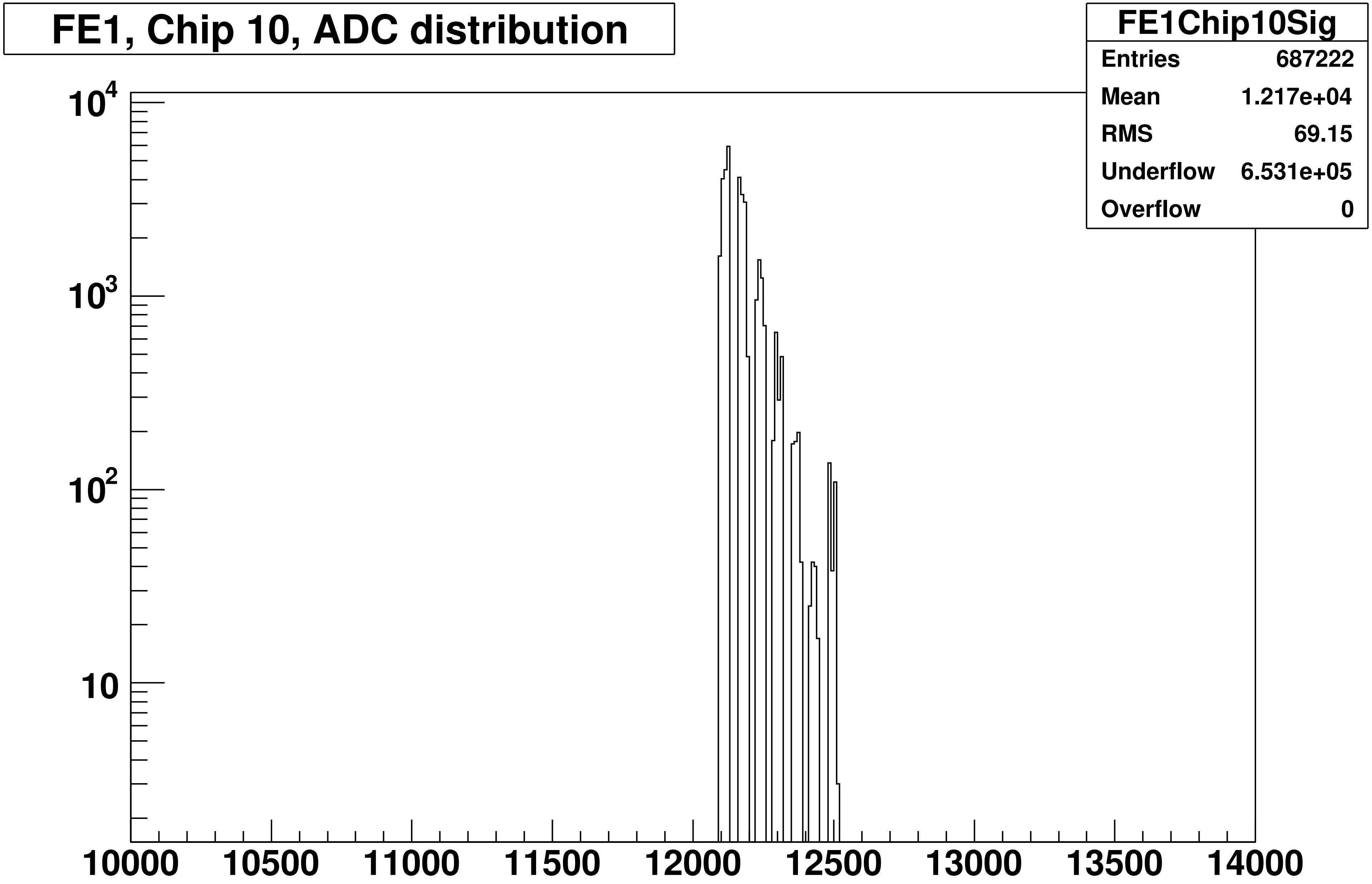
-100
-50
0
50
100
150
200


FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 687222
Mean 1.217e+04
RMS 69.15
Underflow 6.531e+05
Overflow 0
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10^3
10^2
10
10000
10500
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11500
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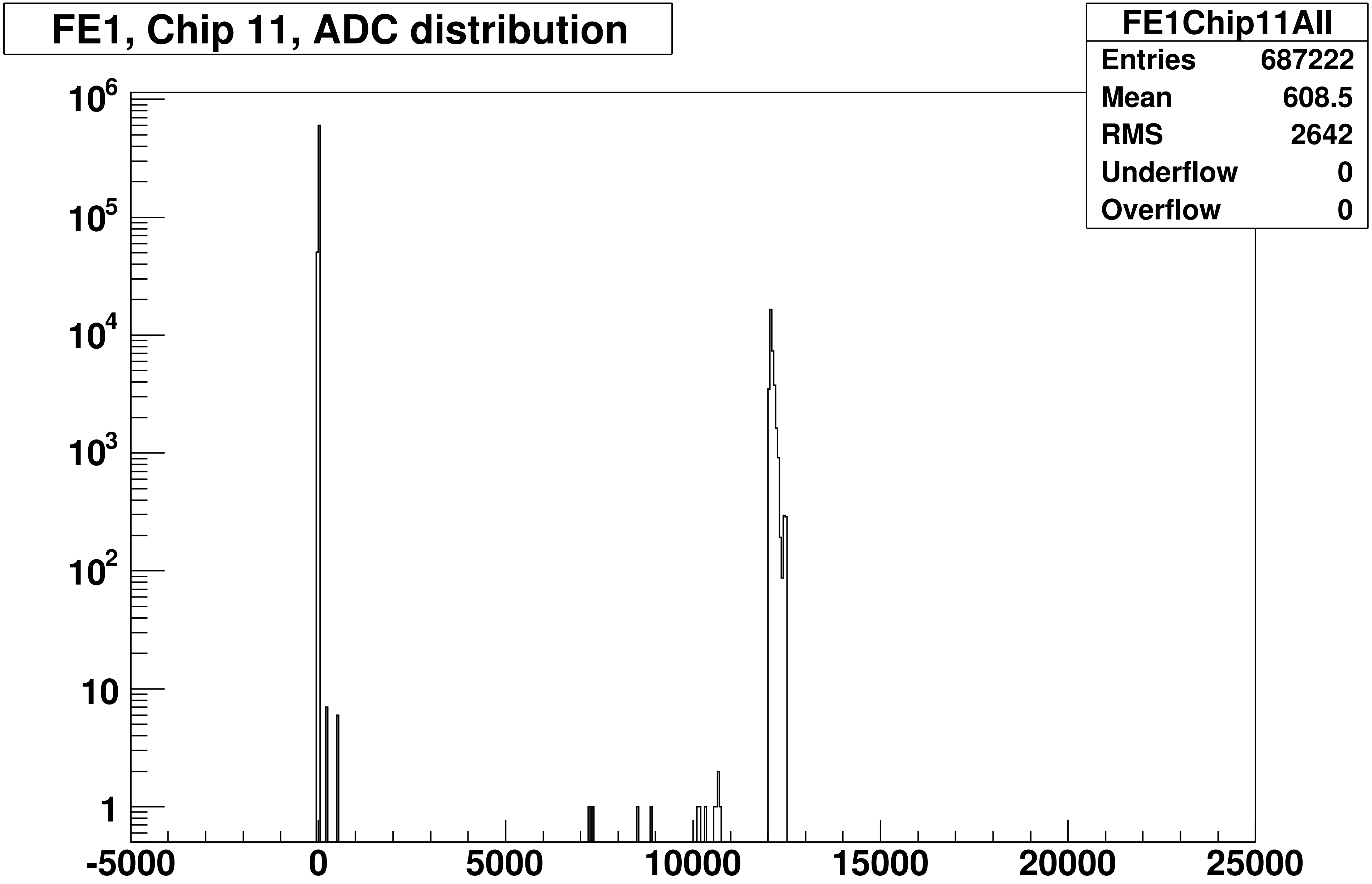
14000


FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 687222
Mean 608.5
RMS 2642
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
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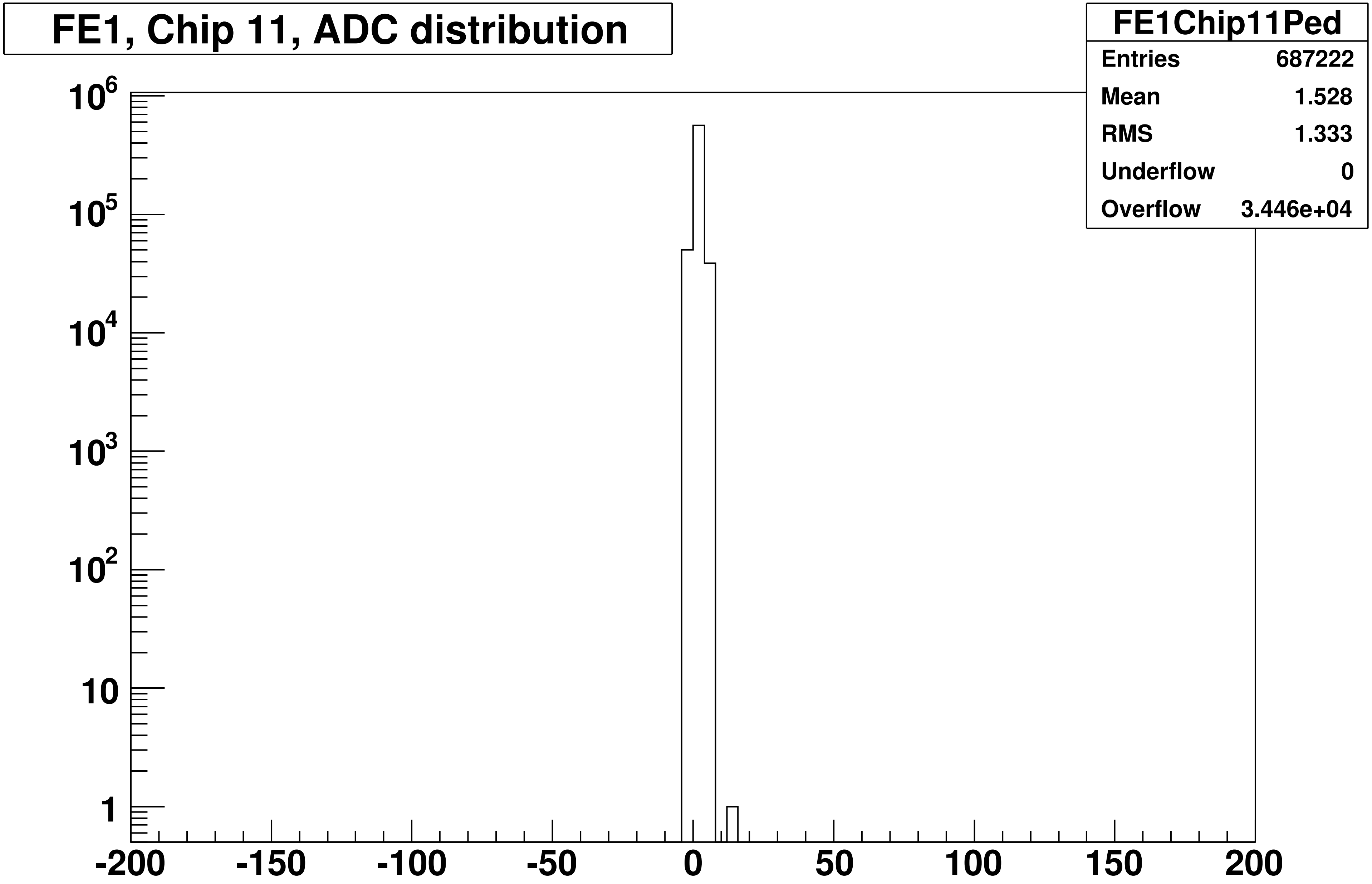
20000
25000


FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 687222
Mean 1.528
RMS 1.333
Underflow 0
Overflow 3.446e+04
10^6
10^5
10^4
10^3
10^2
10
1
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-100
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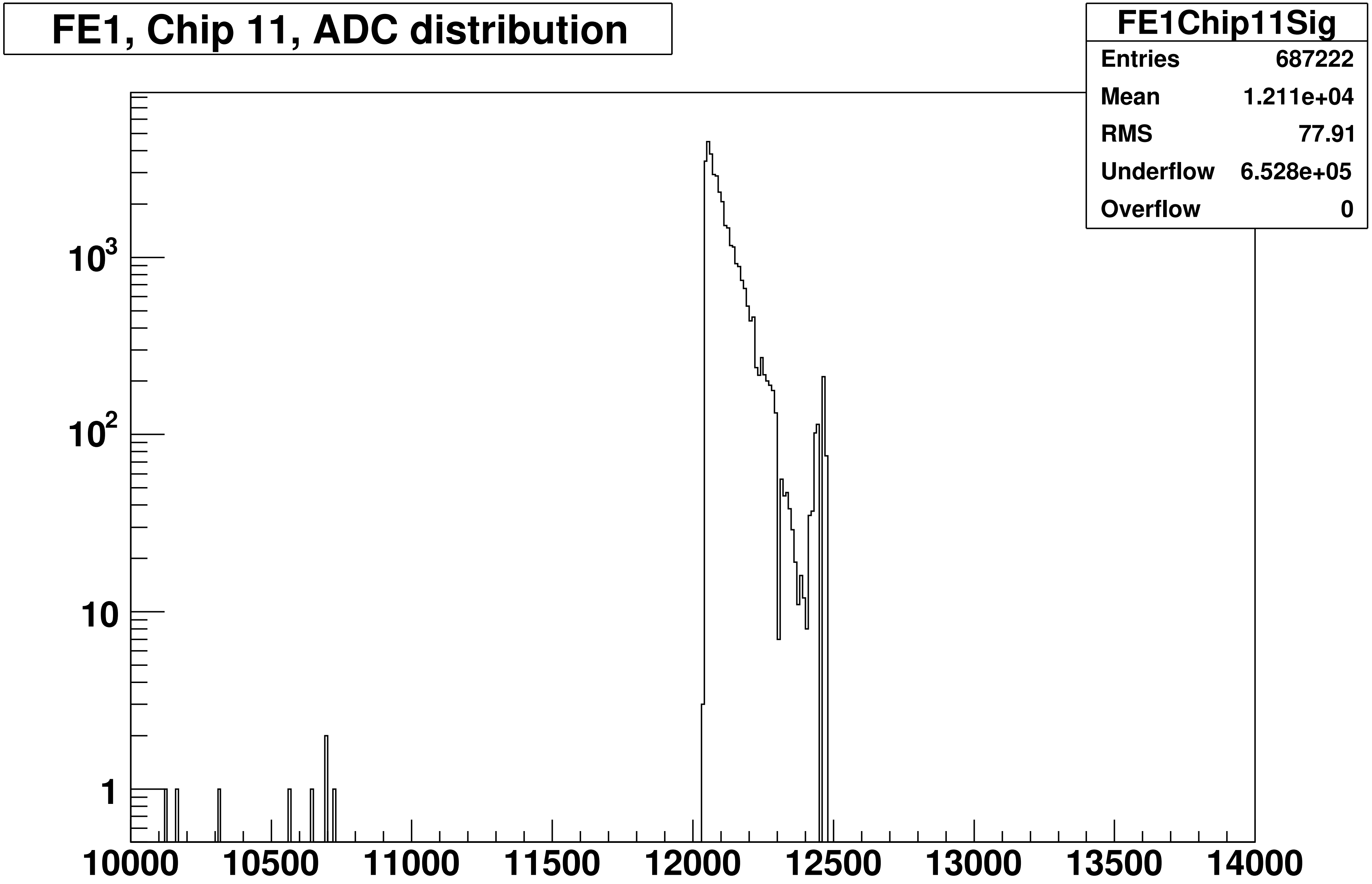
150
200



FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 687222
Mean 1.211e+04
RMS 77.91
Underflow 6.528e+05
Overflow 0
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10^2
10
1
10000
10500
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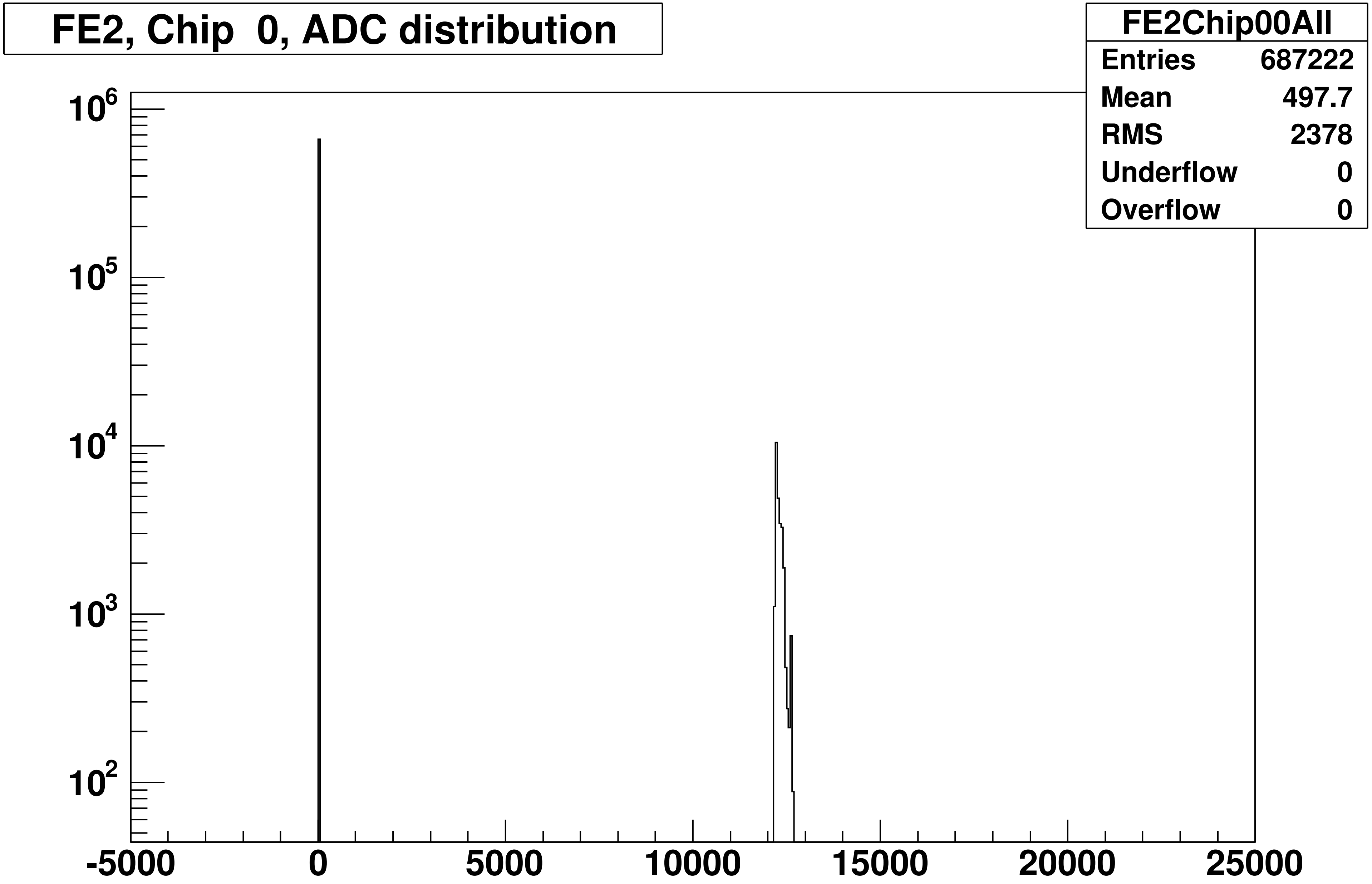

FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 687222
Mean 497.7
RMS 2378
Underflow 0
Overflow 0
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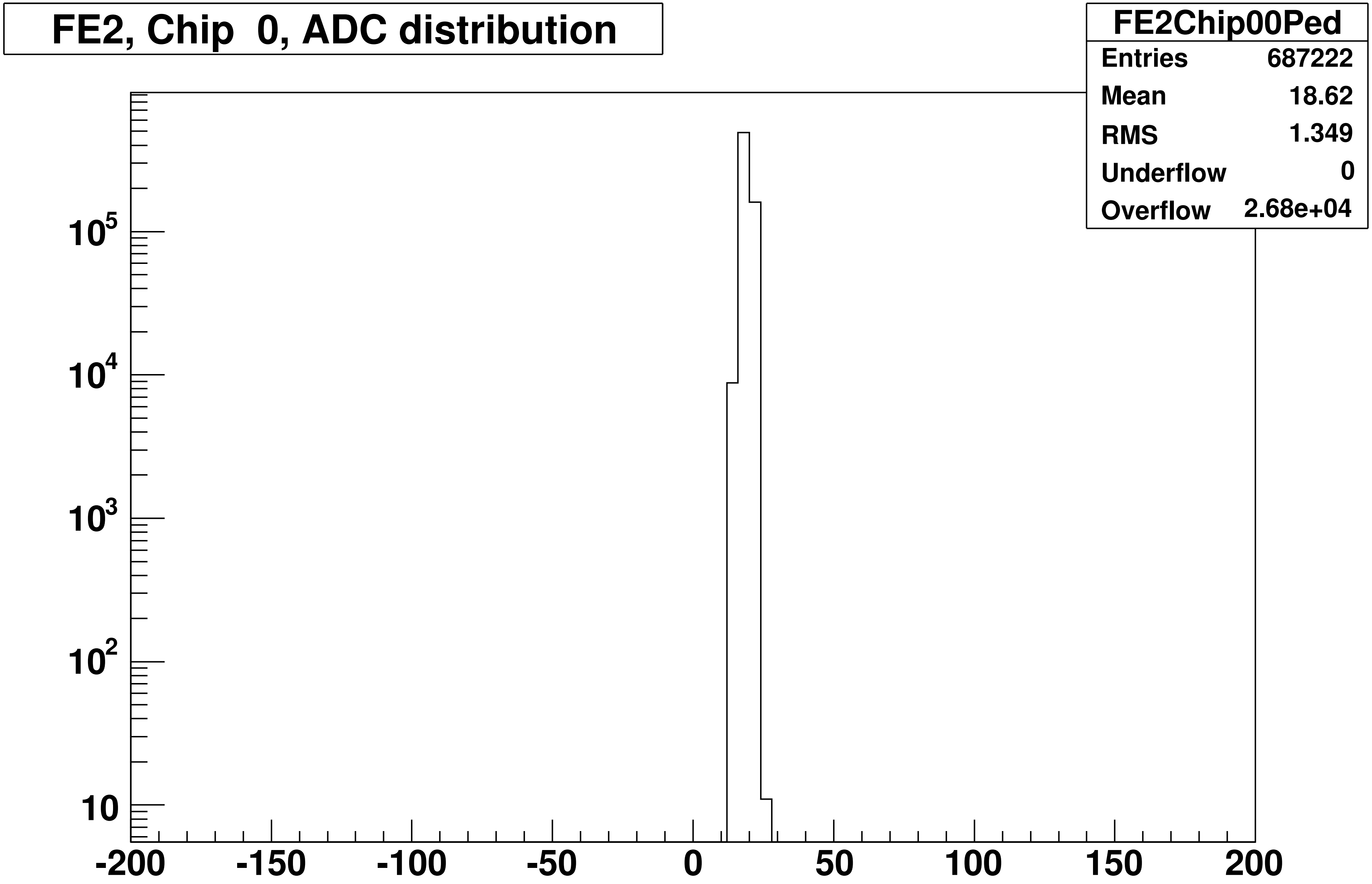
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 687222
Mean 18.62
RMS 1.349
Underflow 0
Overflow 2.68e+04
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10^4
10^3
10^2
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-200
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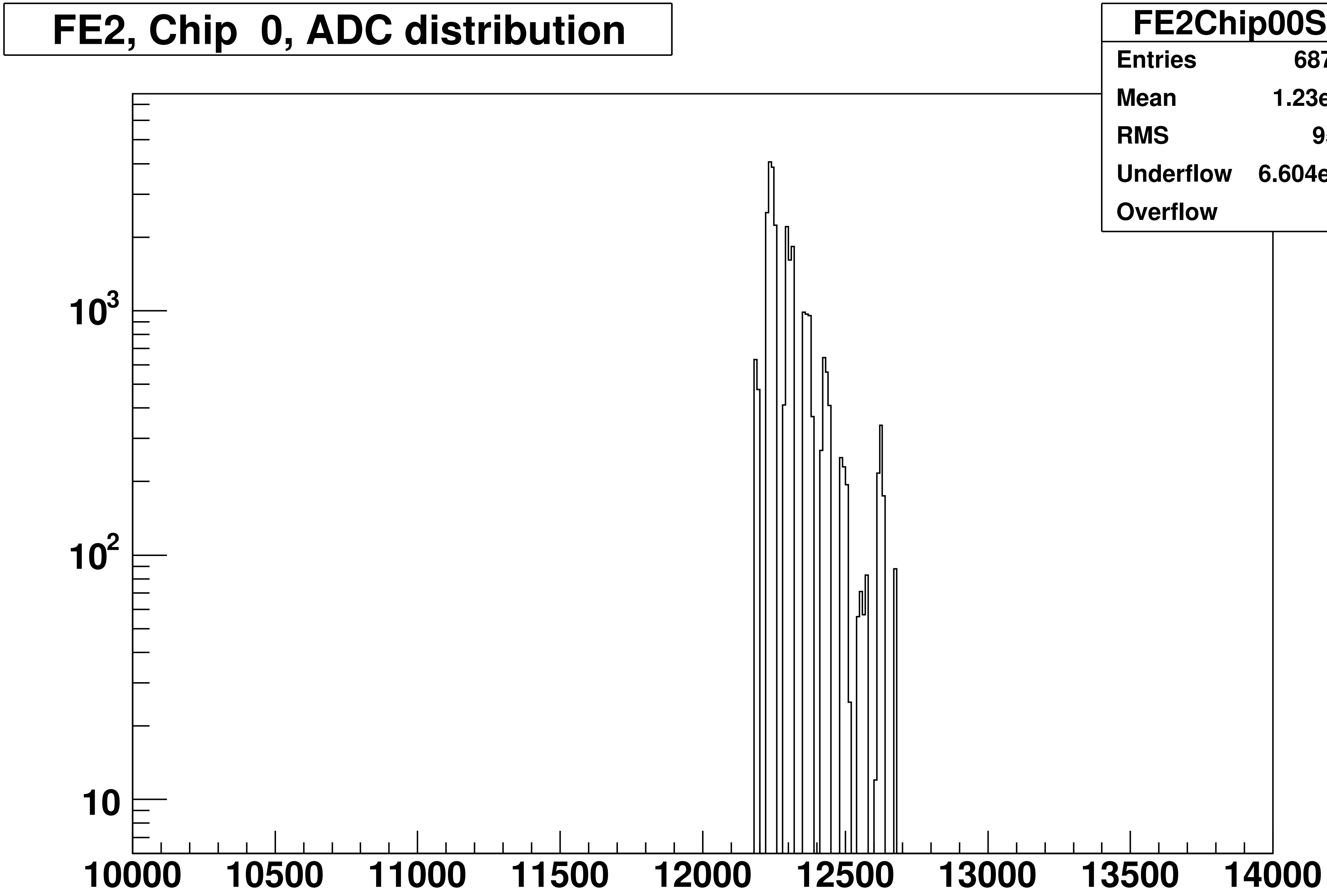
100
150
200


FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 687222
Mean 1.23e+04
RMS 95.46
Underflow 6.604e+05
Overflow 0
10^3
10^2
10
10000
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11500
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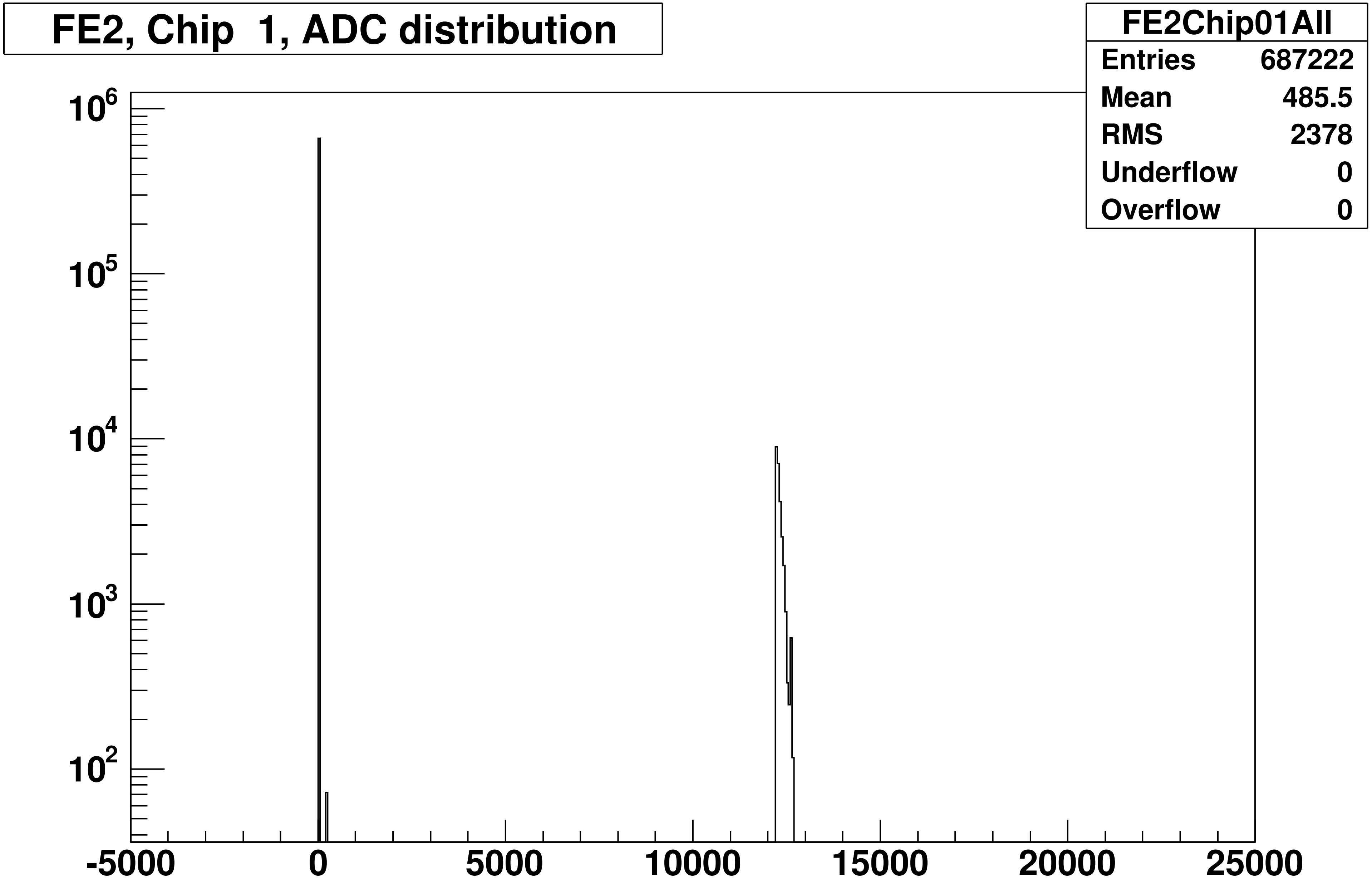
FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 687222
Mean 485.5
RMS 2378
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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10000
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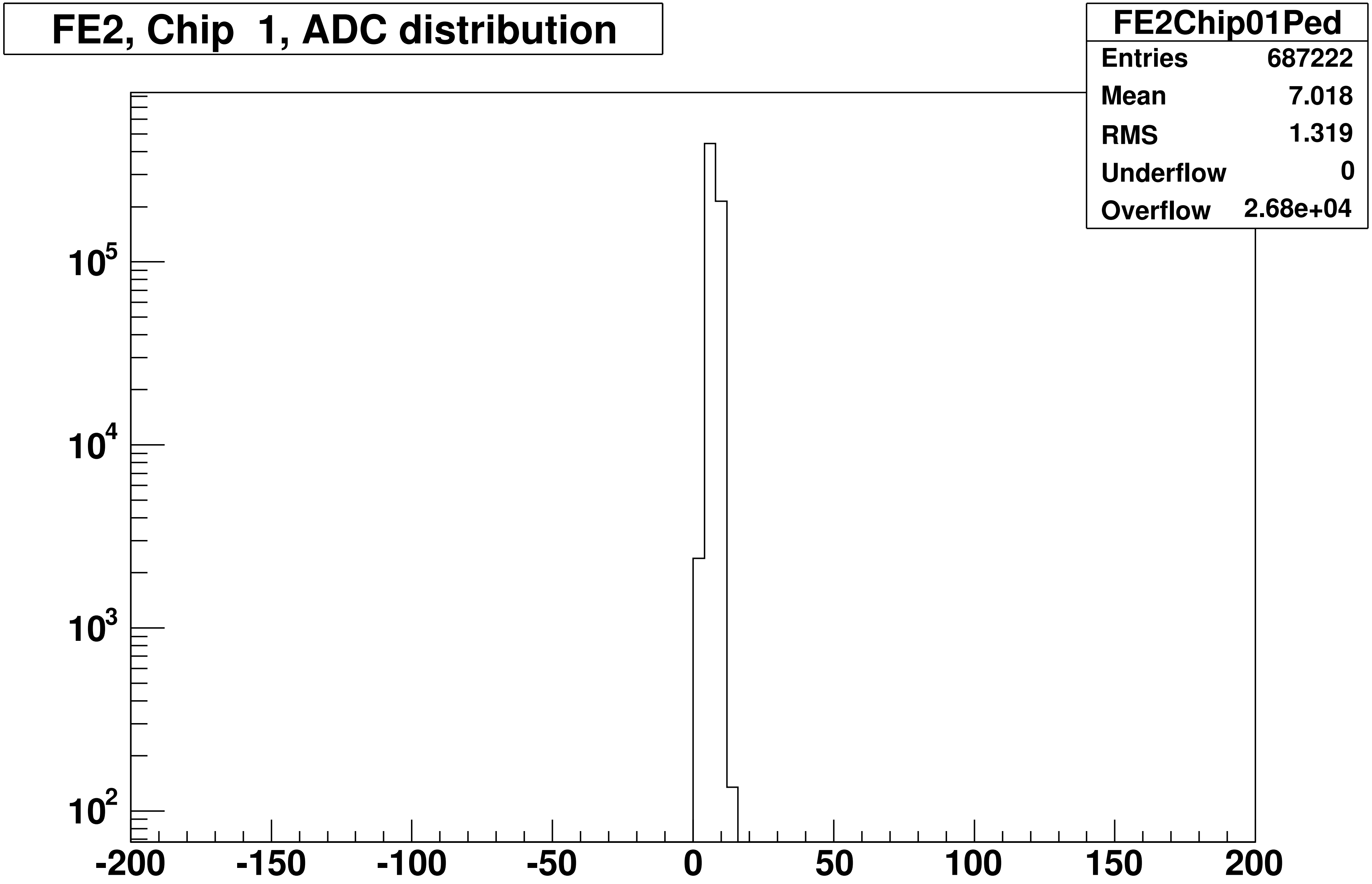
20000
25000


FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 687222
Mean 7.018
RMS 1.319
Underflow 0
Overflow 2.68e+04
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10^3
10^4
10^5
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-100
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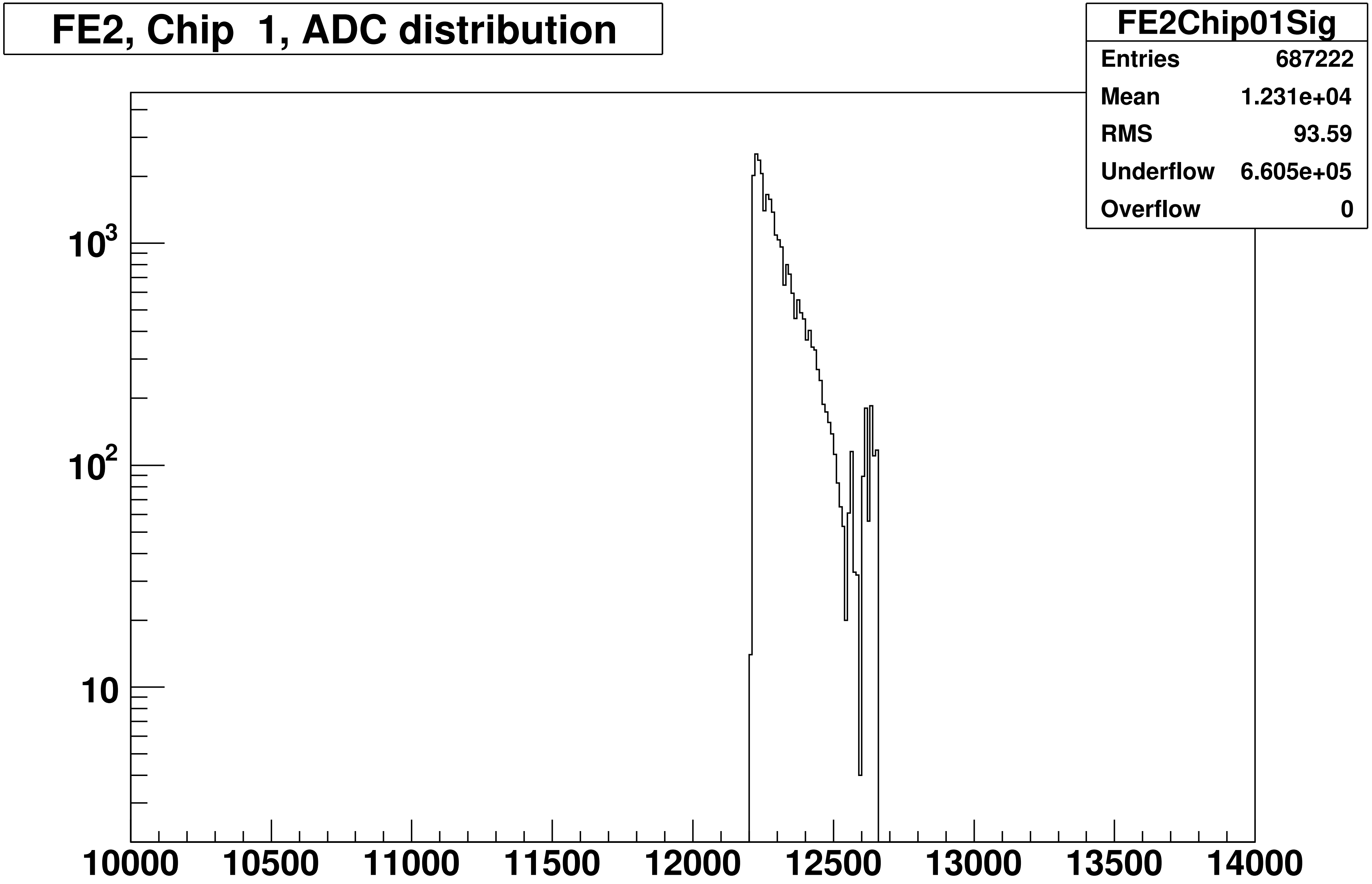
100
150
200


FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 687222
Mean 1.231e+04
RMS 93.59
Underflow 6.605e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
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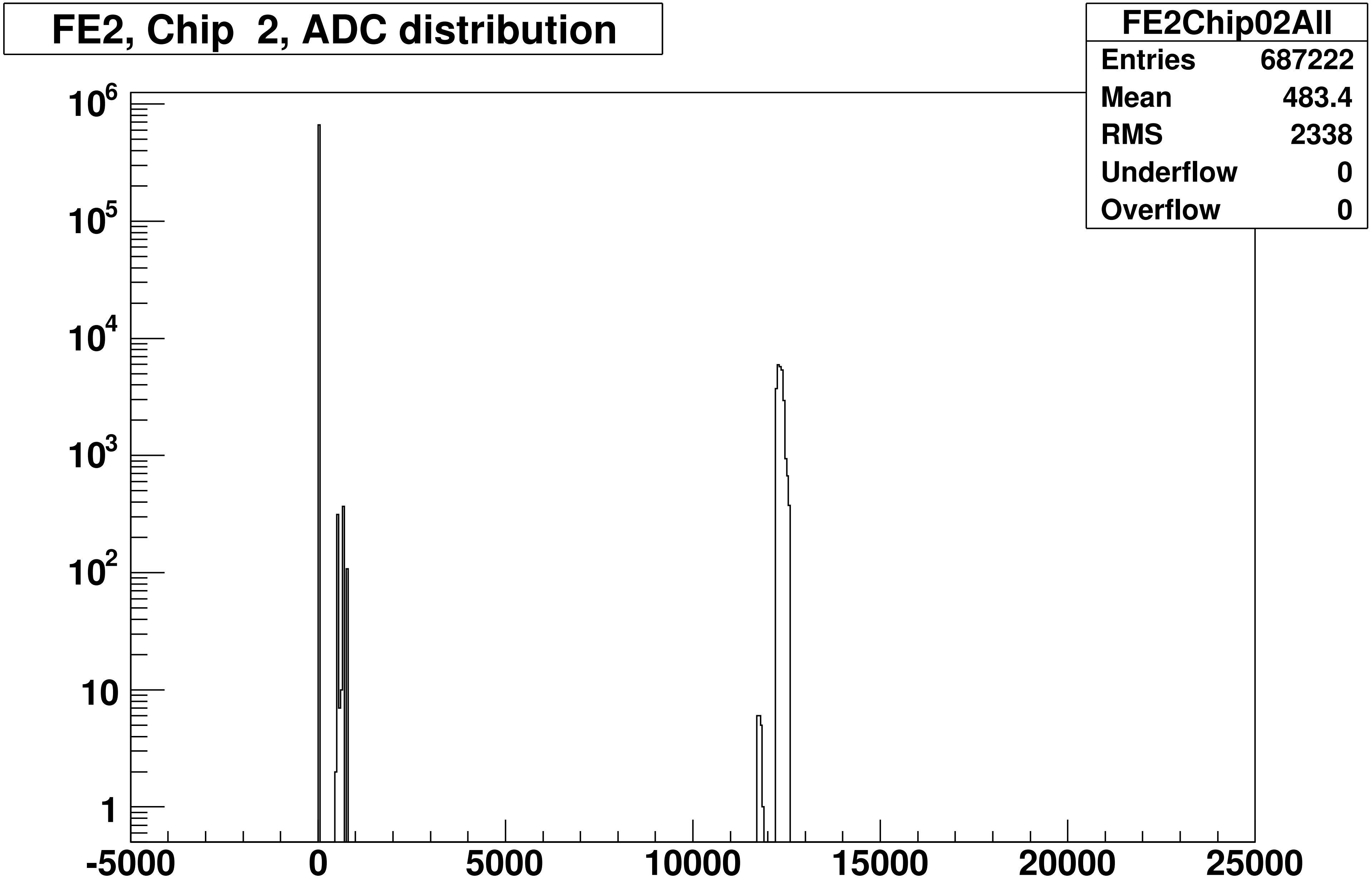
14000


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 687222
Mean 483.4
RMS 2338
Underflow 0
Overflow 0
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10^4
10^3
10^2
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0
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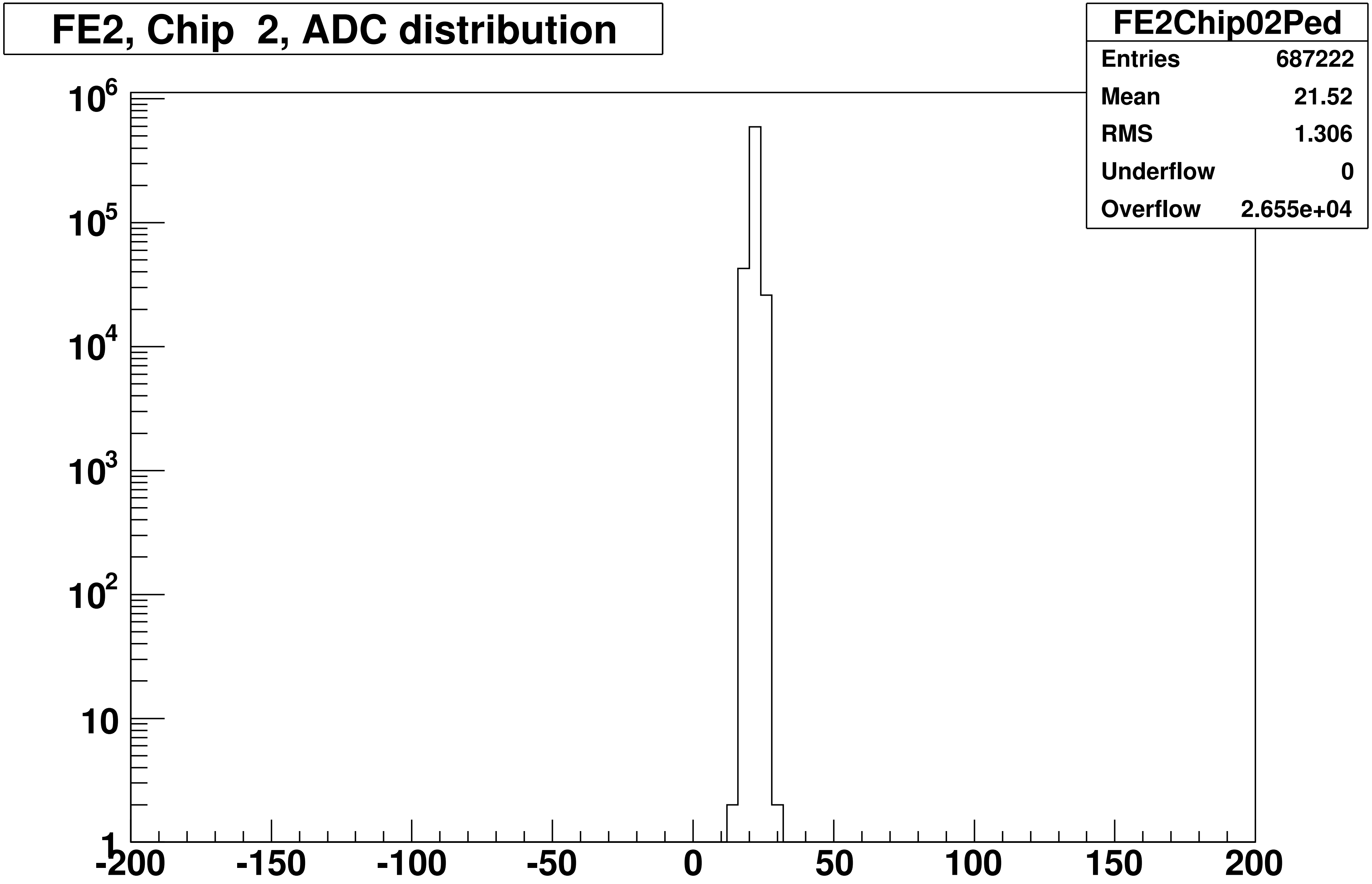
25000


FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 687222
Mean 21.52
RMS 1.306
Underflow 0
Overflow 2.655e+04
10^6
10^5
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10^3
10^2
10
1
-200
-150
-100
-50
0
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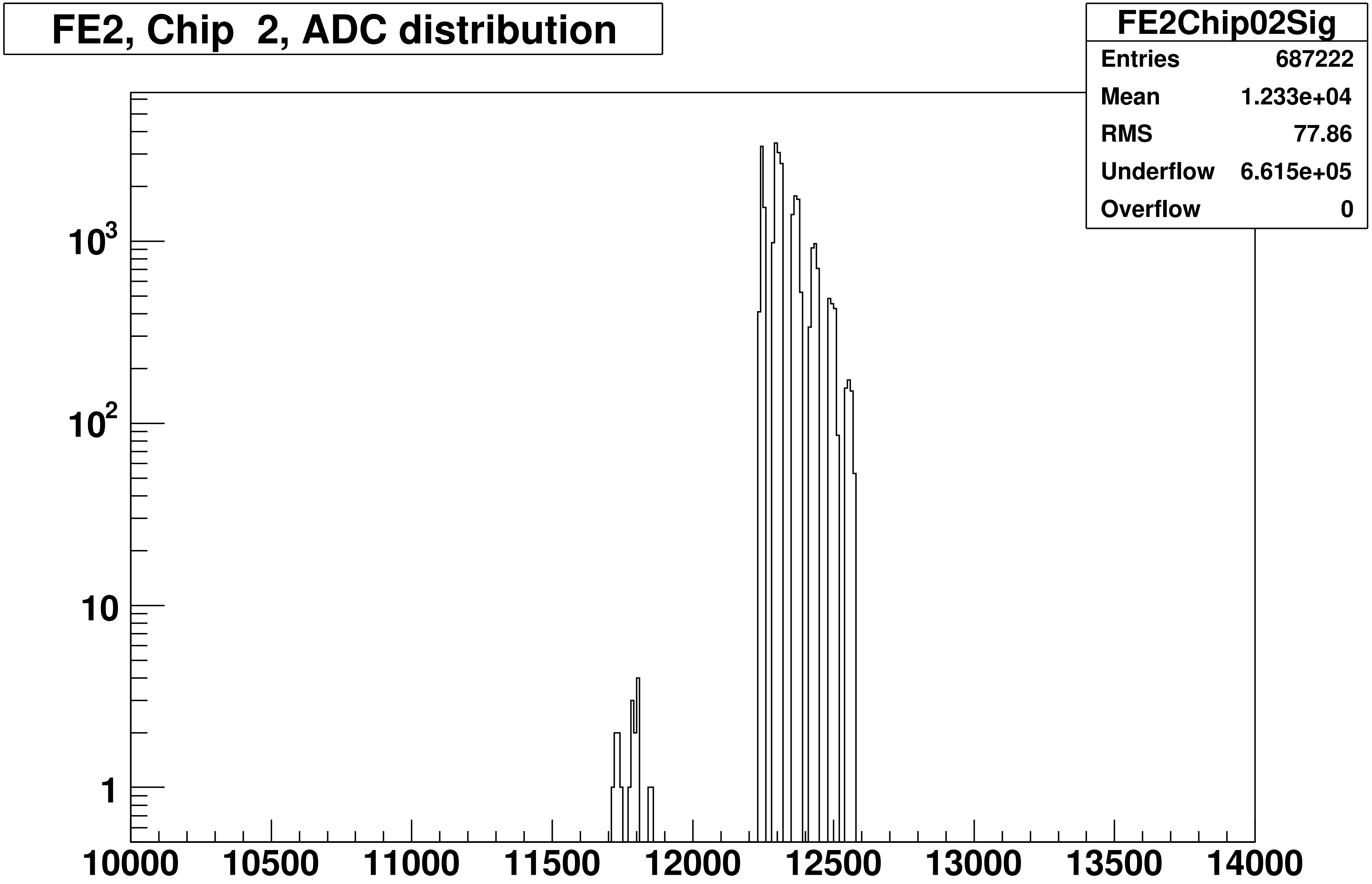
100
150
200


FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 687222
Mean 1.233e+04
RMS 77.86
Underflow 6.615e+05
Overflow 0
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10^2
10
1
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11500
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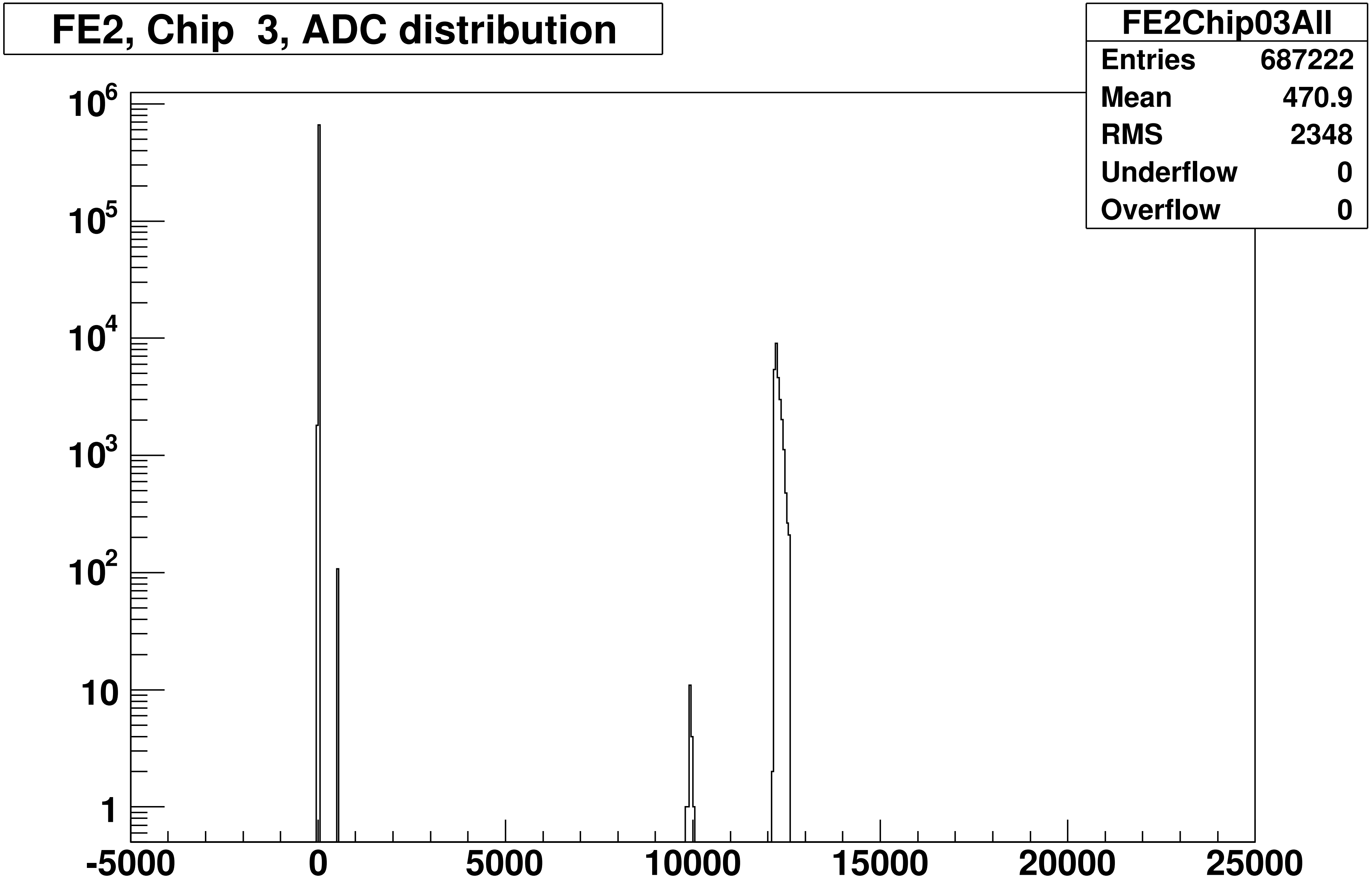
FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 687222
Mean 470.9
RMS 2348
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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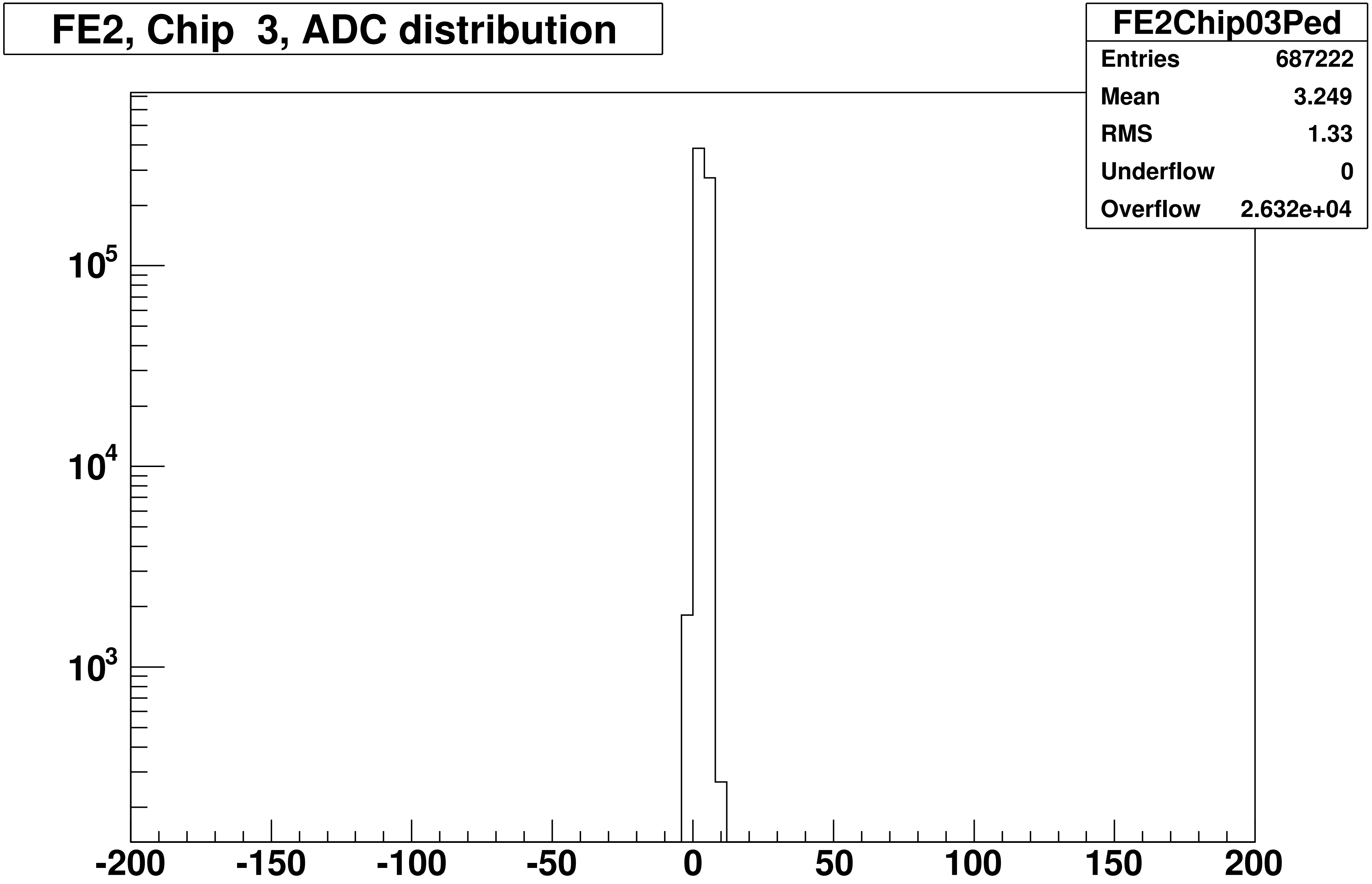

FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 687222
Mean 3.249
RMS 1.33
Underflow 0
Overflow 2.632e+04
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10^4
10^3
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0
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150
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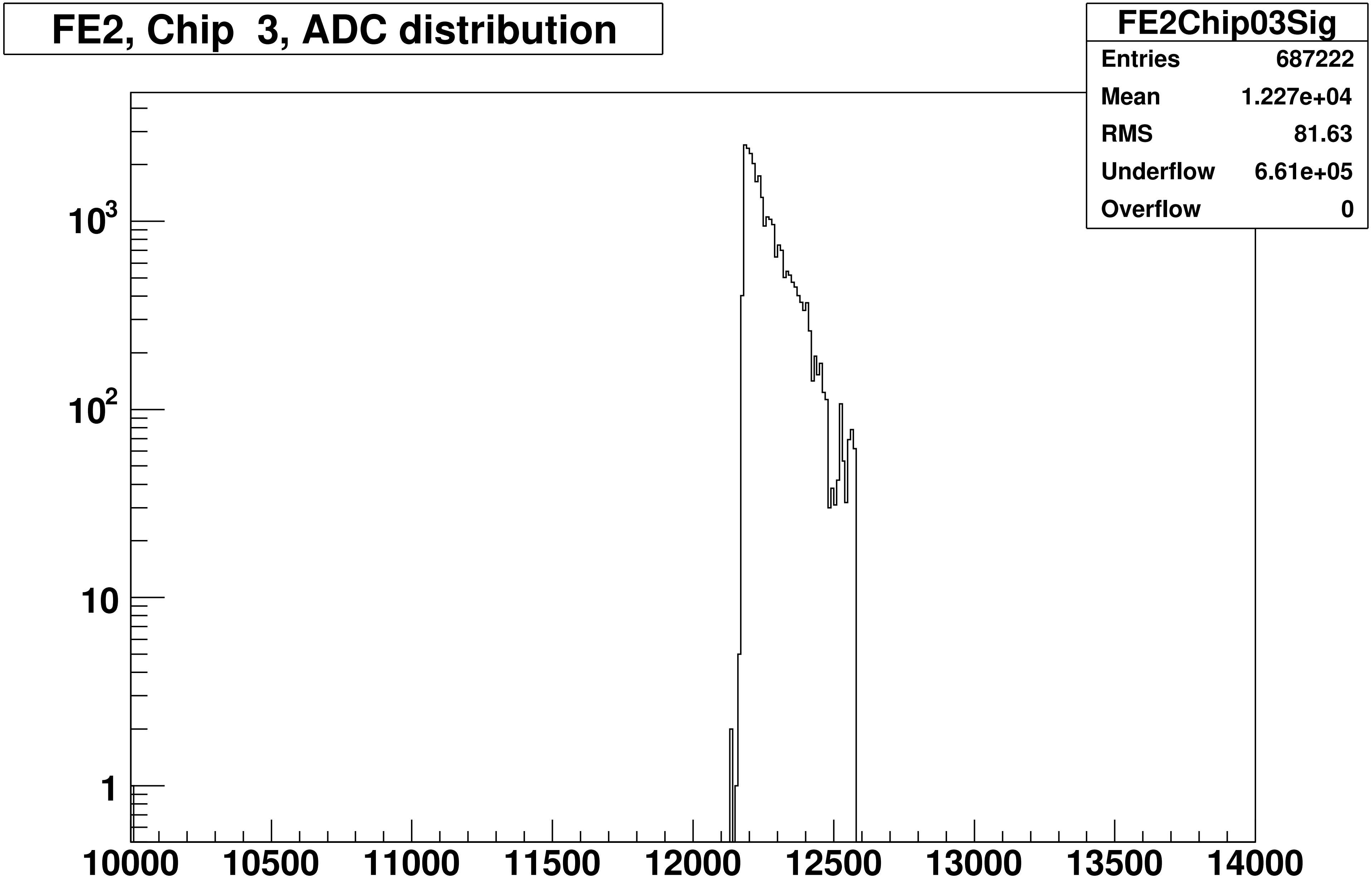
FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 687222
Mean 1.227e+04
RMS 81.63
Underflow 6.61e+05
Overflow 0
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10^2
10
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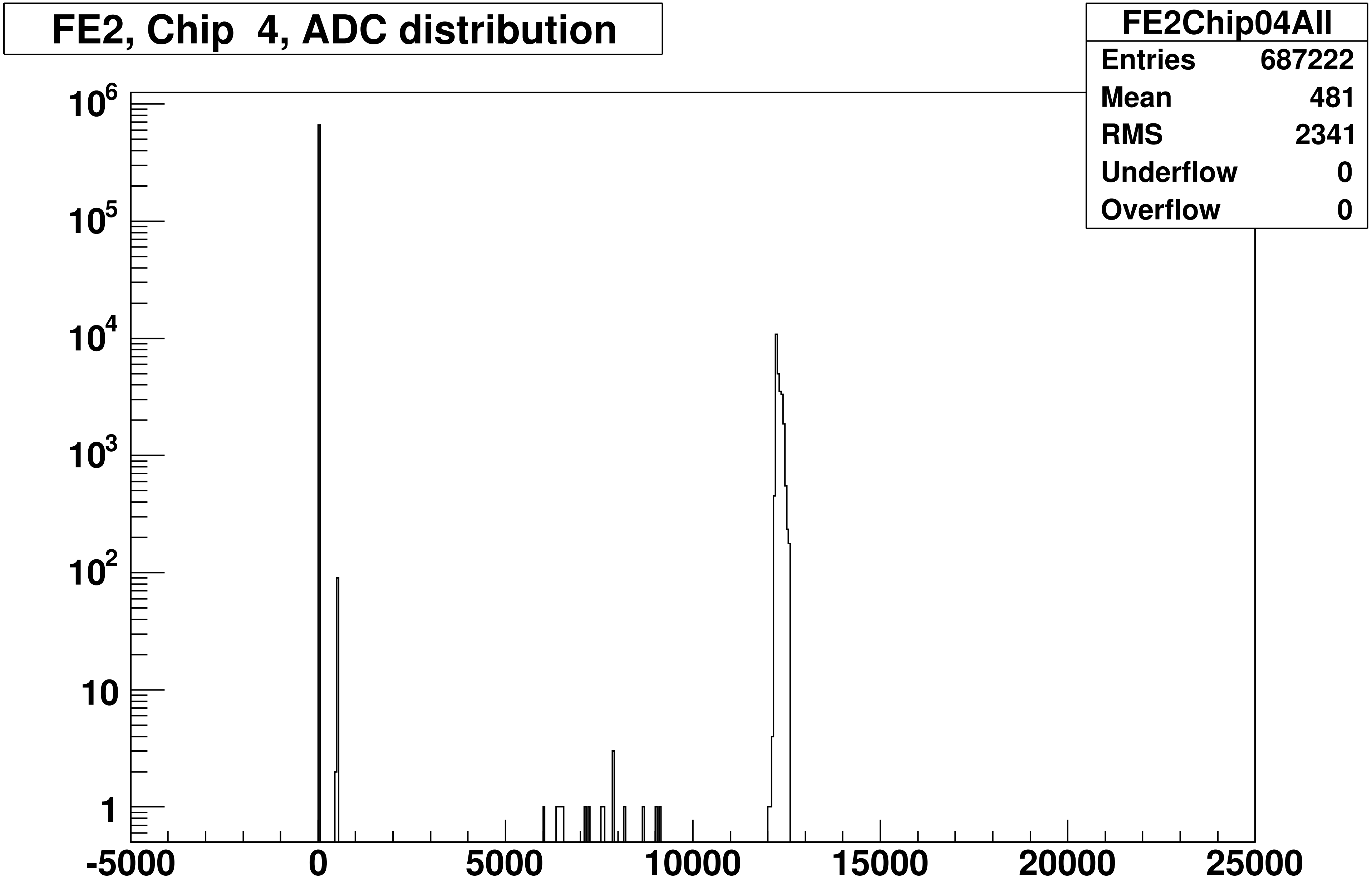
FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 687222
Mean 481
RMS 2341
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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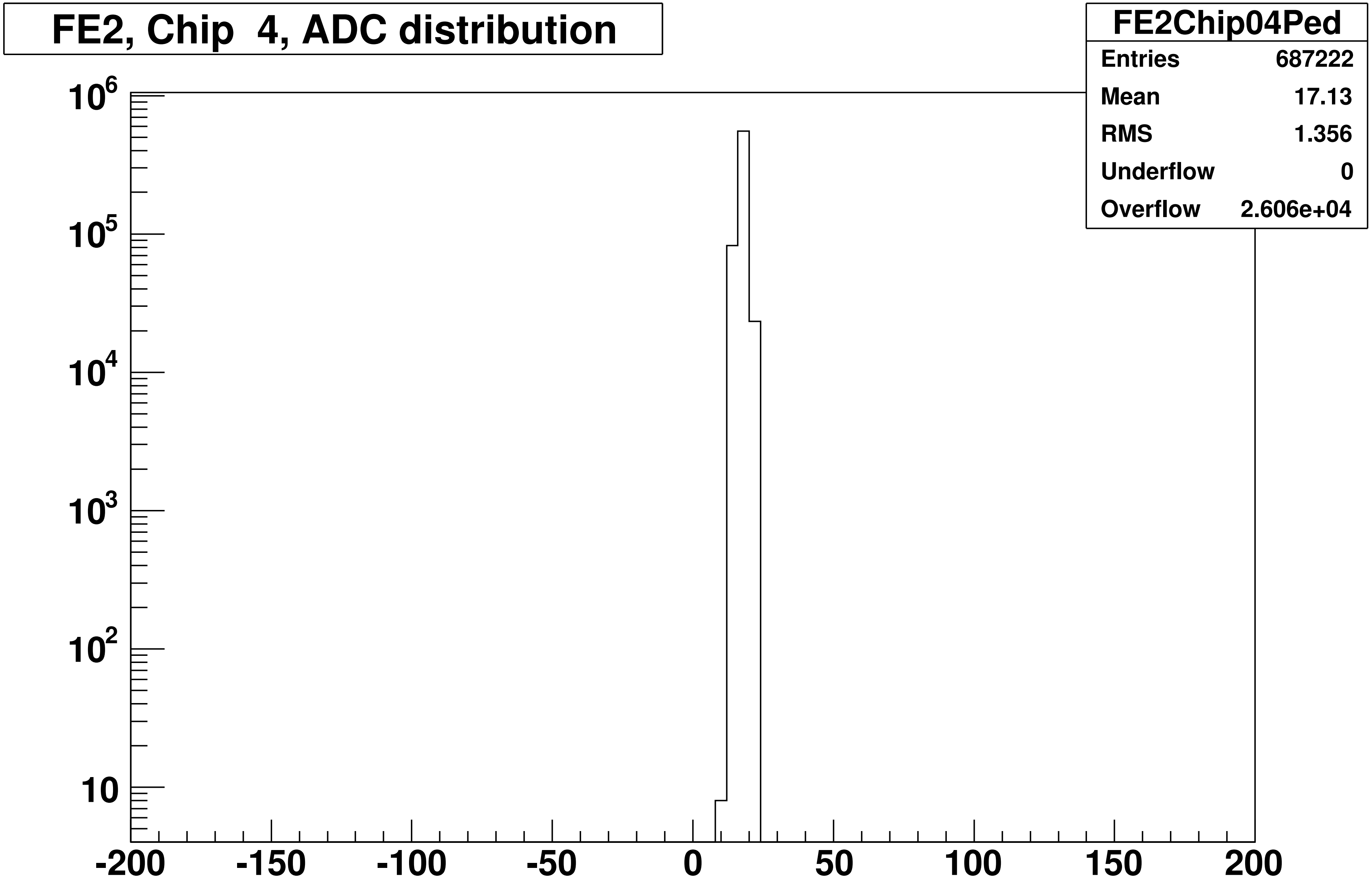

FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 687222
Mean 17.13
RMS 1.356
Underflow 0
Overflow 2.606e+04
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10^4
10^3
10^2
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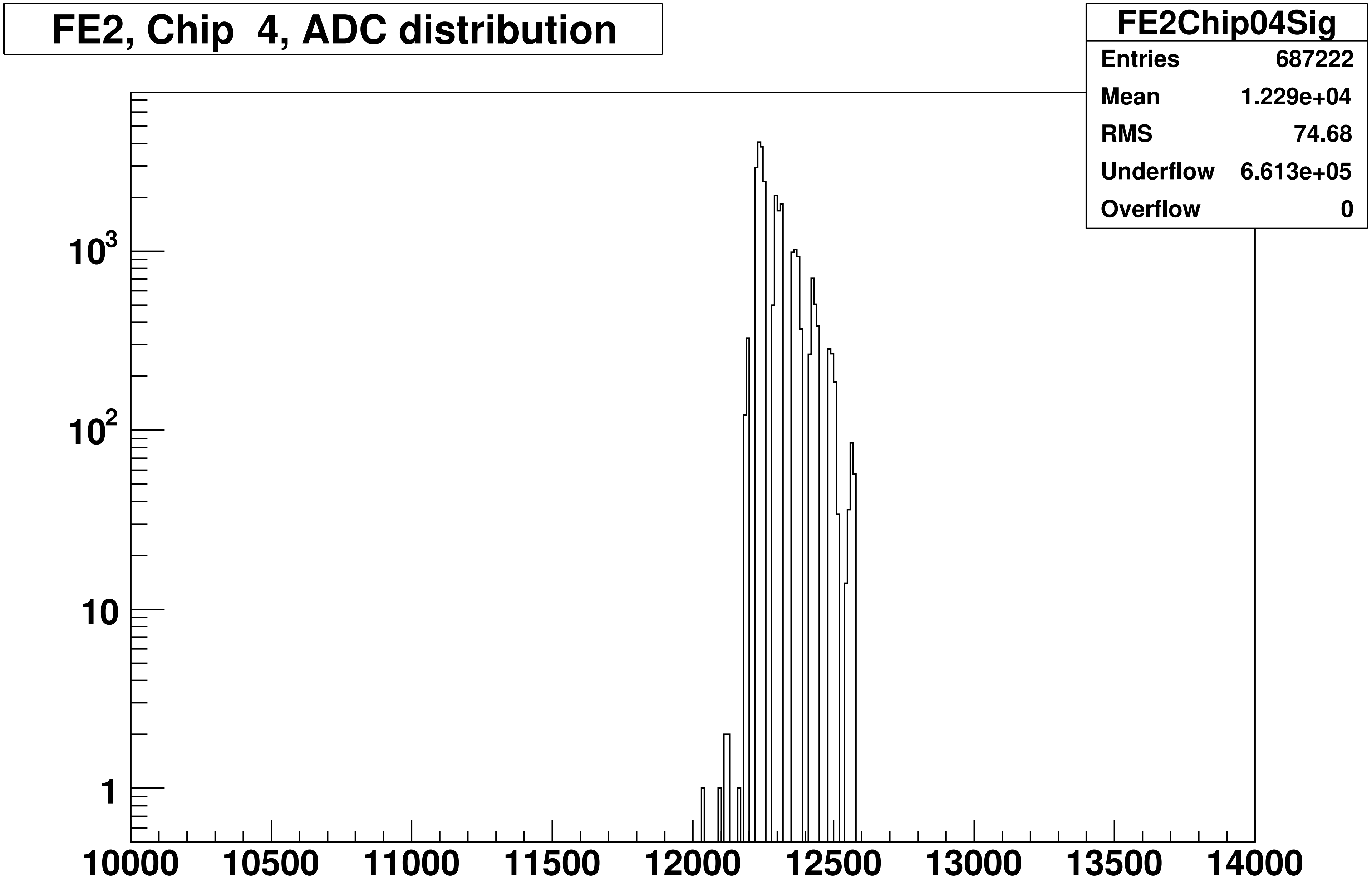
FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 687222
Mean 1.229e+04
RMS 74.68
Underflow 6.613e+05
Overflow 0
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10^2
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1
10000
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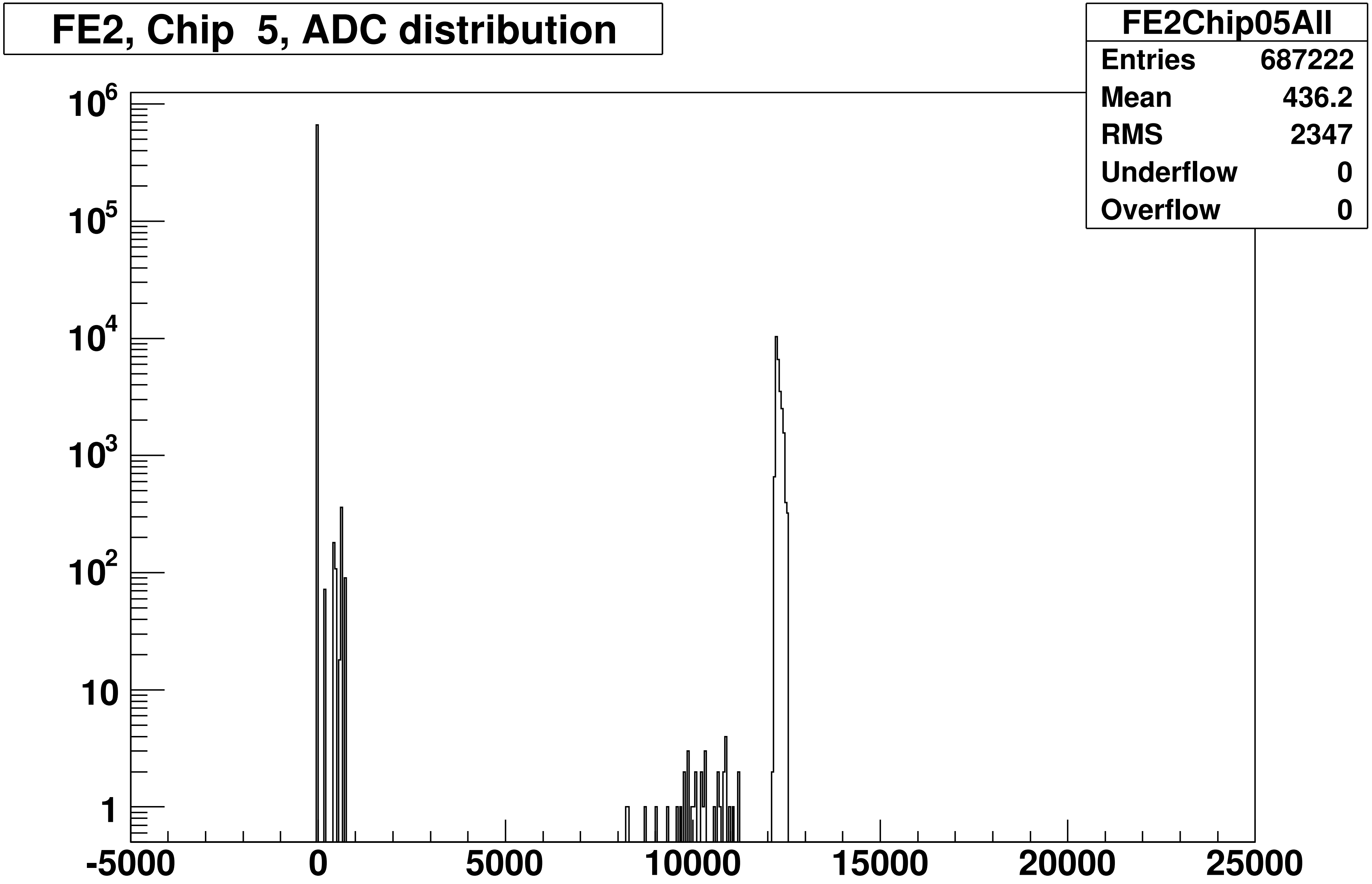
FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 687222
Mean 436.2
RMS 2347
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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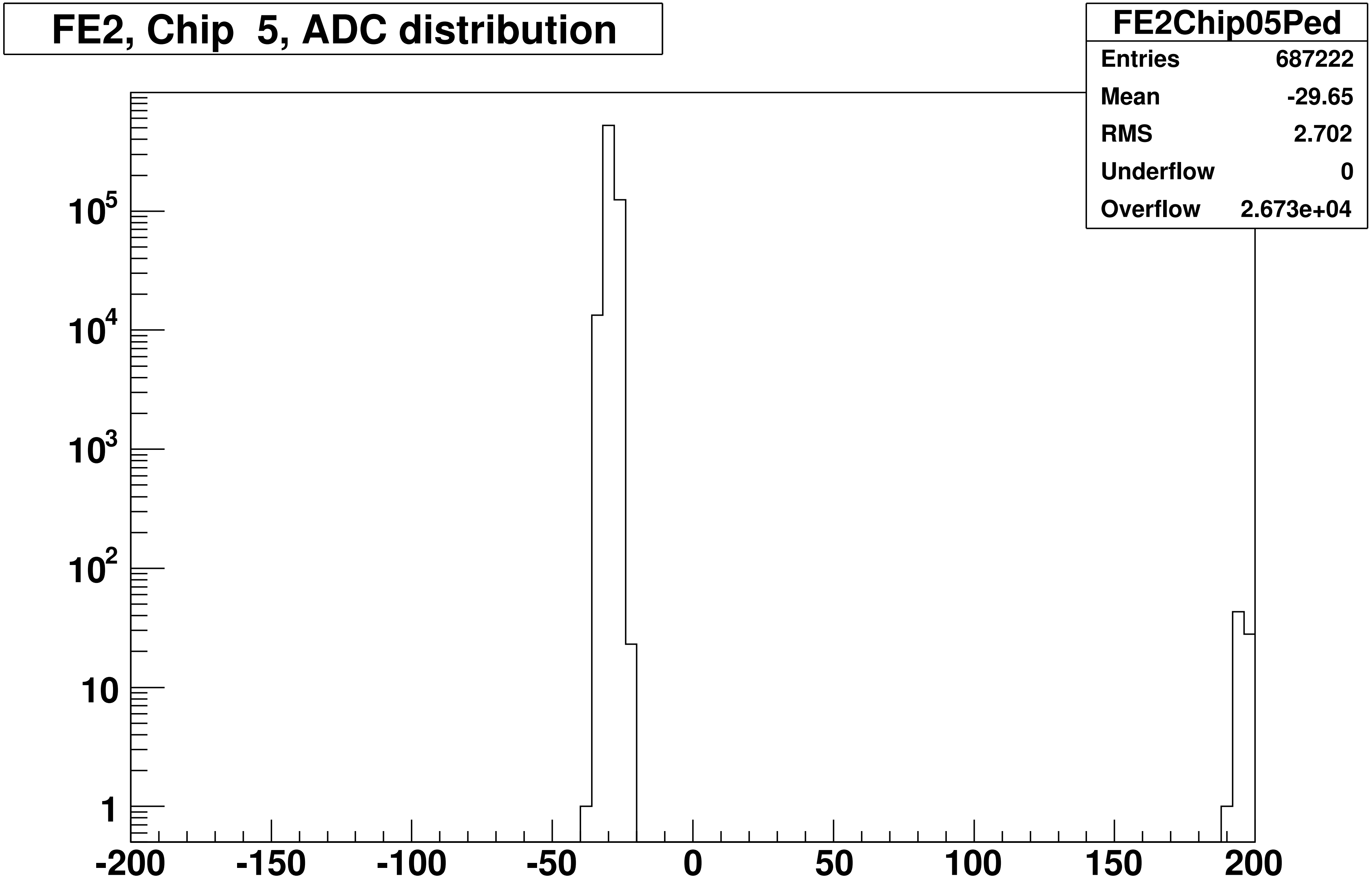

FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 687222
Mean -29.65
RMS 2.702
Underflow 0
Overflow 2.673e+04
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10^4
10^3
10^2
10
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150
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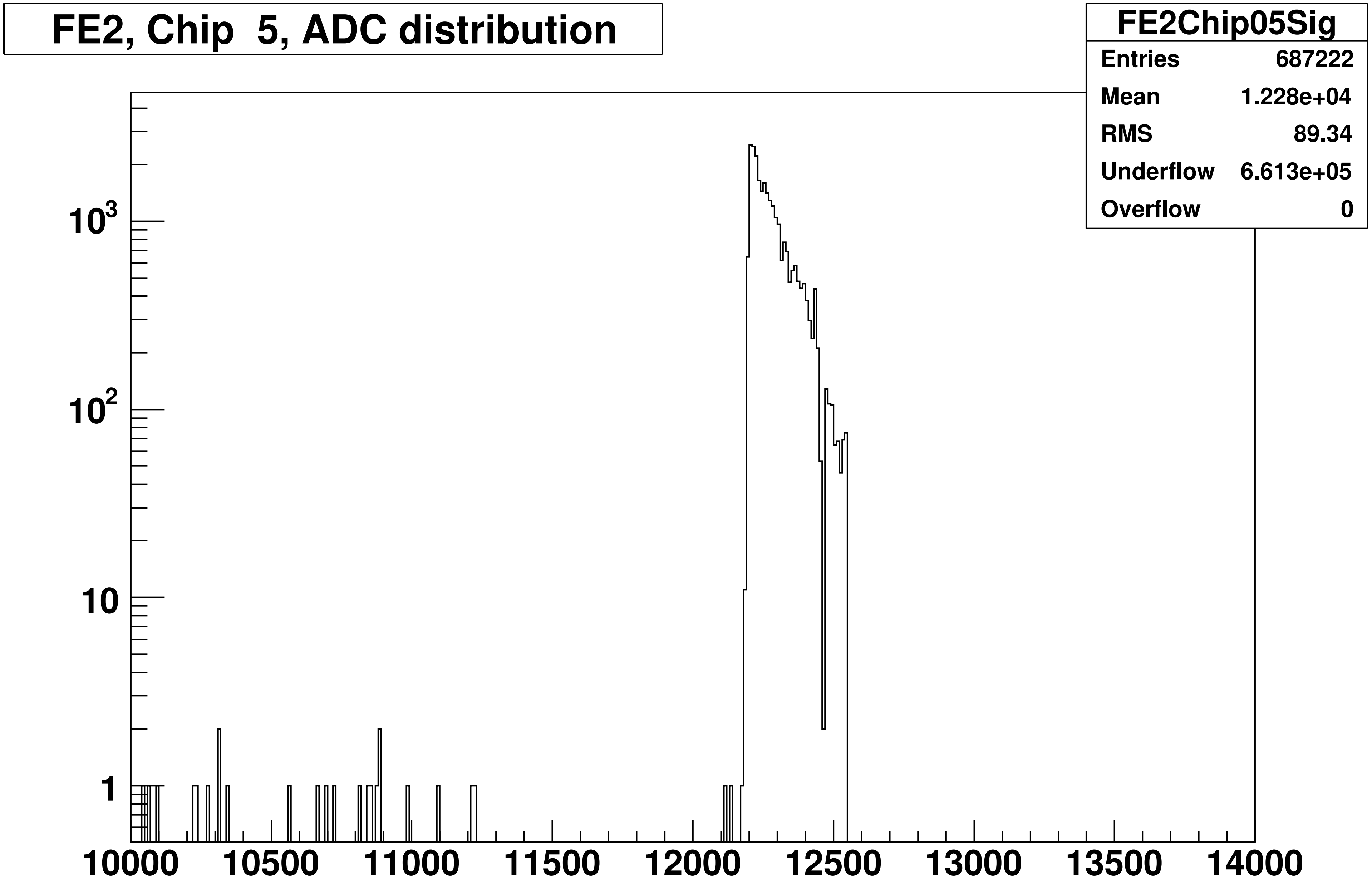
FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 687222
Mean 1.228e+04
RMS 89.34
Underflow 6.613e+05
Overflow 0
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10^2
10
1
10000
10500
11000
11500
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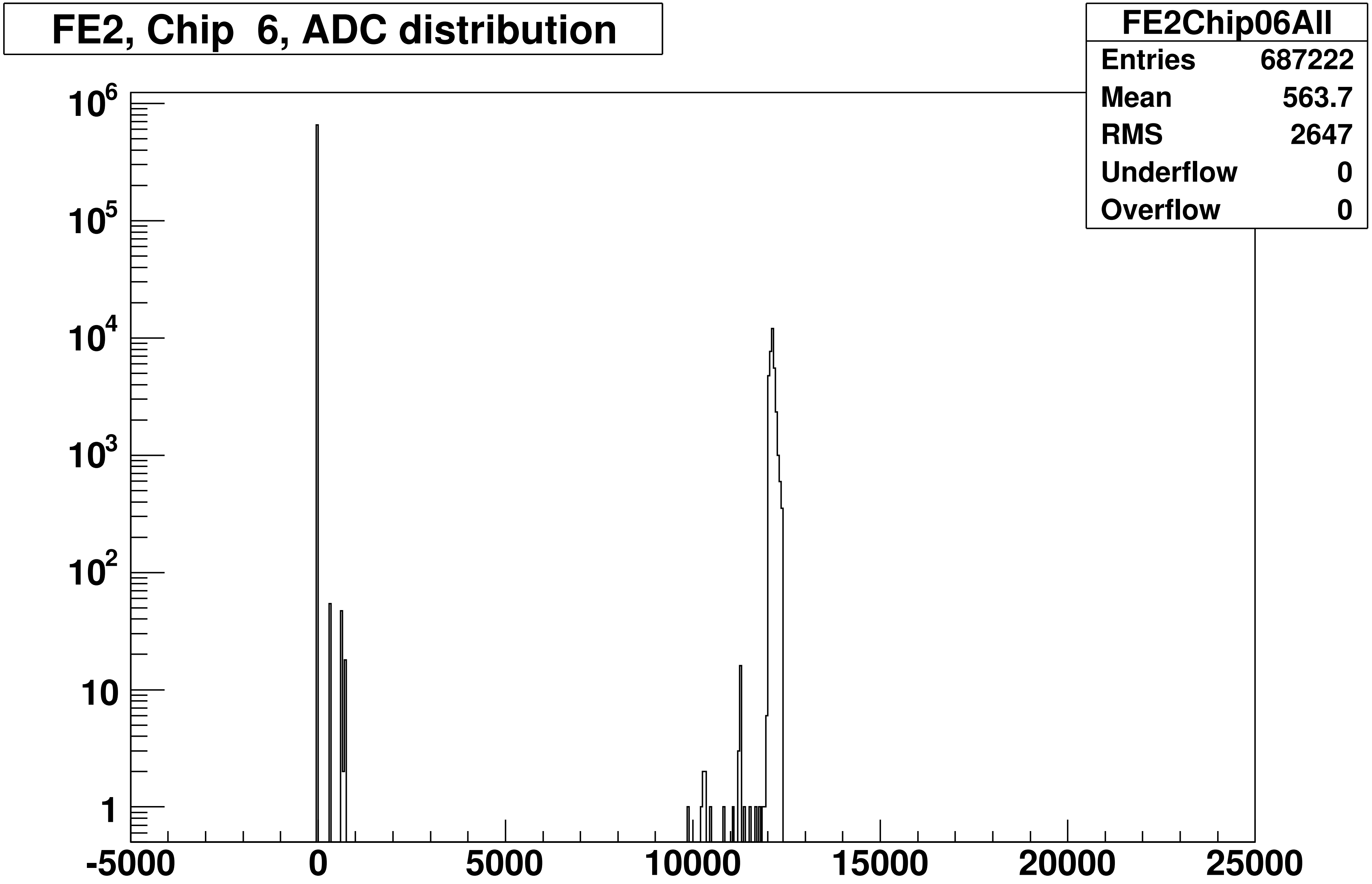
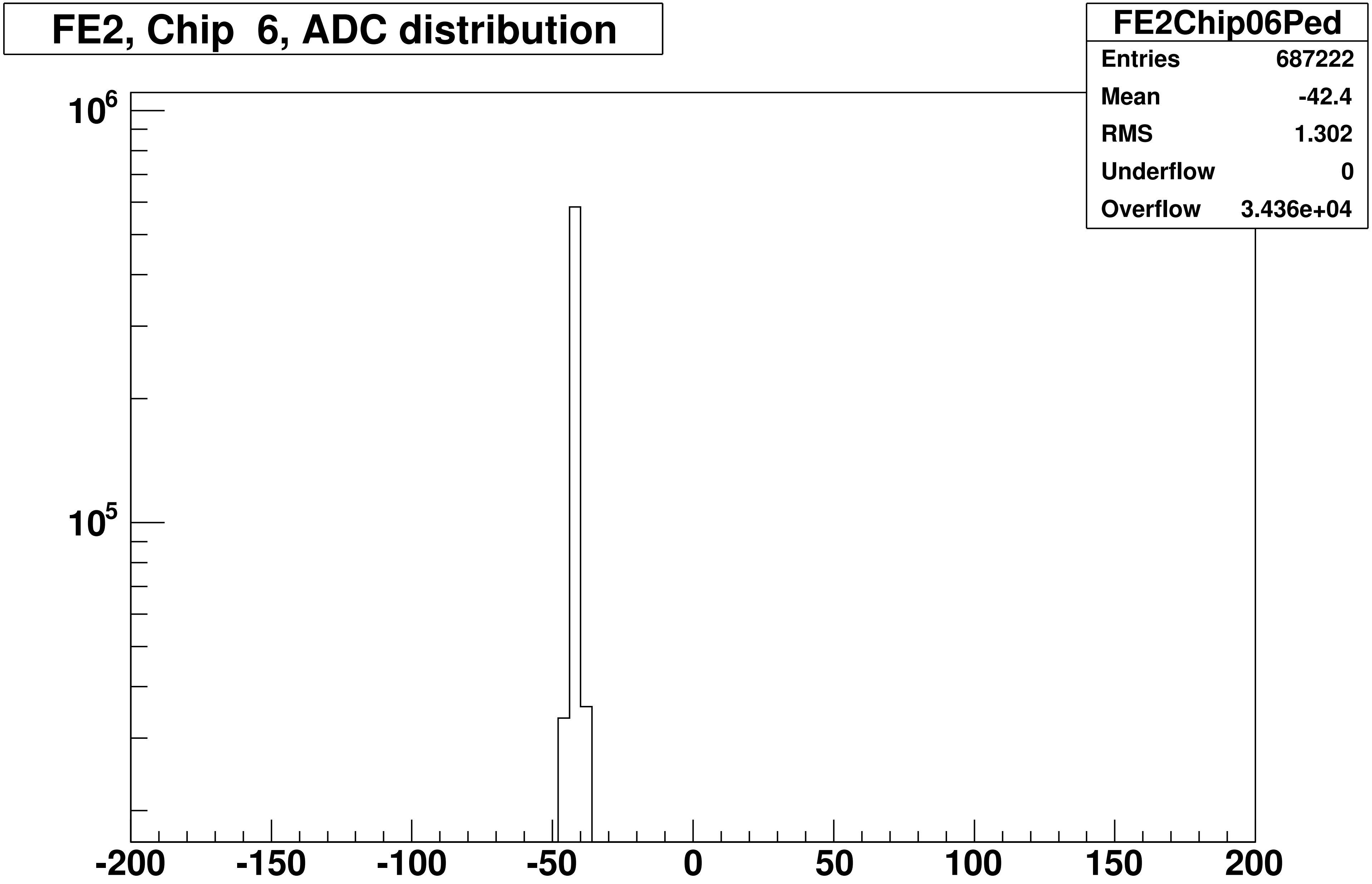
13500
14000



FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 687222
Mean 563.7
RMS 2647
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 687222
Mean -42.4
RMS 1.302
Underflow 0
Overflow 3.436e+04
10^6
10^5
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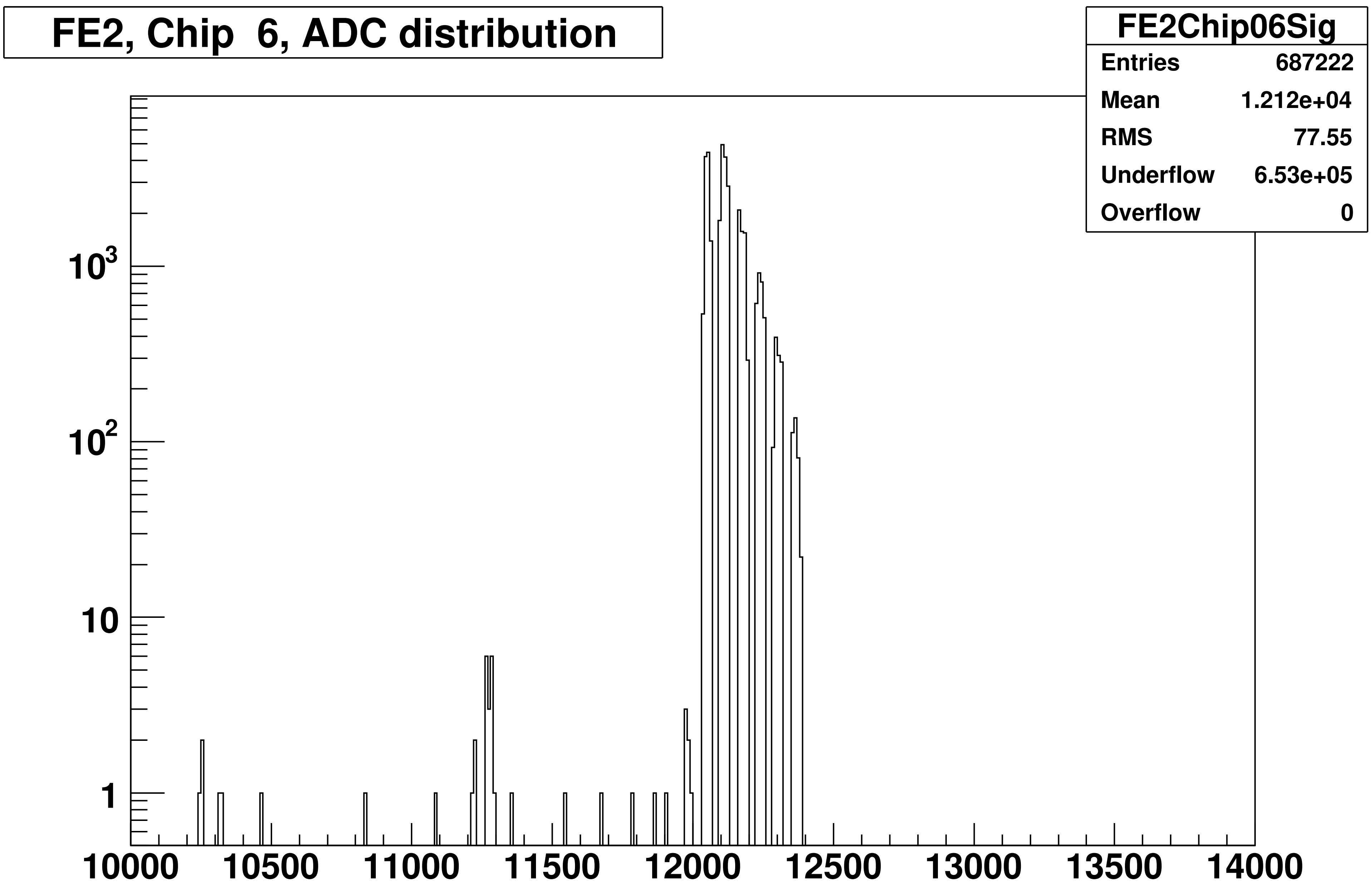
150
200



FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 687222
Mean 1.212e+04
RMS 77.55
Underflow 6.53e+05
Overflow 0
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10^2
10
1
10000
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11000
11500
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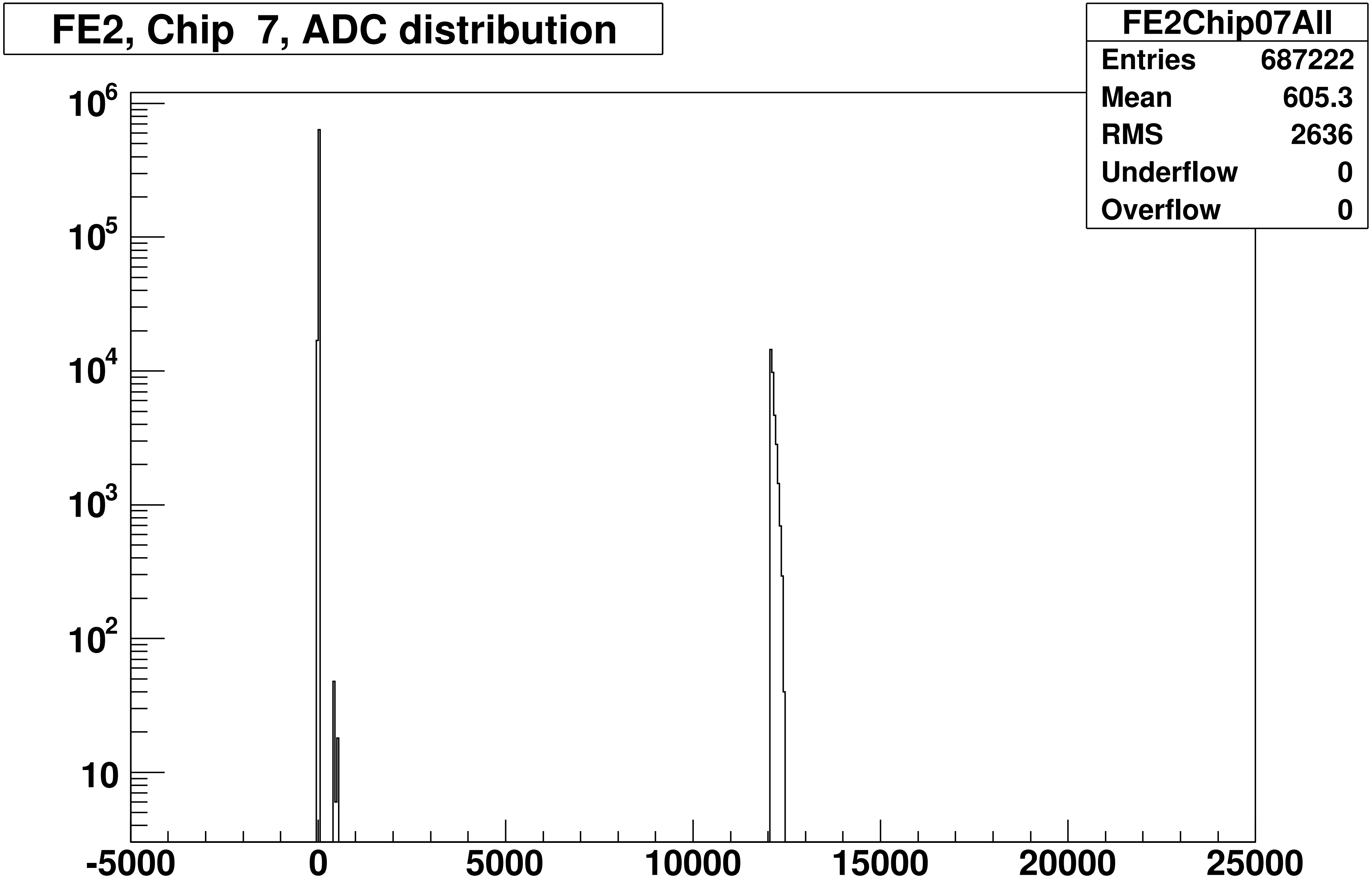
FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 687222
Mean 605.3
RMS 2636
Underflow 0
Overflow 0
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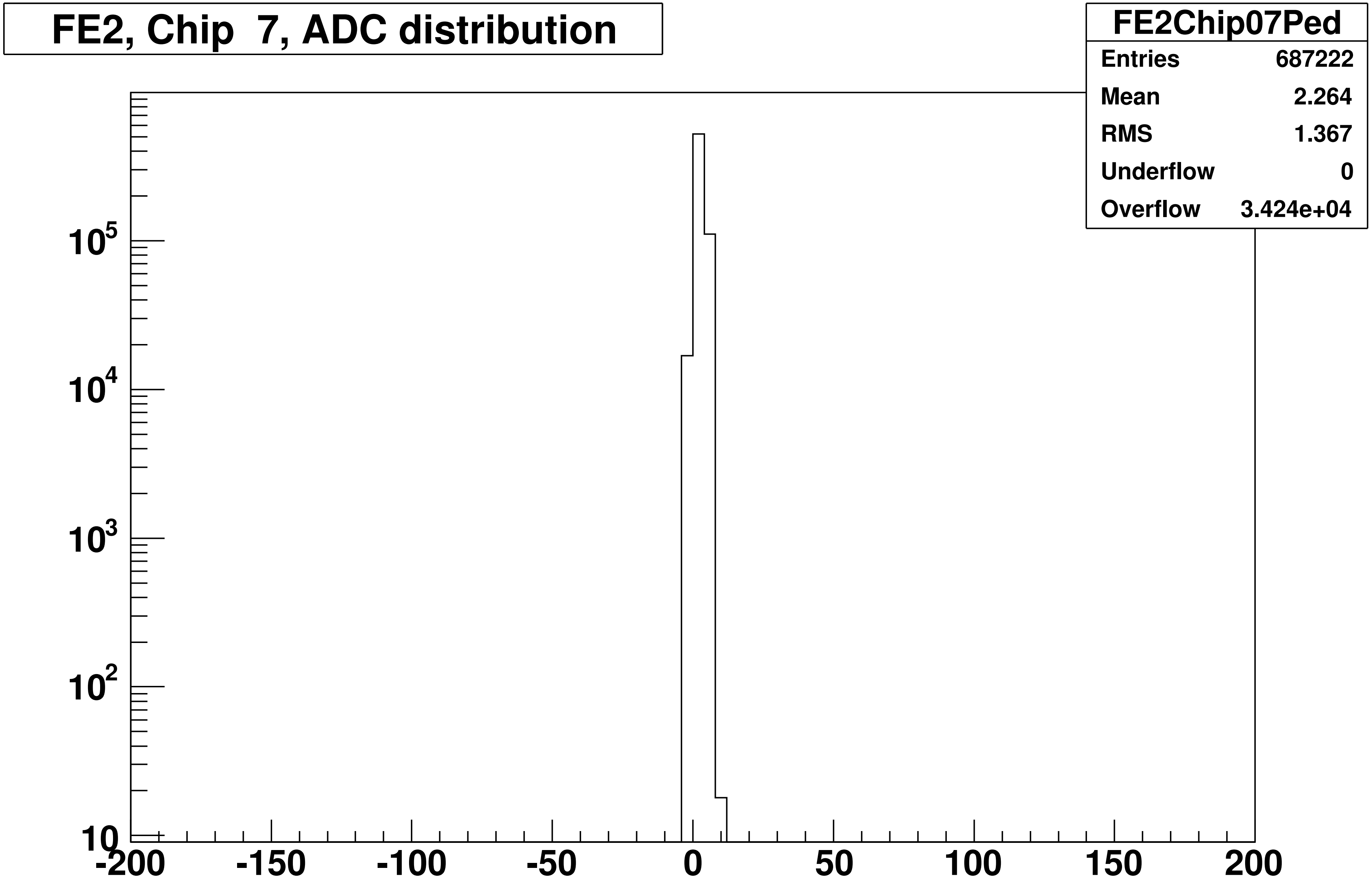
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 687222
Mean 2.264
RMS 1.367
Underflow 0
Overflow 3.424e+04
10
10^2
10^3
10^4
10^5
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-100
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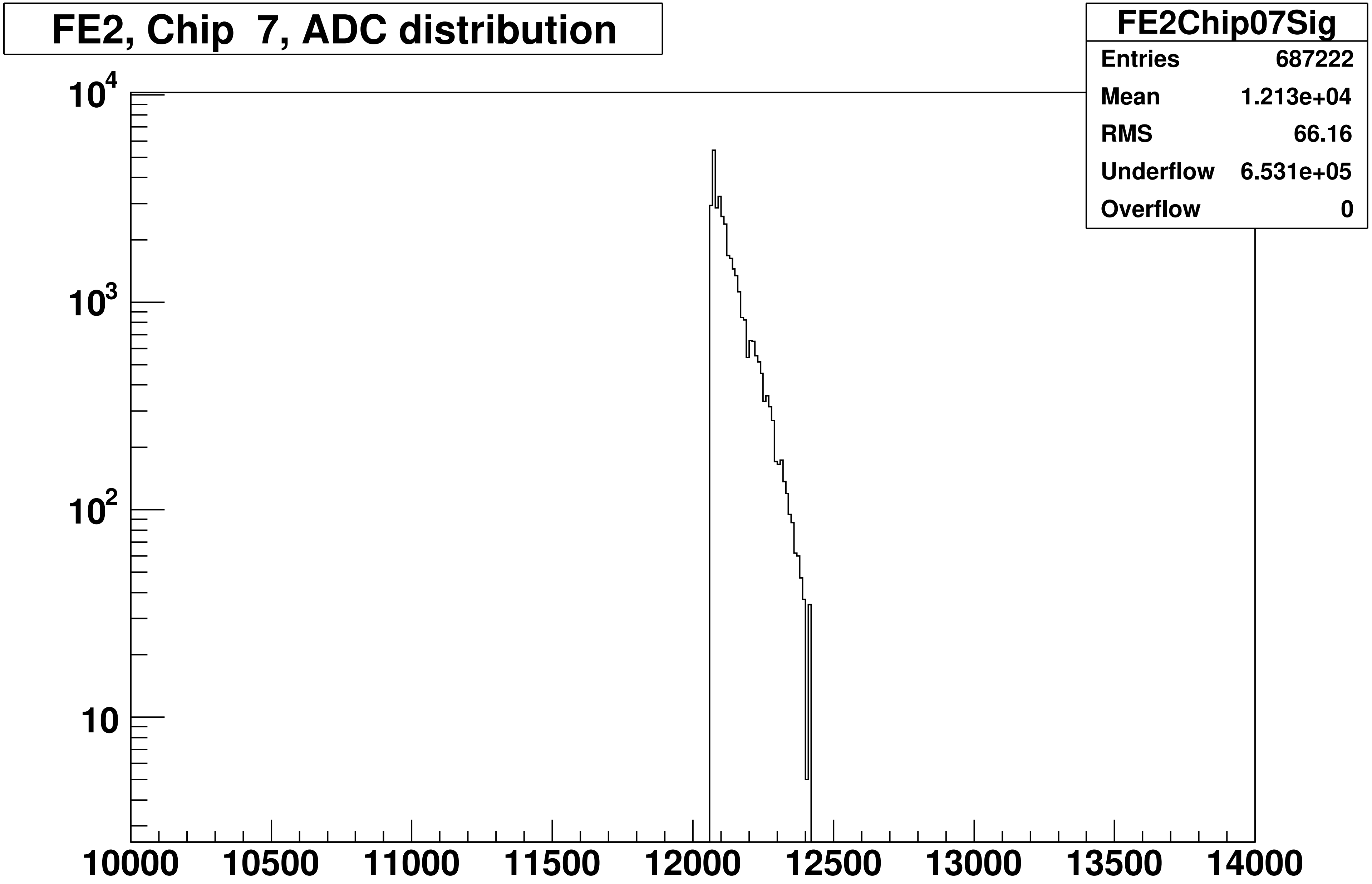
0
50
100
150
200



FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 687222
Mean 1.213e+04
RMS 66.16
Underflow 6.531e+05
Overflow 0
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10^3
10^2
10
10000
10500
11000
11500
12000
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13000
13500
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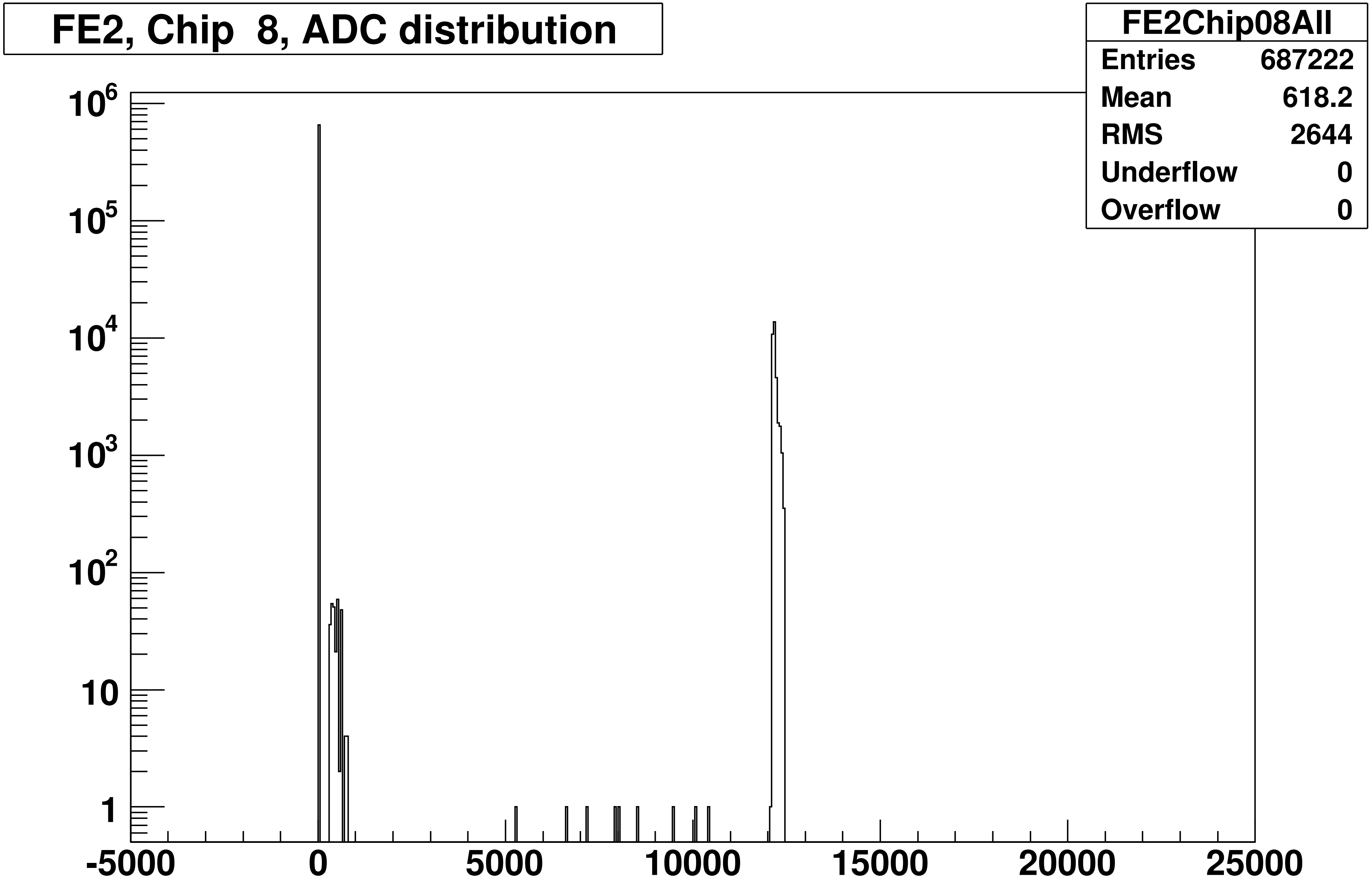
FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 687222
Mean 618.2
RMS 2644
Underflow 0
Overflow 0
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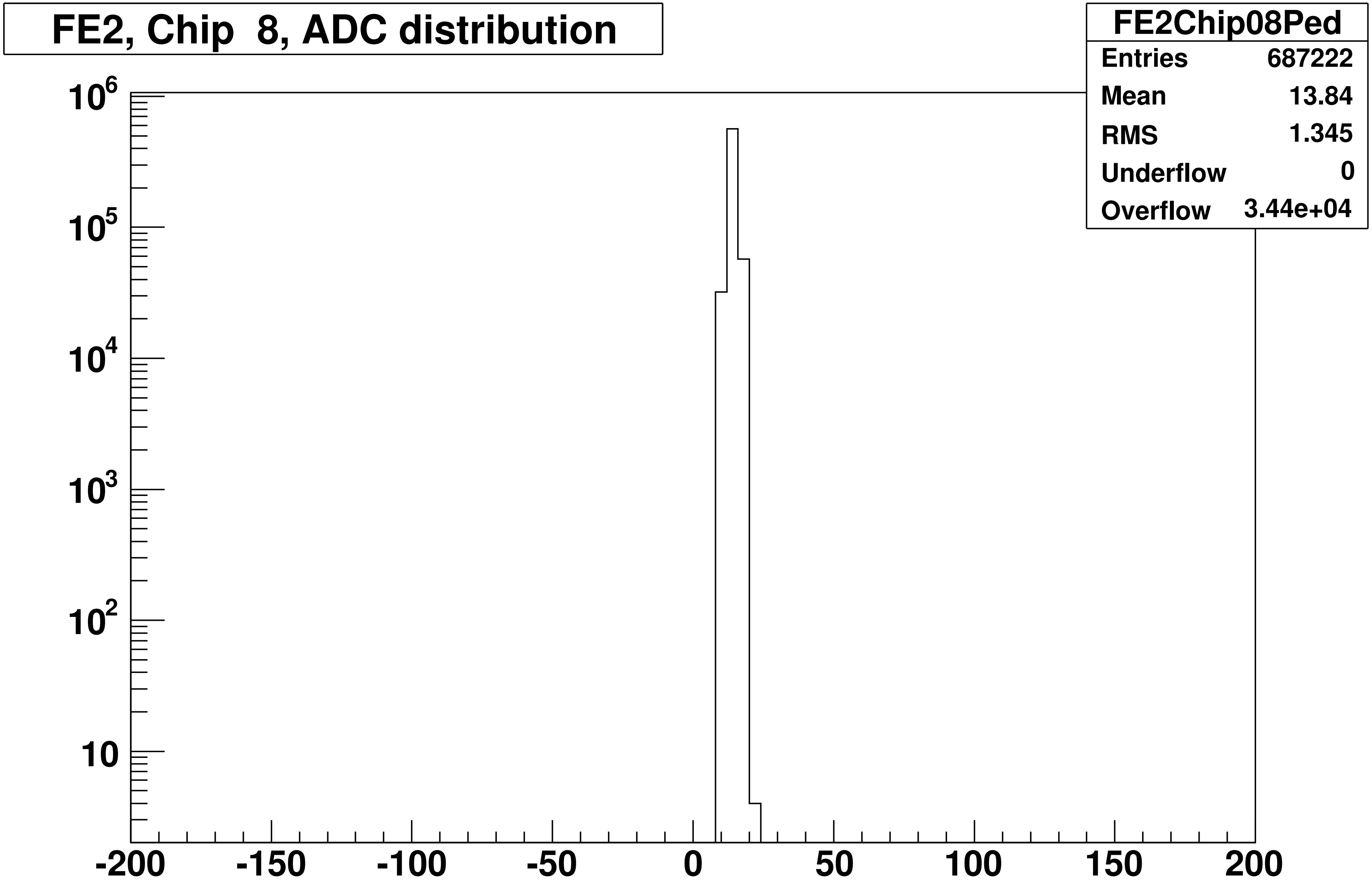
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 687222
Mean 13.84
RMS 1.345
Underflow 0
Overflow 3.44e+04
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10^5
10^4
10^3
10^2
10
-200
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-100
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150
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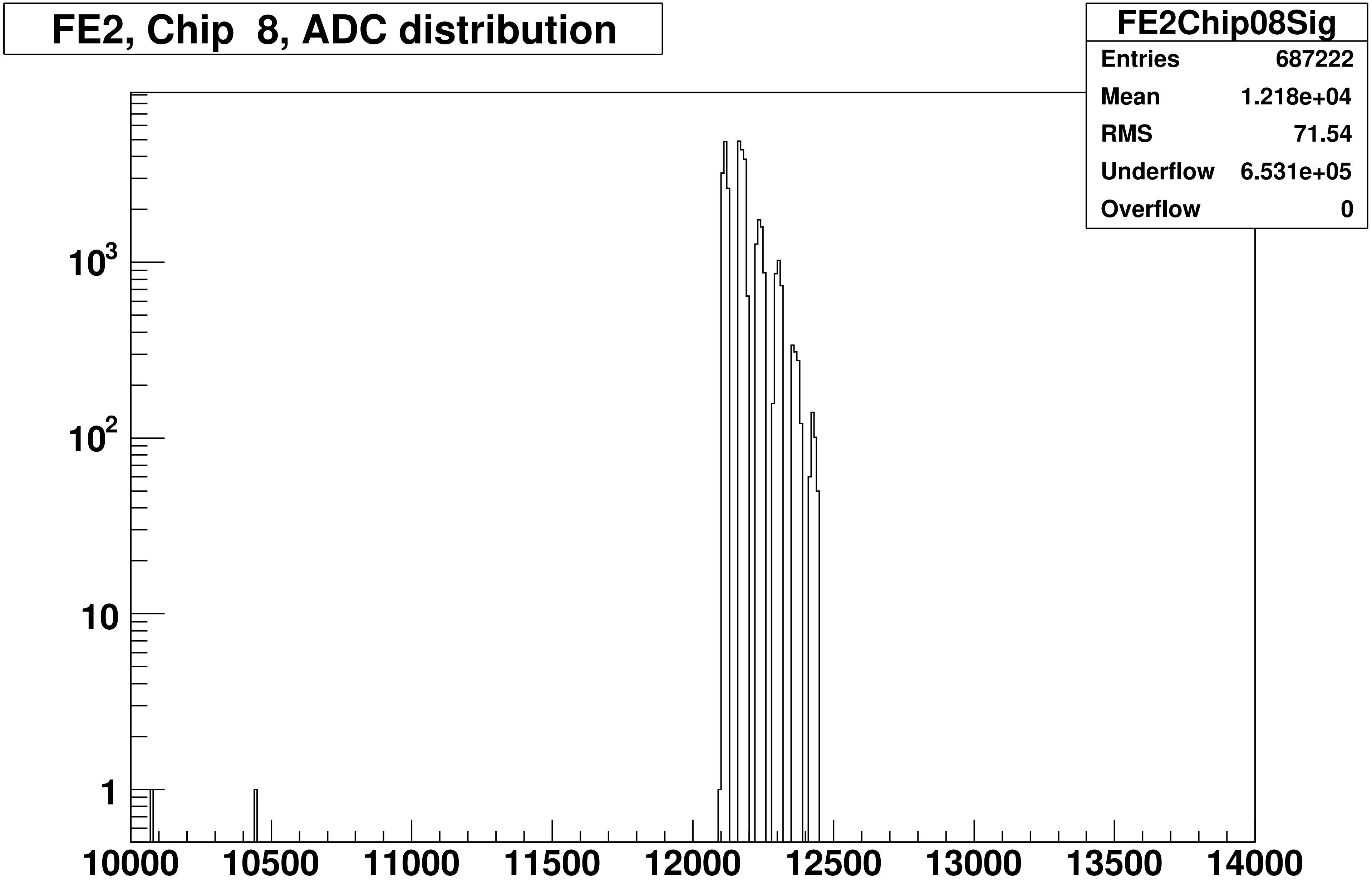
FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 687222
Mean 1.218e+04
RMS 71.54
Underflow 6.531e+05
Overflow 0
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10^2
10
1
10000
10500
11000
11500
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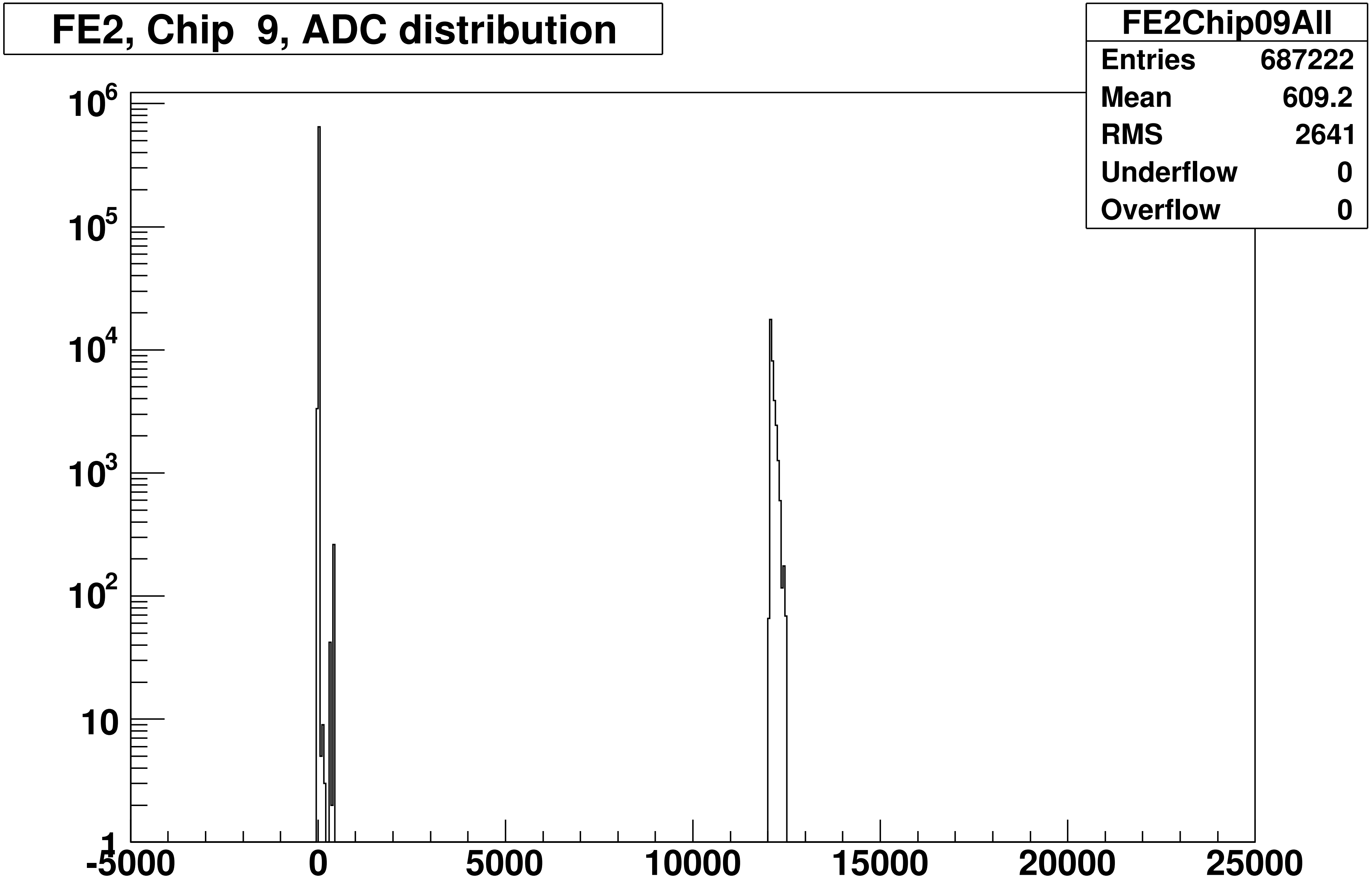
FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 687222
Mean 609.2
RMS 2641
Underflow 0
Overflow 0
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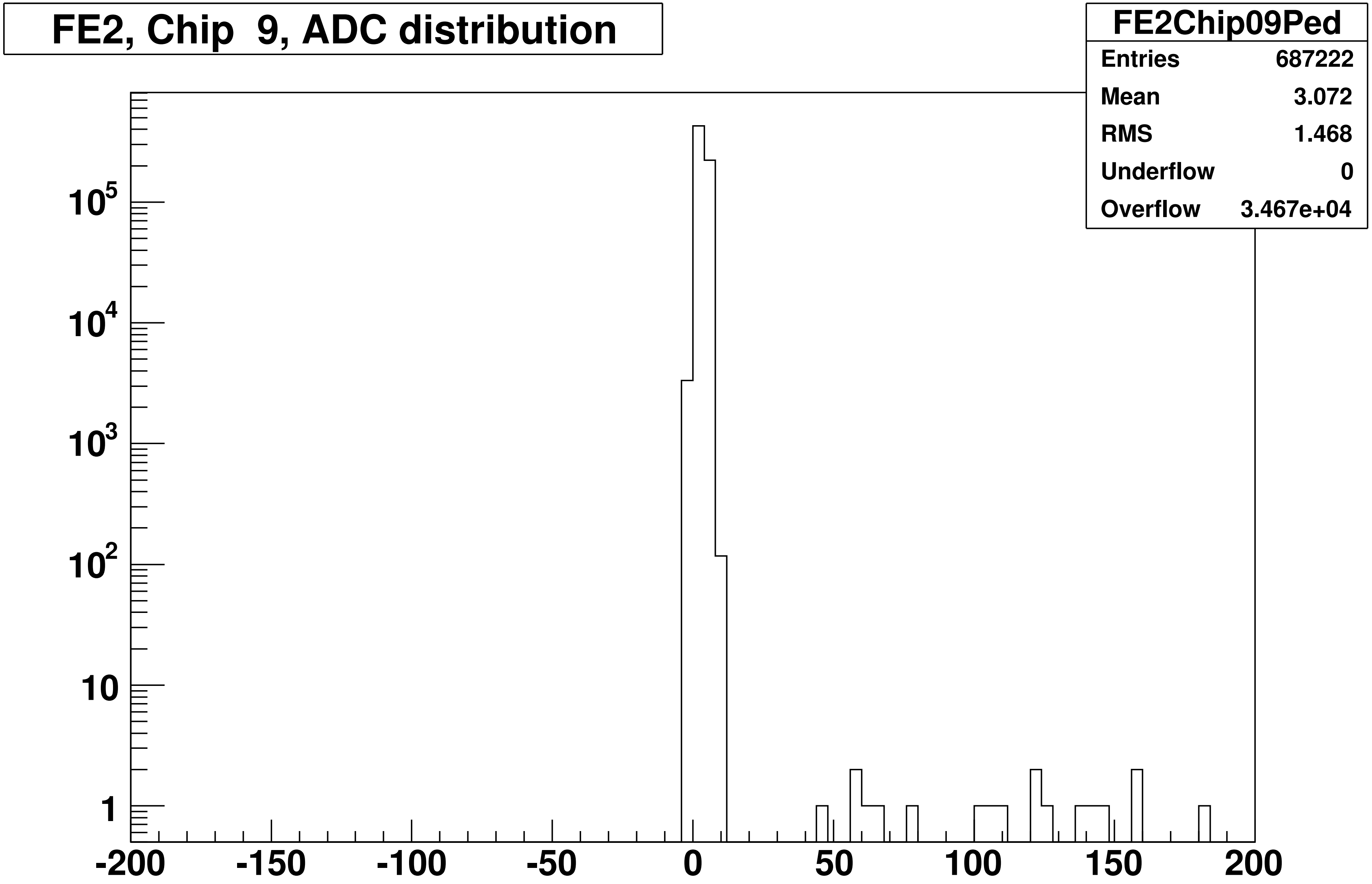
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 687222
Mean 3.072
RMS 1.468
Underflow 0
Overflow 3.467e+04
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10^4
10^3
10^2
10
1
-200
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-100
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0
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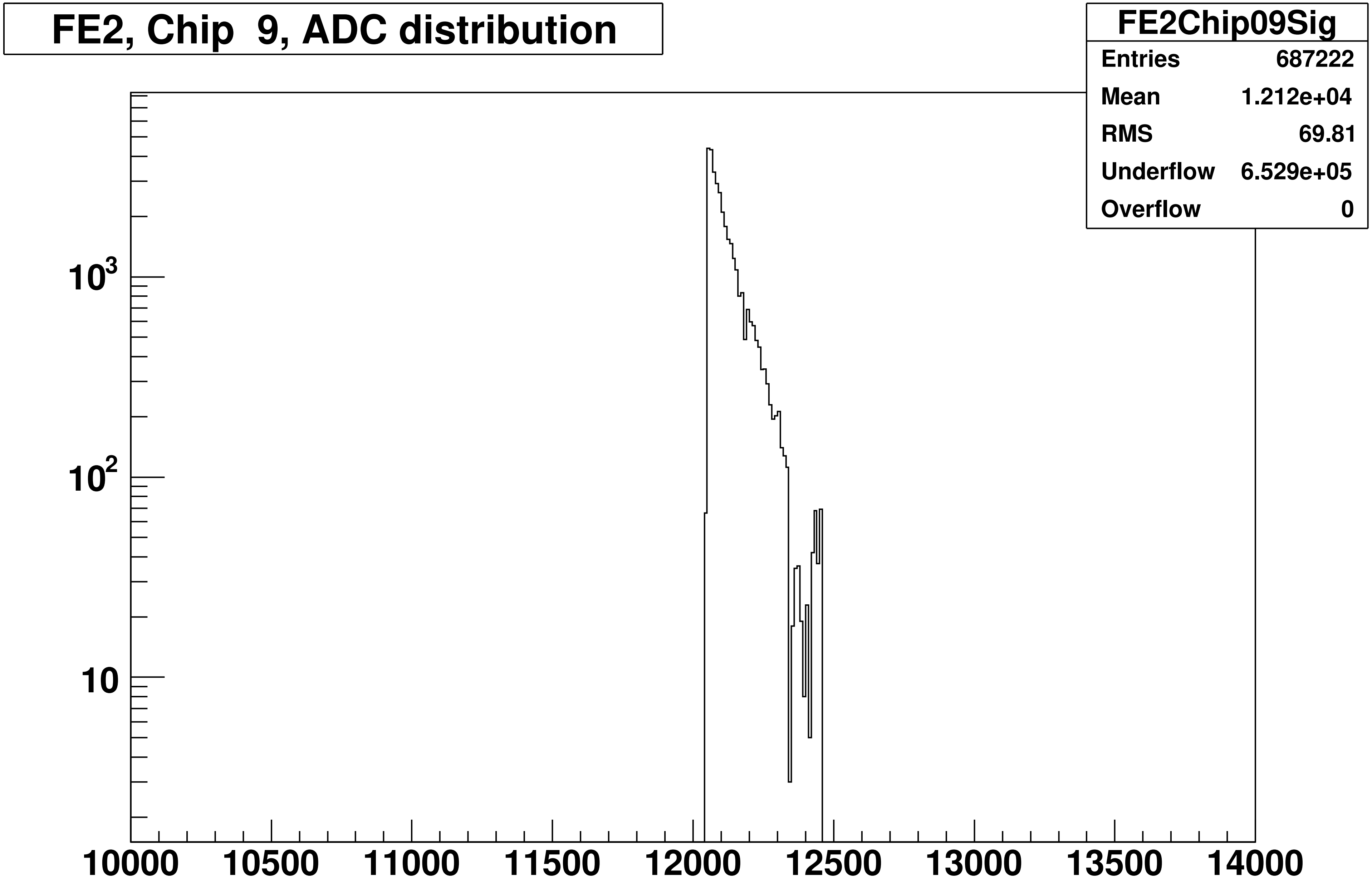
100
150
200


FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 687222
Mean 1.212e+04
RMS 69.81
Underflow 6.529e+05
Overflow 0
10
10^2
10^3
10000
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11500
12000
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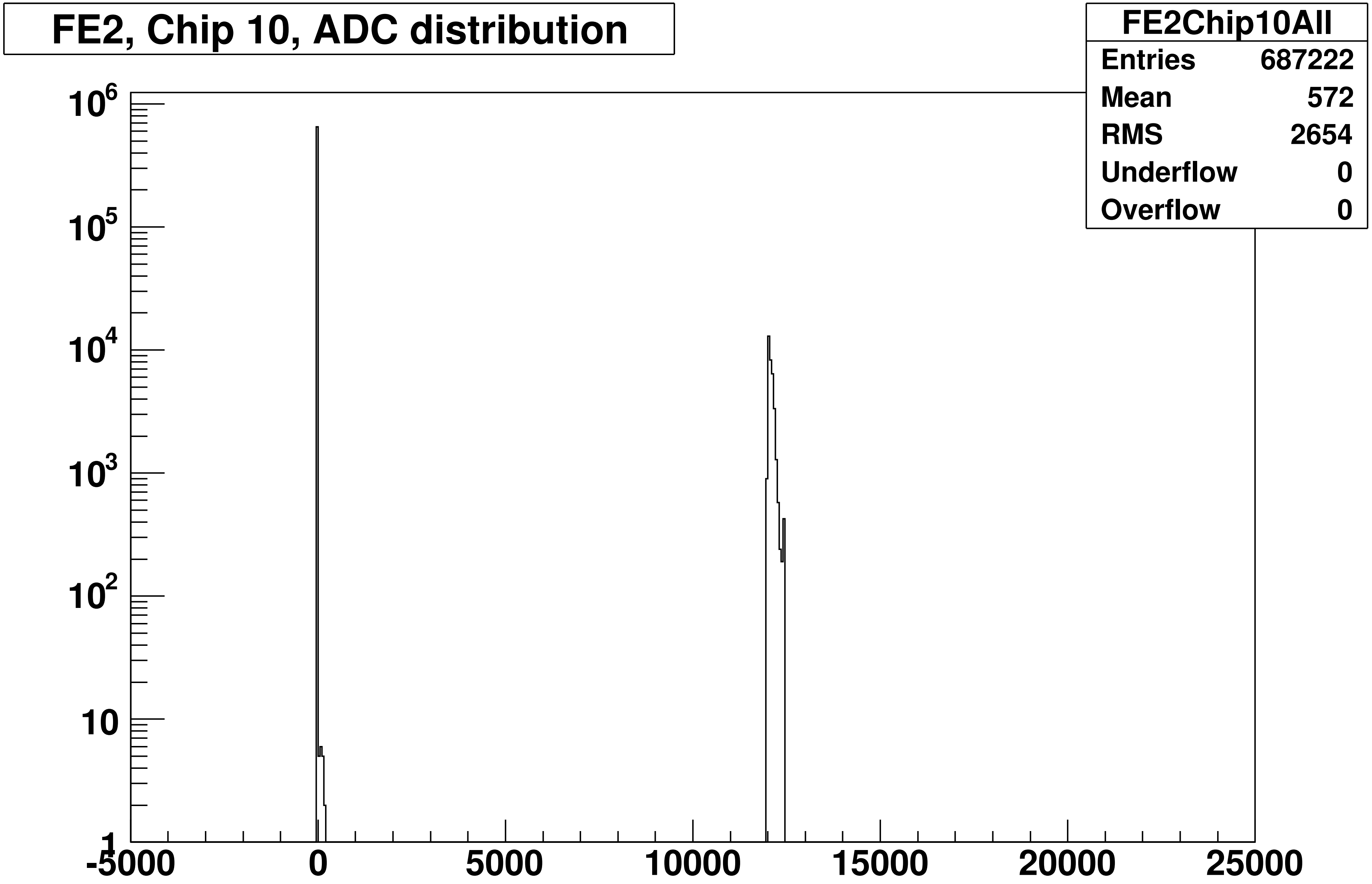
14000



FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 687222
Mean 572
RMS 2654
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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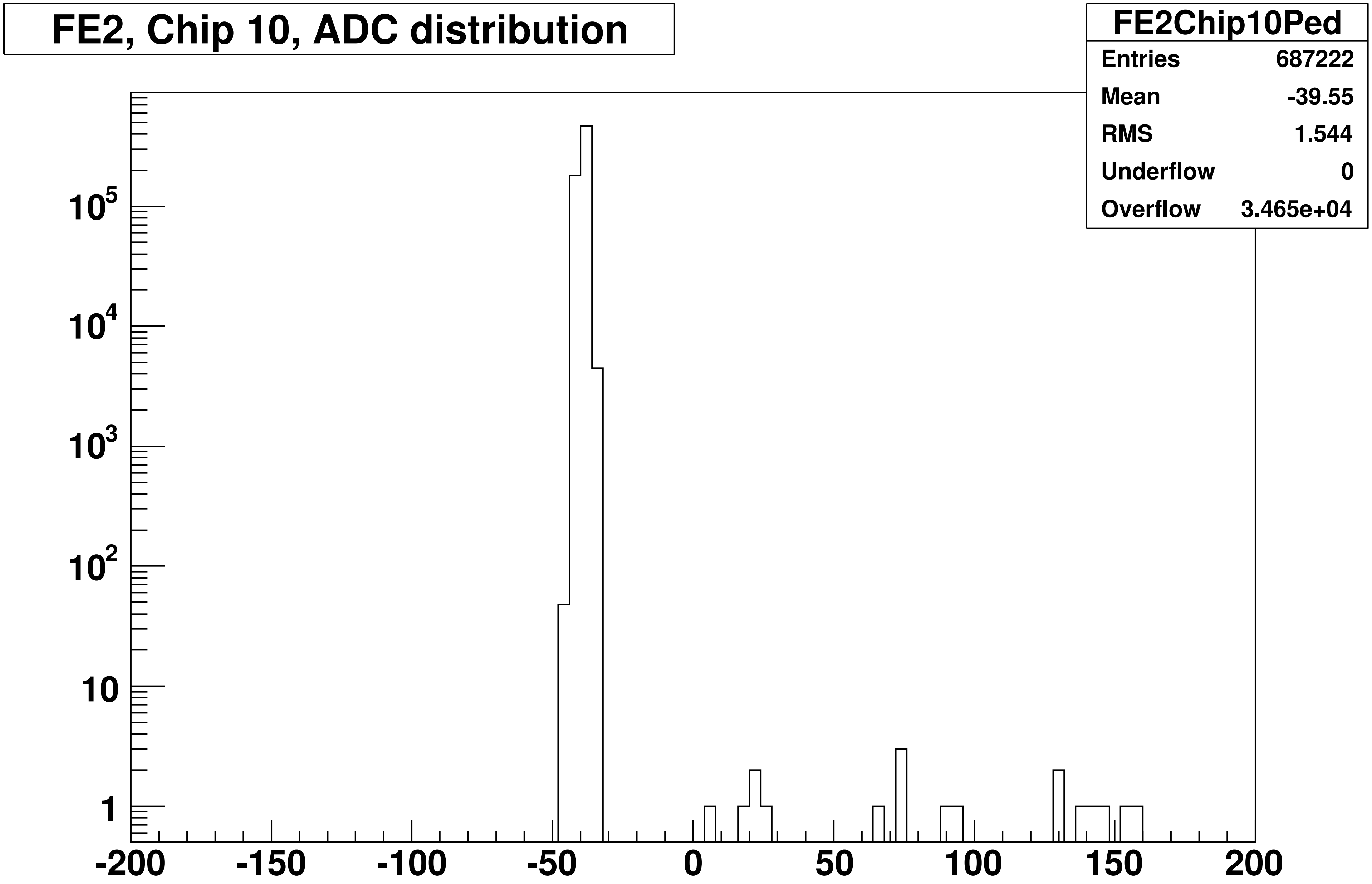
0
5000
10000
15000
20000
25000


FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 687222
Mean -39.55
RMS 1.544
Underflow 0
Overflow 3.465e+04
10^5
10^4
10^3
10^2
10
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150
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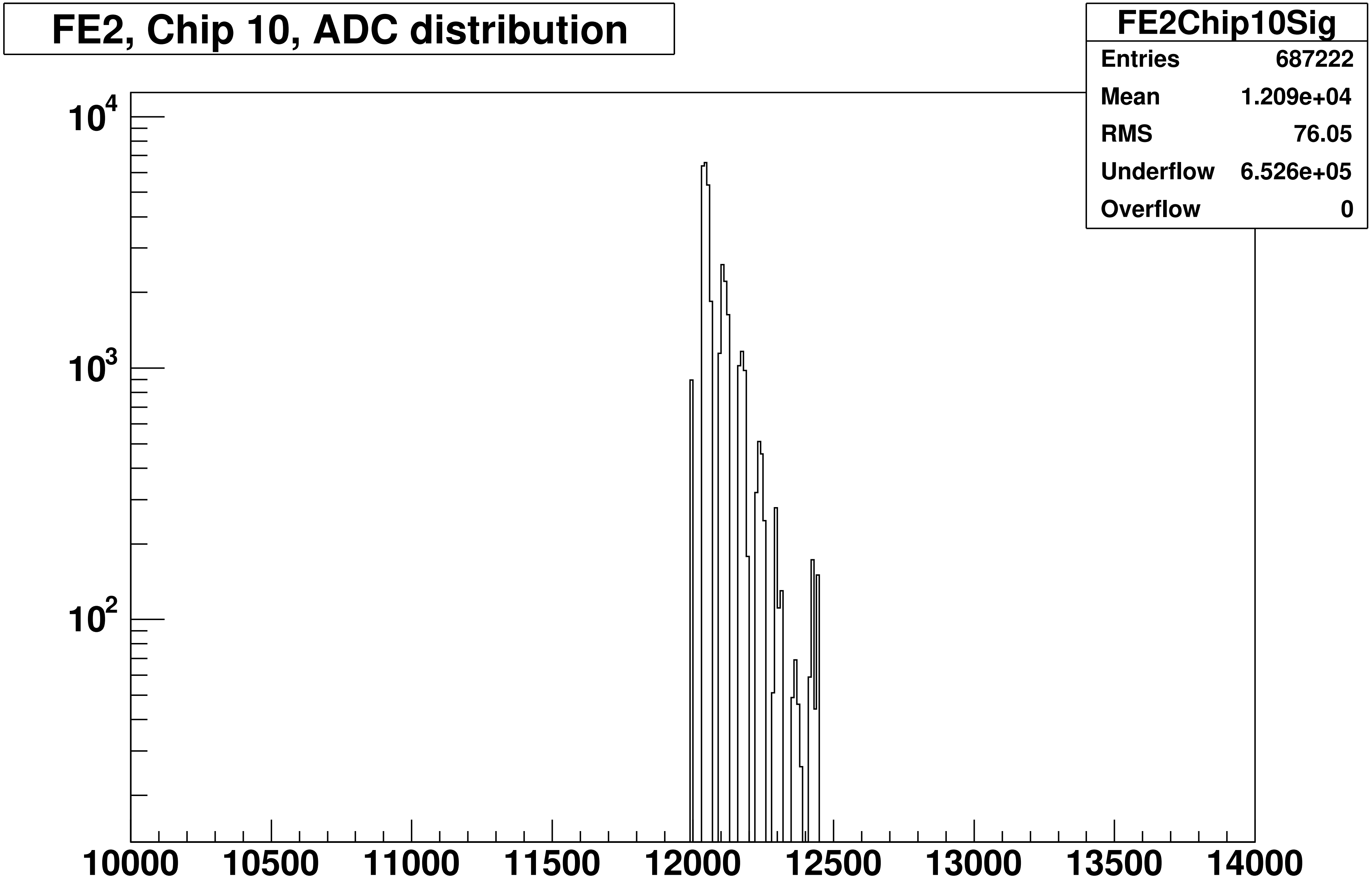

FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 687222
Mean 1.209e+04
RMS 76.05
Underflow 6.526e+05
Overflow 0
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10^3
10^2
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10500
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11500
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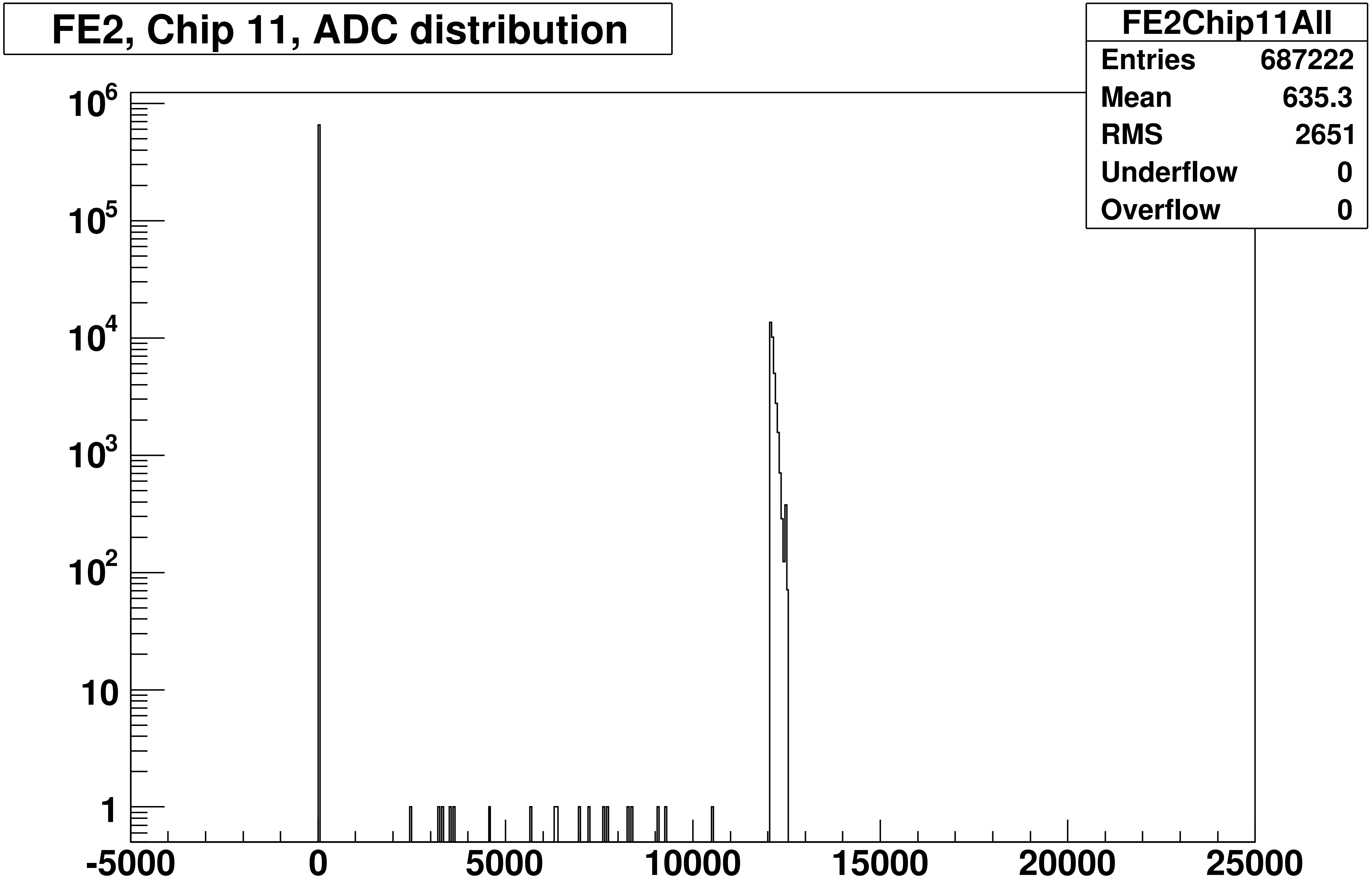
FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 687222
Mean 635.3
RMS 2651
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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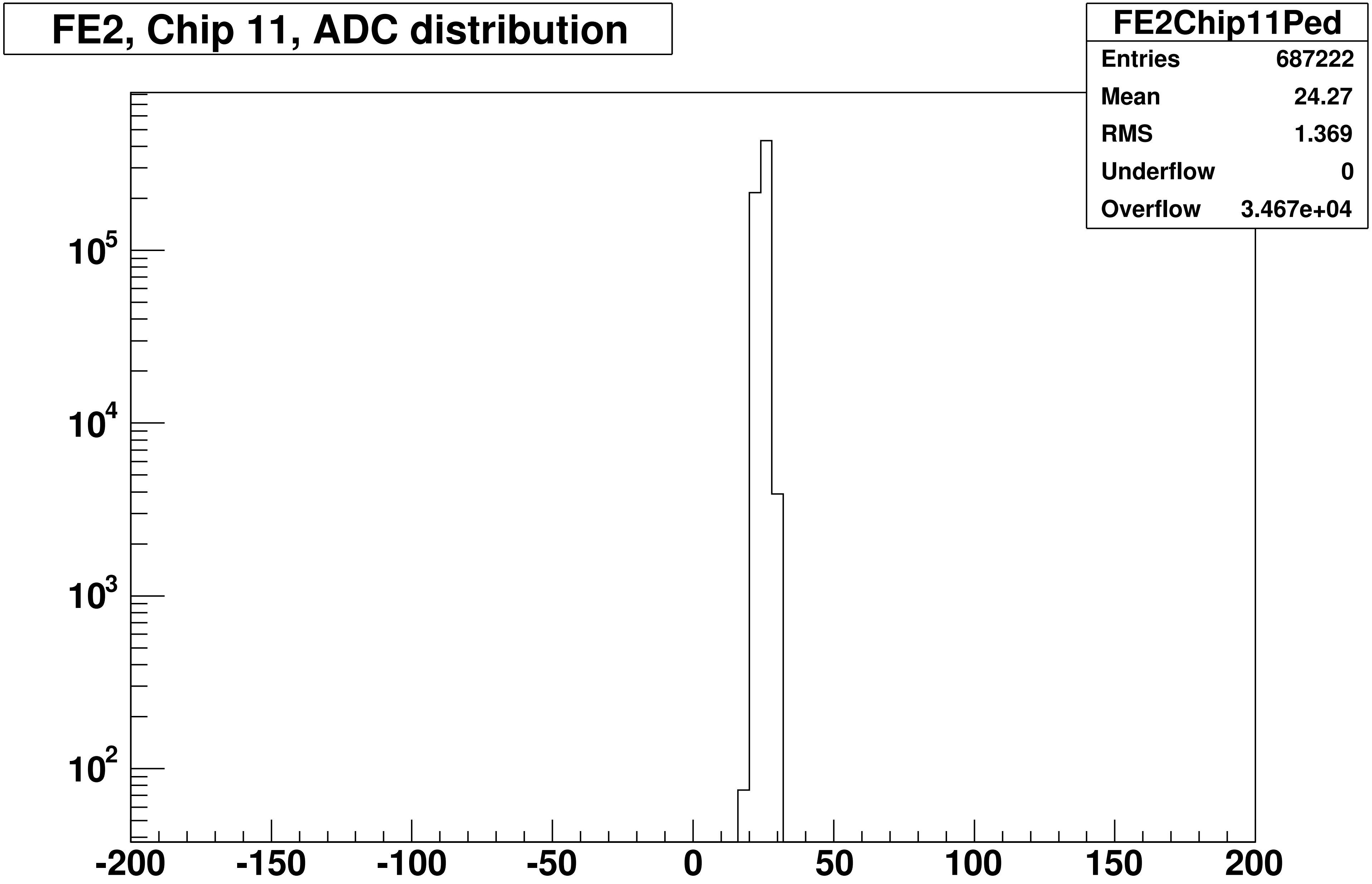

FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 687222
Mean 24.27
RMS 1.369
Underflow 0
Overflow 3.467e+04
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10^4
10^3
10^2
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-100
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0
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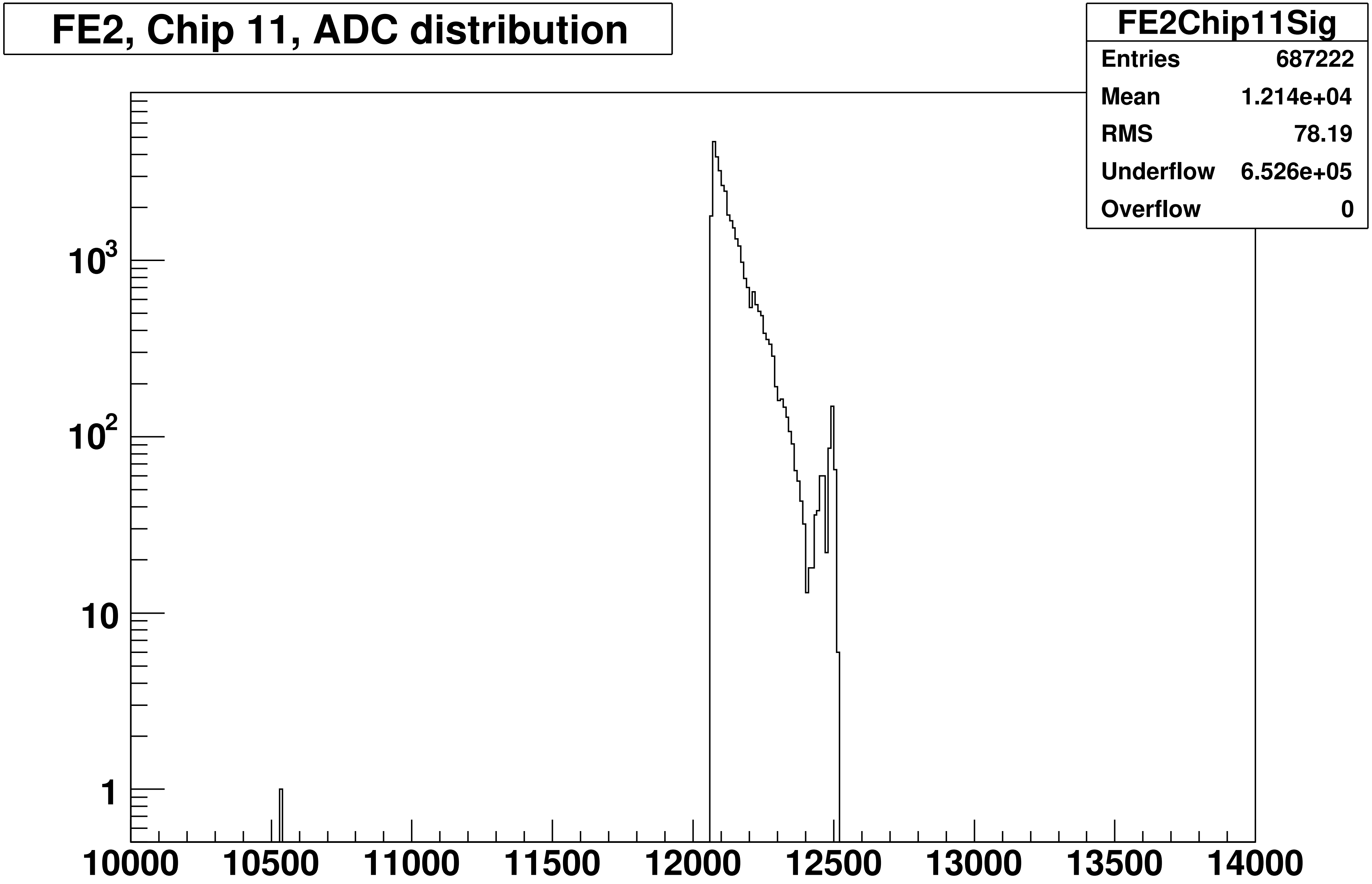
100
150
200


FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 687222
Mean 1.214e+04
RMS 78.19
Underflow 6.526e+05
Overflow 0
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10^2
10
1
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10500
11000
11500
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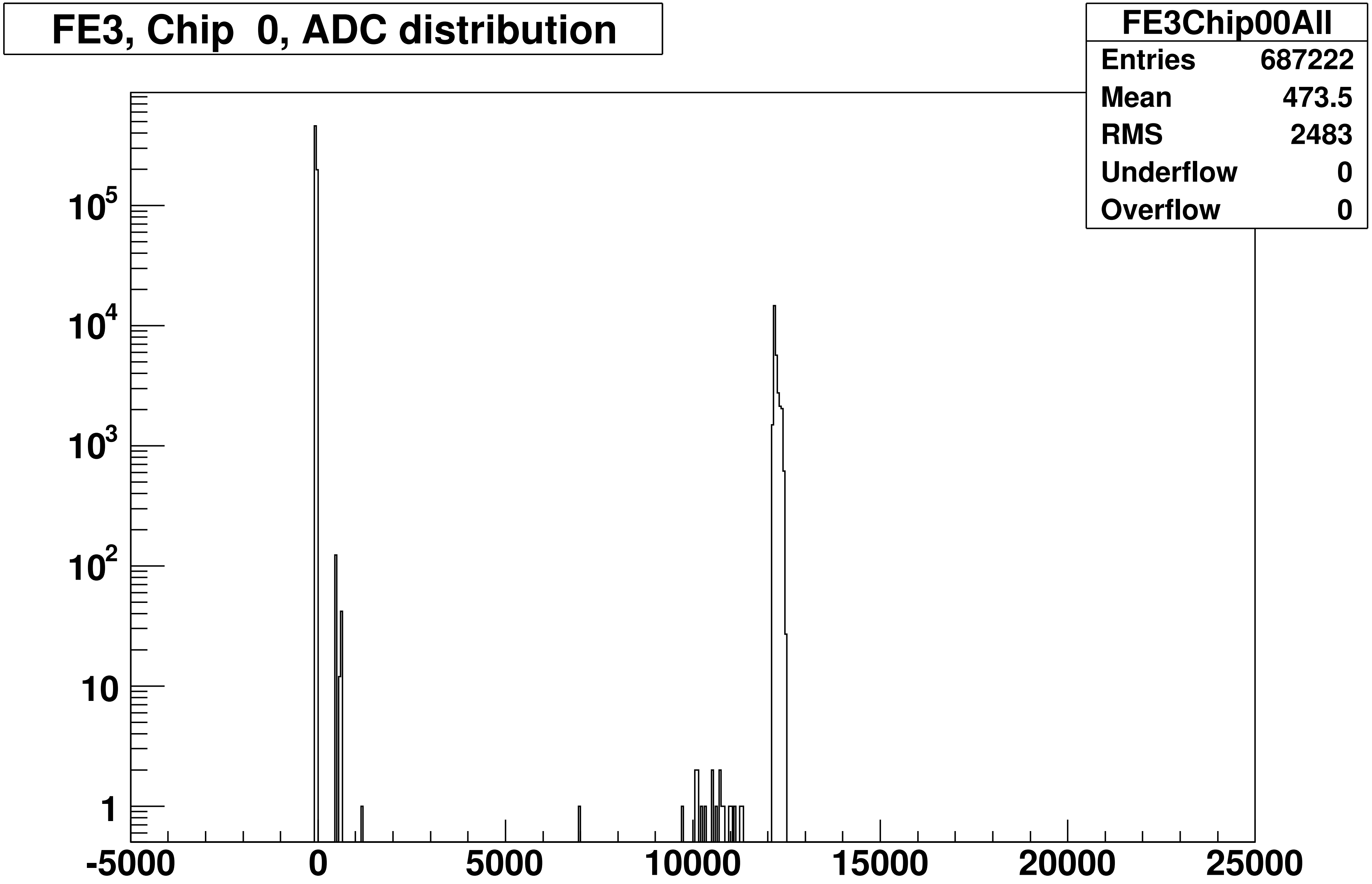
FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 687222
Mean 473.5
RMS 2483
Underflow 0
Overflow 0
10^5
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10^3
10^2
10
1
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0
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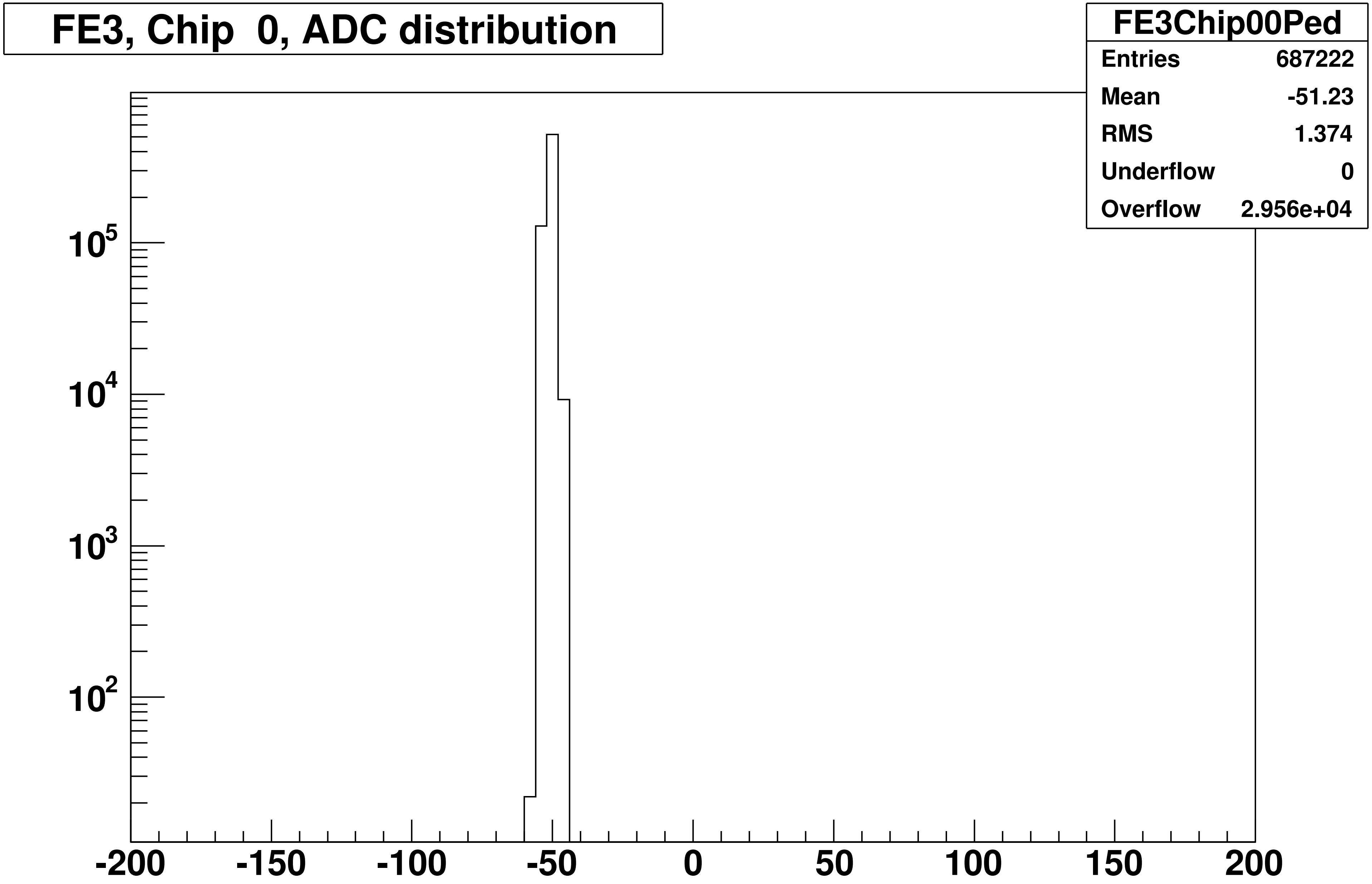
10000
15000
20000
25000


FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 687222
Mean -51.23
RMS 1.374
Underflow 0
Overflow 2.956e+04
10^5
10^4
10^3
10^2
-200
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-100
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0
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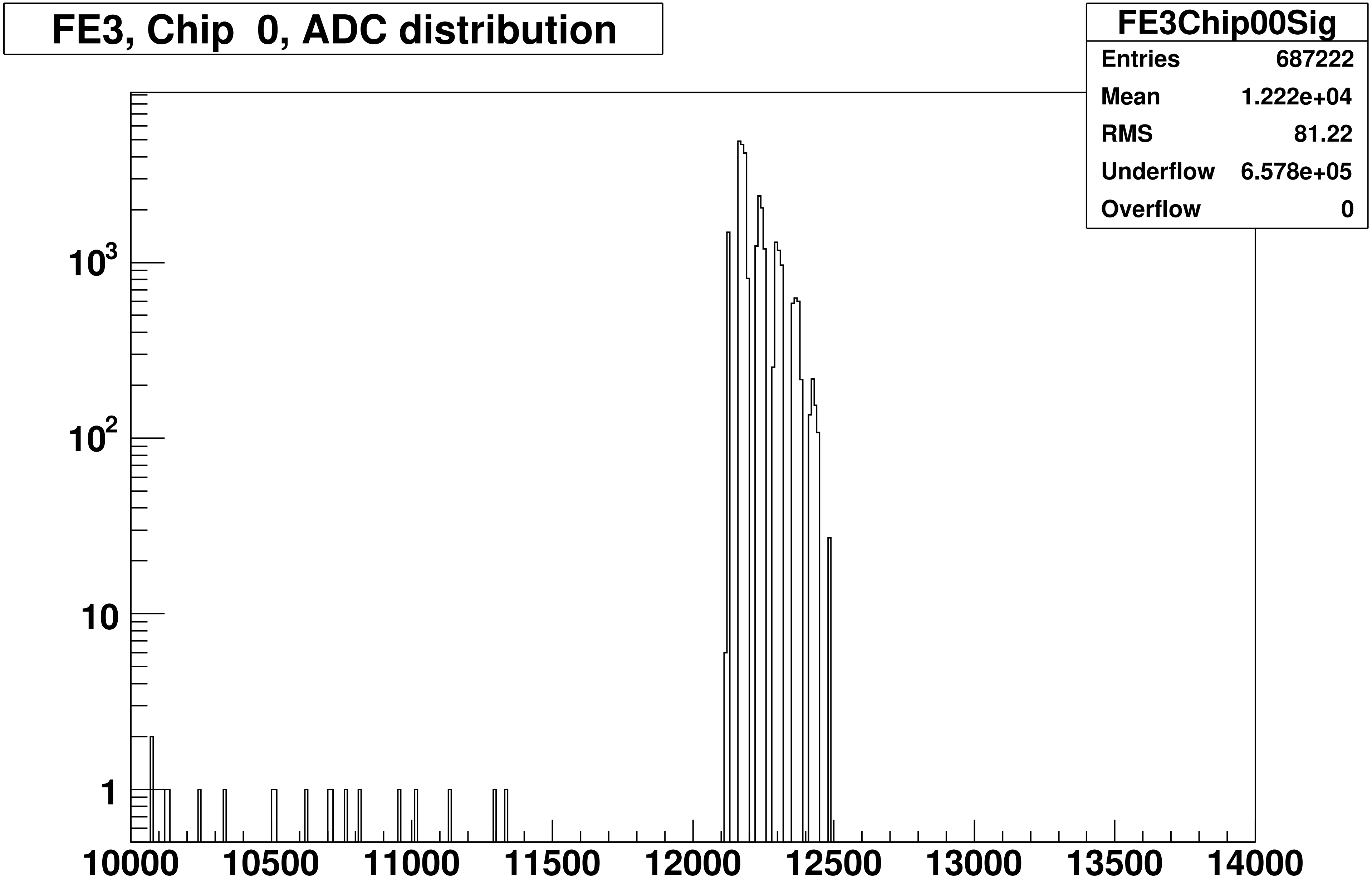
150
200


FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 687222
Mean 1.222e+04
RMS 81.22
Underflow 6.578e+05
Overflow 0
1
10
10^2
10^3
10000
10500
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11500
12000
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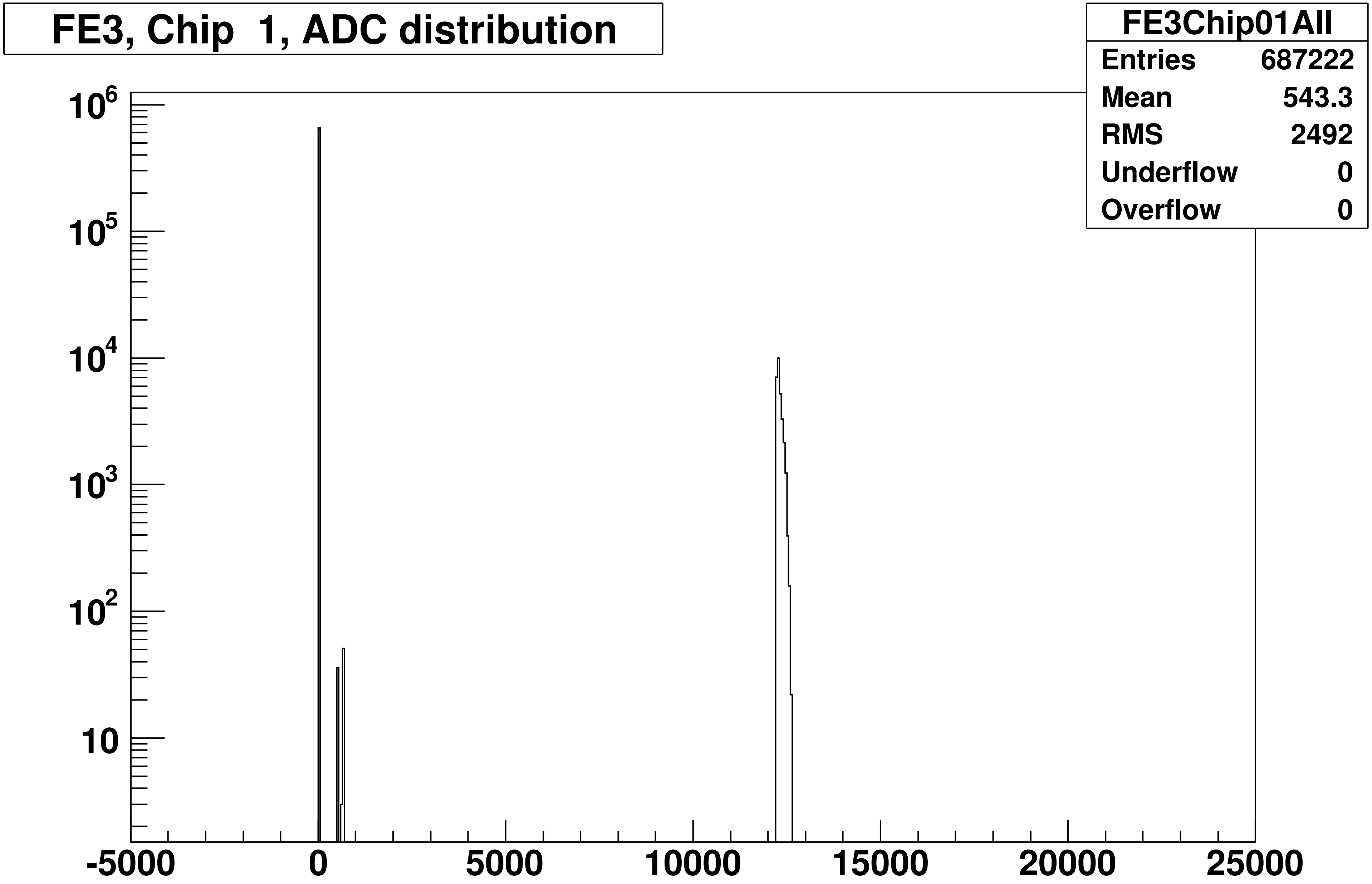
FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 687222
Mean 543.3
RMS 2492
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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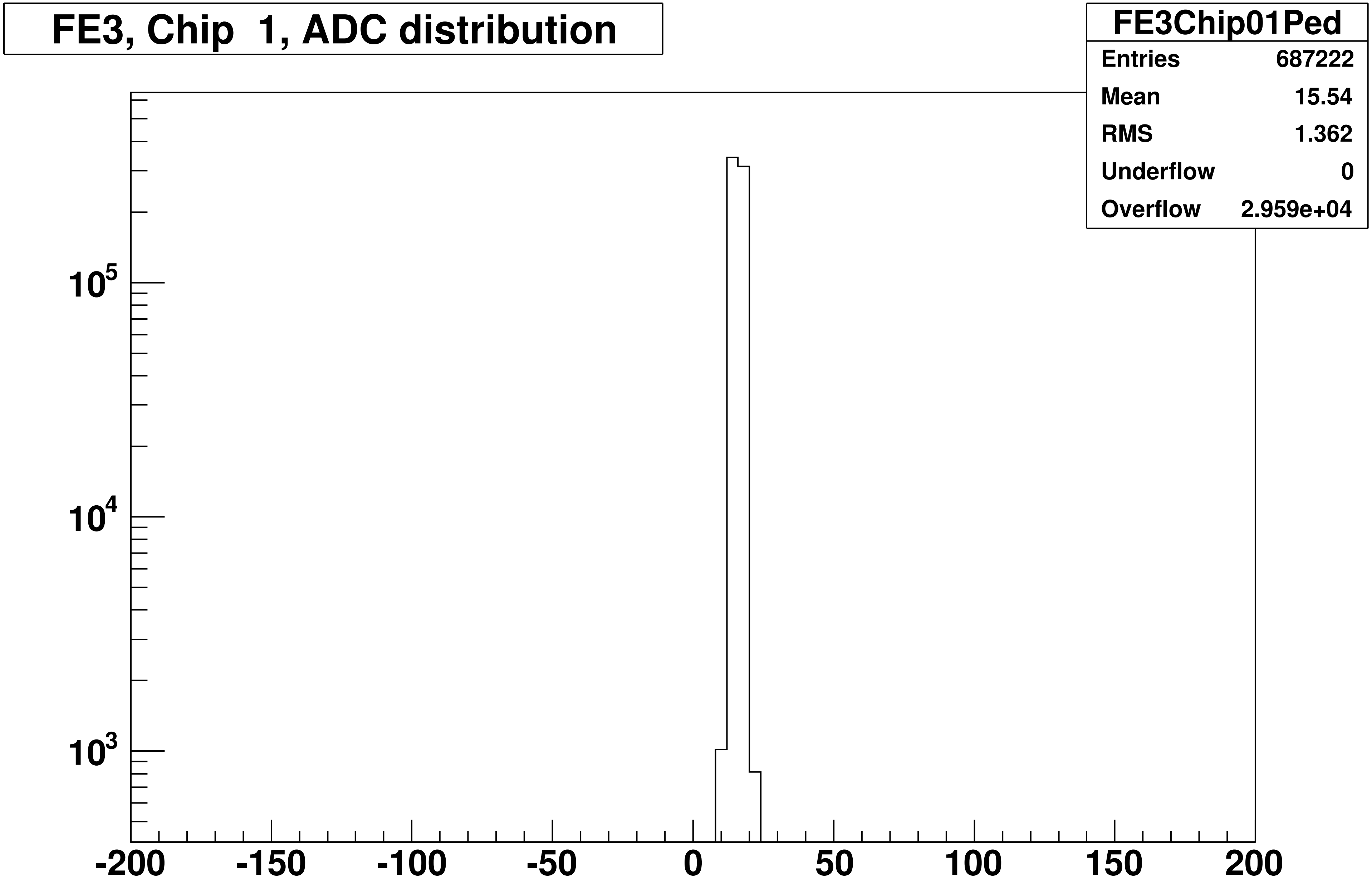
FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 687222
Mean 15.54
RMS 1.362
Underflow 0
Overflow 2.959e+04
10^5
10^4
10^3
-200
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-100
-50
0
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100
150
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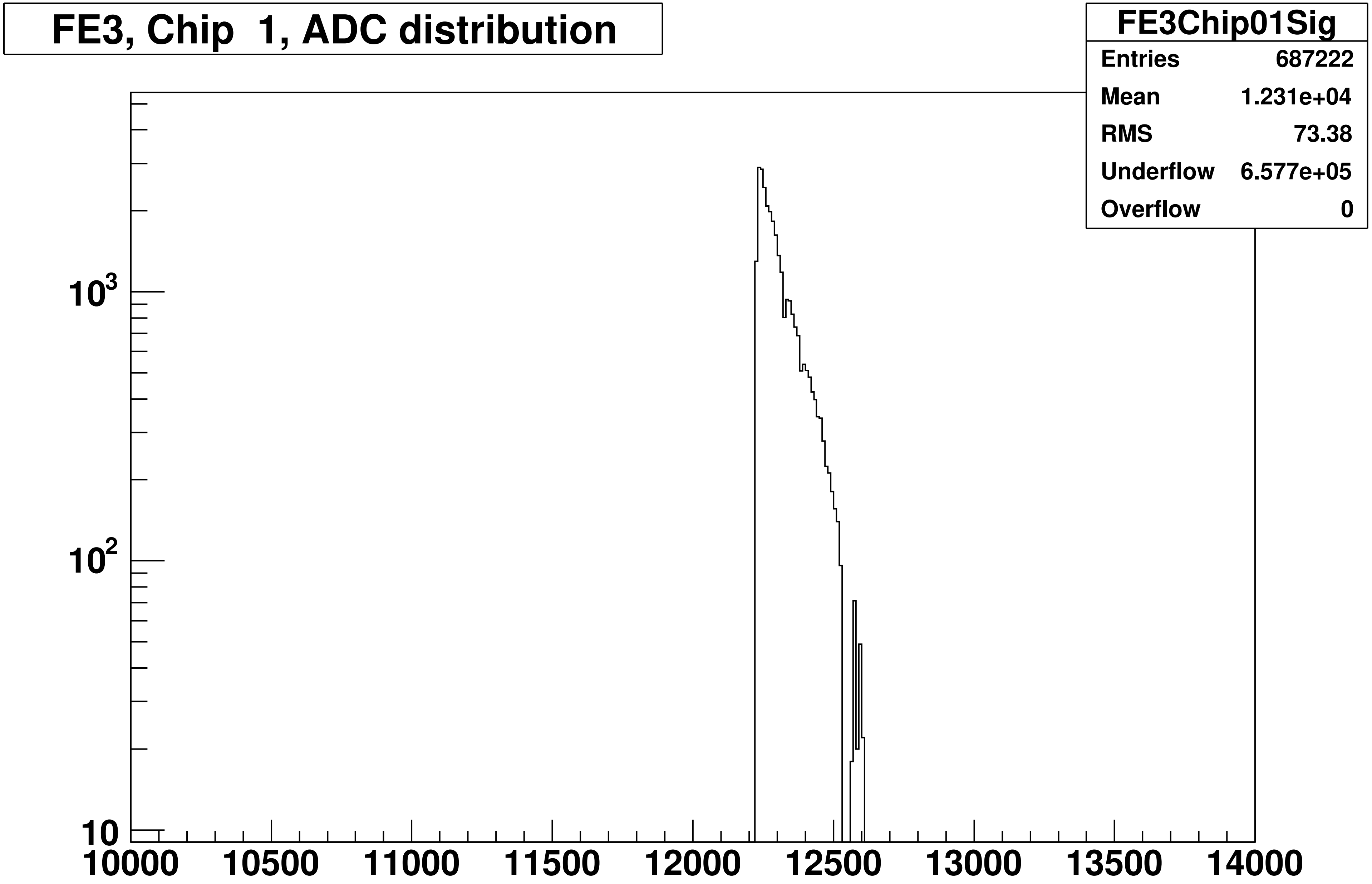
FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 687222
Mean 1.231e+04
RMS 73.38
Underflow 6.577e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
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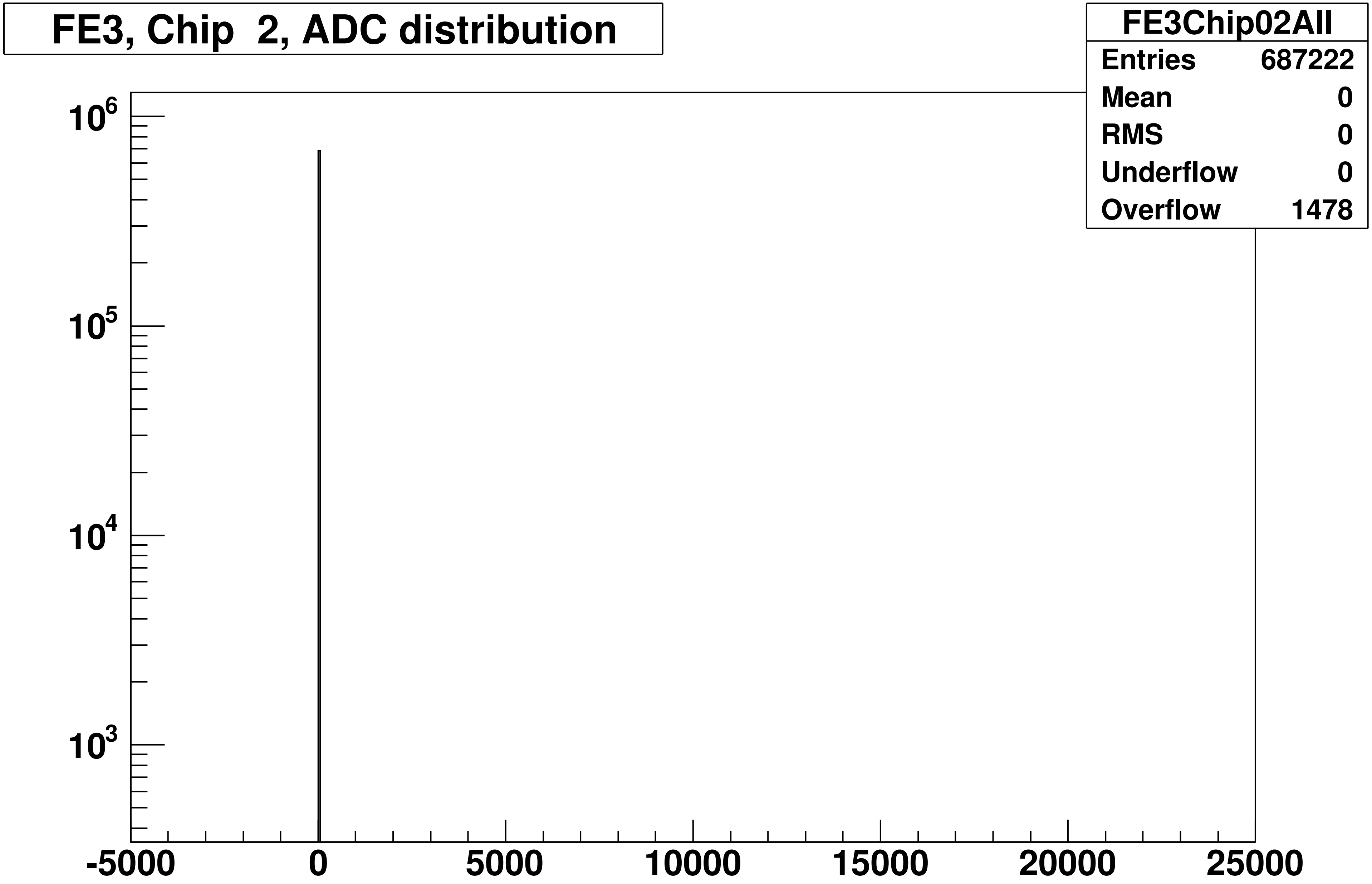
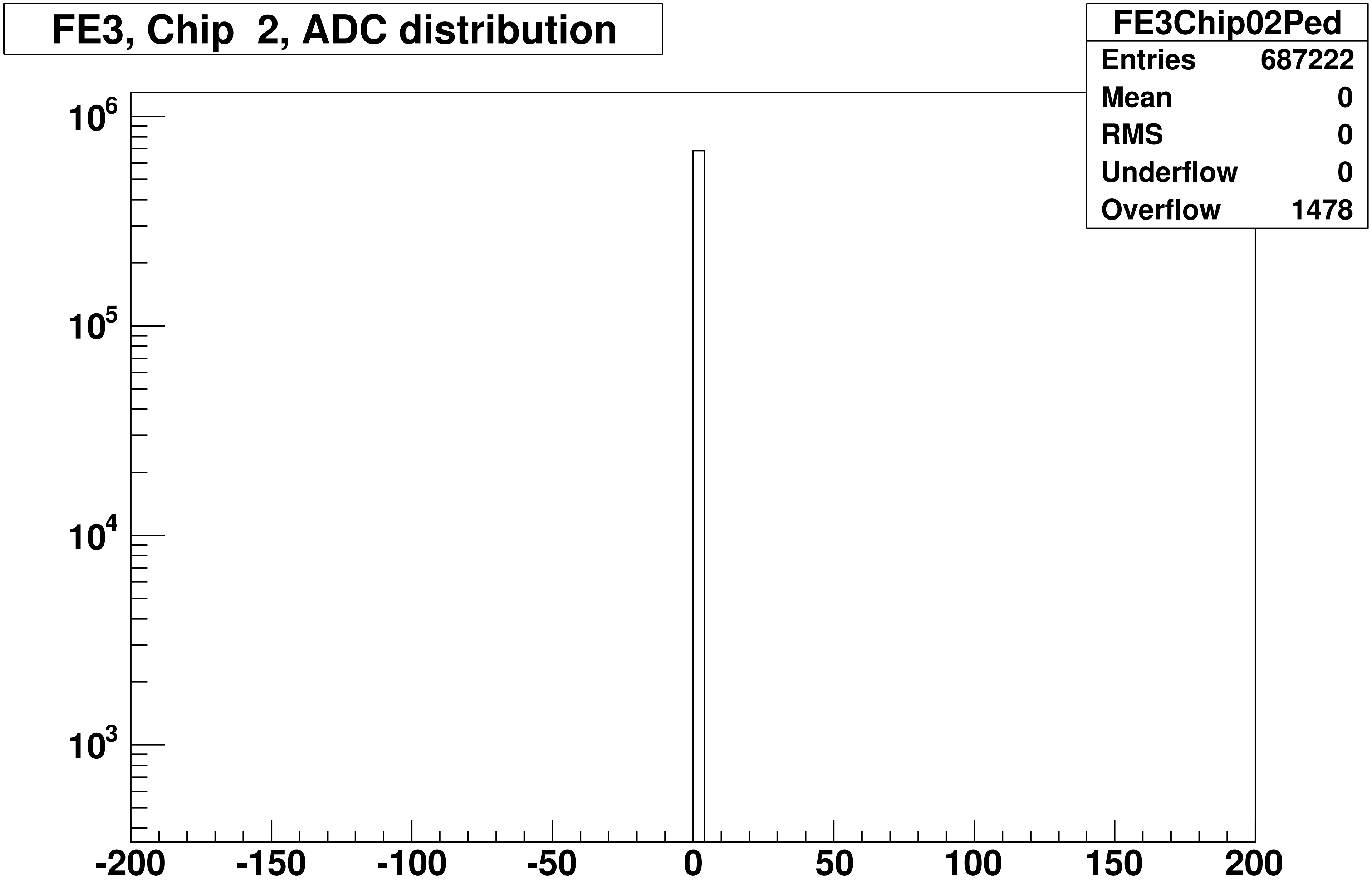

FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 687222
Mean 0
RMS 0
Underflow 0
Overflow 1478
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 687222
Mean 0
RMS 0
Underflow 0
Overflow 1478
10^6
10^5
10^4
10^3
-200
-150
-100
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0
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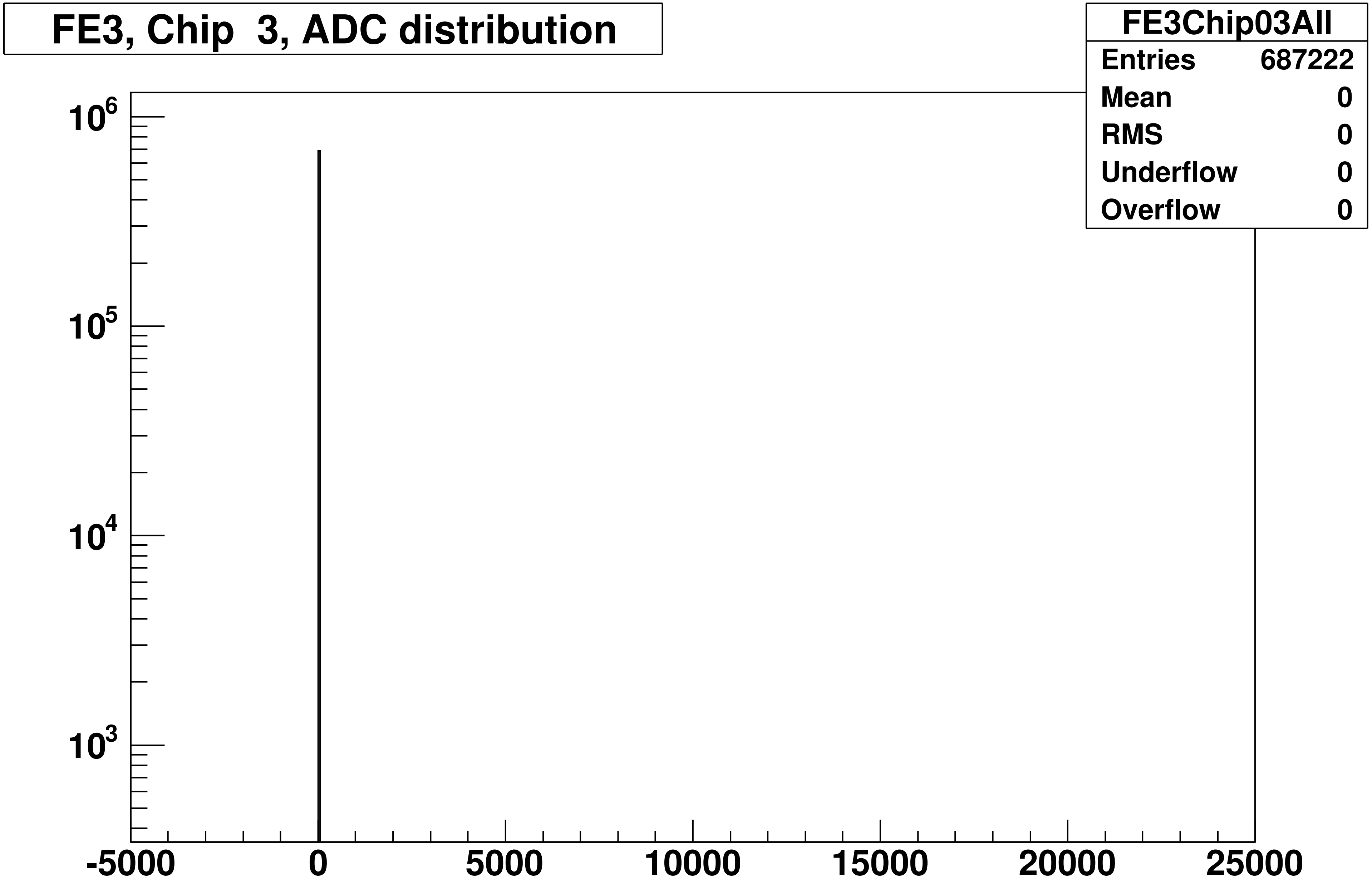
100
150
200


FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 687222
Mean 0
RMS 0
Underflow 0
Overflow 0
10^6
10^5
10^4
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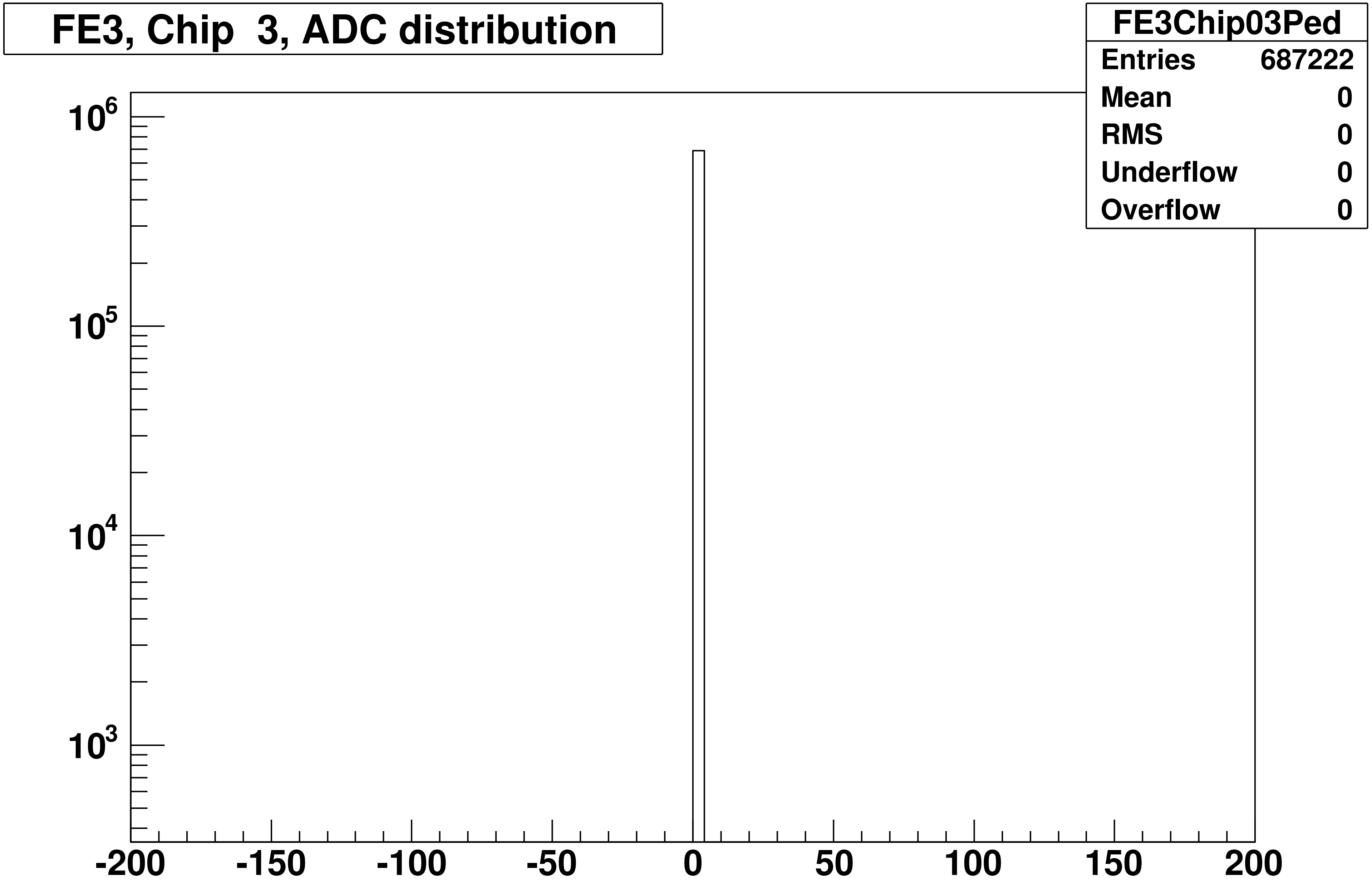
FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 687222
Mean 0
RMS 0
Underflow 0
Overflow 0
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10^5
10^4
10^3
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-150
-100
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0
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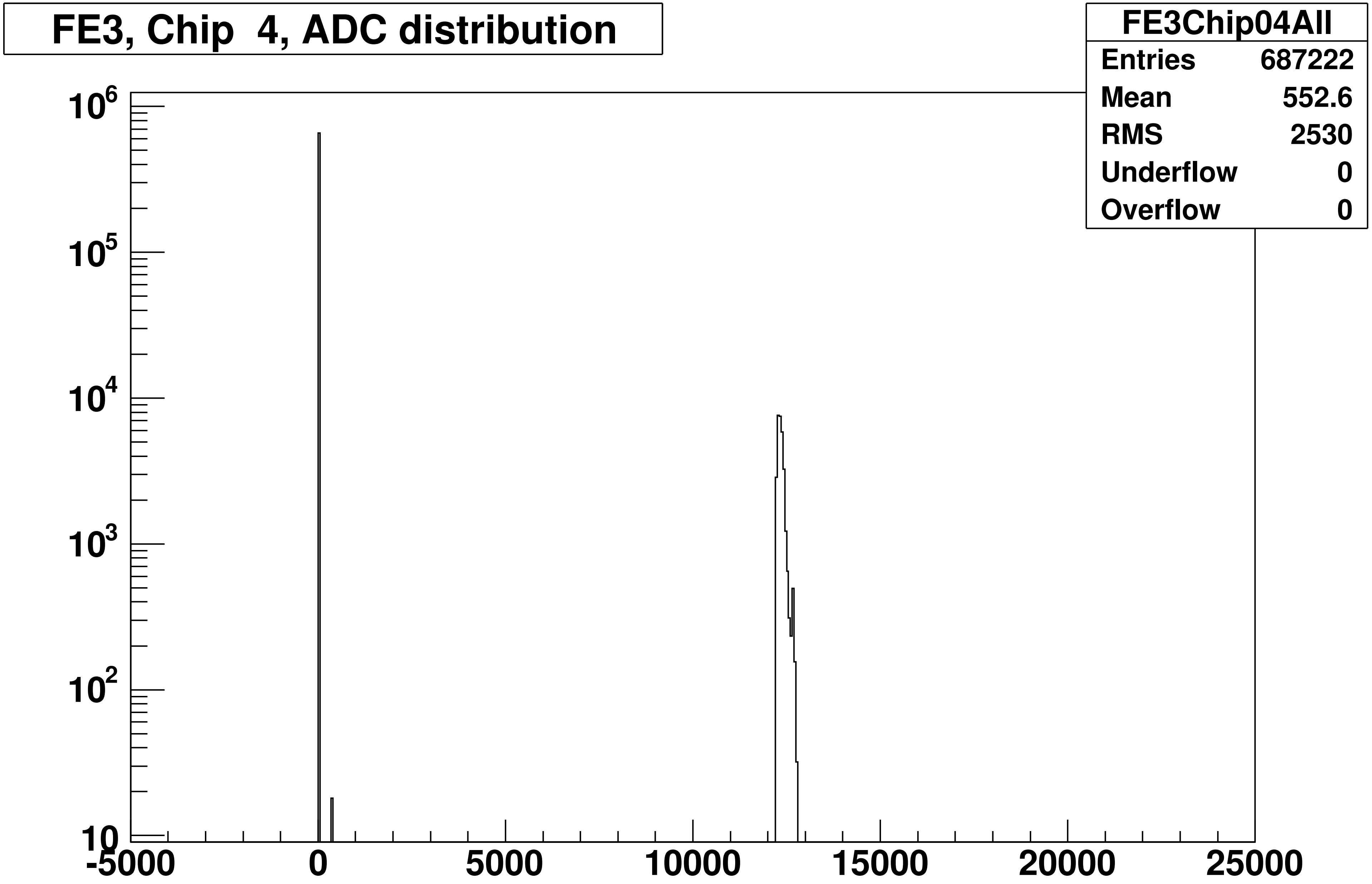
100
150
200



FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 687222
Mean 552.6
RMS 2530
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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5000
10000
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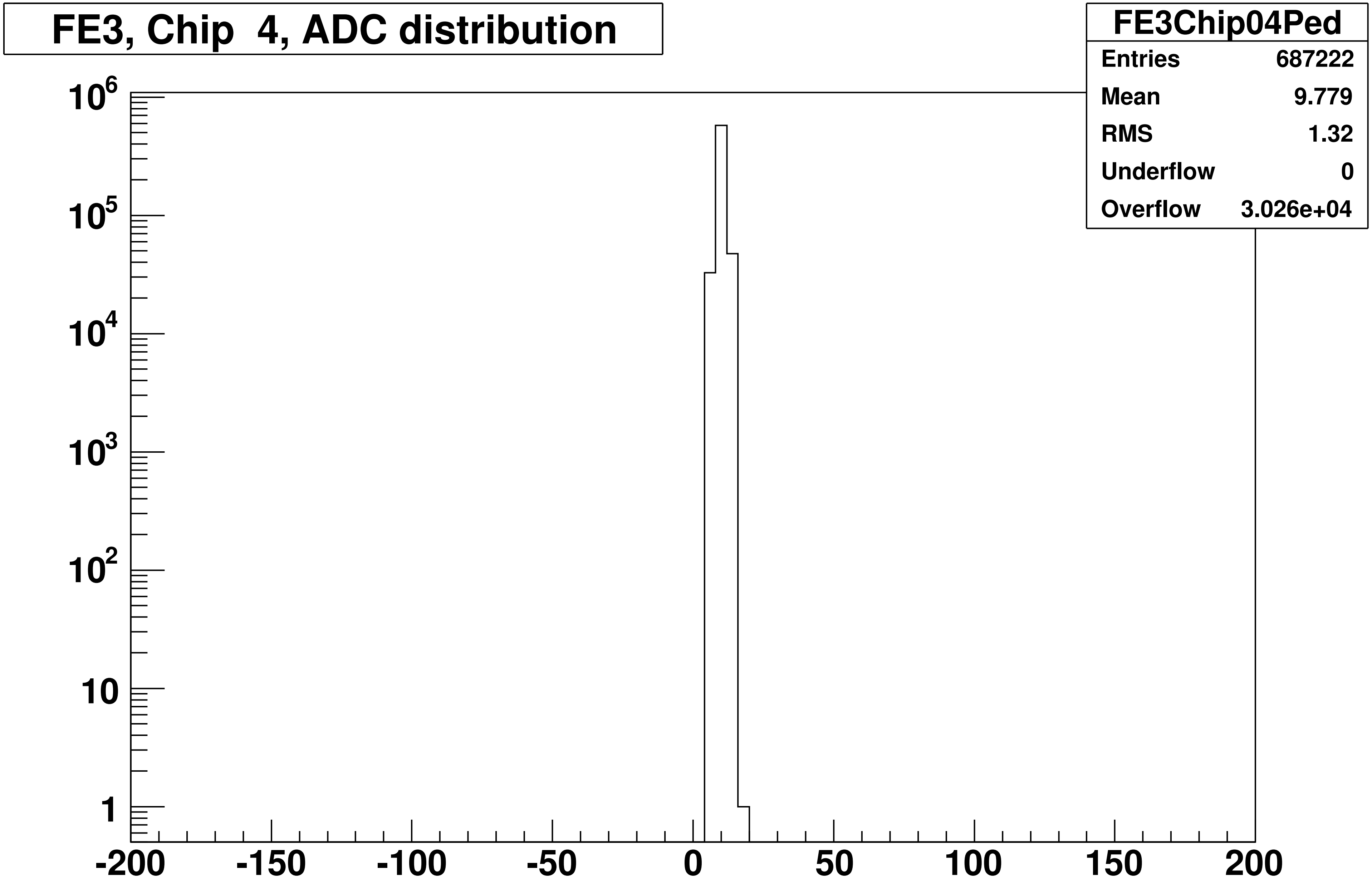
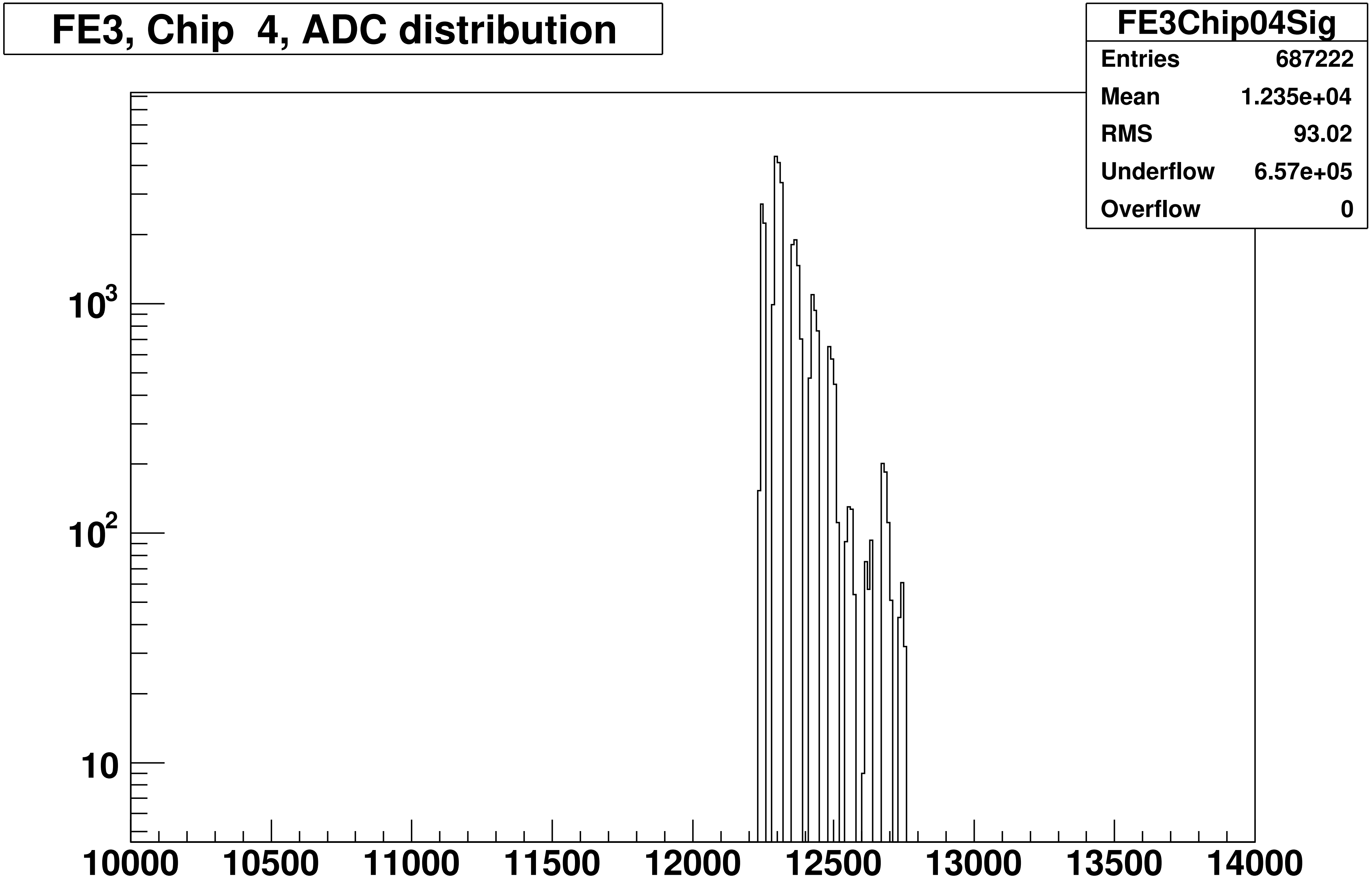
20000
25000


FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 687222
Mean 9.779
RMS 1.32
Underflow 0
Overflow 3.026e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 687222
Mean 1.235e+04
RMS 93.02
Underflow 6.57e+05
Overflow 0
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10^2
10
10000
10500
11000
11500
12000
12500
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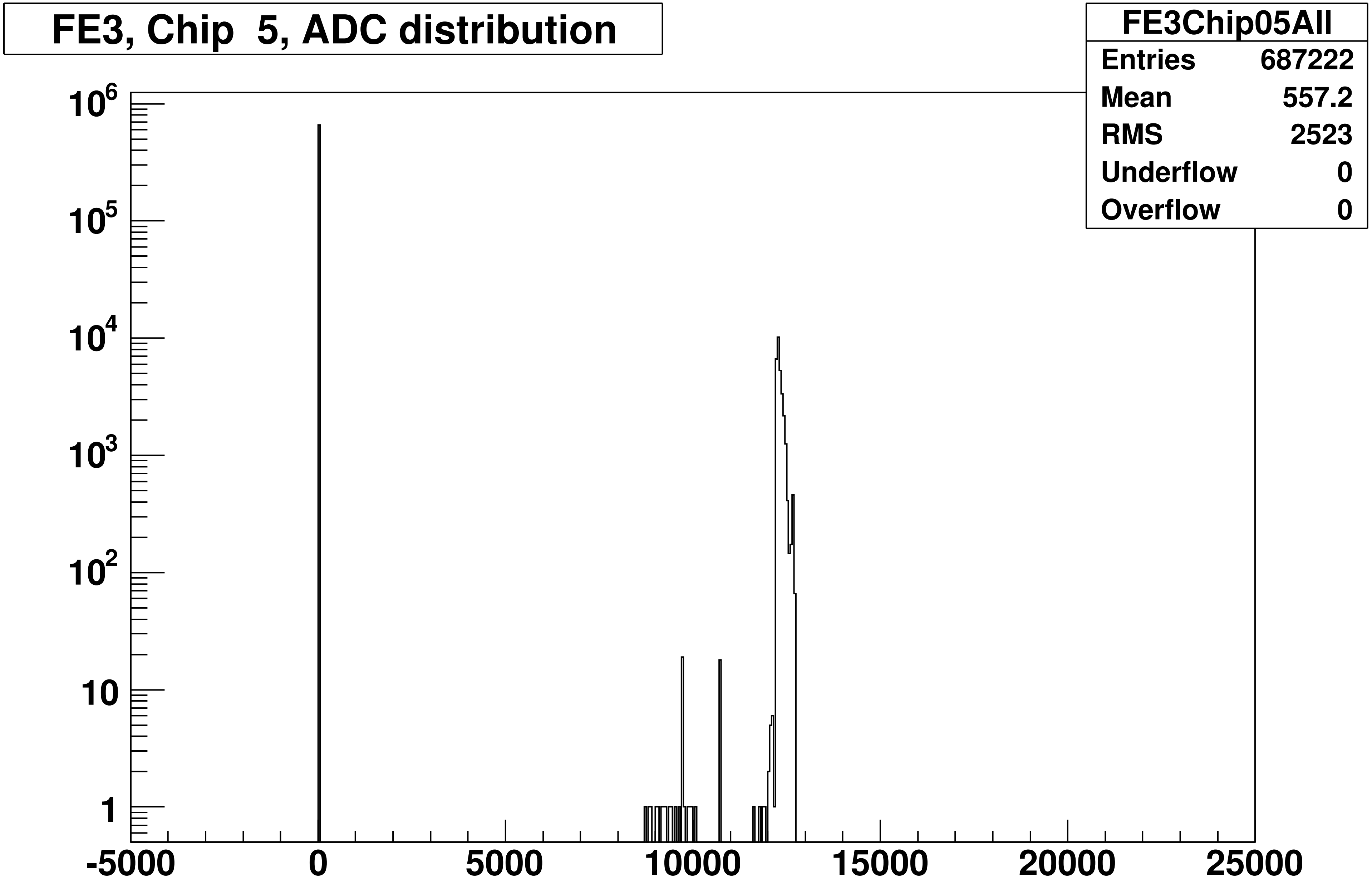

FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 687222
Mean 557.2
RMS 2523
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
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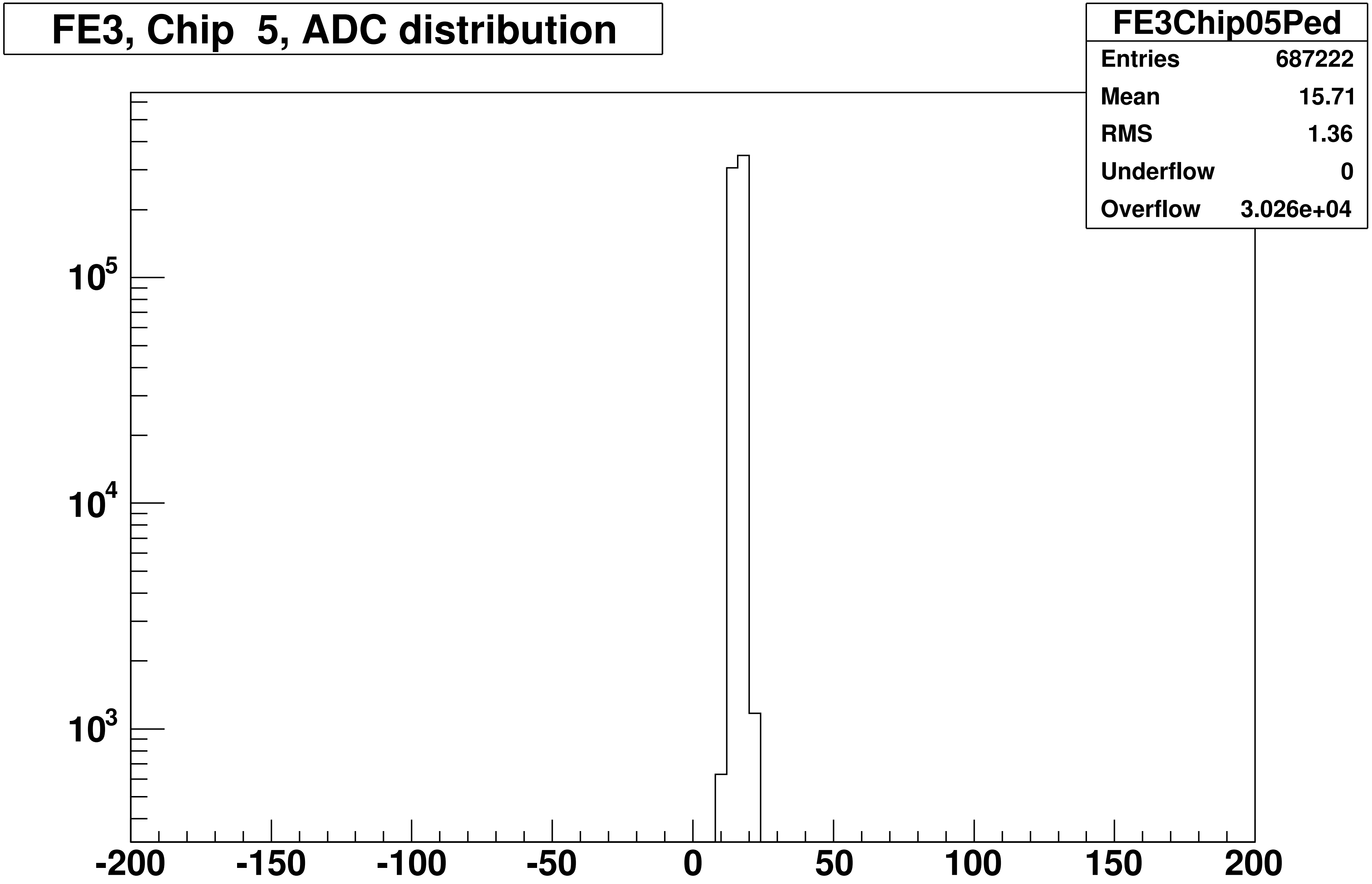

FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 687222
Mean 15.71
RMS 1.36
Underflow 0
Overflow 3.026e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
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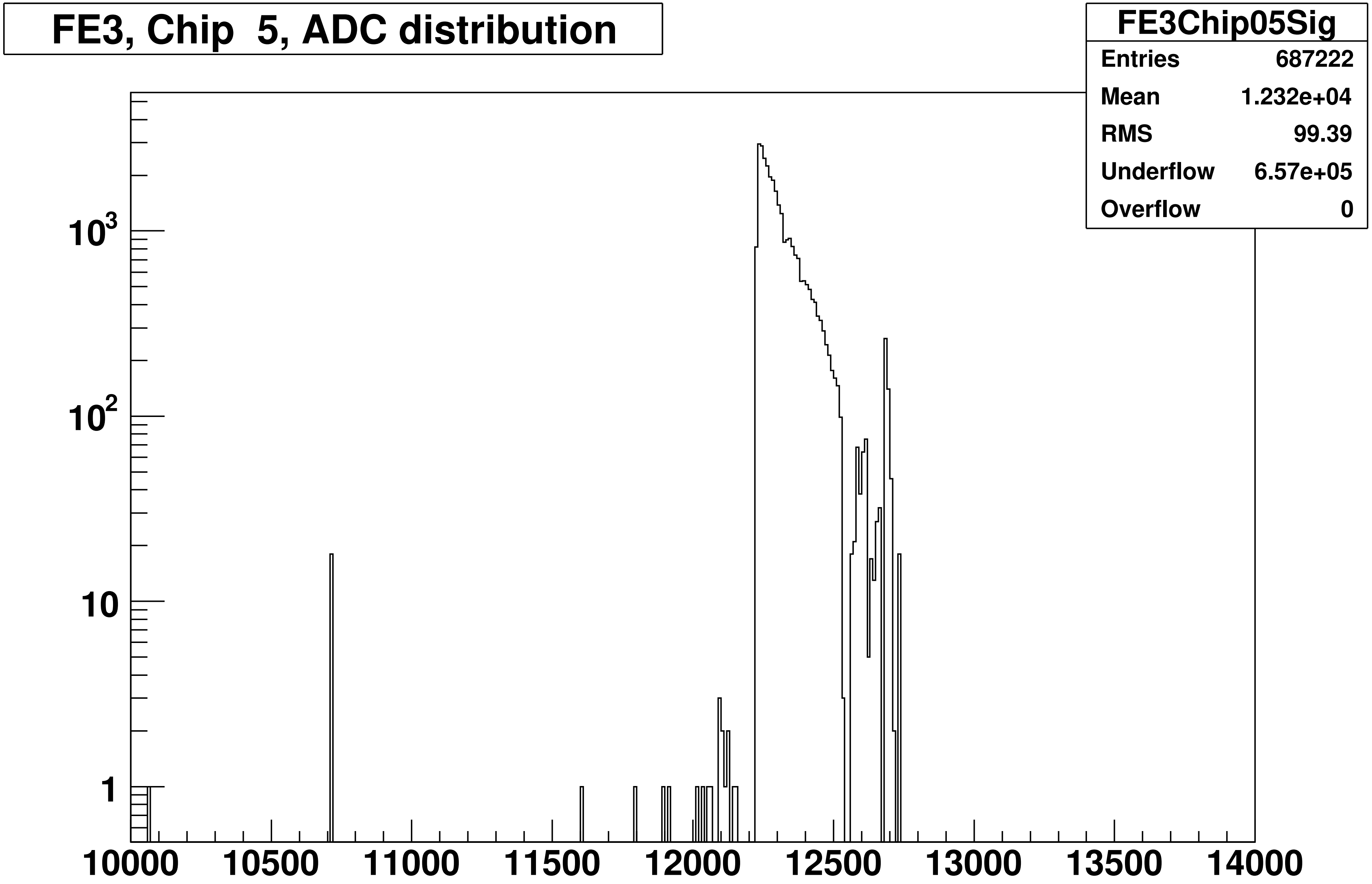
FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 687222
Mean 1.232e+04
RMS 99.39
Underflow 6.57e+05
Overflow 0
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10^2
10
1
10000
10500
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11500
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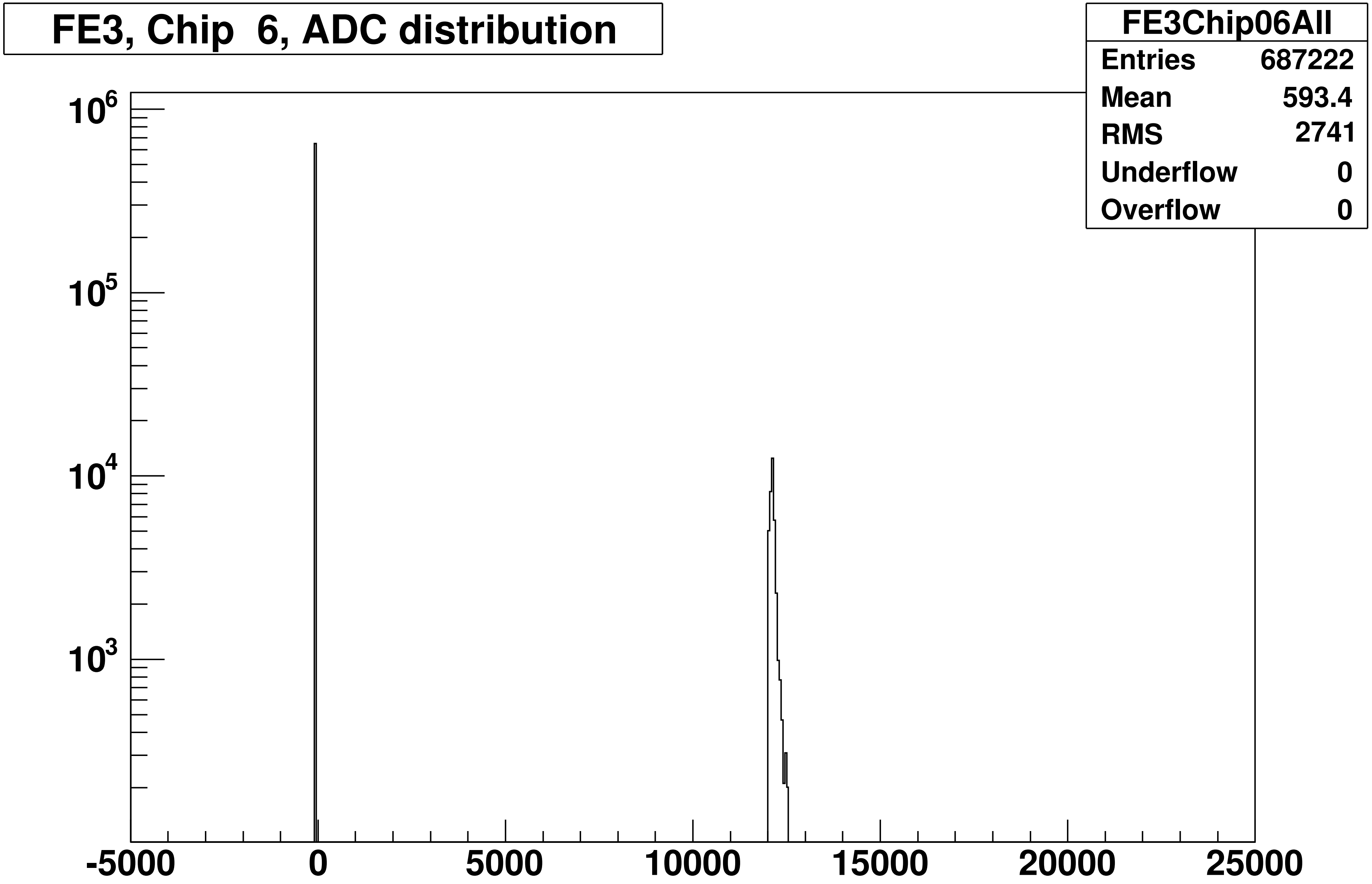
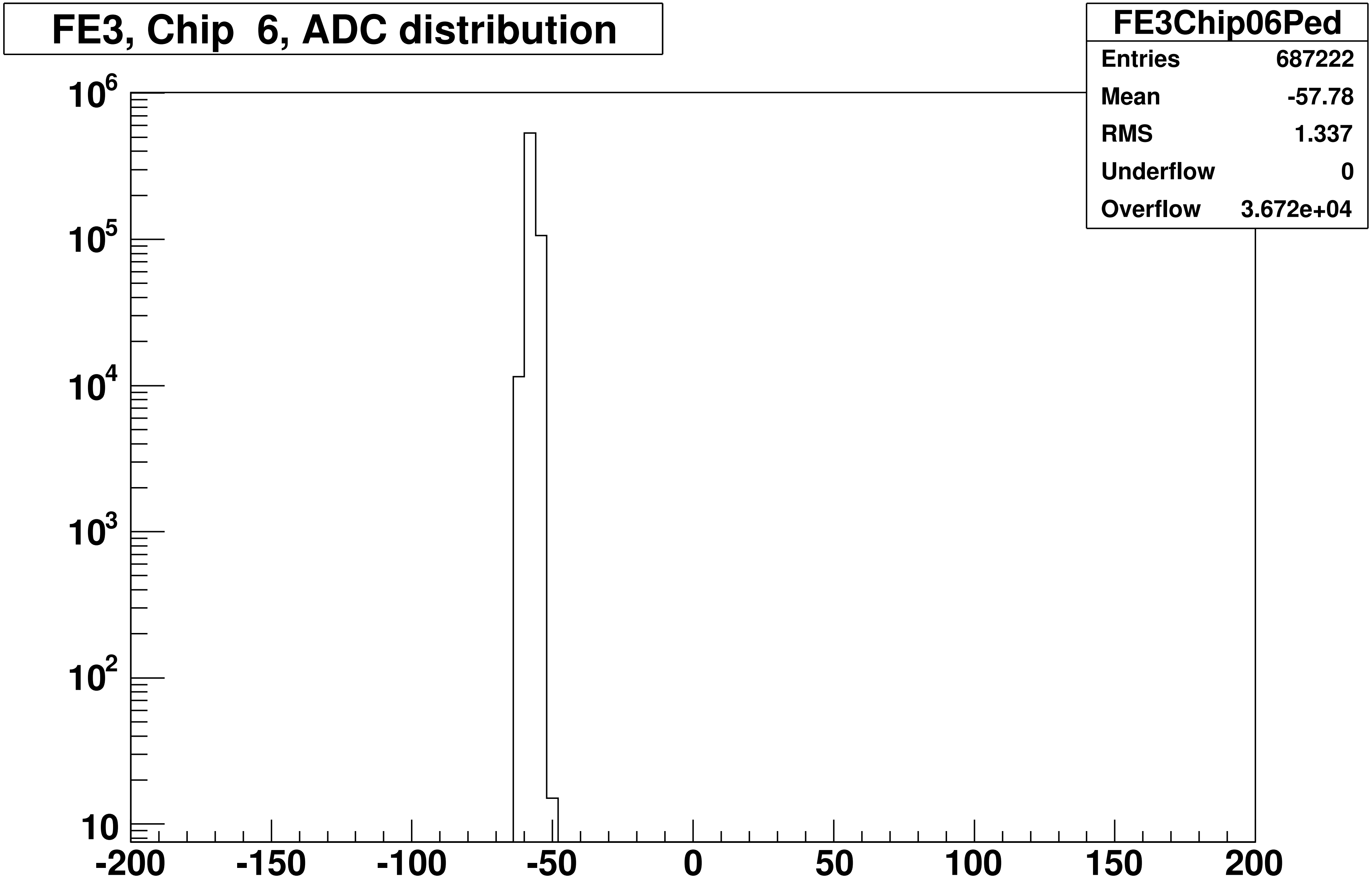


FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 687222
Mean 593.4
RMS 2741
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 687222
Mean -57.78
RMS 1.337
Underflow 0
Overflow 3.672e+04
10^6
10^5
10^4
10^3
10^2
10
-200
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50
100
150
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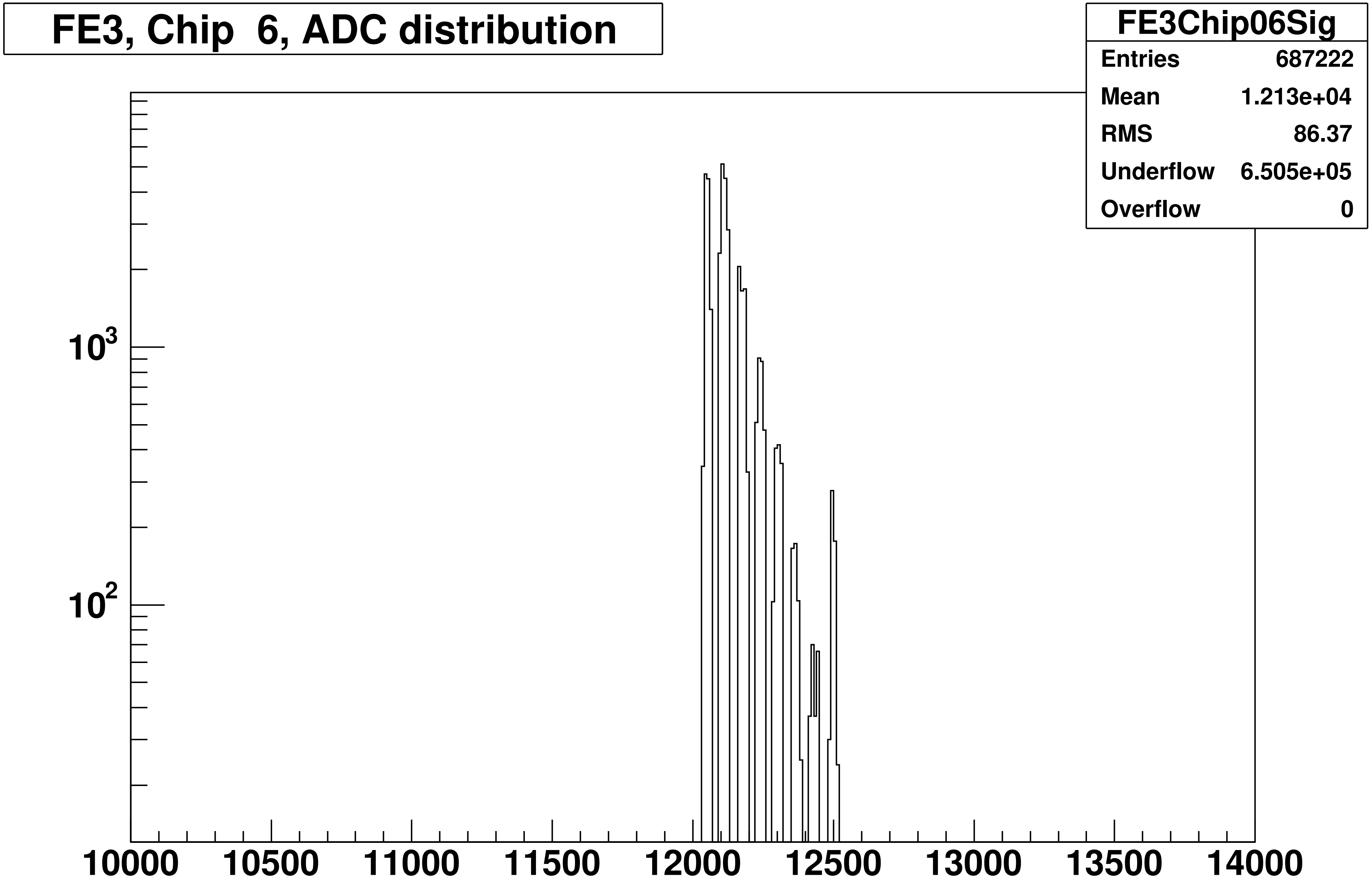
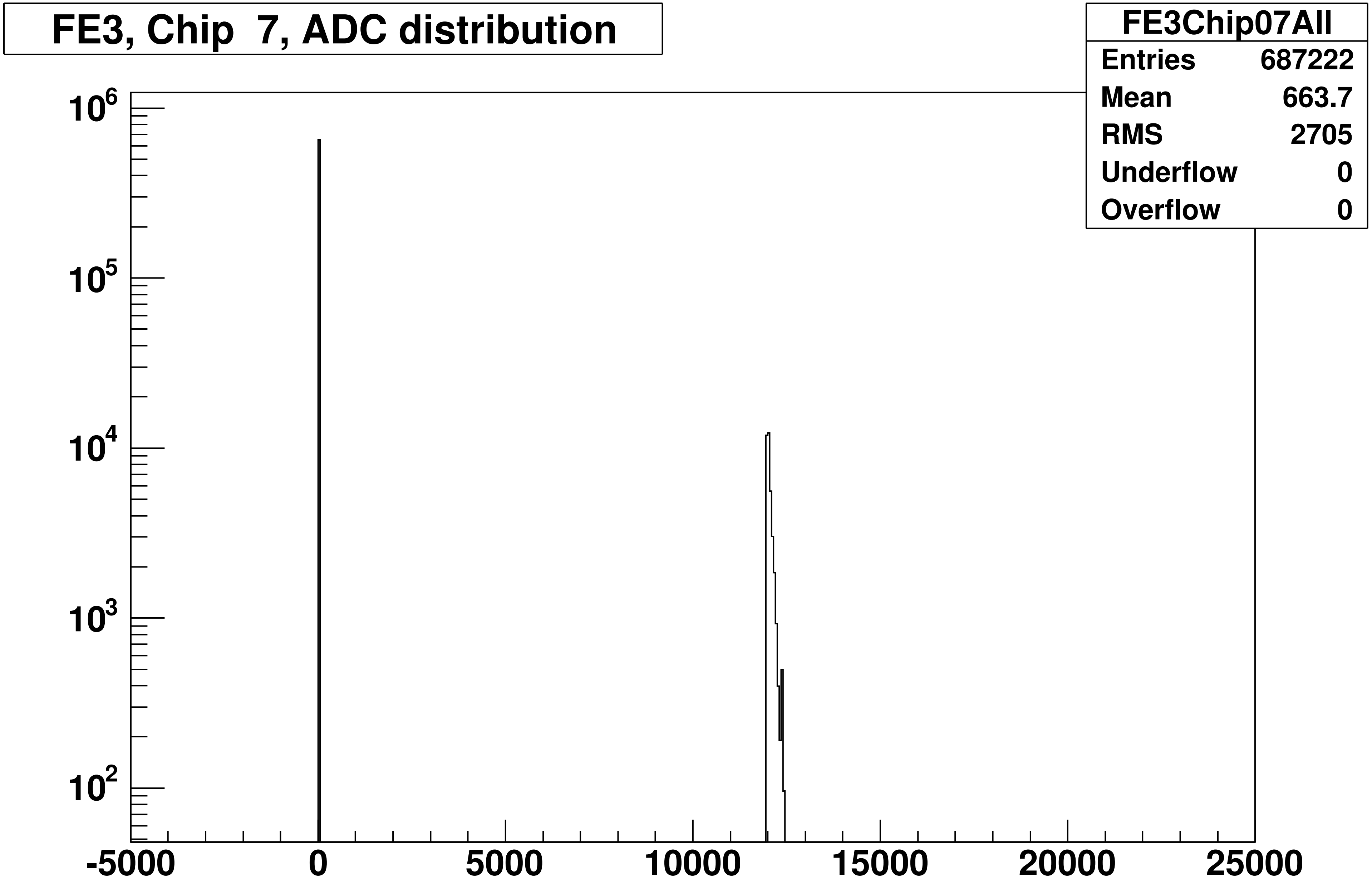
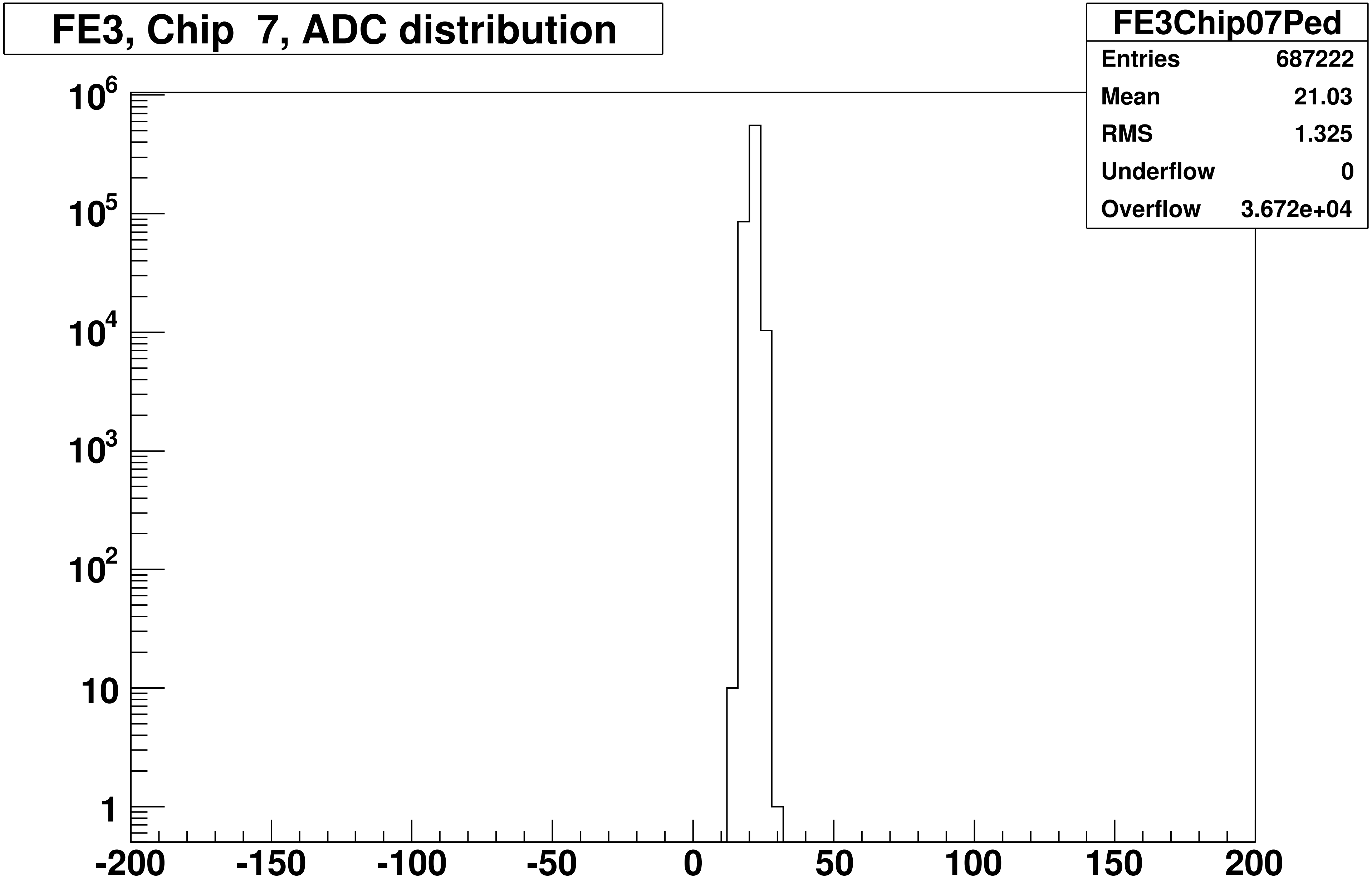


FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 687222
Mean 1.213e+04
RMS 86.37
Underflow 6.505e+05
Overflow 0
10^3
10^2
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 687222
Mean 663.7
RMS 2705
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 687222
Mean 21.03
RMS 1.325
Underflow 0
Overflow 3.672e+04
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10^5
10^4
10^3
10^2
10
1
-200
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150
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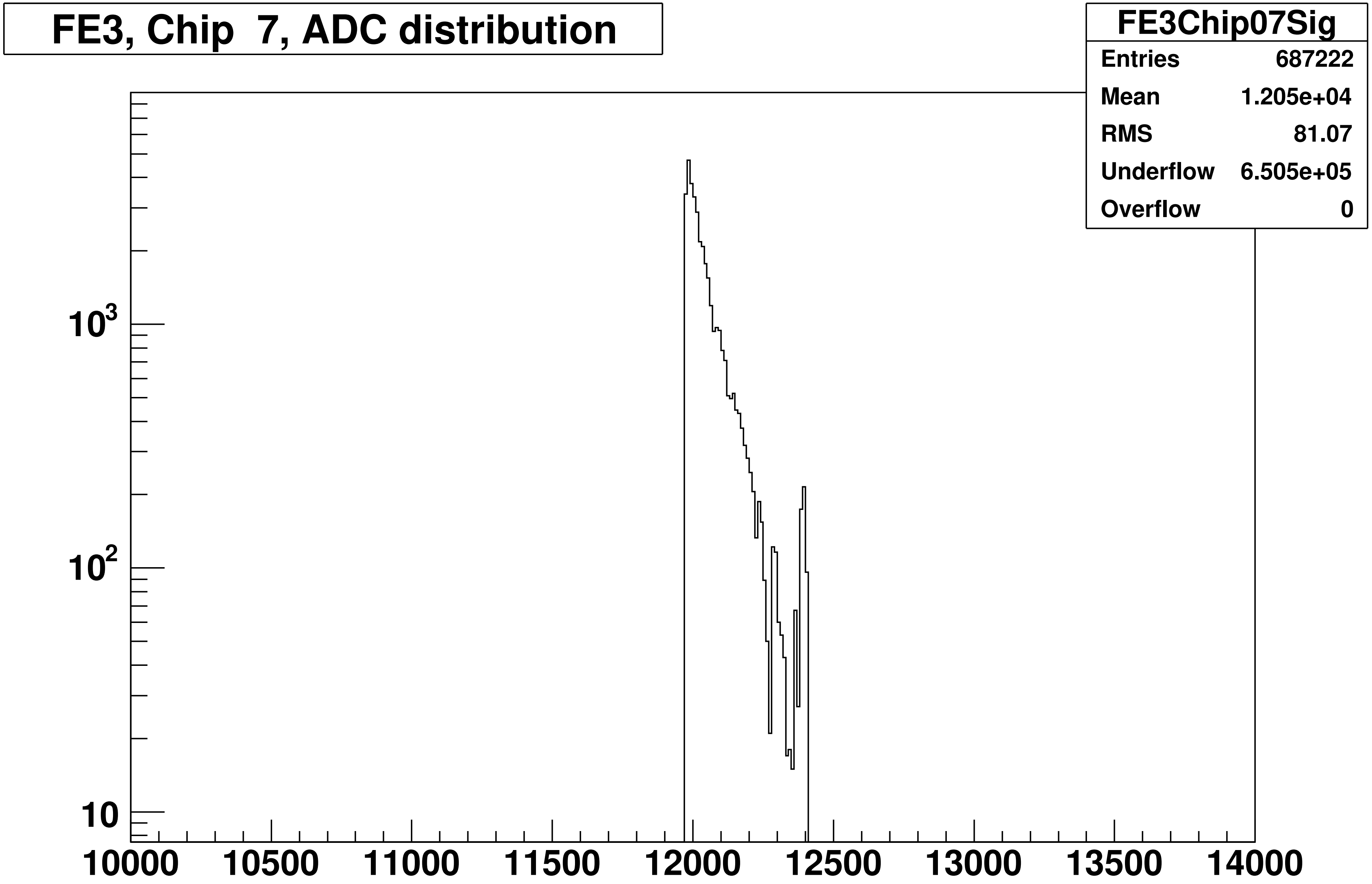
FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 687222
Mean 1.205e+04
RMS 81.07
Underflow 6.505e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
13000
13500
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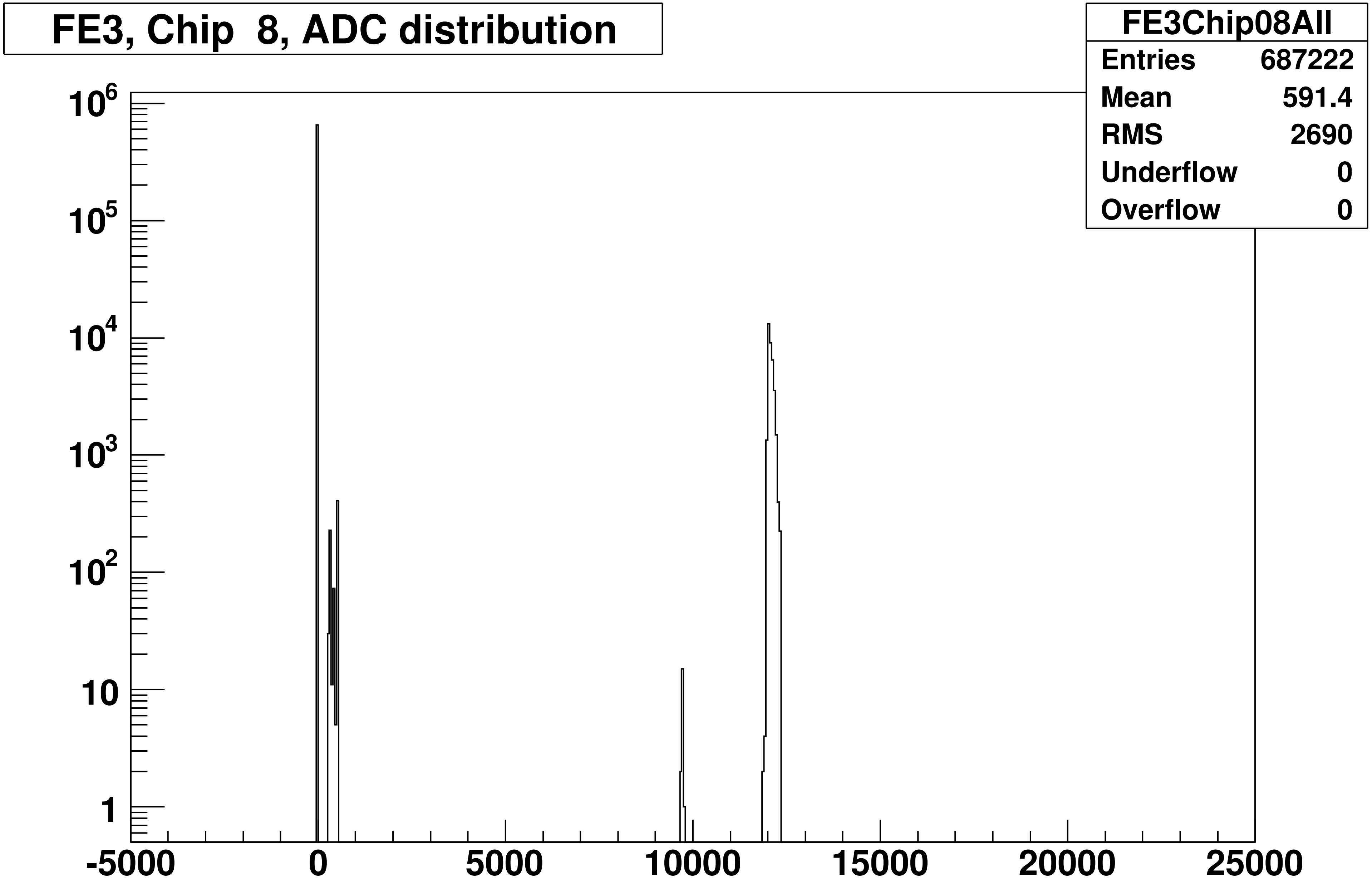
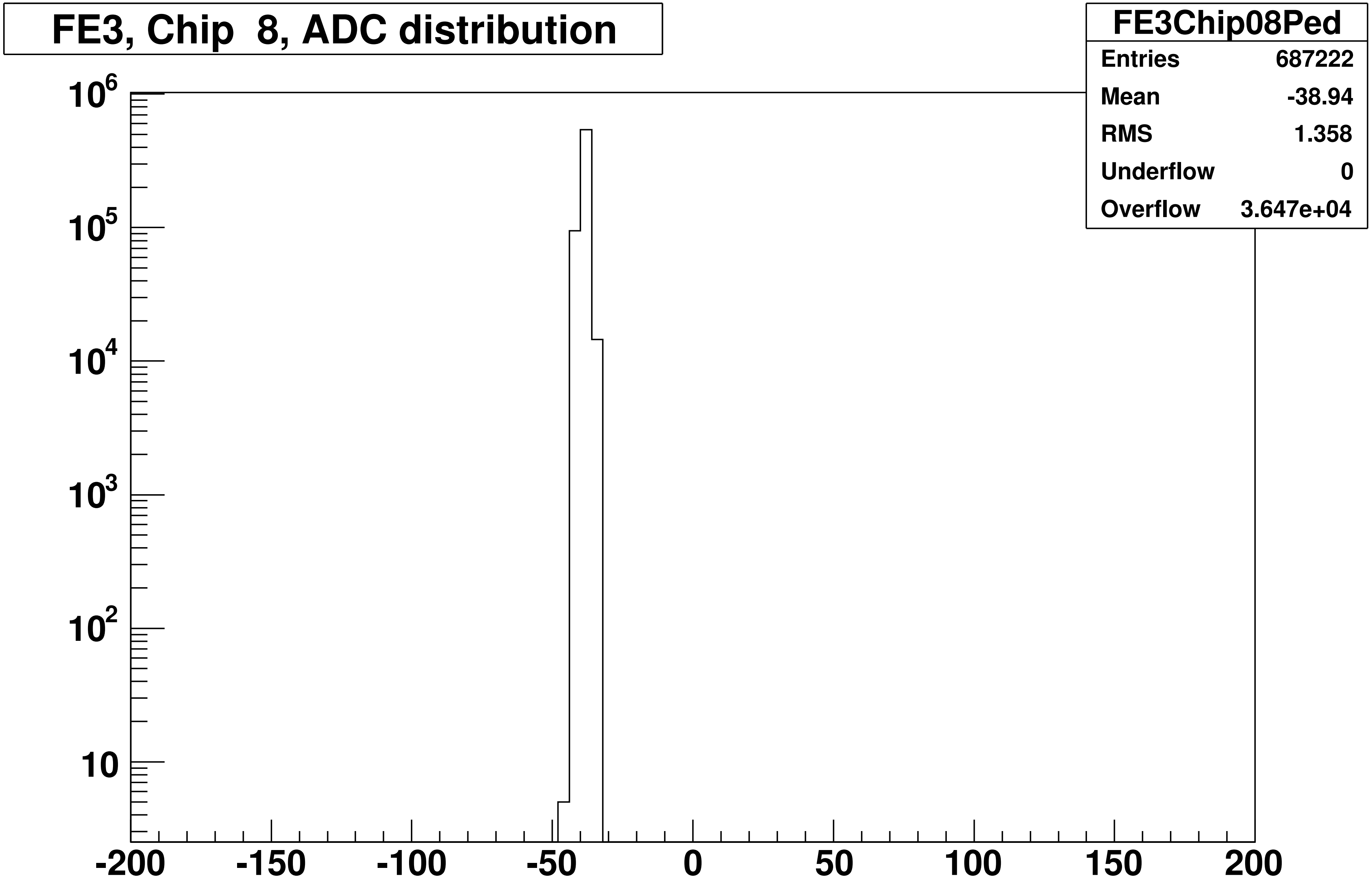

FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 687222
Mean 591.4
RMS 2690
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 687222
Mean -38.94
RMS 1.358
Underflow 0
Overflow 3.647e+04
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10^5
10^4
10^3
10^2
10
-200
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100
150
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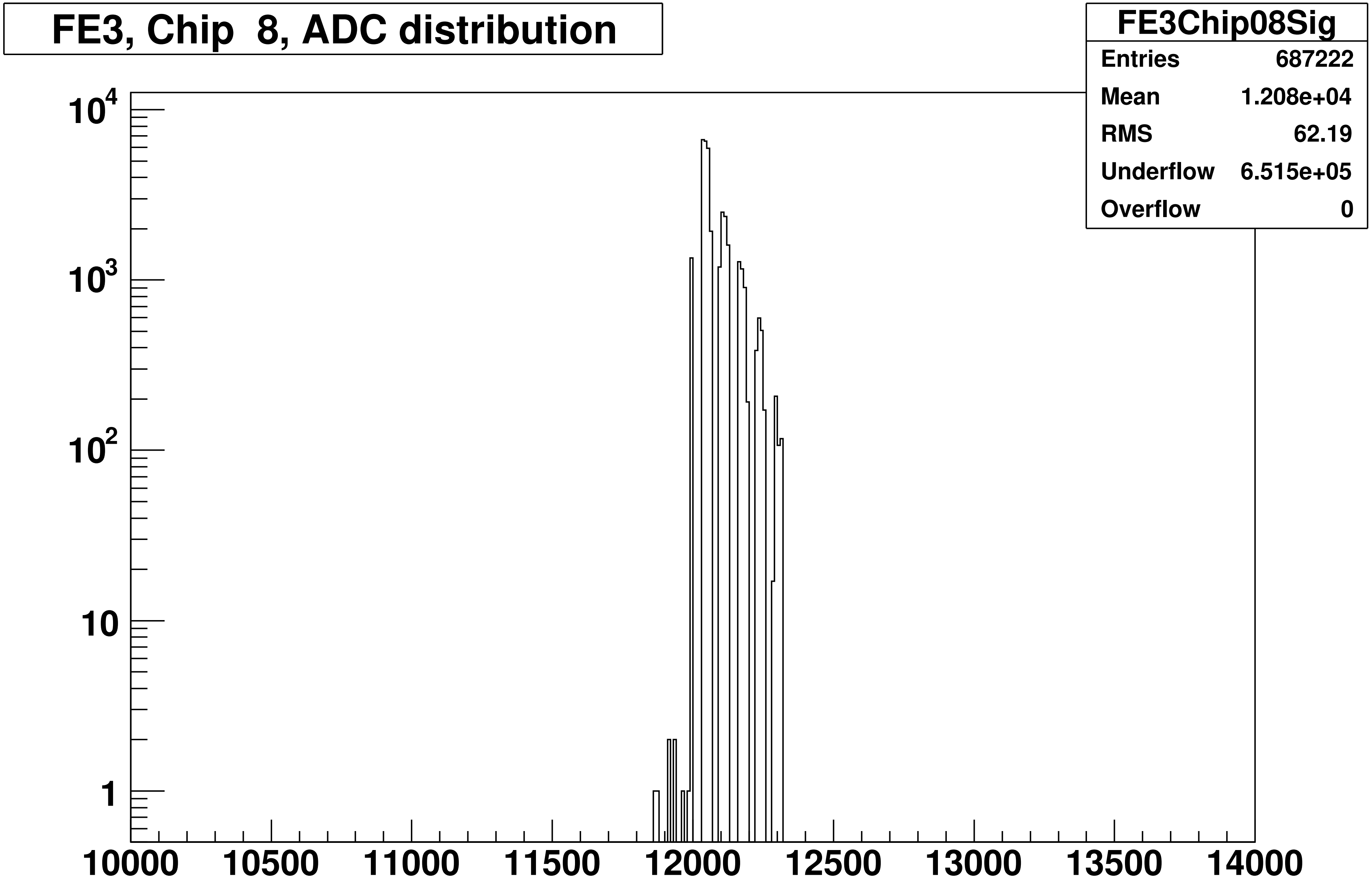
FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 687222
Mean 1.208e+04
RMS 62.19
Underflow 6.515e+05
Overflow 0
10^4
10^3
10^2
10
1
10000
10500
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11500
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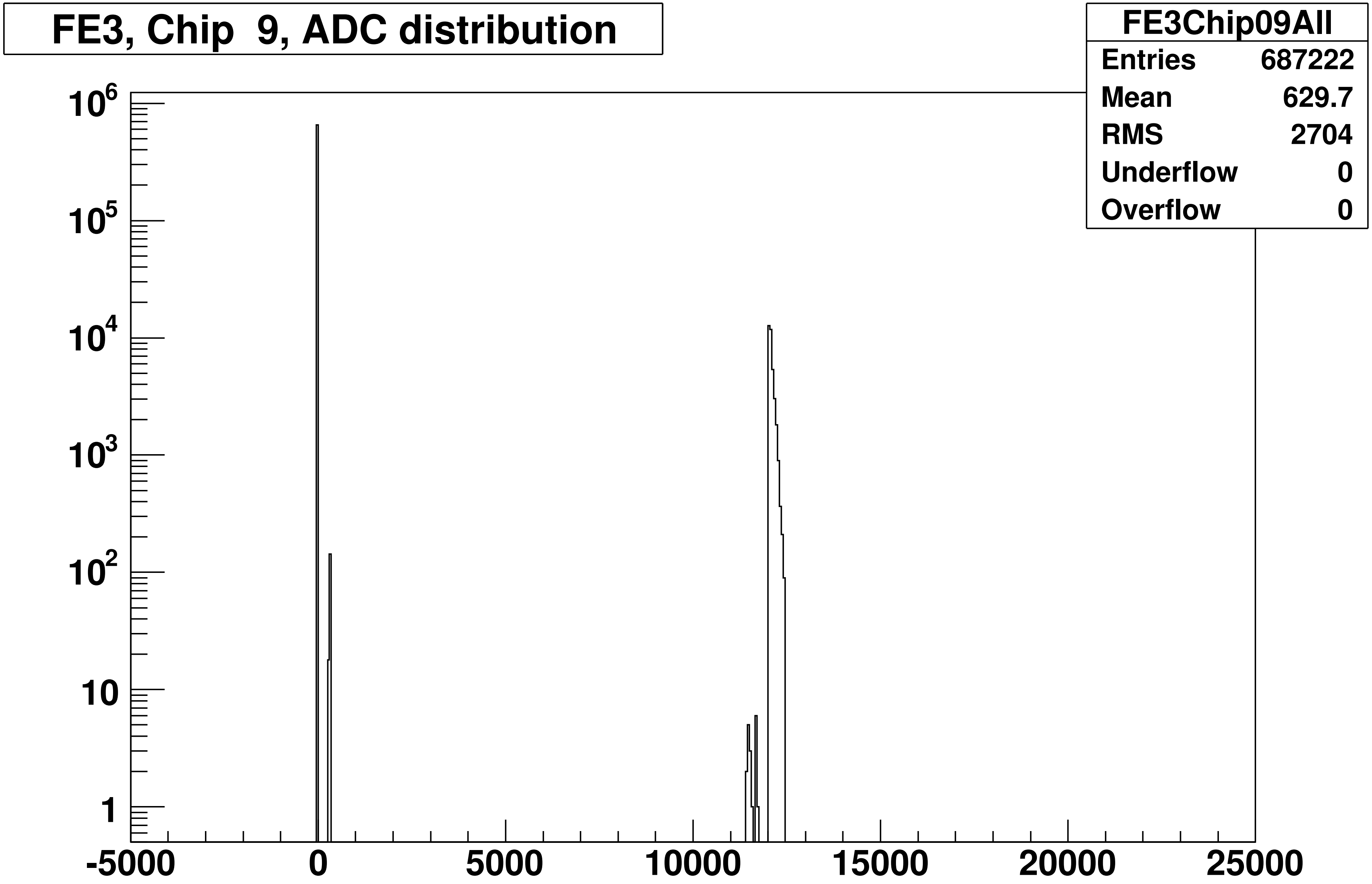
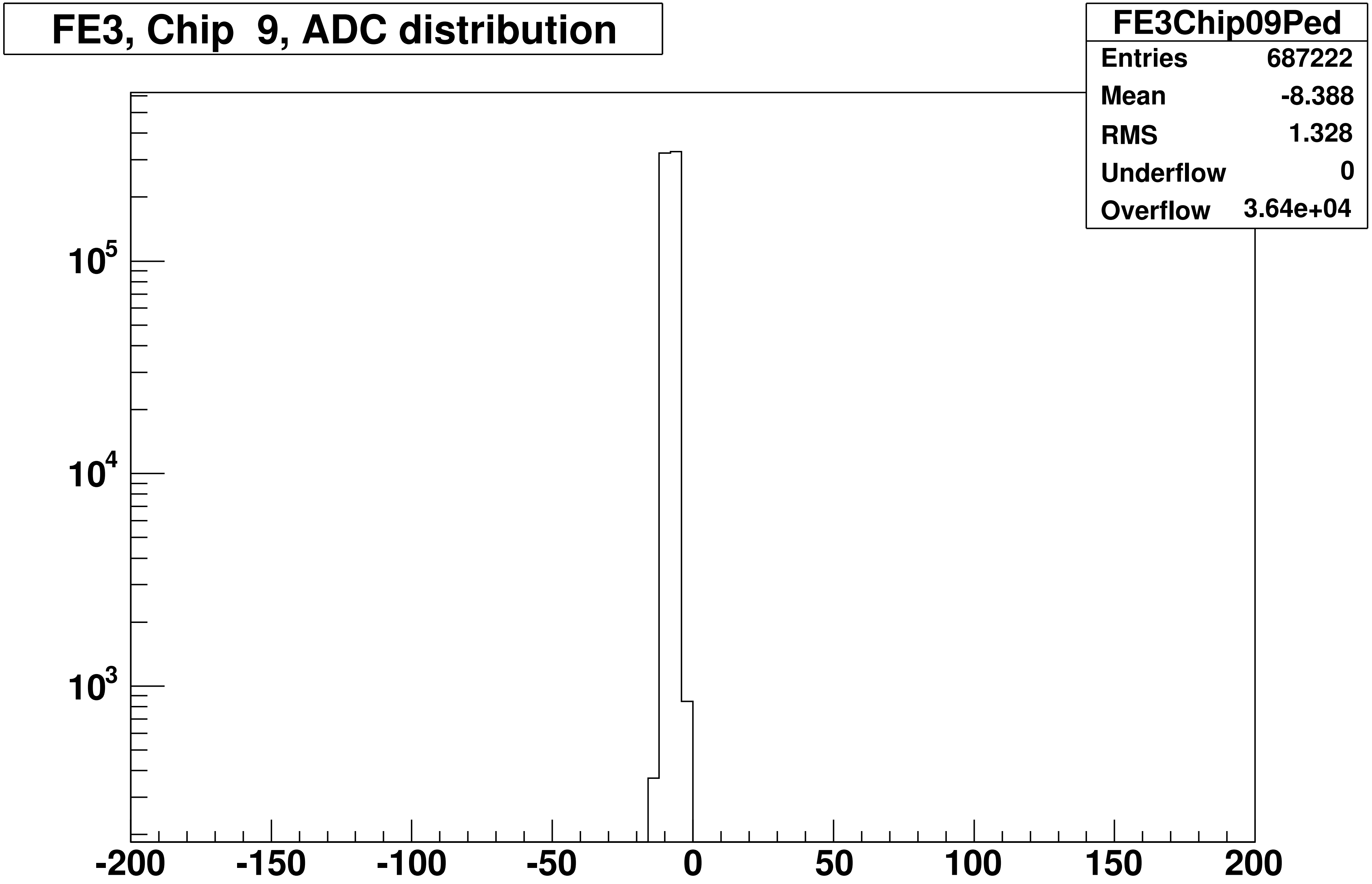
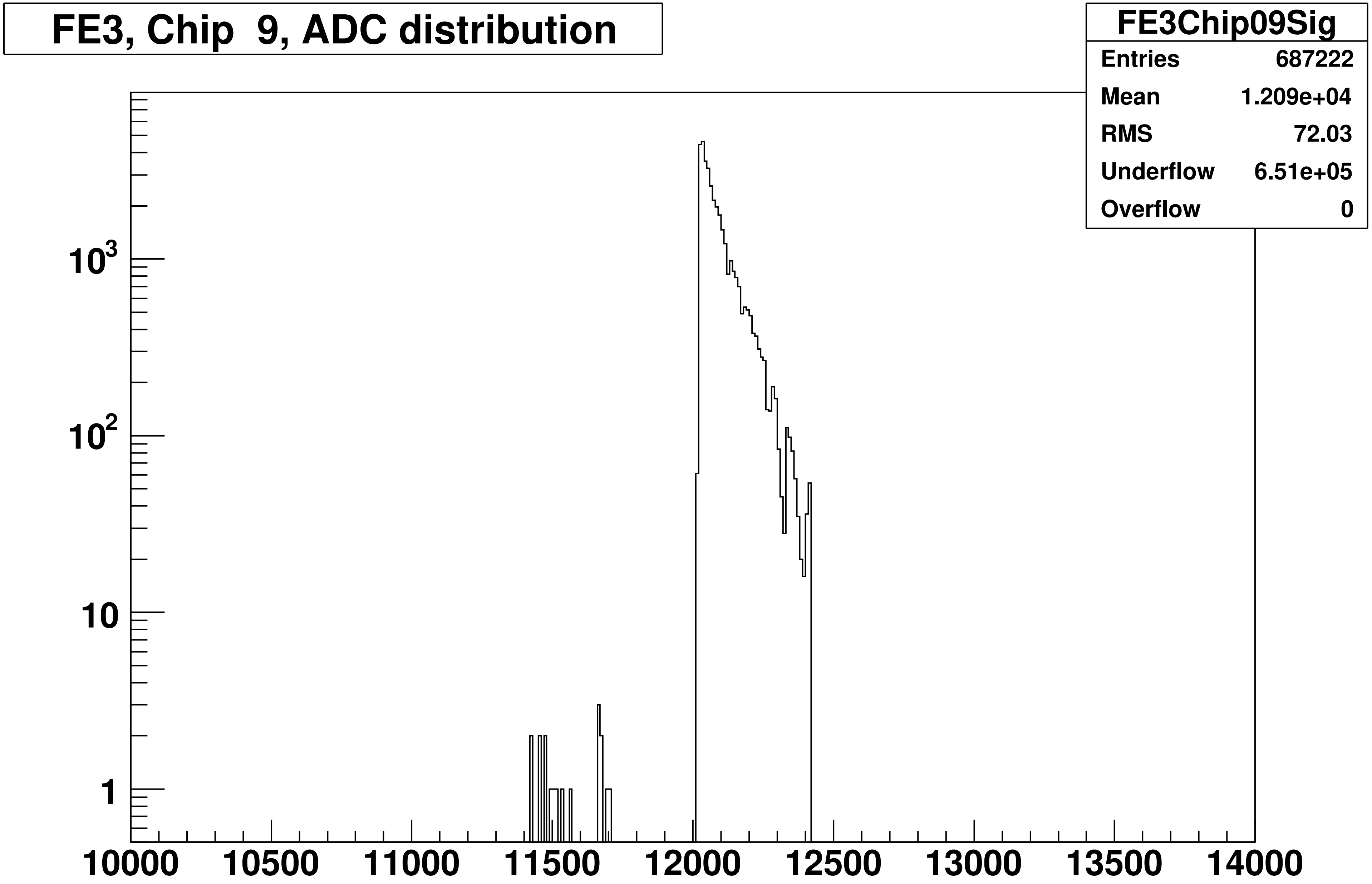
13000
13500
14000

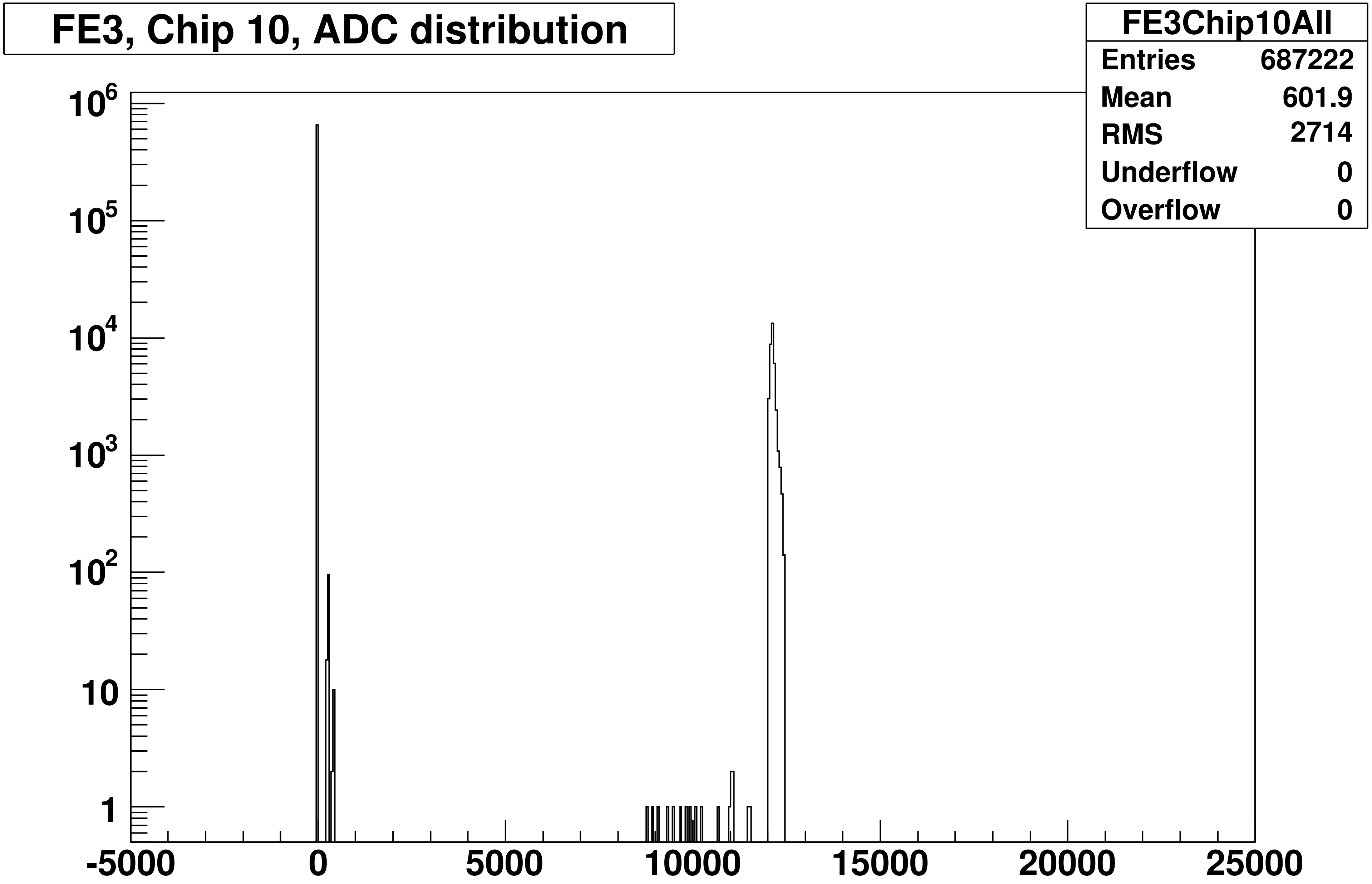


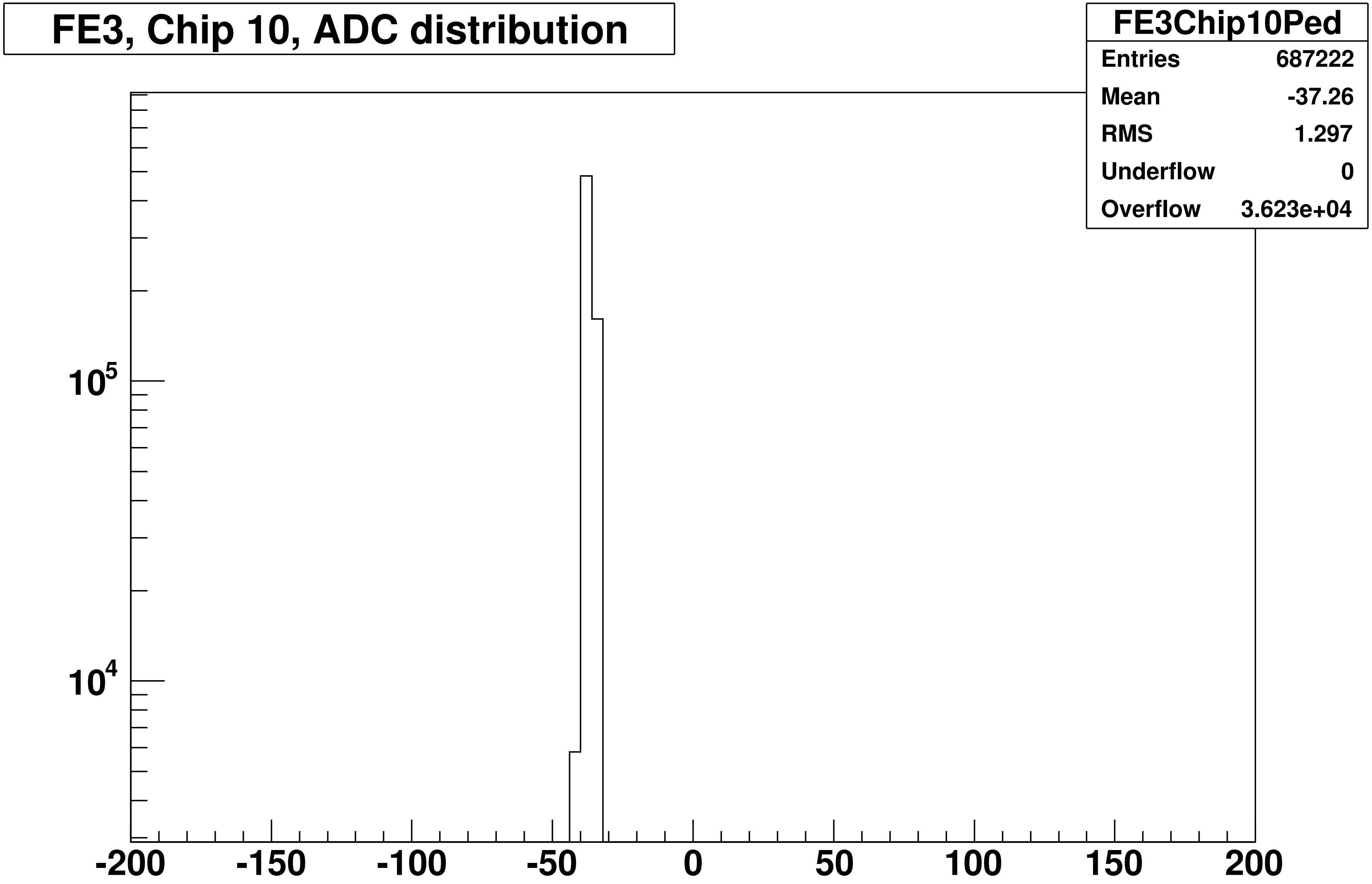
FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 687222
Mean 629.7
RMS 2704
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 687222
Mean -8.388
RMS 1.328
Underflow 0
Overflow 3.64e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200

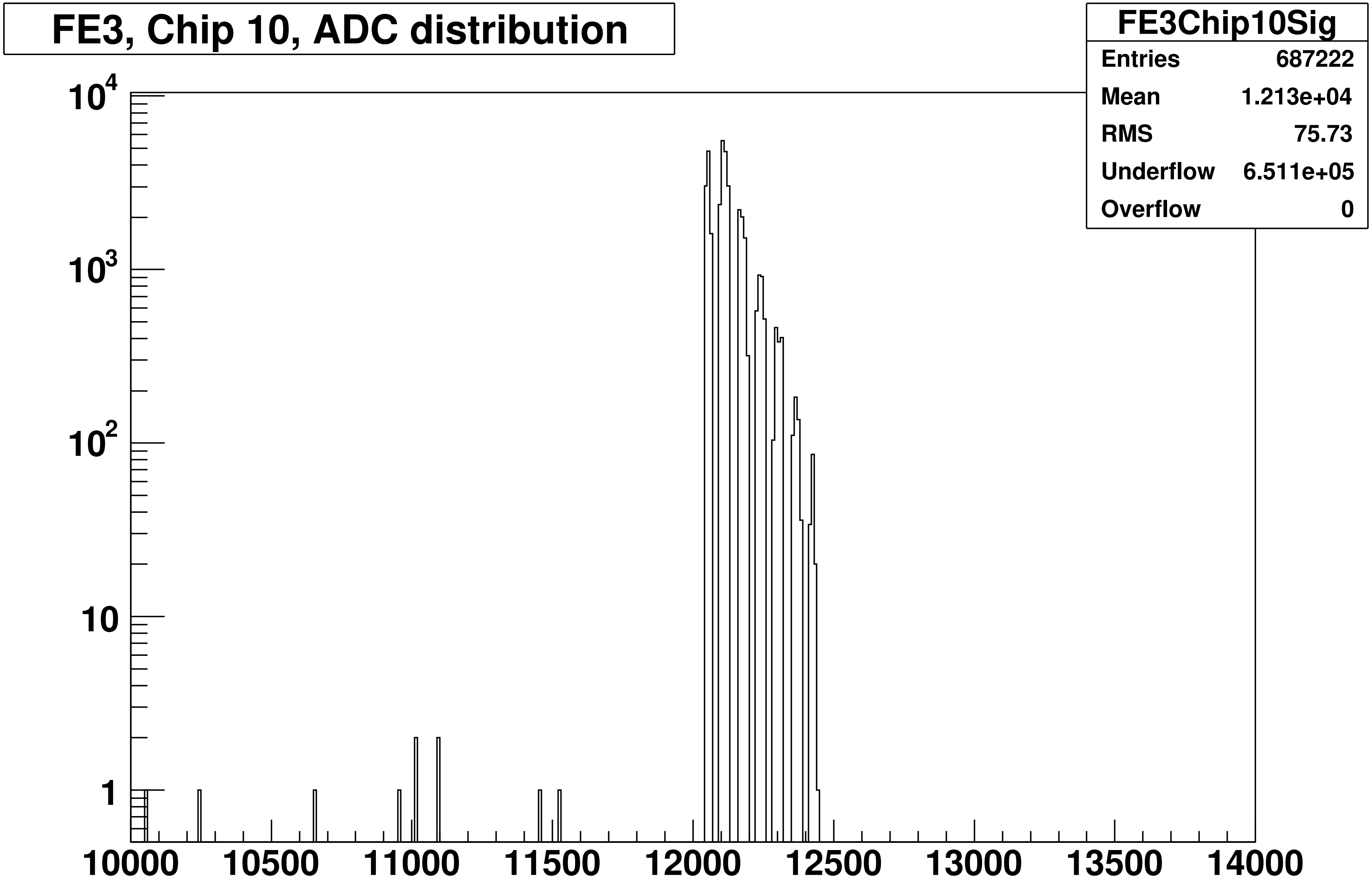


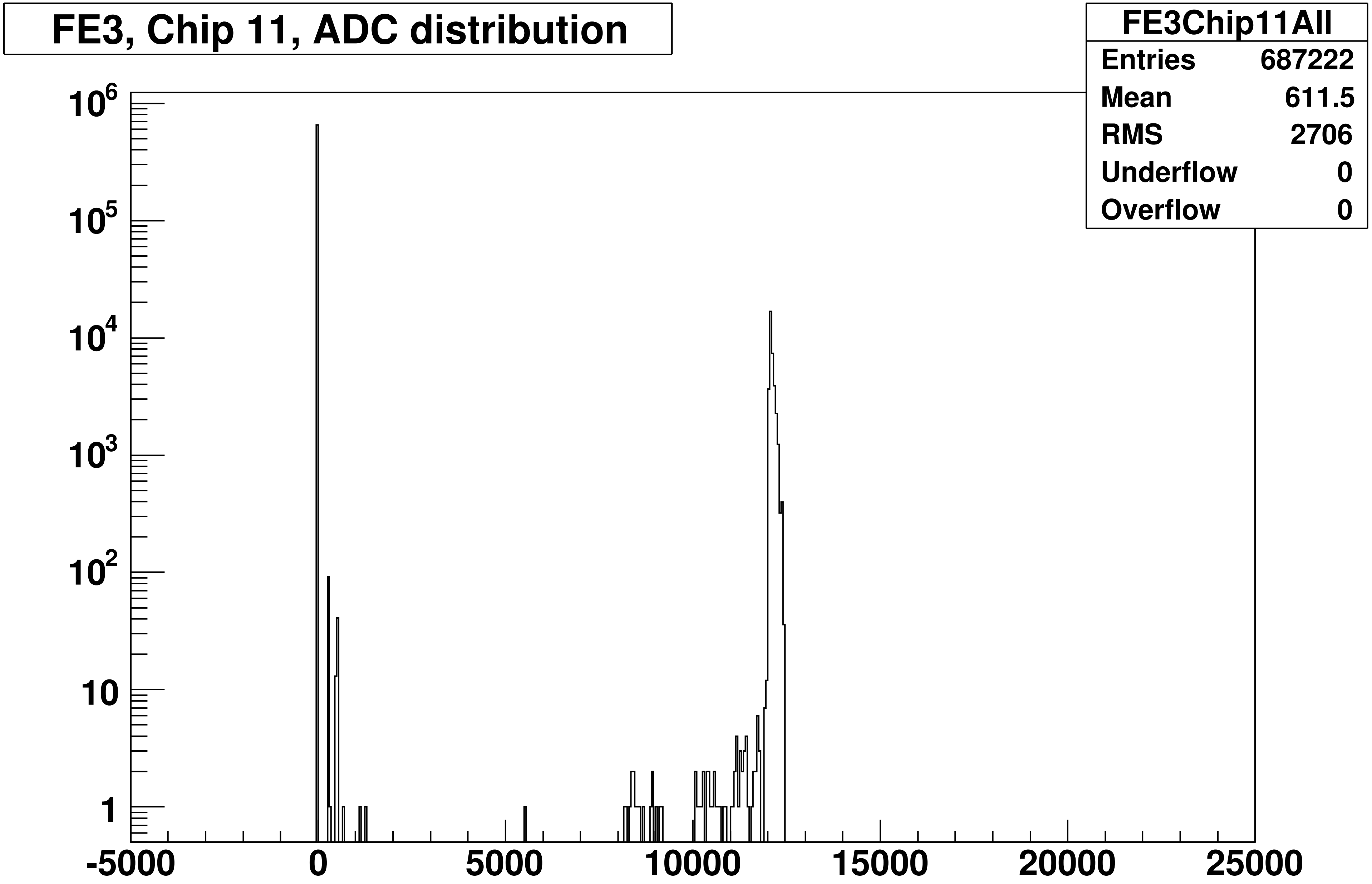
FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 687222
Mean 1.209e+04
RMS 72.03
Underflow 6.51e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

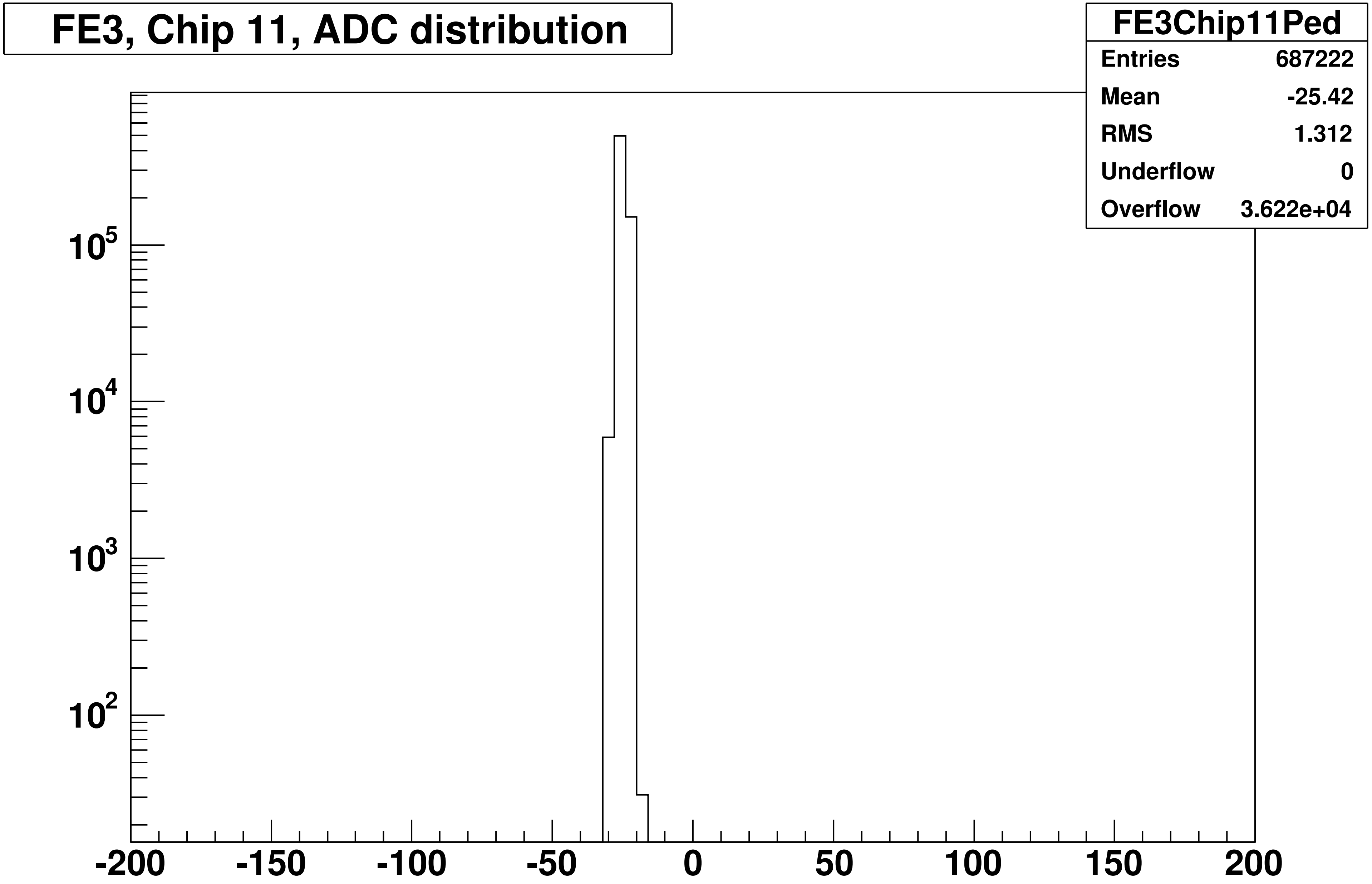

FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 687222
Mean 601.9
RMS 2714
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



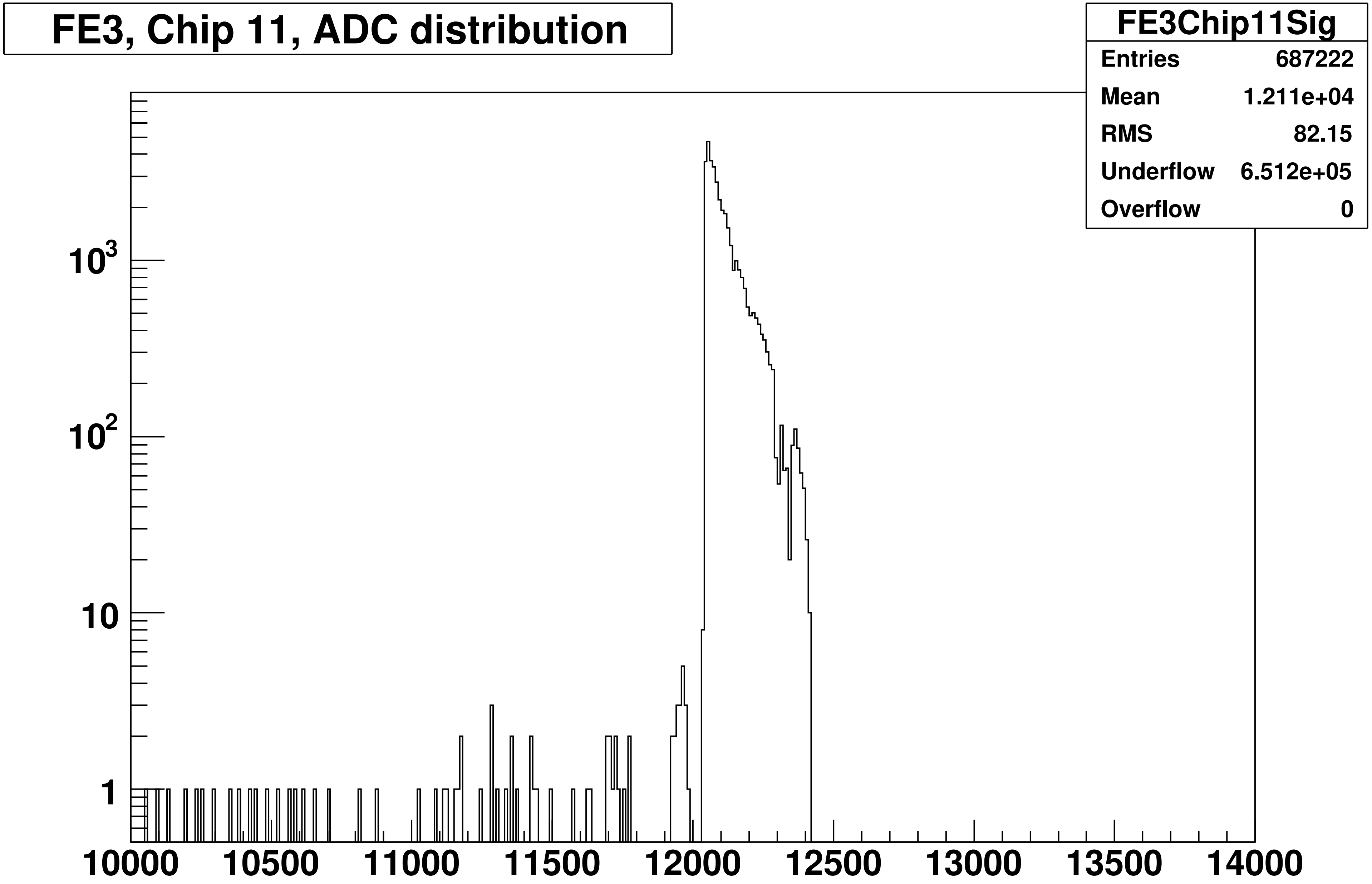
FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 687222
Mean -37.26
RMS 1.297
Underflow 0
Overflow 3.623e+04
10^5
10^4
-200
-150
-100
-50
0
50
100
150
200

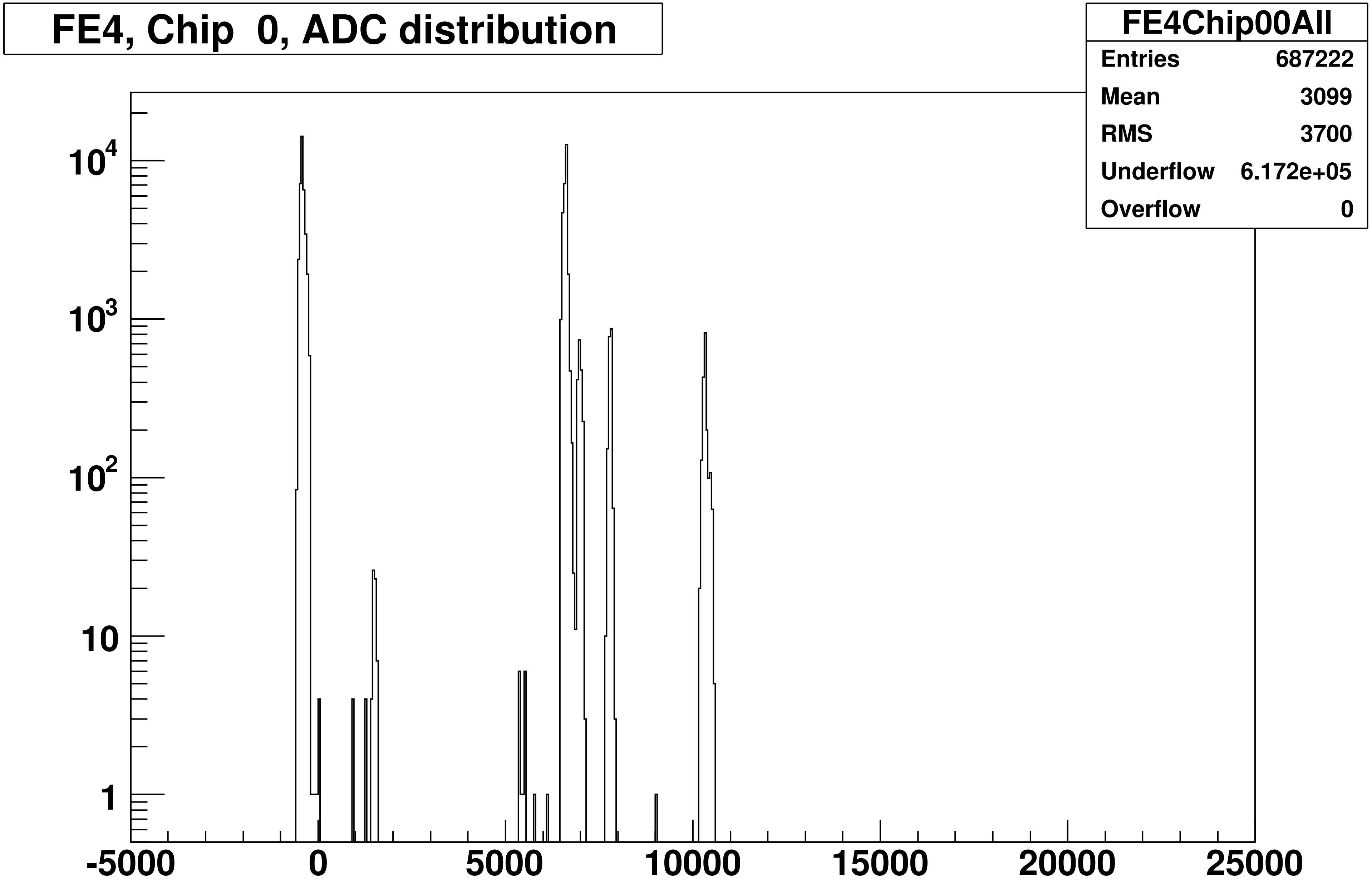

FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 687222
Mean 1.213e+04
RMS 75.73
Underflow 6.511e+05
Overflow 0
10^4
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

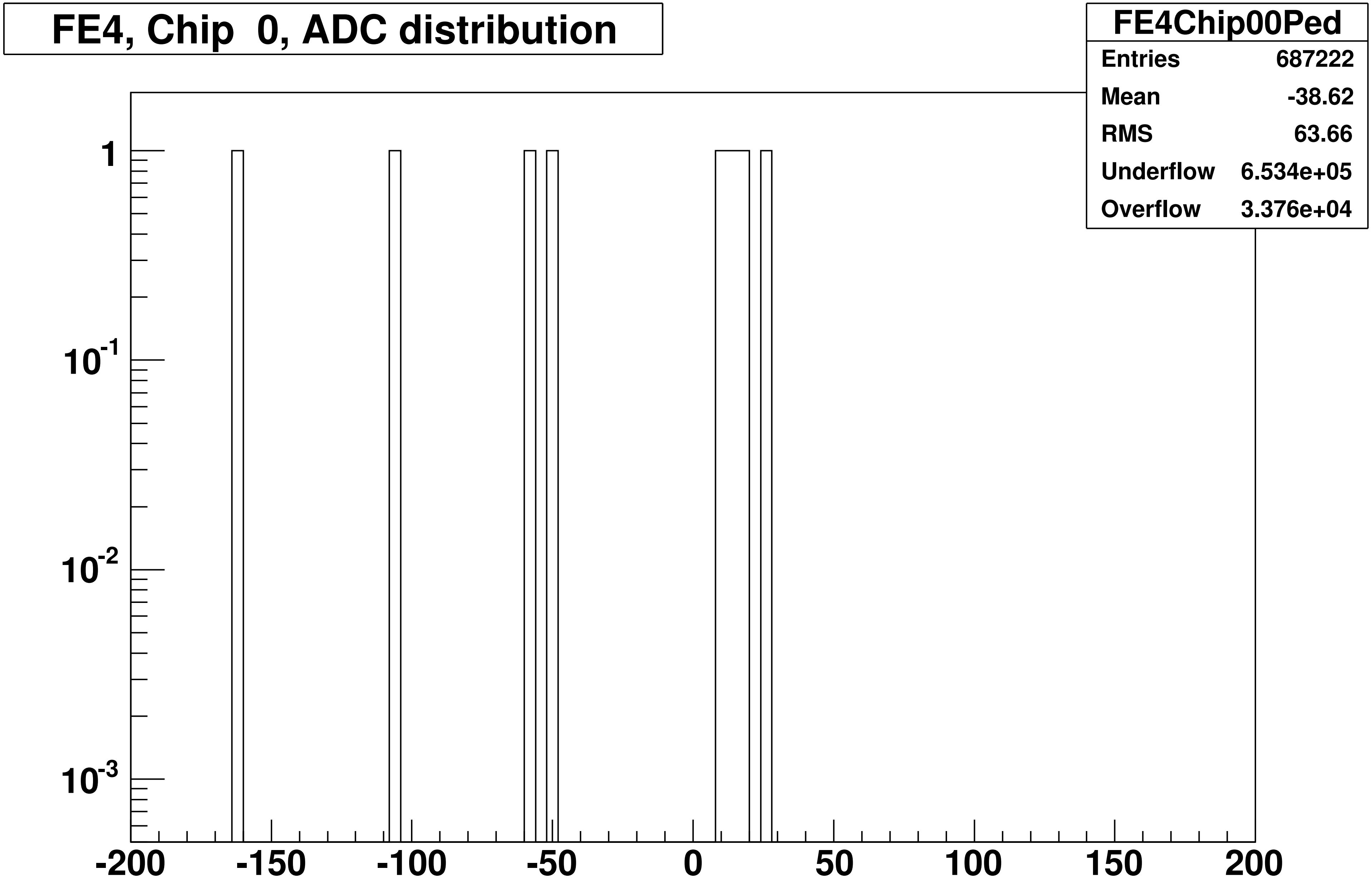

FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 687222
Mean 611.5
RMS 2706
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

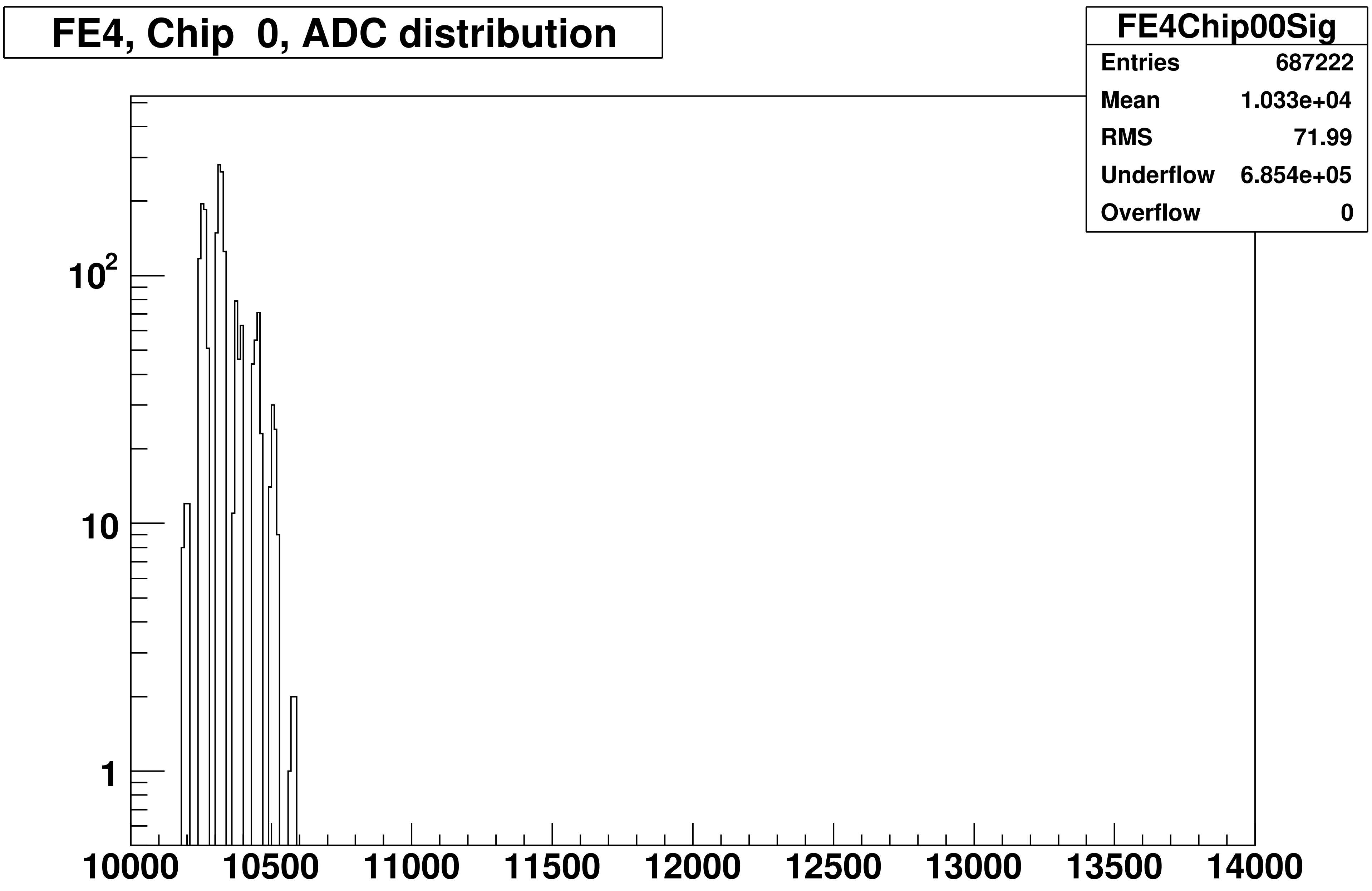


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 687222
Mean -25.42
RMS 1.312
Underflow 0
Overflow 3.622e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 687222
Mean 1.211e+04
RMS 82.15
Underflow 6.512e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 687222
Mean 3099
RMS 3700
Underflow 6.172e+05
Overflow 0
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 687222
Mean -38.62
RMS 63.66
Underflow 6.534e+05
Overflow 3.376e+04
1
10^-1
10^-2
10^-3
-200 -150 -100 -50 0 50 100 150 200


FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 687222
Mean 1.033e+04
RMS 71.99
Underflow 6.854e+05
Overflow 0
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10
1
10000
10500
11000
11500
12000
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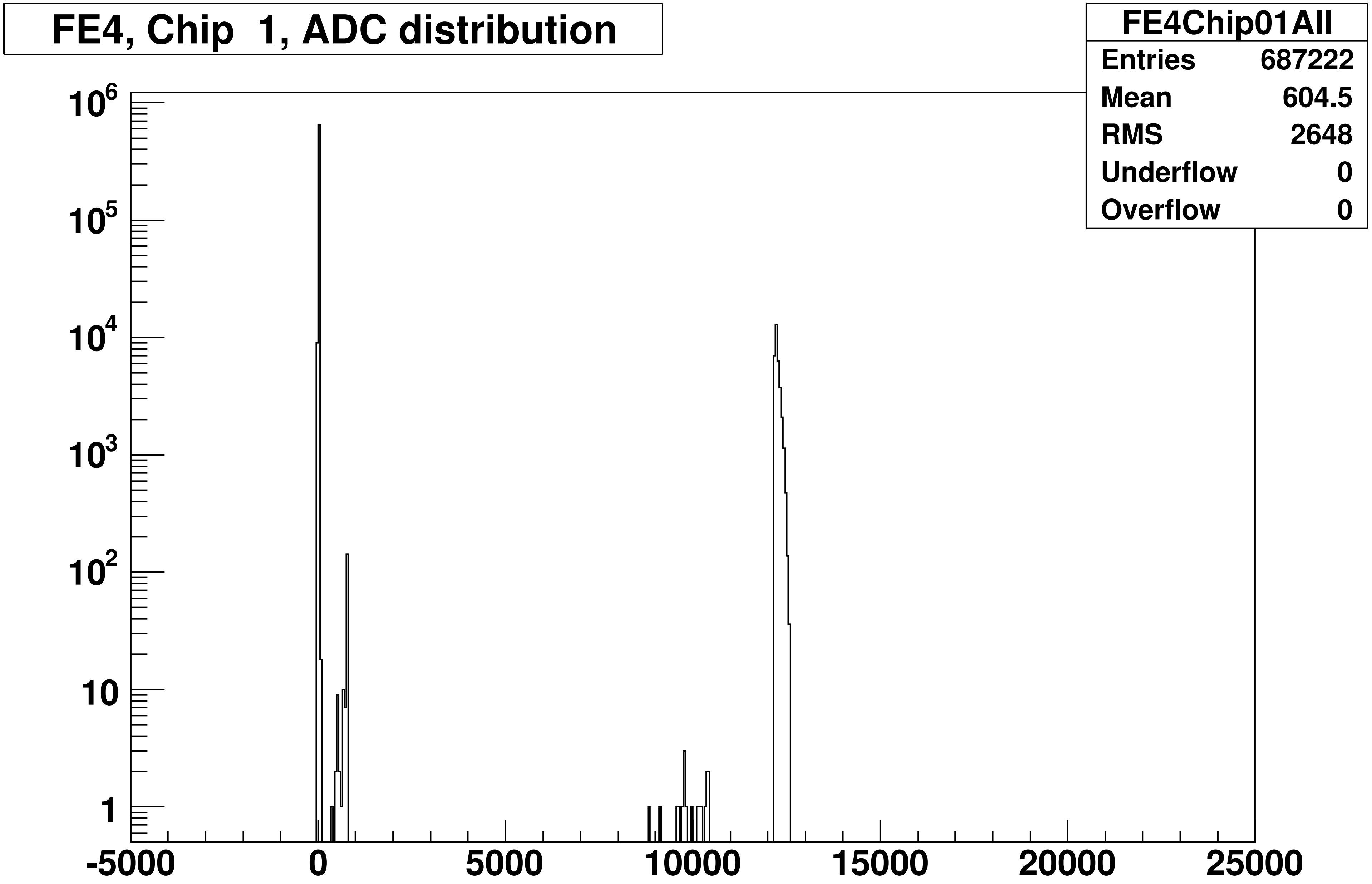
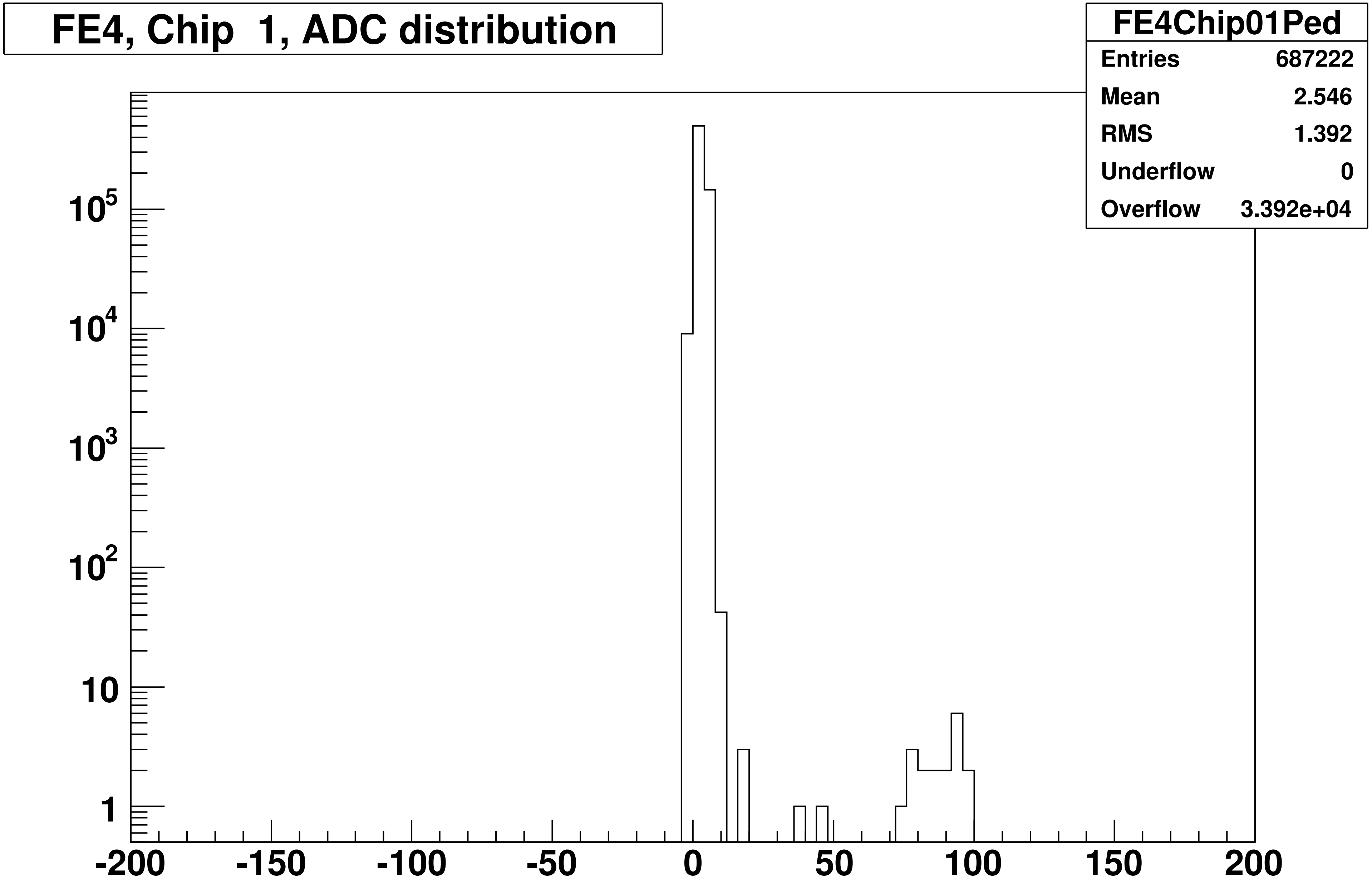
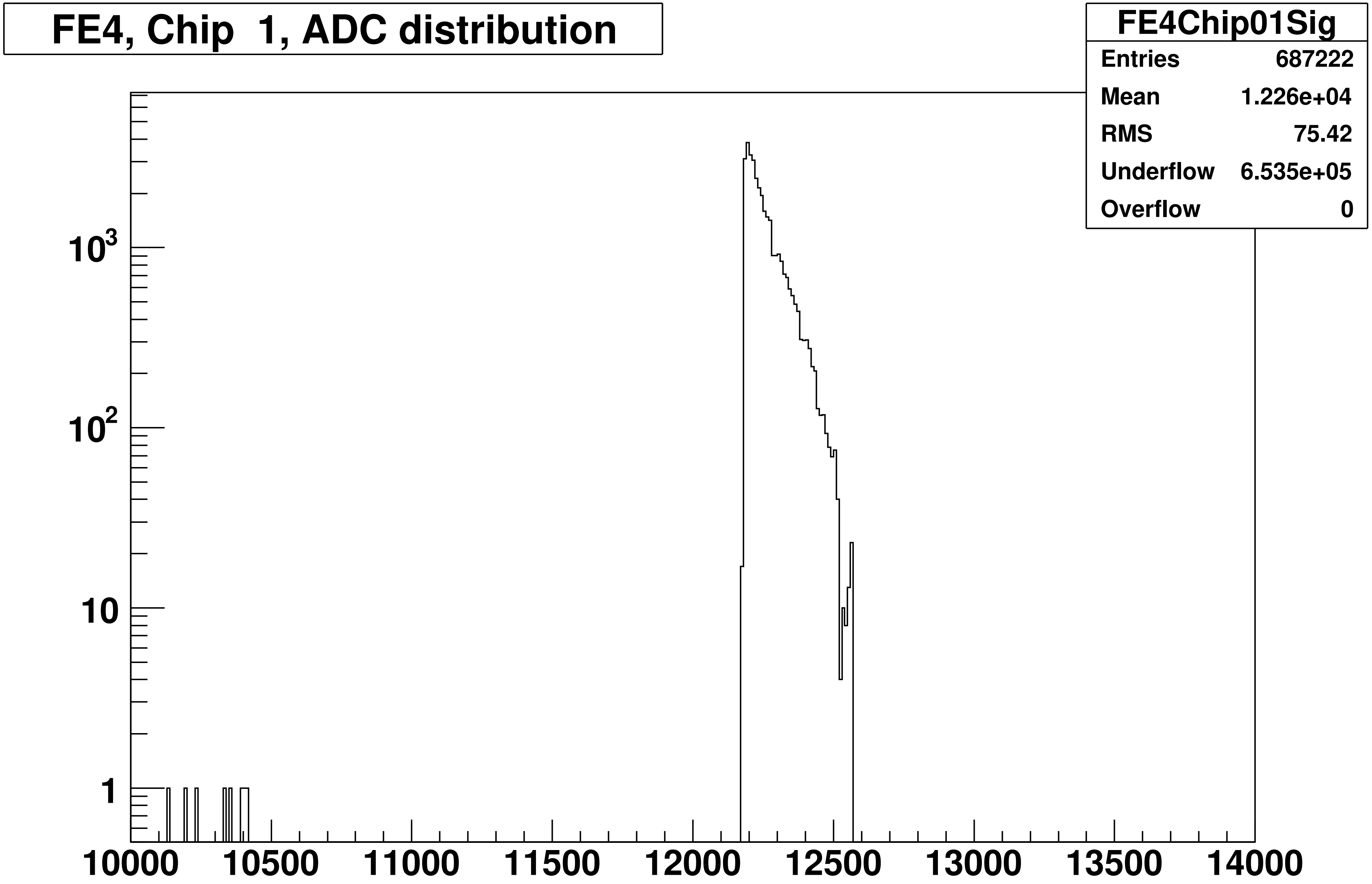

FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 687222
Mean 604.5
RMS 2648
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 687222
Mean 2.546
RMS 1.392
Underflow 0
Overflow 3.392e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 687222
Mean 1.226e+04
RMS 75.42
Underflow 6.535e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
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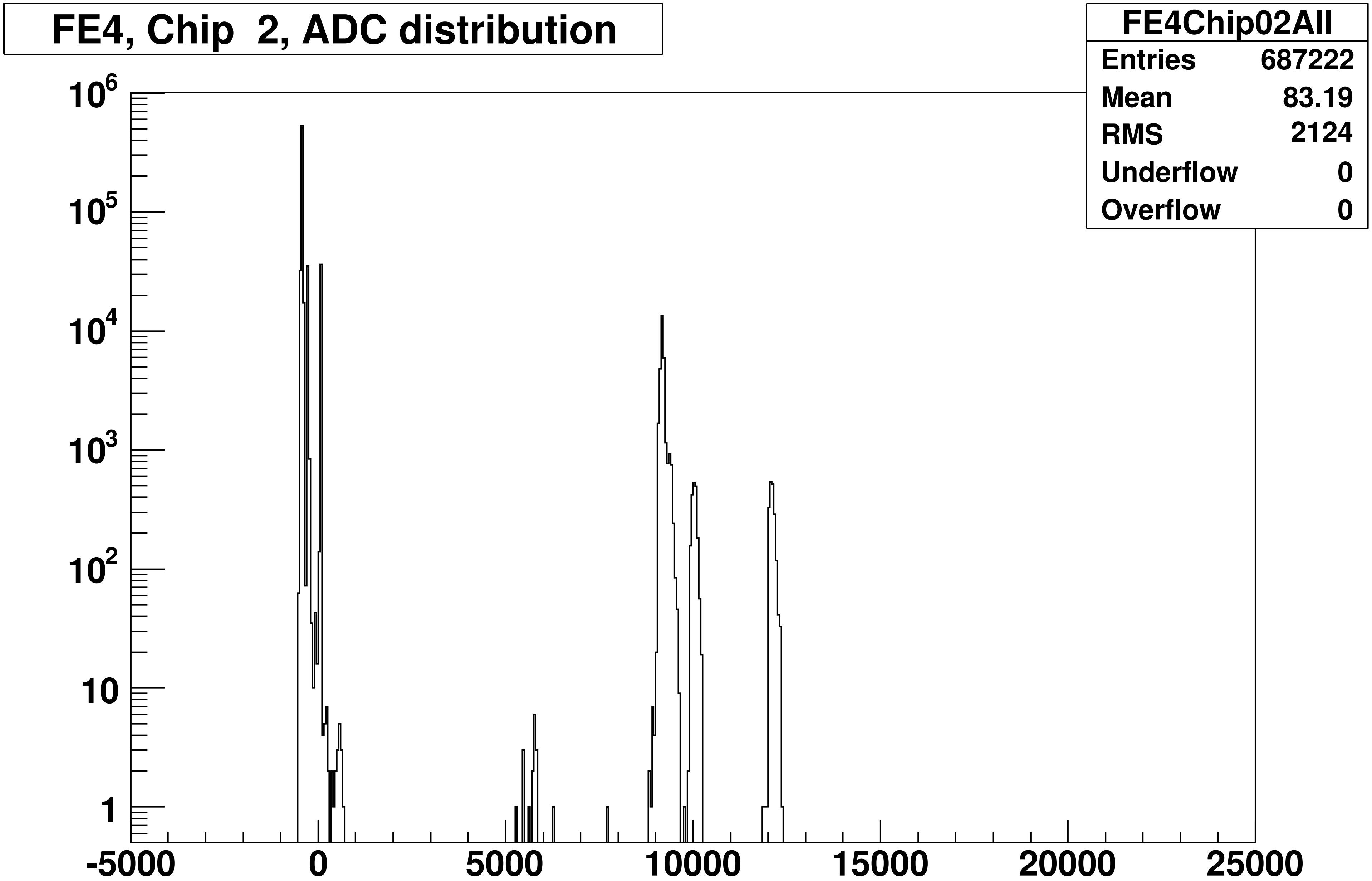
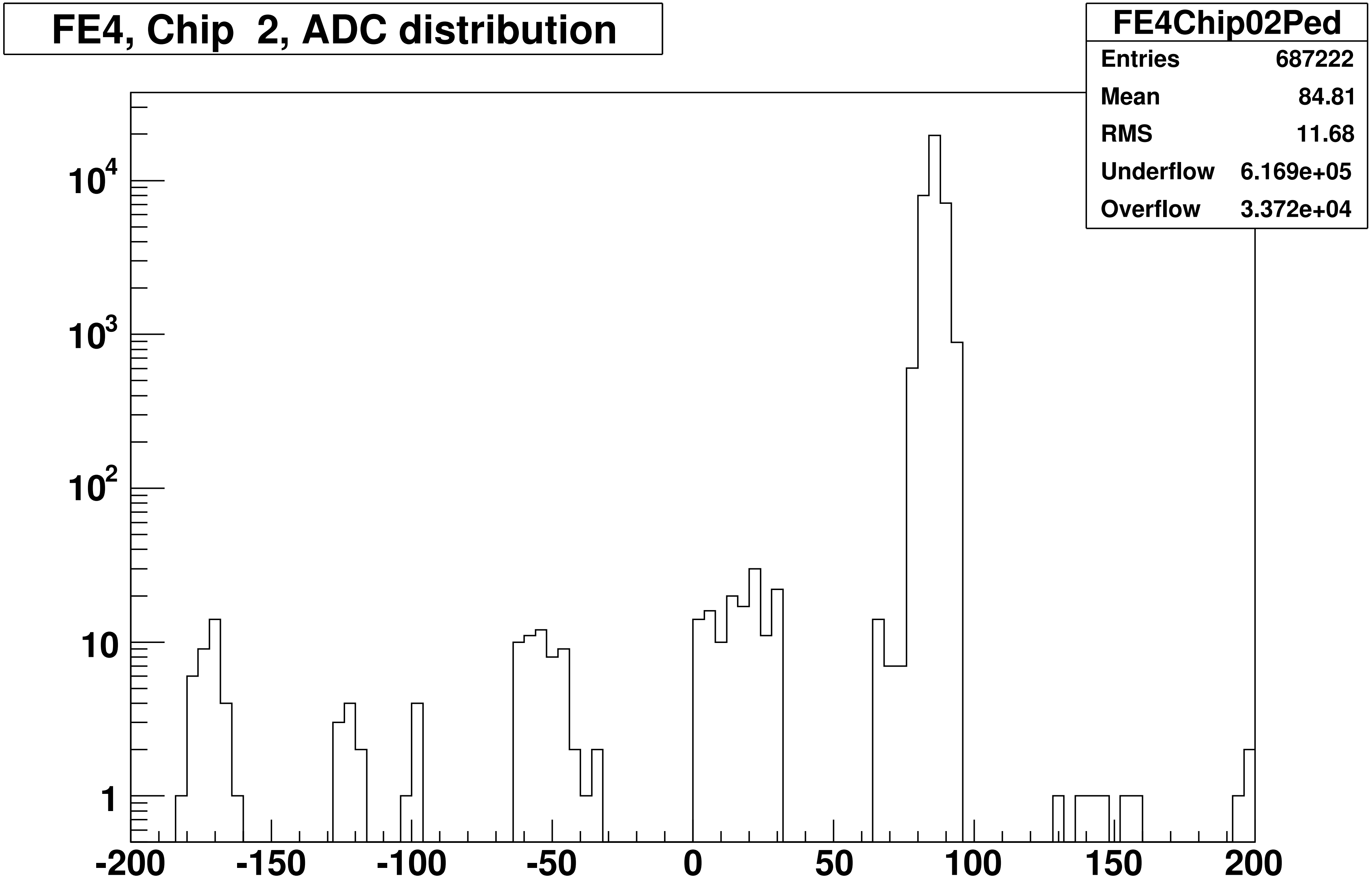
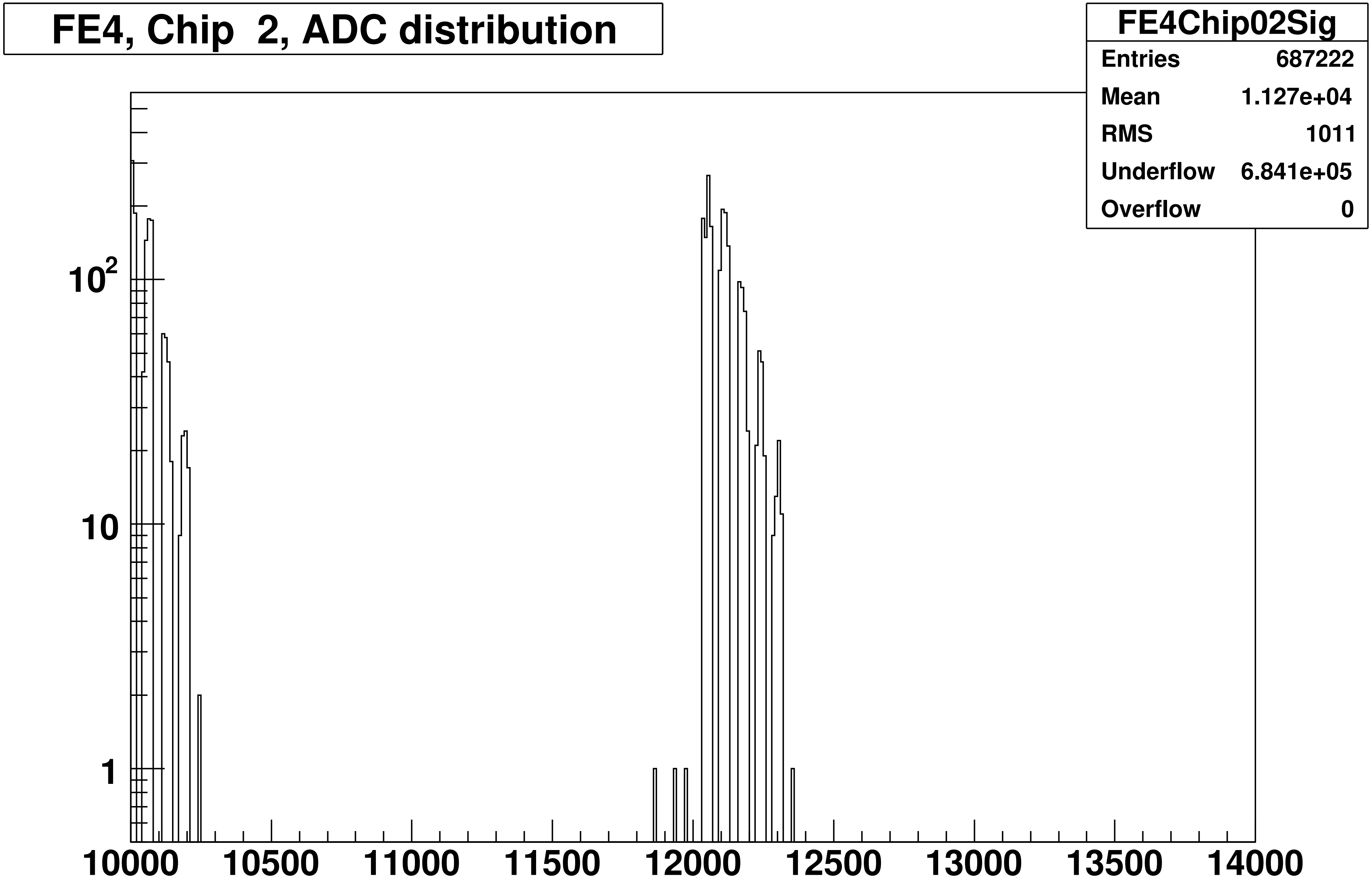
13000
13500
14000


FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 687222
Mean 83.19
RMS 2124
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 687222
Mean 84.81
RMS 11.68
Underflow 6.169e+05
Overflow 3.372e+04
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 687222
Mean 1.127e+04
RMS 1011
Underflow 6.841e+05
Overflow 0
10^2
10
1
10000
10500
11000
11500
12000
12500
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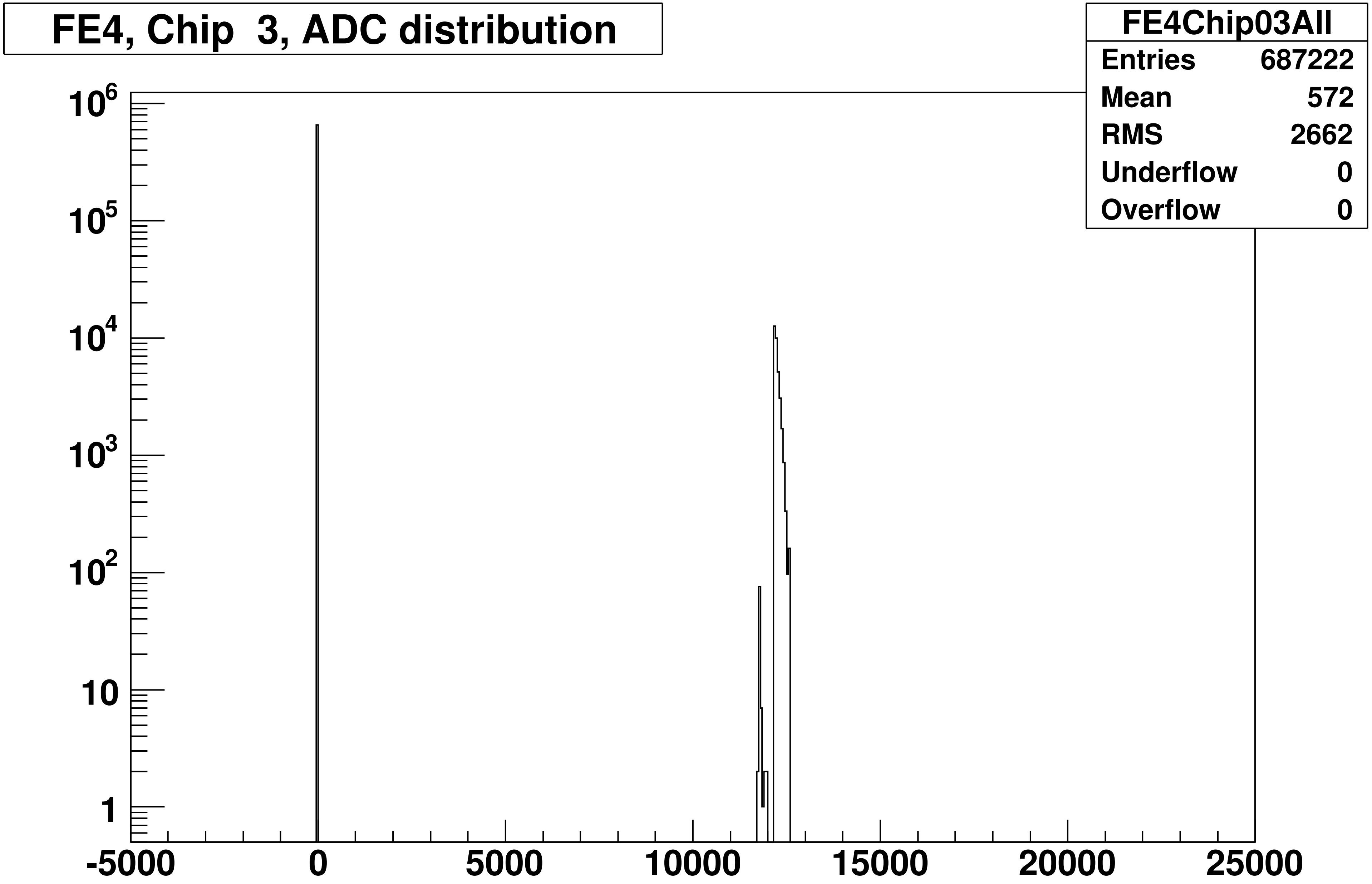
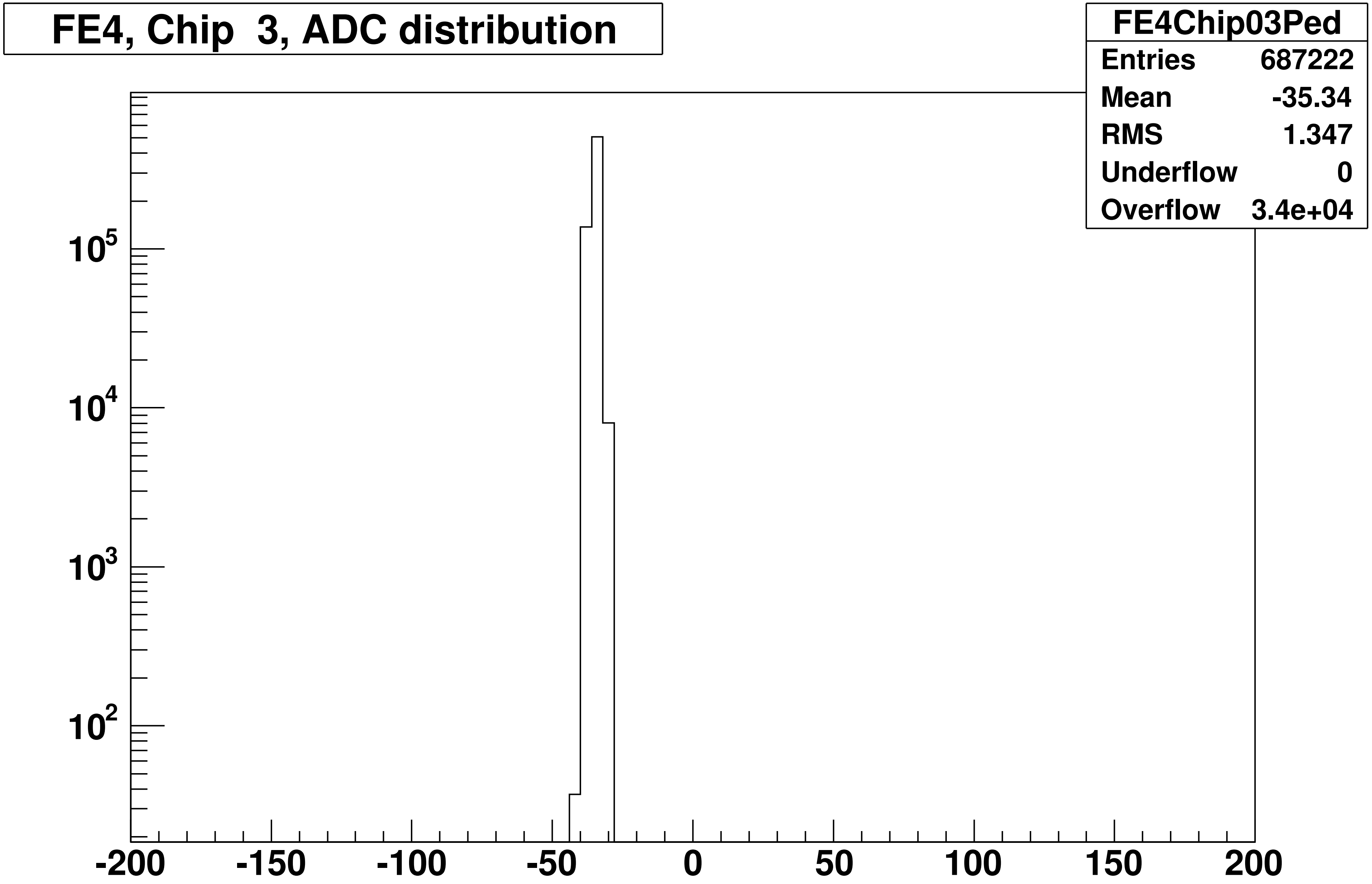
14000



FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 687222
Mean 572
RMS 2662
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 687222
Mean -35.34
RMS 1.347
Underflow 0
Overflow 3.4e+04
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10^4
10^3
10^2
-200
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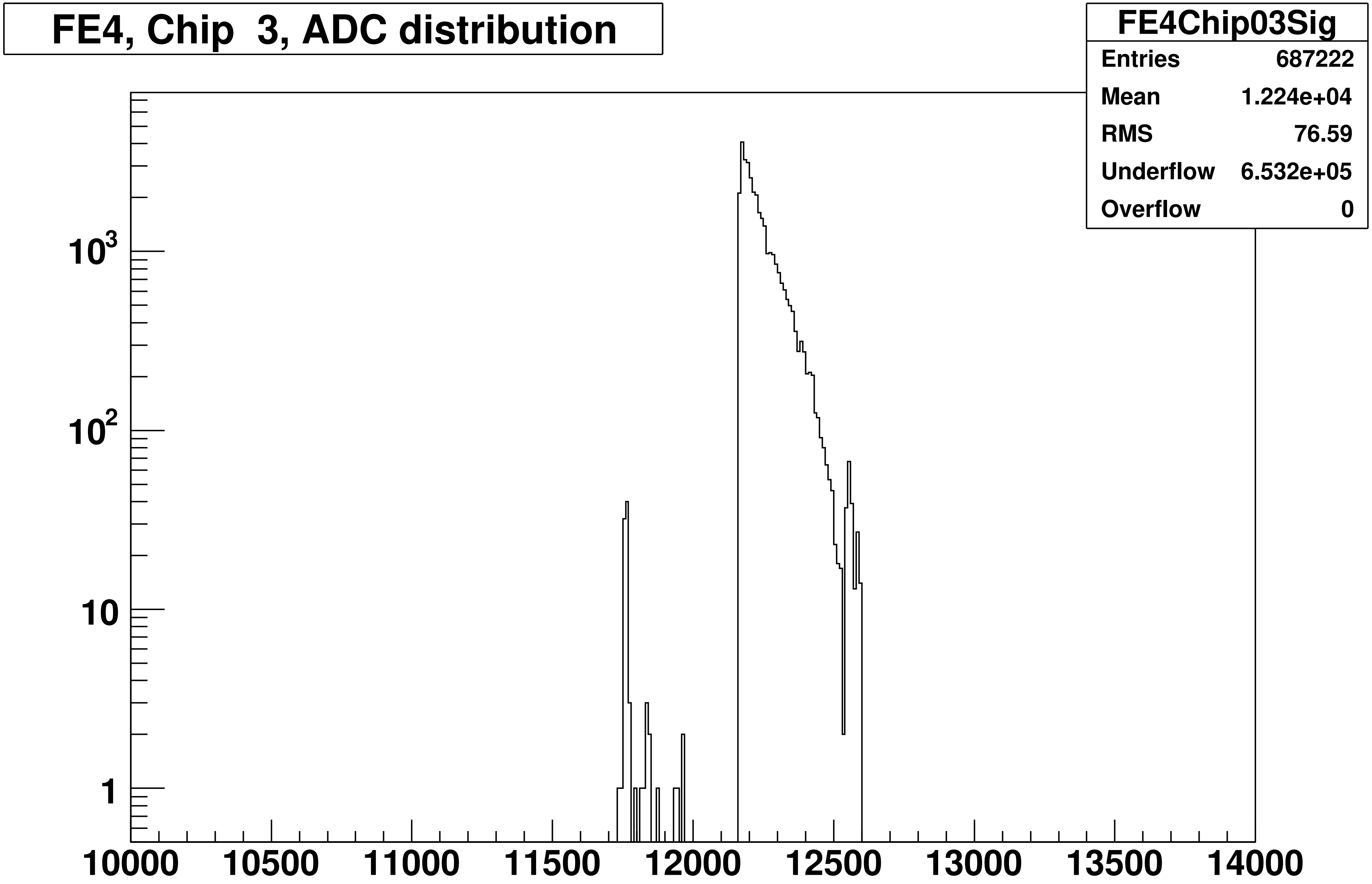
-100
-50
0
50
100
150
200



FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 687222
Mean 1.224e+04
RMS 76.59
Underflow 6.532e+05
Overflow 0
10^3
10^2
10
1
10000
10500
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11500
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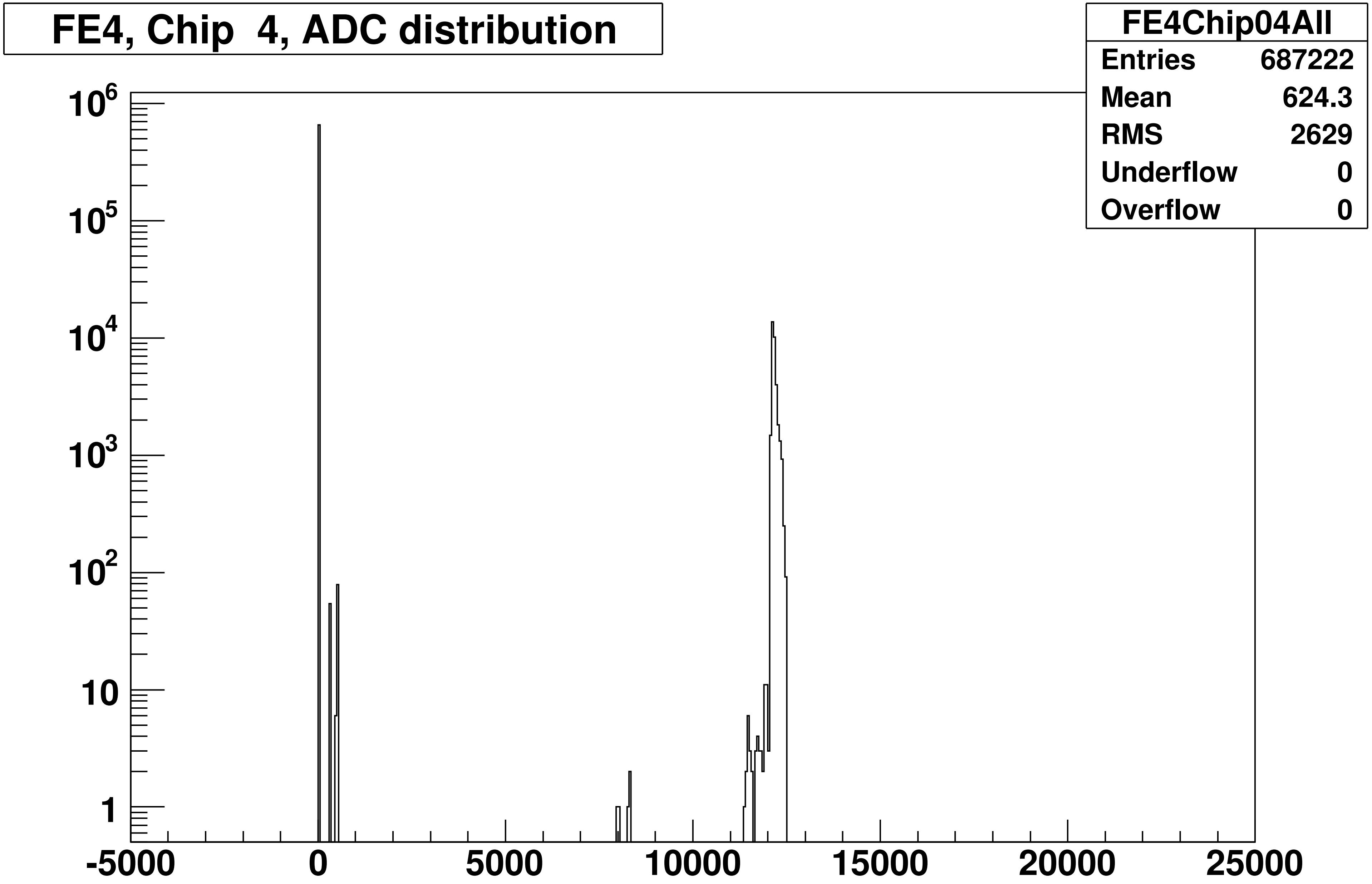
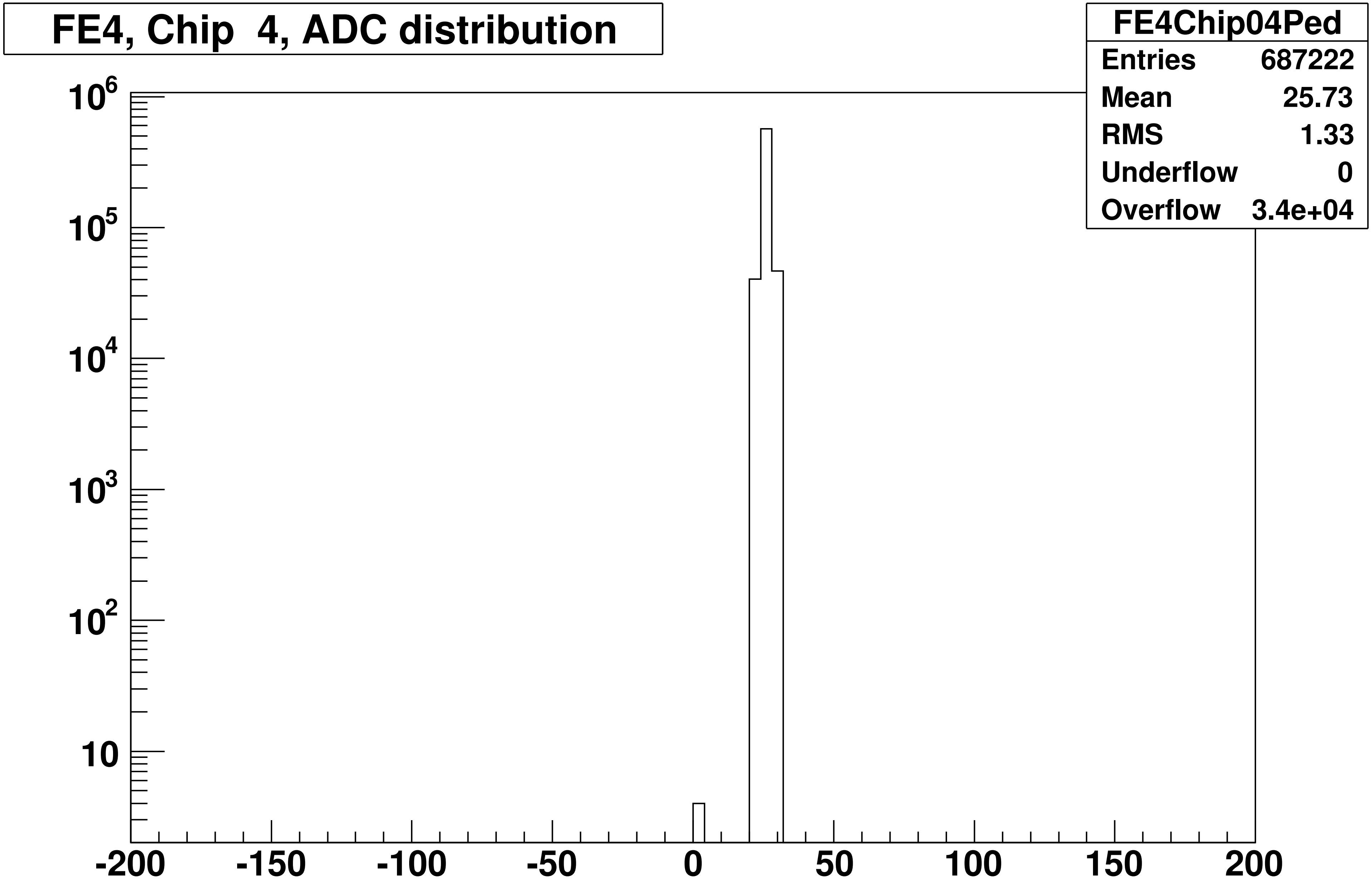
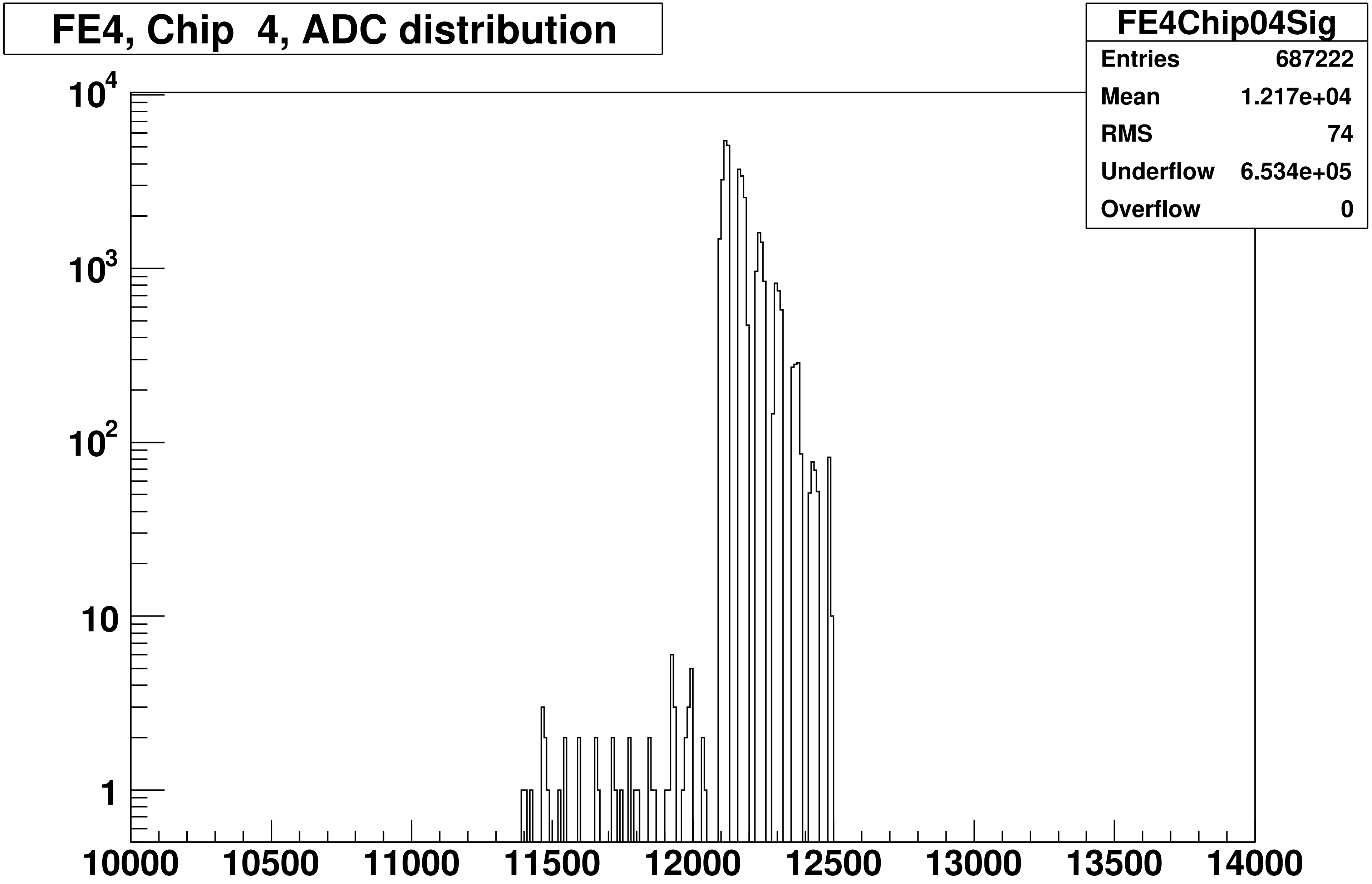

FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 687222
Mean 624.3
RMS 2629
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 687222
Mean 25.73
RMS 1.33
Underflow 0
Overflow 3.4e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 687222
Mean 1.217e+04
RMS 74
Underflow 6.534e+05
Overflow 0
10^4
10^3
10^2
10
1
10000
10500
11000
11500
12000
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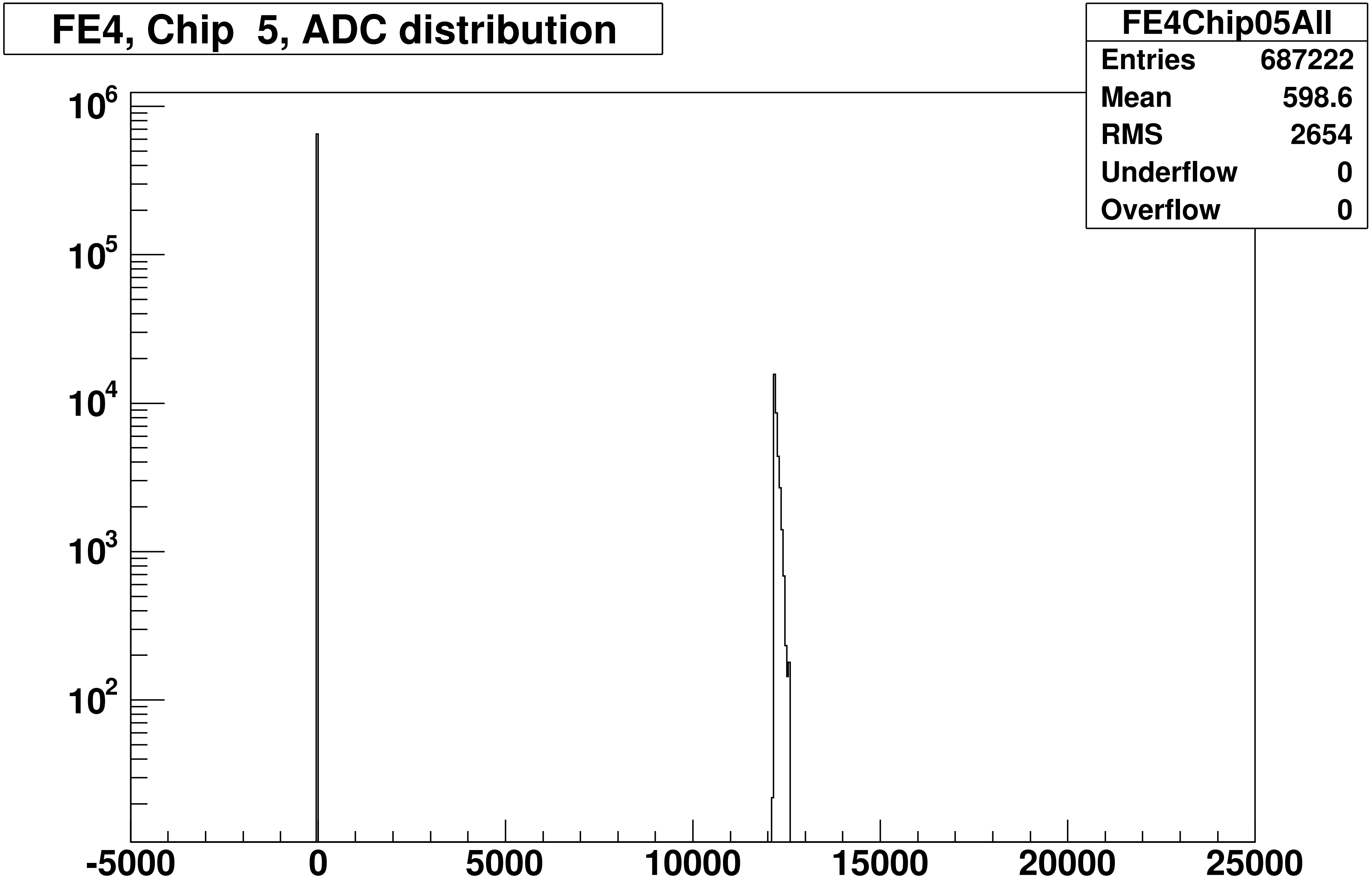
14000


FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 687222
Mean 598.6
RMS 2654
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
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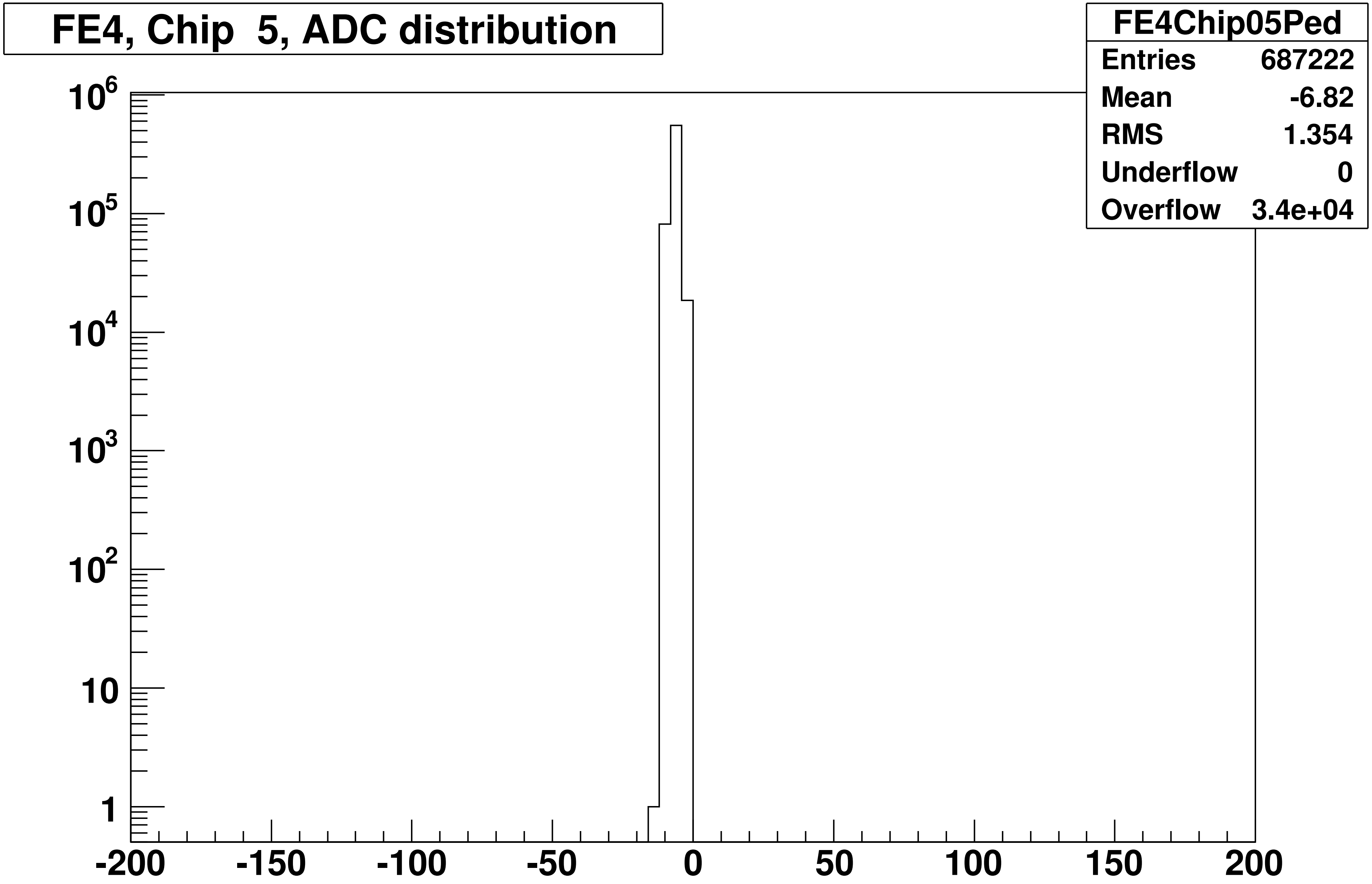
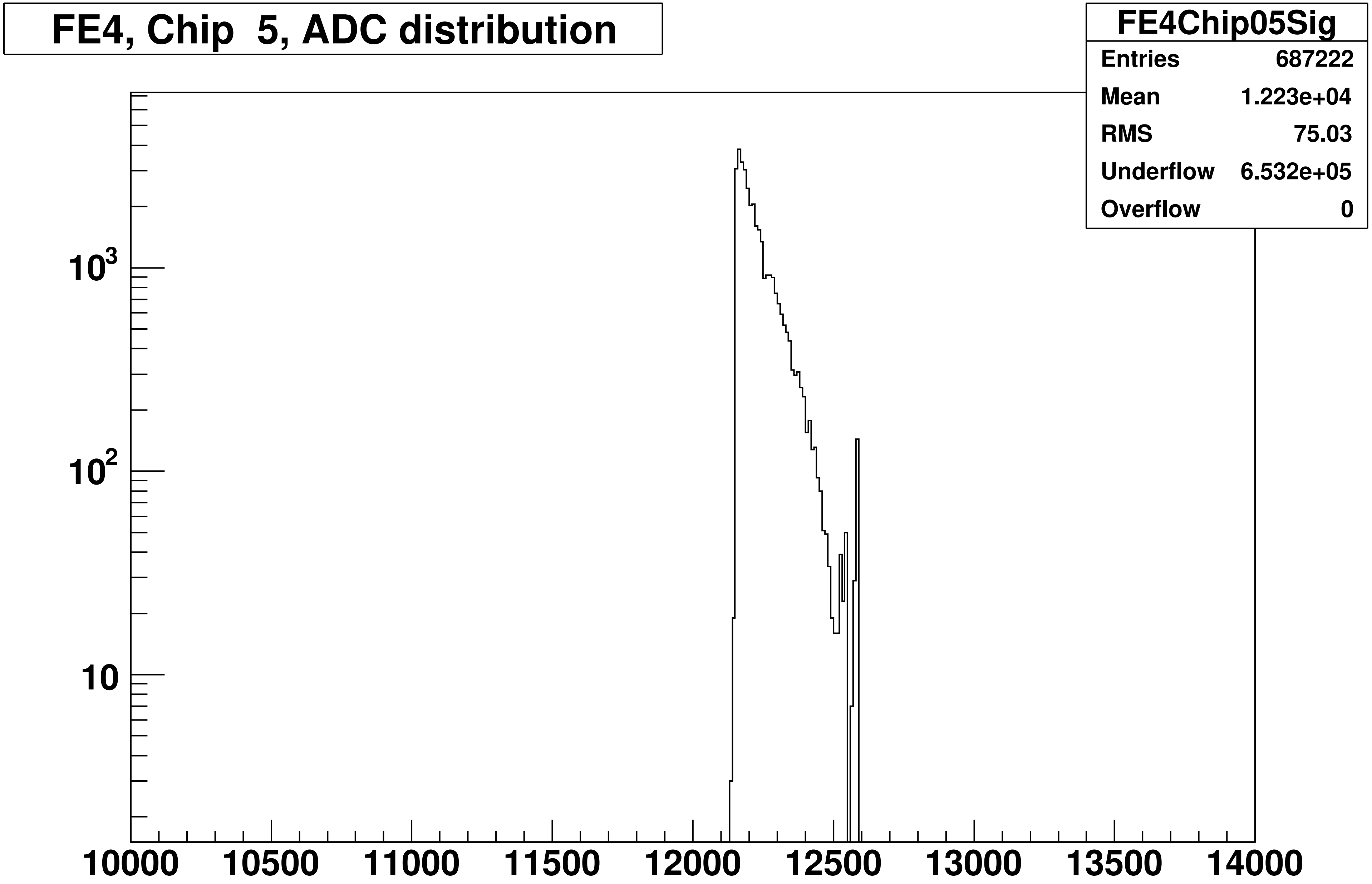
0
5000
10000
15000
20000
25000


FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 687222
Mean -6.82
RMS 1.354
Underflow 0
Overflow 3.4e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 687222
Mean 1.223e+04
RMS 75.03
Underflow 6.532e+05
Overflow 0
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10^2
10
10000
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11500
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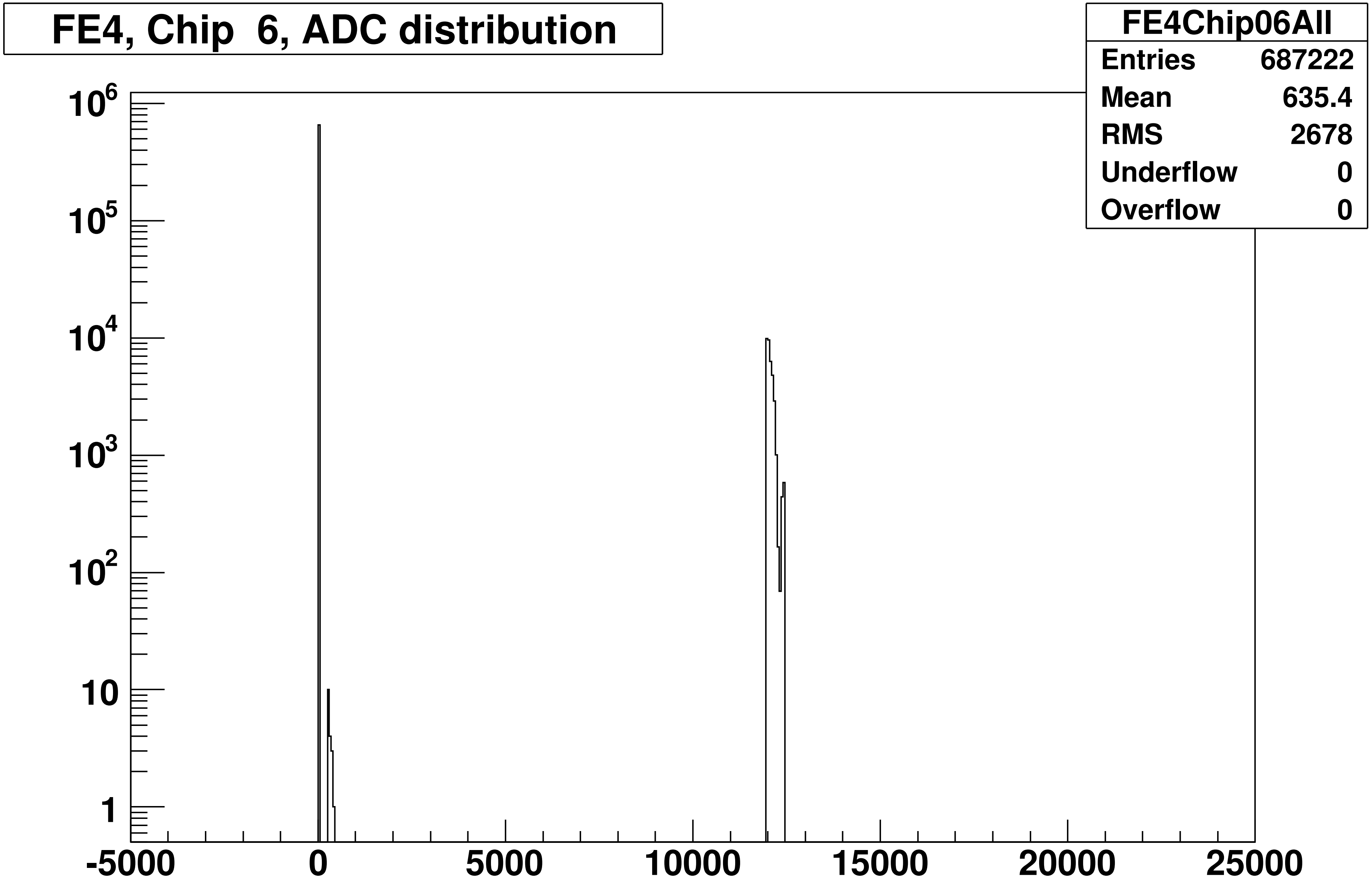
FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 687222
Mean 635.4
RMS 2678
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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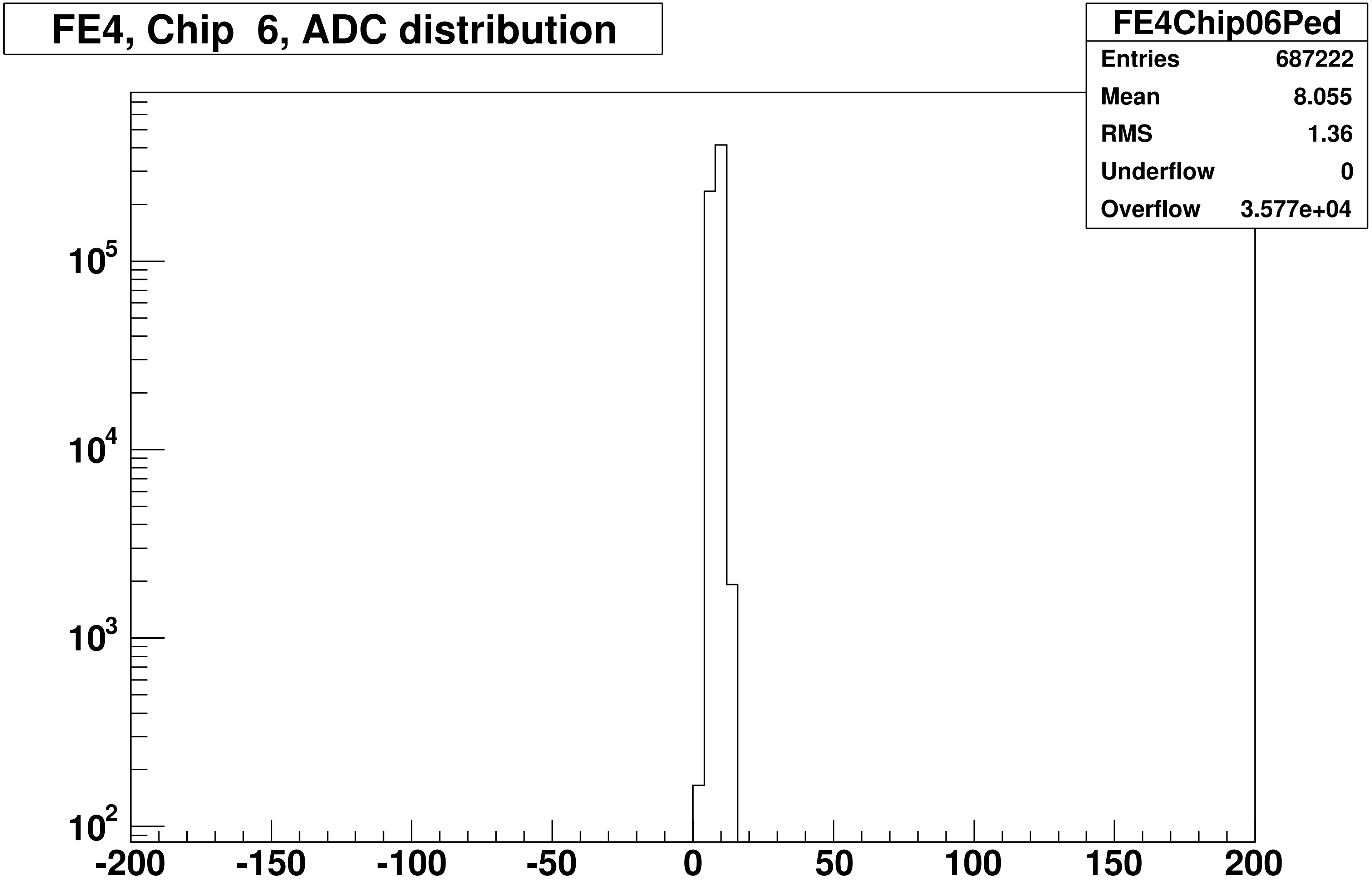
FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 687222
Mean 8.055
RMS 1.36
Underflow 0
Overflow 3.577e+04
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10^4
10^3
10^2
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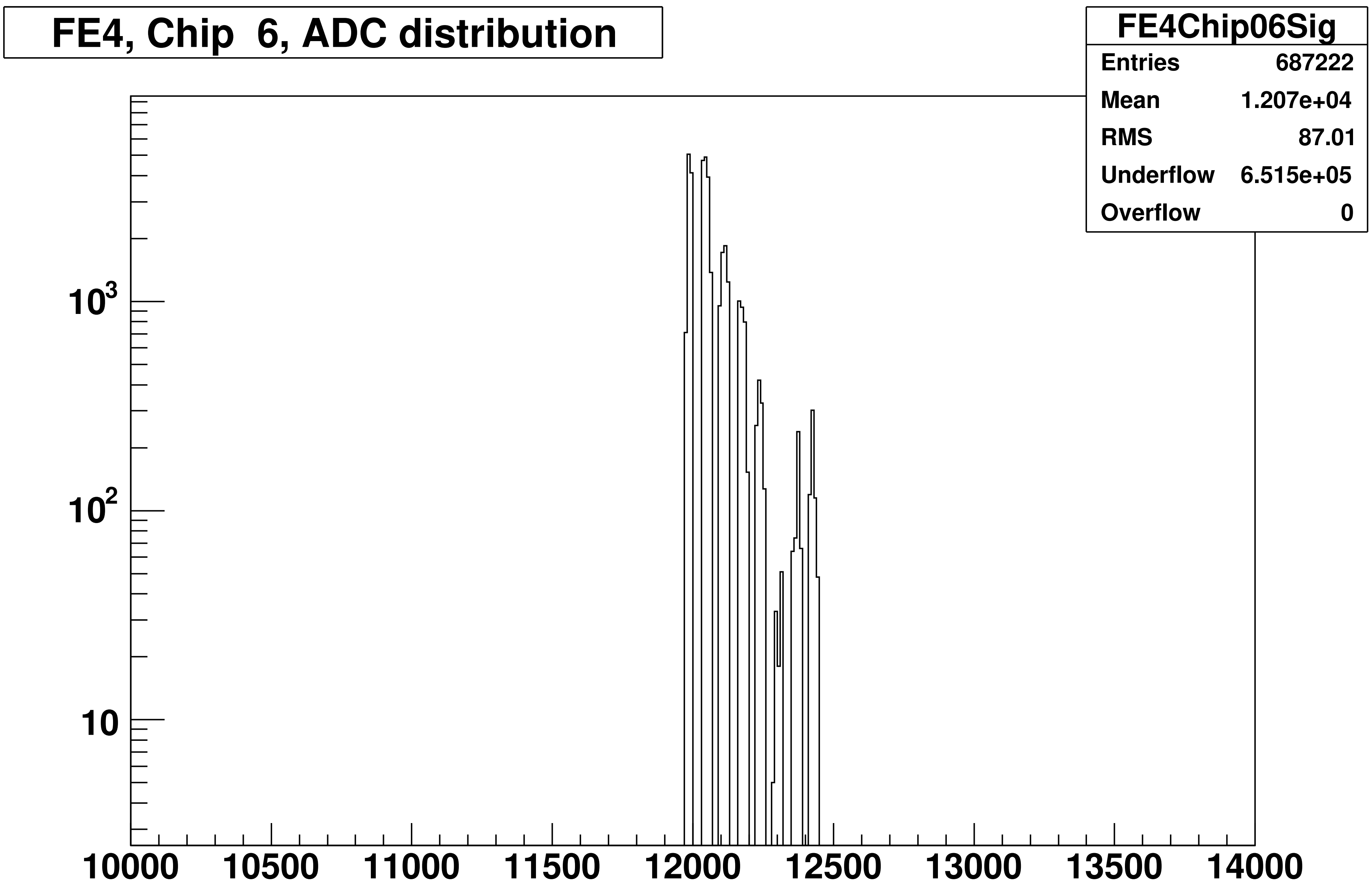
FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 687222
Mean 1.207e+04
RMS 87.01
Underflow 6.515e+05
Overflow 0
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10^2
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10000
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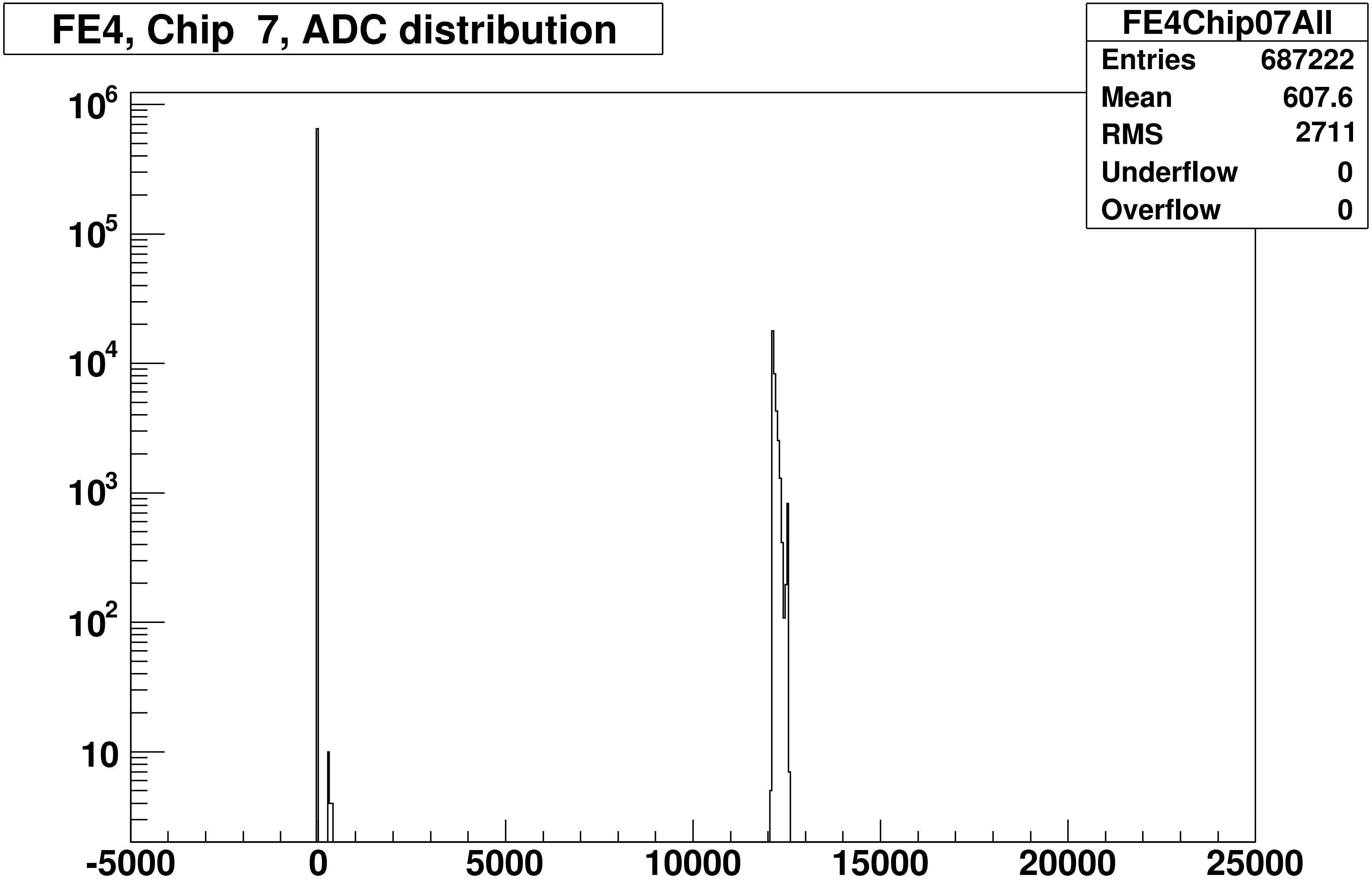
FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 687222
Mean 607.6
RMS 2711
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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10000
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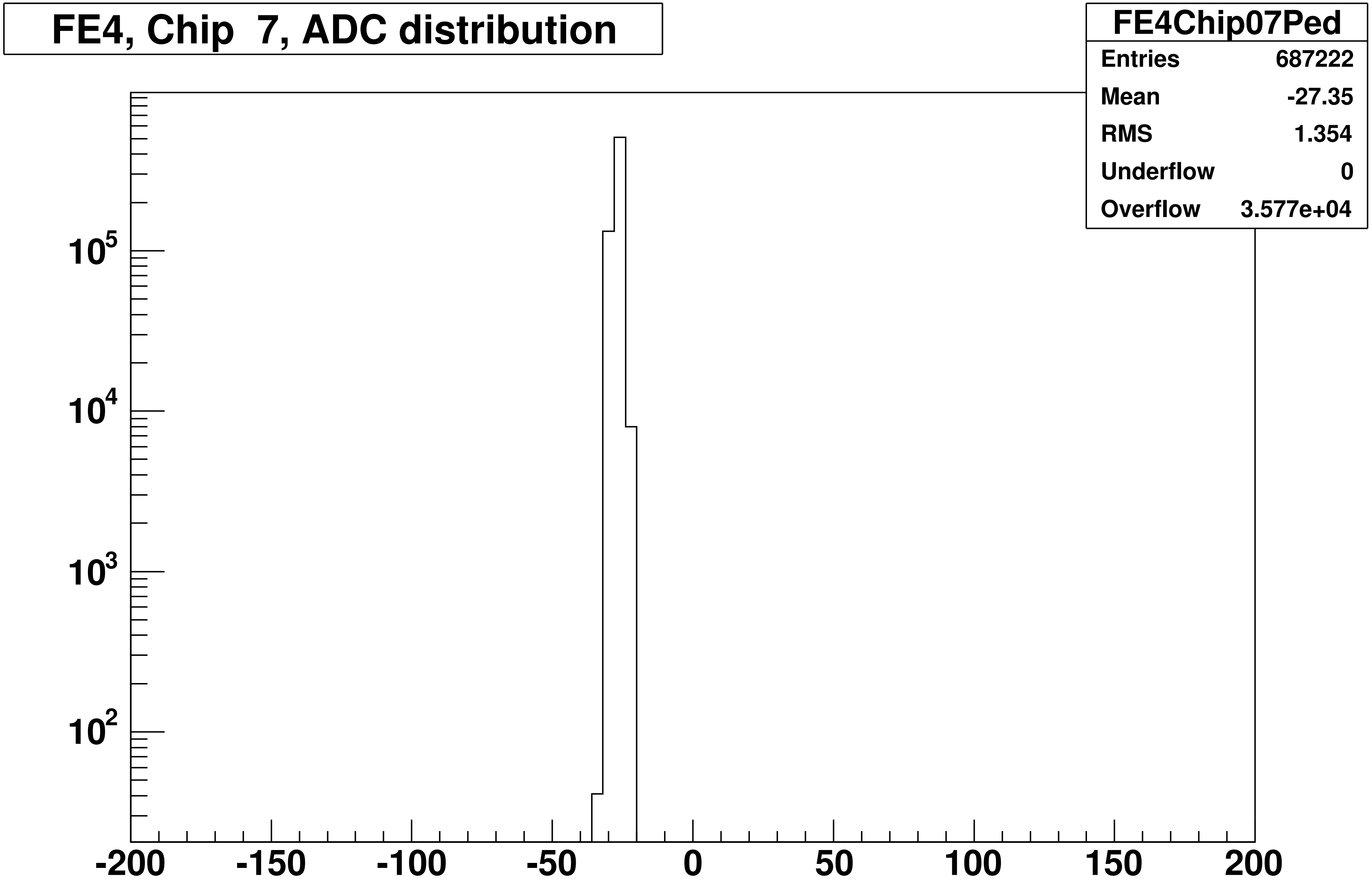
25000


FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 687222
Mean -27.35
RMS 1.354
Underflow 0
Overflow 3.577e+04
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10^4
10^3
10^2
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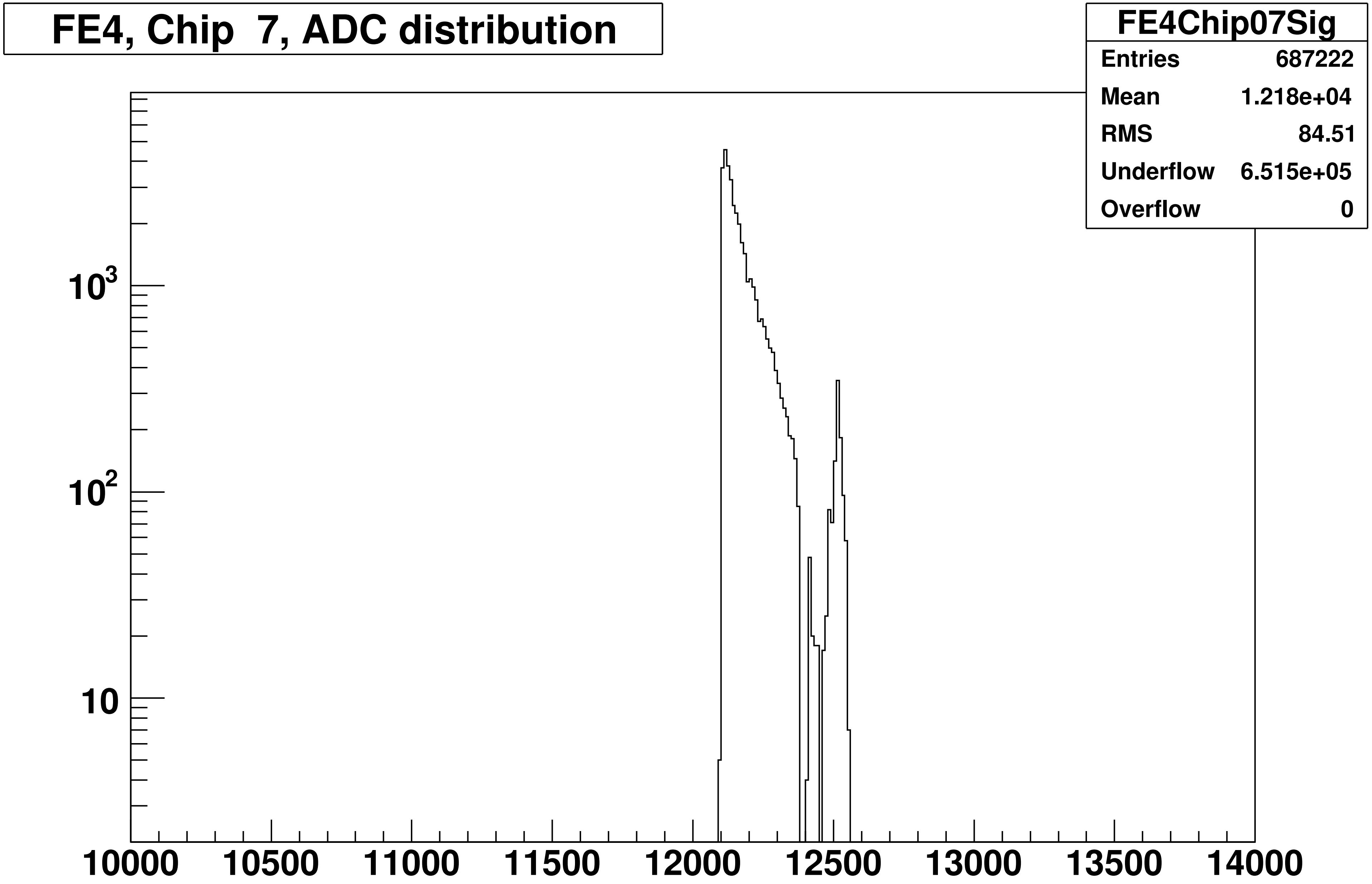

FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 687222
Mean 1.218e+04
RMS 84.51
Underflow 6.515e+05
Overflow 0
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10^2
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10000
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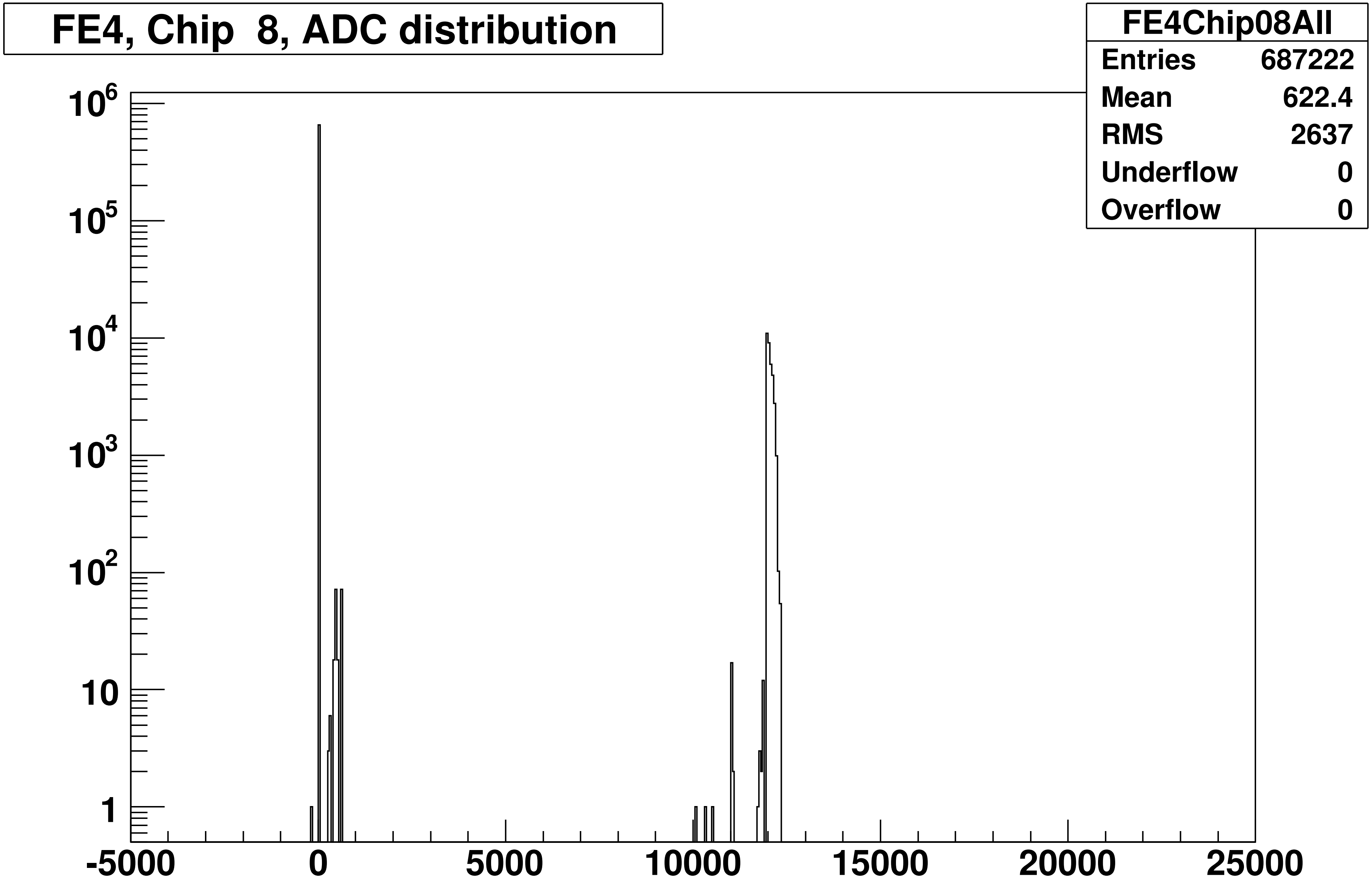

FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 687222
Mean 622.4
RMS 2637
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
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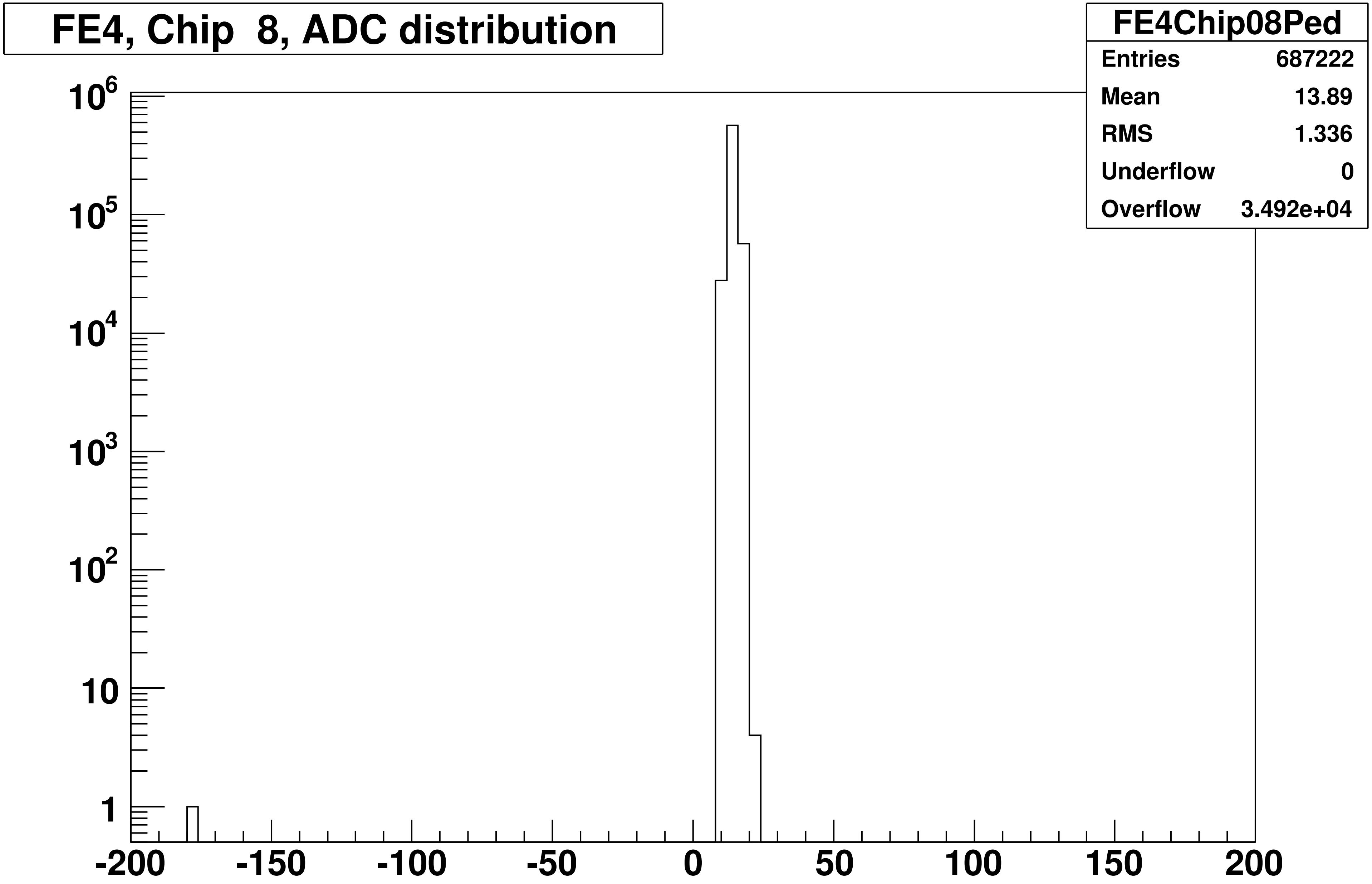
FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 687222
Mean 13.89
RMS 1.336
Underflow 0
Overflow 3.492e+04
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10^5
10^4
10^3
10^2
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1
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150
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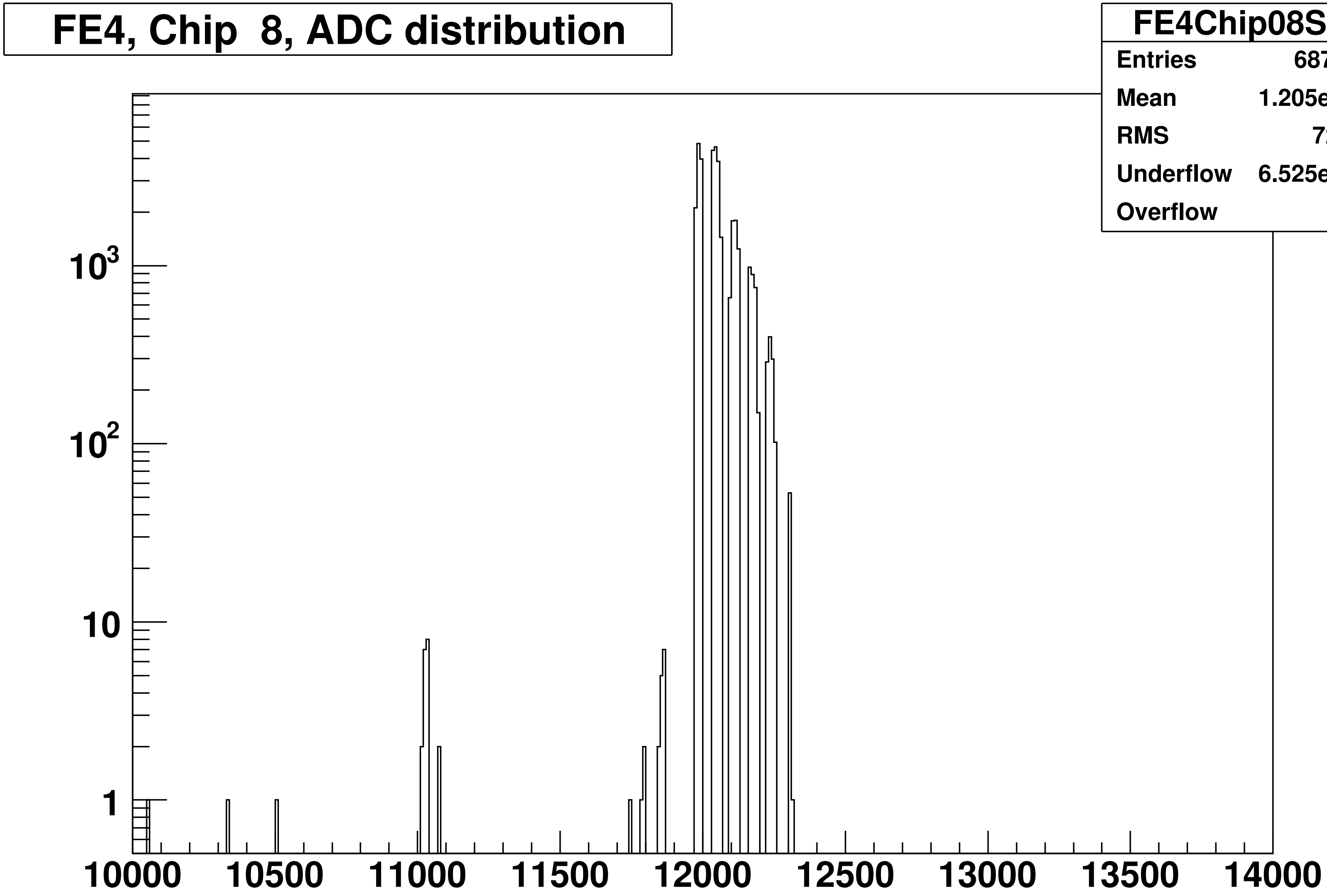
FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 687222
Mean 1.205e+04
RMS 72.16
Underflow 6.525e+05
Overflow 0
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10²
10
1
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10500
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11500
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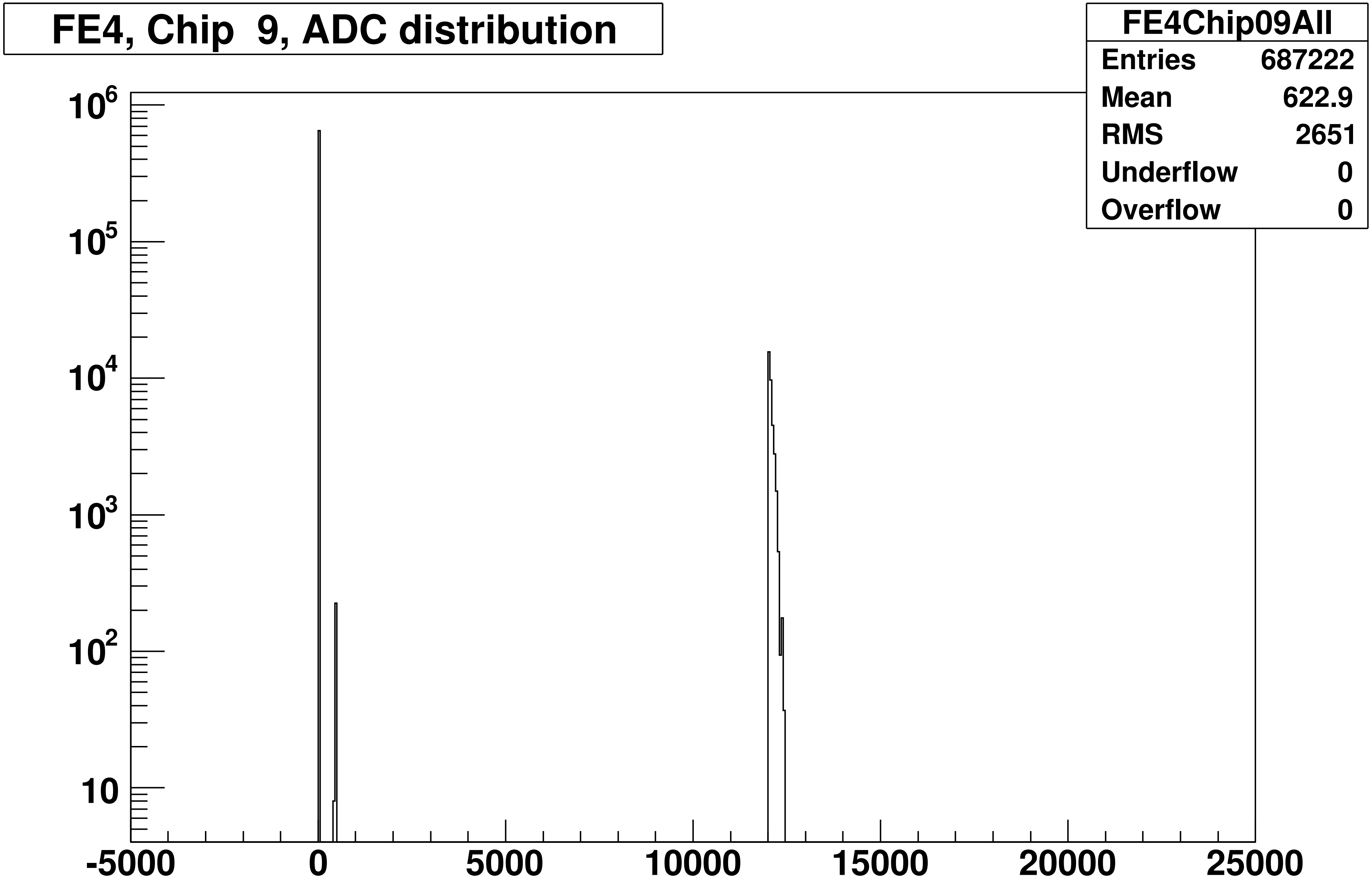
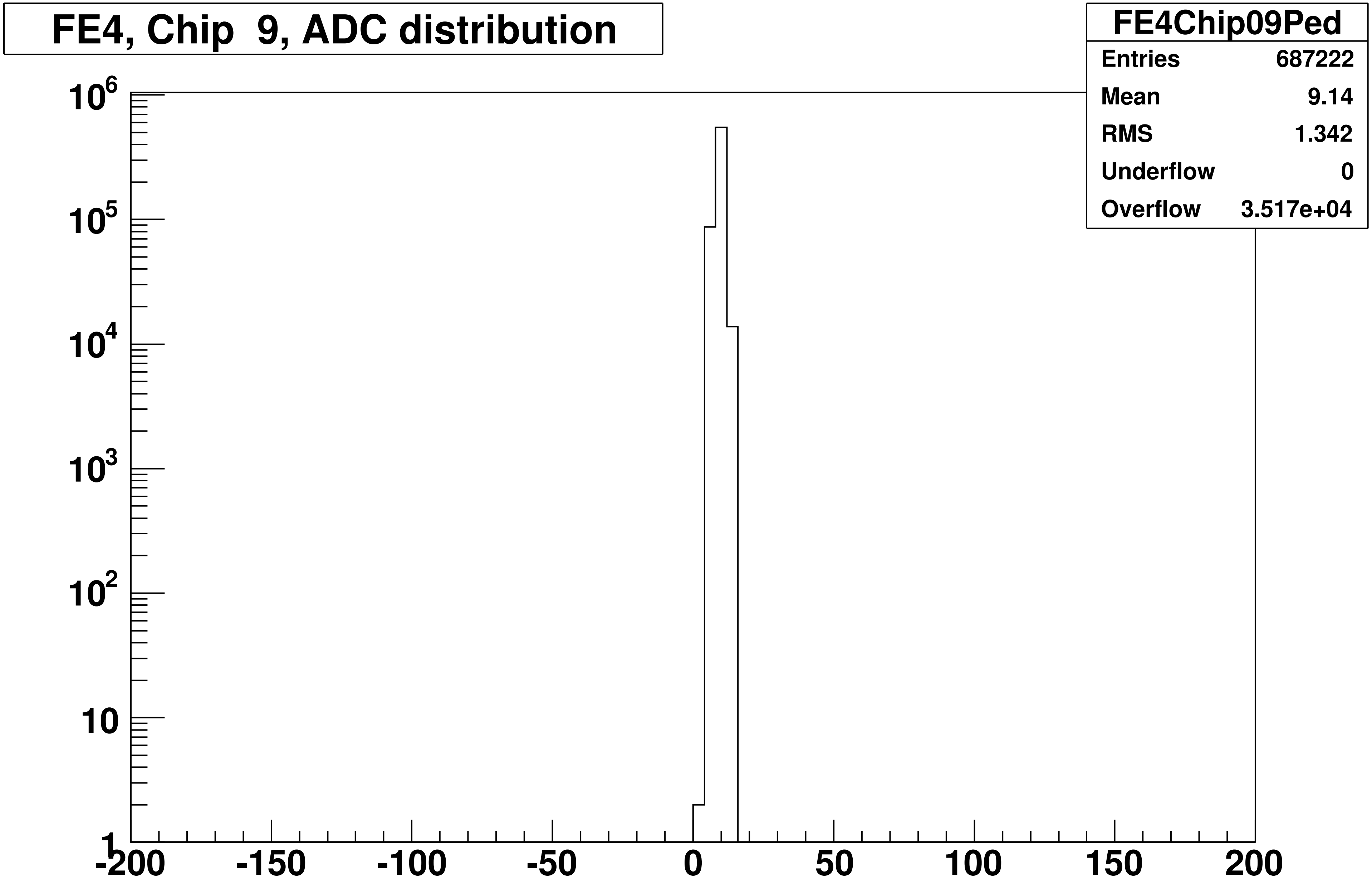

FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 687222
Mean 622.9
RMS 2651
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 687222
Mean 9.14
RMS 1.342
Underflow 0
Overflow 3.517e+04
10^6
10^5
10^4
10^3
10^2
10
1
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0
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100
150
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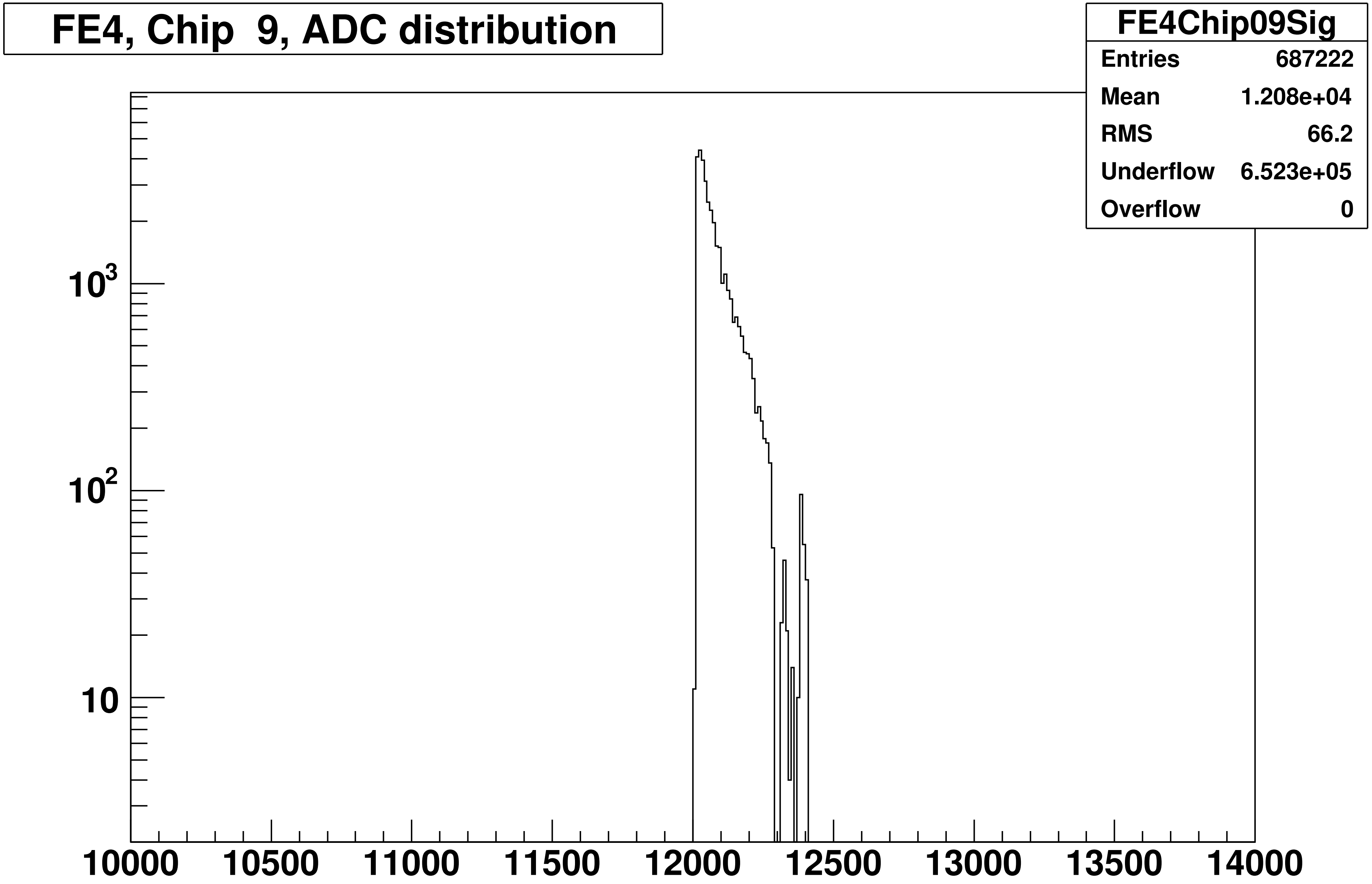

FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 687222
Mean 1.208e+04
RMS 66.2
Underflow 6.523e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
12000
12500
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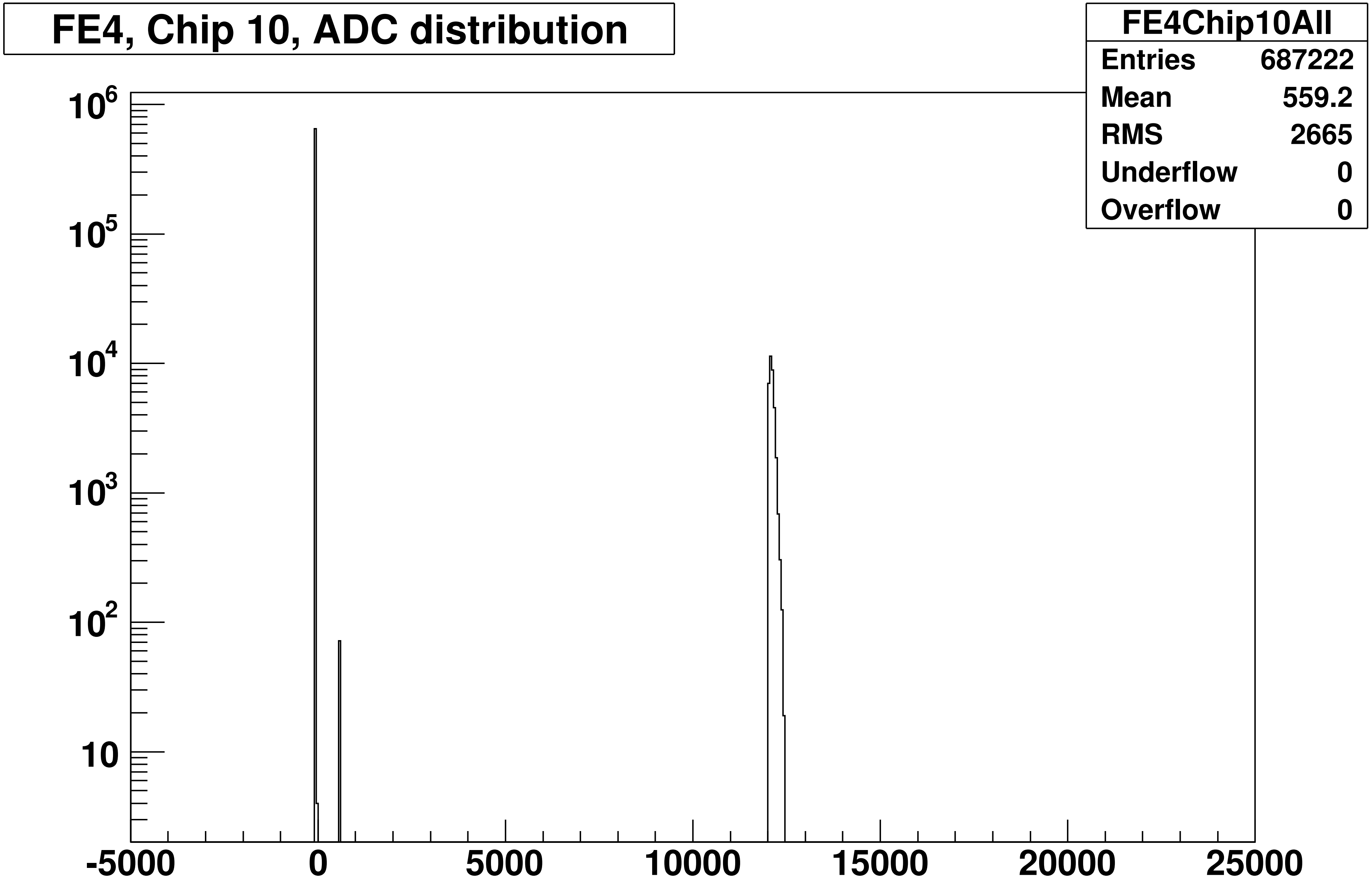
13500
14000


FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 687222
Mean 559.2
RMS 2665
Underflow 0
Overflow 0
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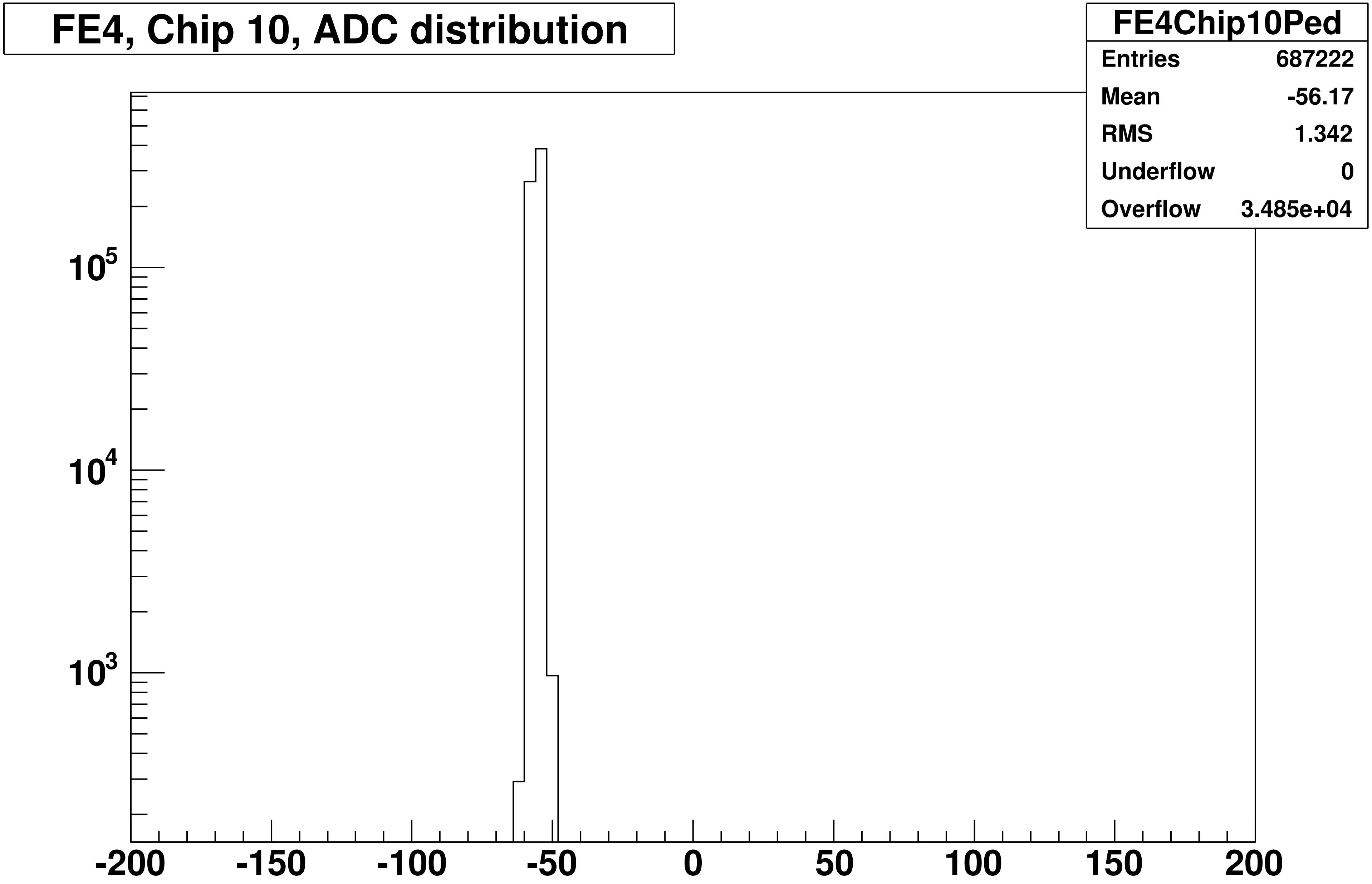
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 687222
Mean -56.17
RMS 1.342
Underflow 0
Overflow 3.485e+04
10^5
10^4
10^3
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-150
-100
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0
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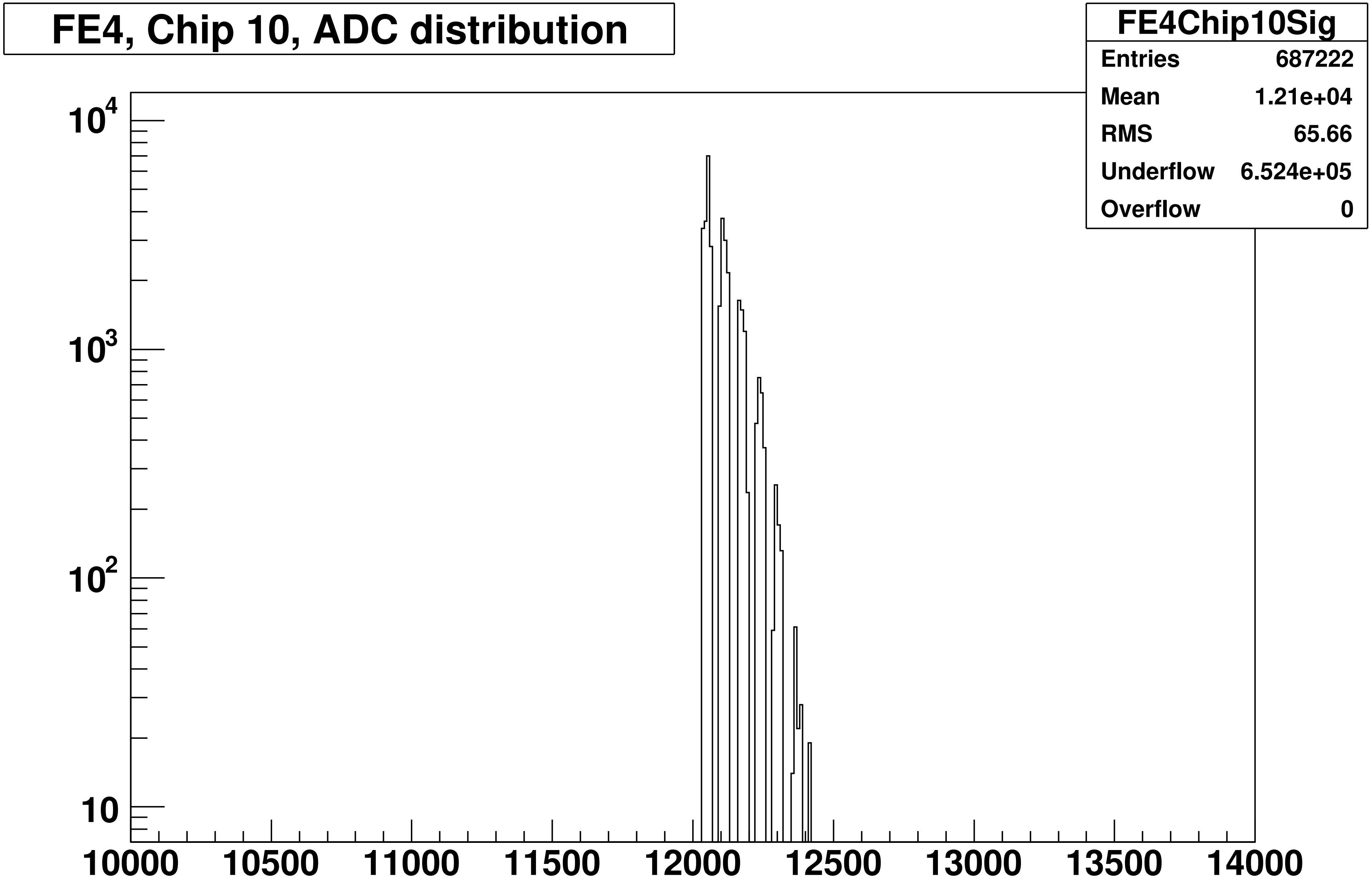
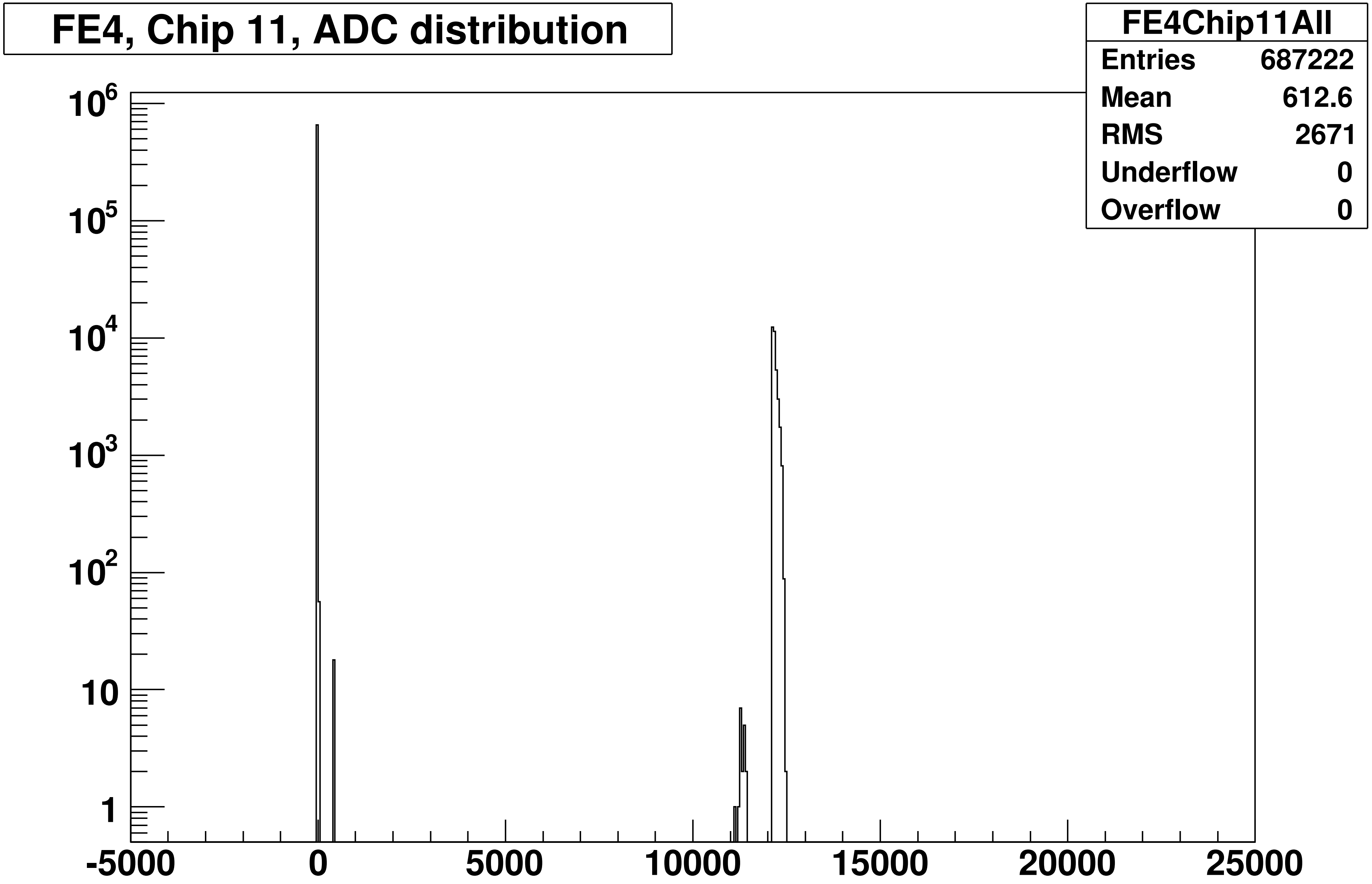
100
150
200


FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 687222
Mean 1.21e+04
RMS 65.66
Underflow 6.524e+05
Overflow 0
10^4
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 687222
Mean 612.6
RMS 2671
Underflow 0
Overflow 0
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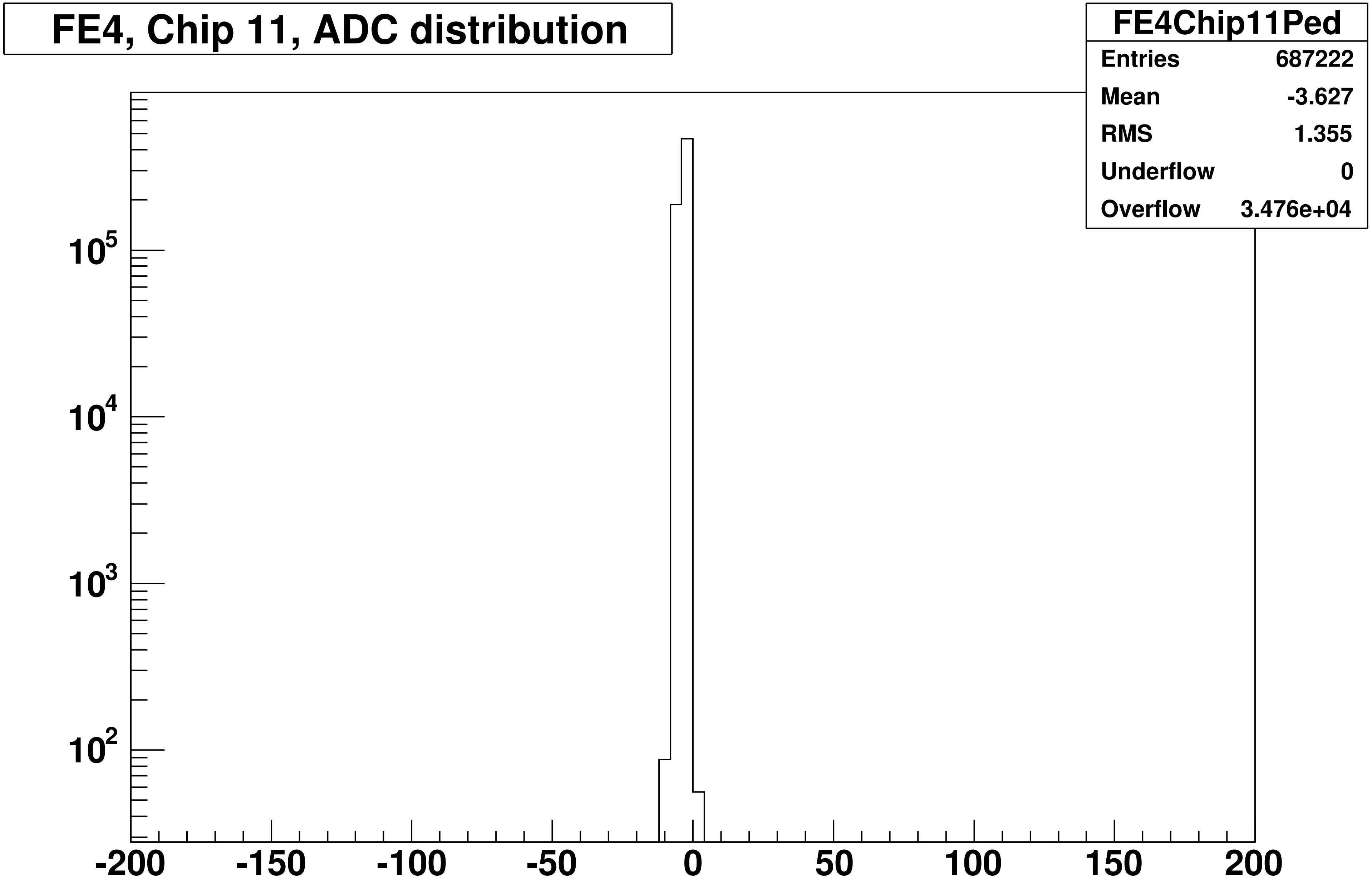
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 687222
Mean -3.627
RMS 1.355
Underflow 0
Overflow 3.476e+04
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10^4
10^3
10^2
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-100
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0
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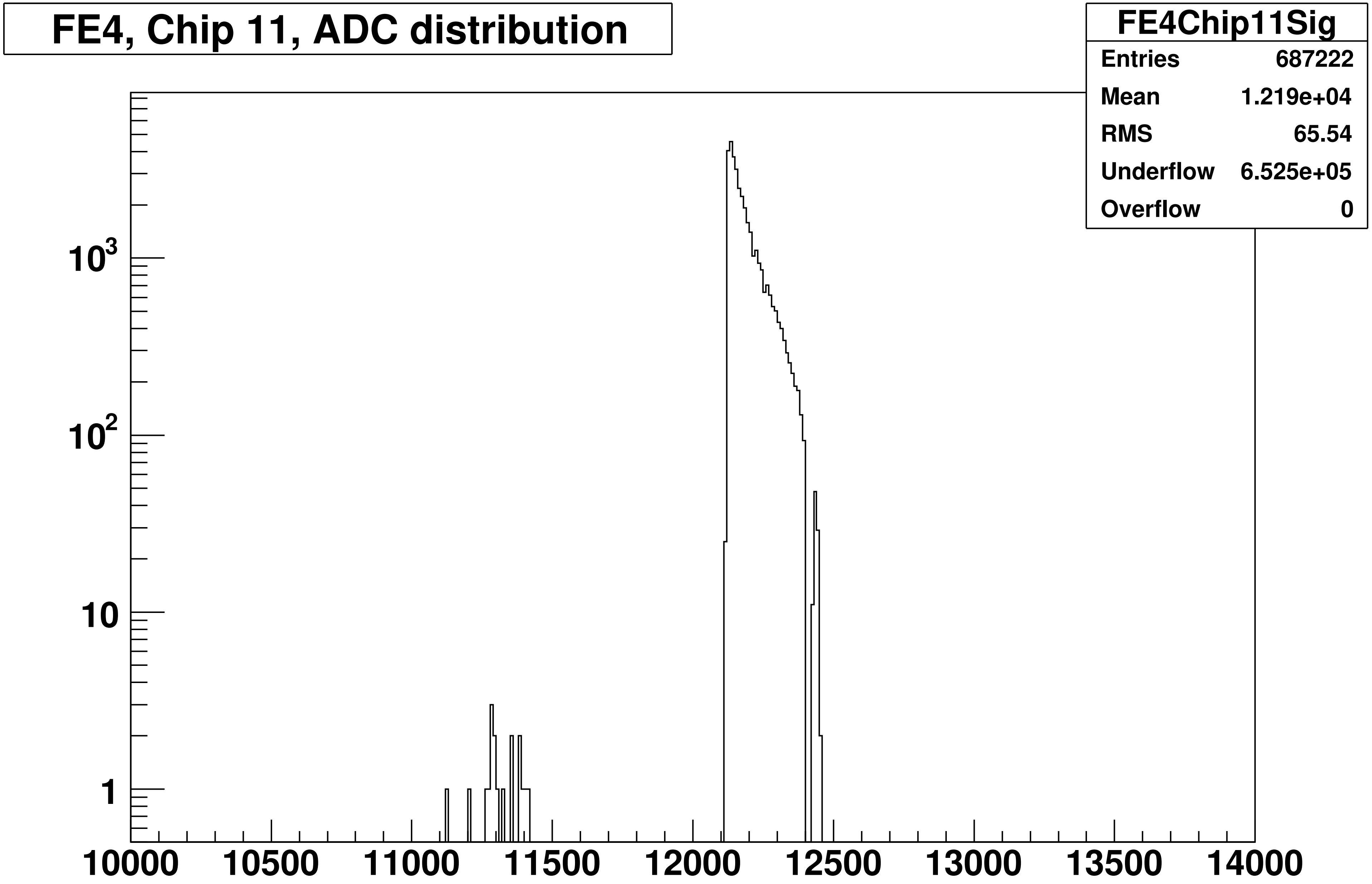
100
150
200



FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 687222
Mean 1.219e+04
RMS 65.54
Underflow 6.525e+05
Overflow 0
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10^2
10
1
10000
10500
11000
11500
12000
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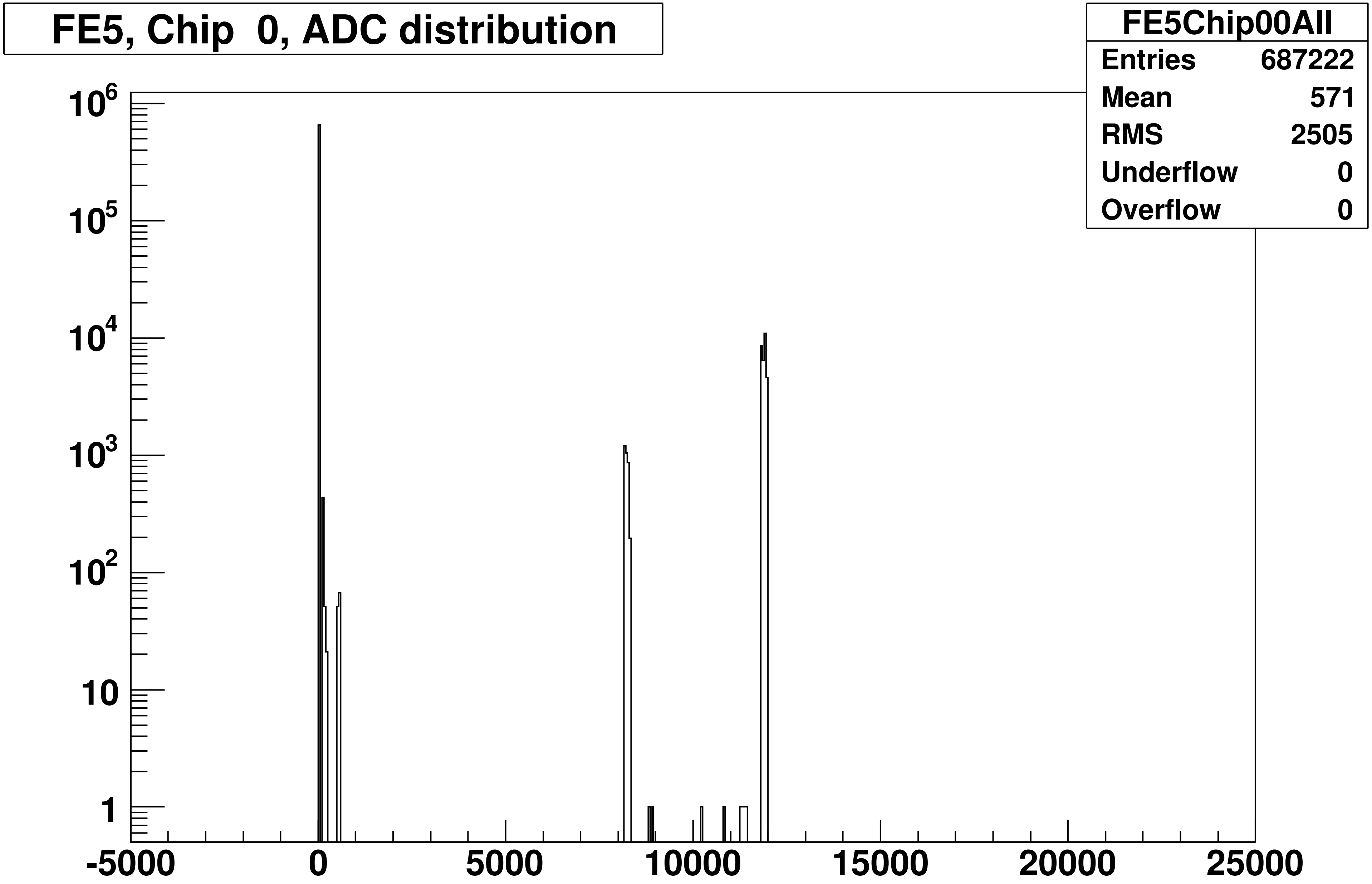
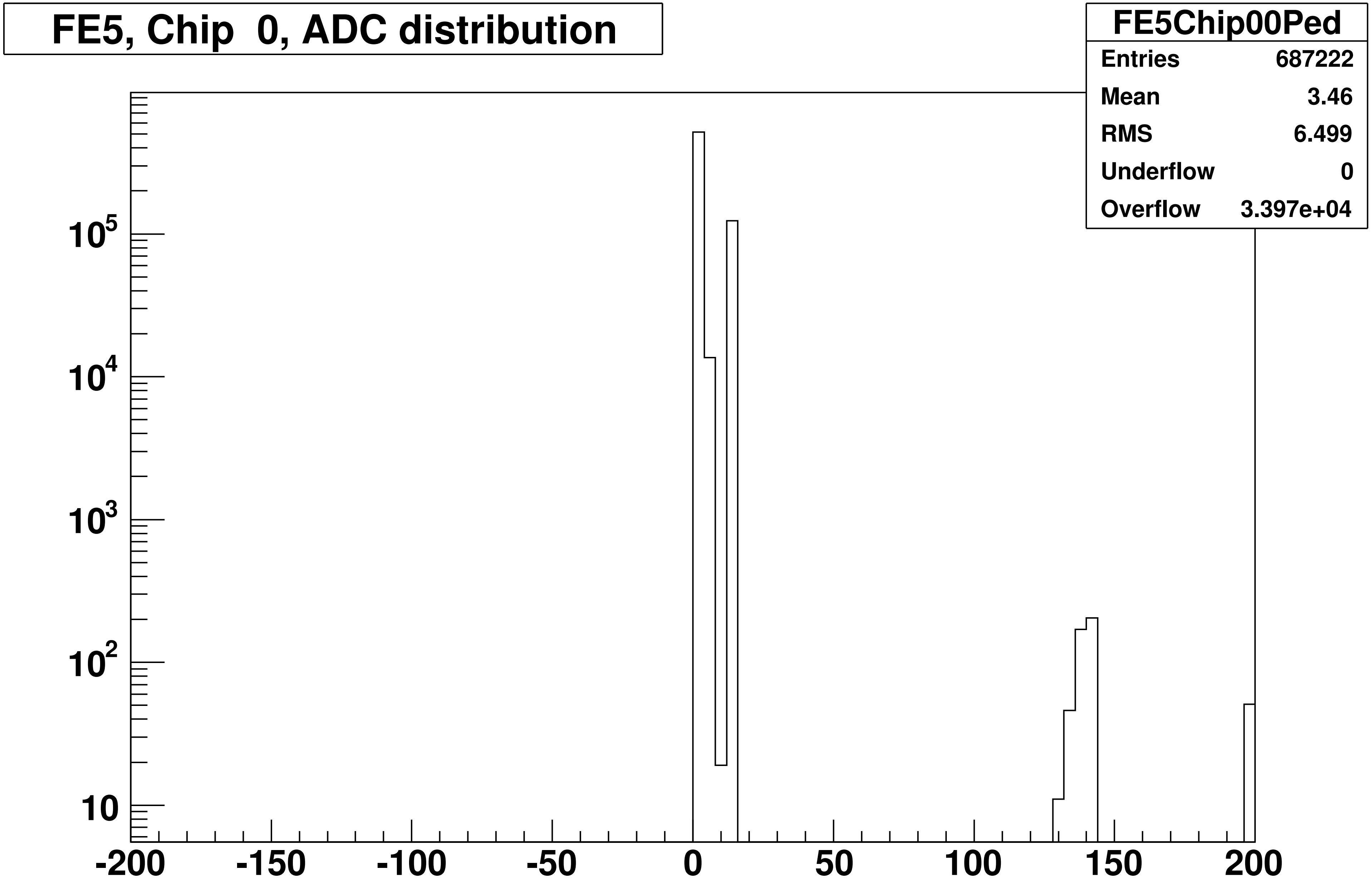
13500
14000


FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 687222
Mean 571
RMS 2505
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 687222
Mean 3.46
RMS 6.499
Underflow 0
Overflow 3.397e+04
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10^4
10^3
10^2
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-100
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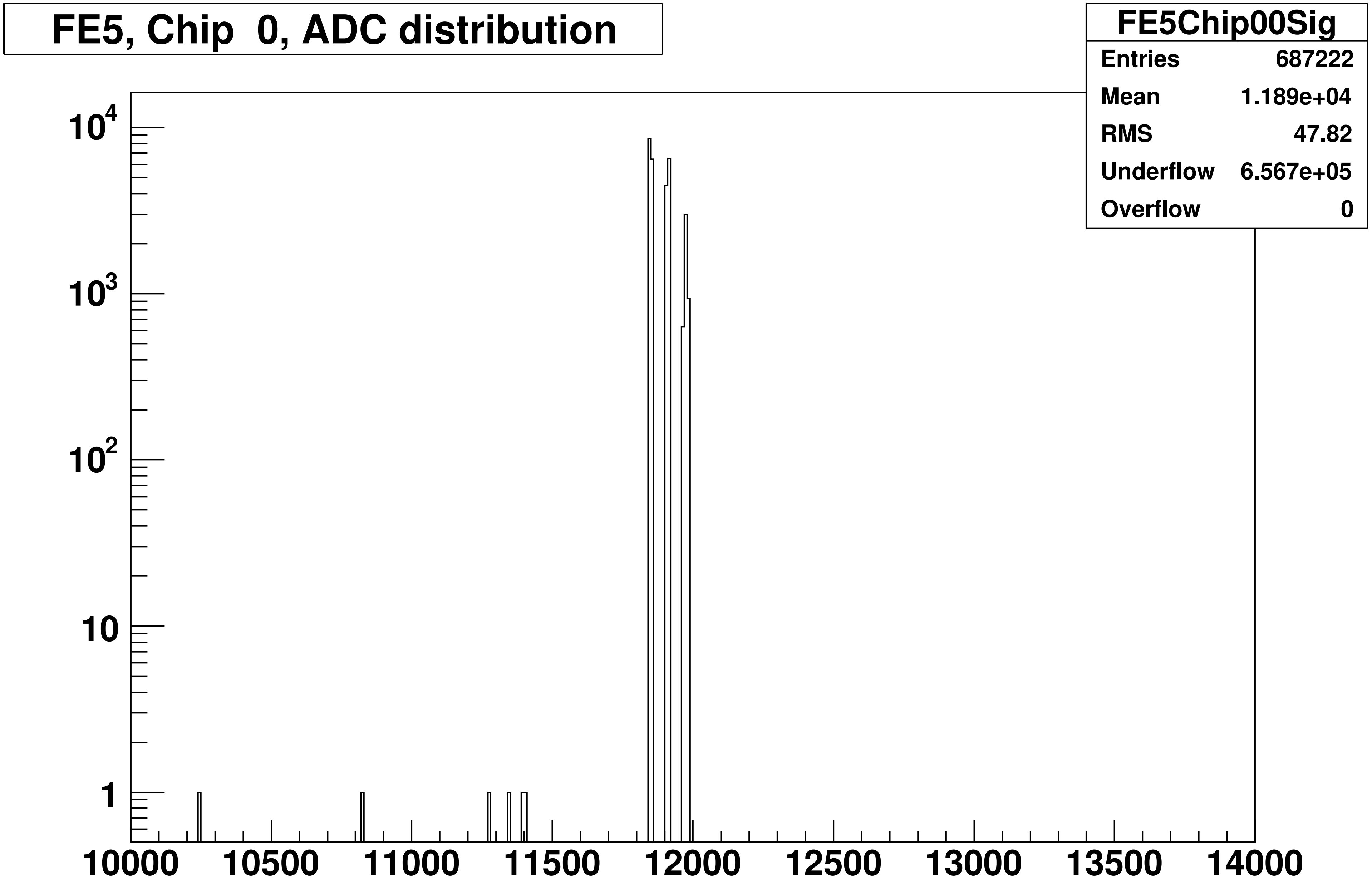
50
100
150
200


FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 687222
Mean 1.189e+04
RMS 47.82
Underflow 6.567e+05
Overflow 0
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10^3
10^2
10
1
10000
10500
11000
11500
12000
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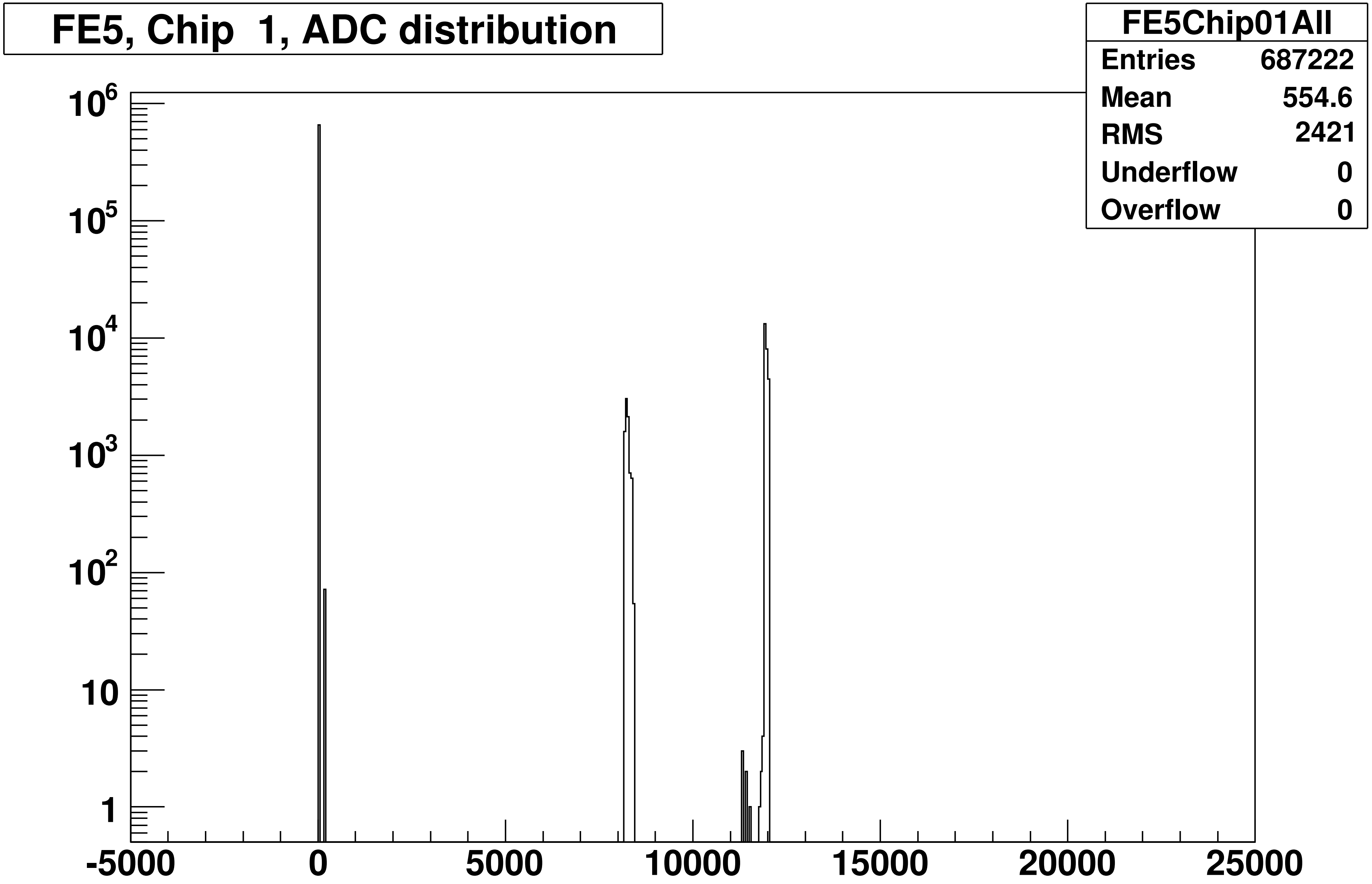
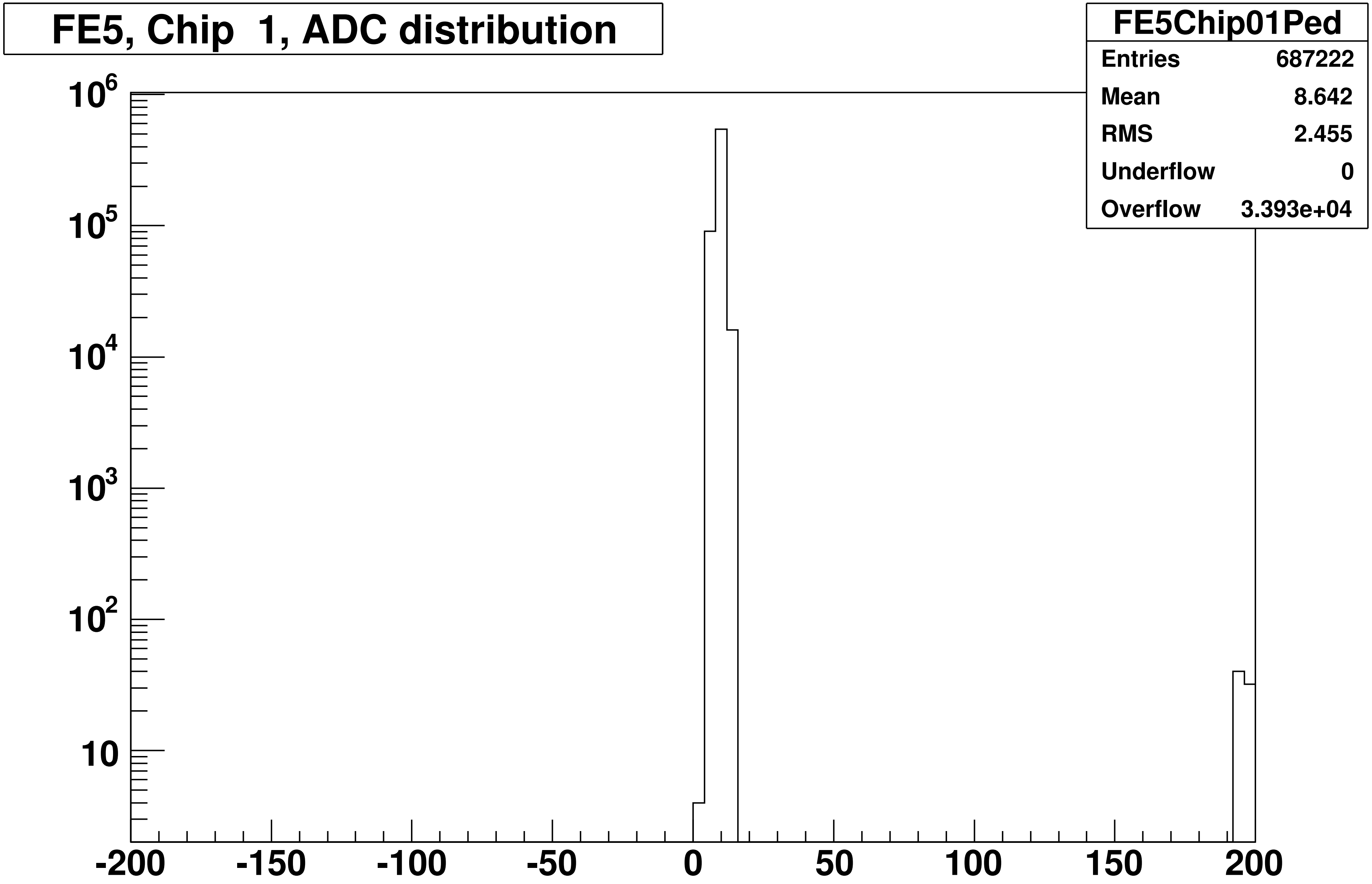
13500
14000


FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 687222
Mean 554.6
RMS 2421
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 687222
Mean 8.642
RMS 2.455
Underflow 0
Overflow 3.393e+04
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10^5
10^4
10^3
10^2
10
-200
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-100
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0
50
100
150
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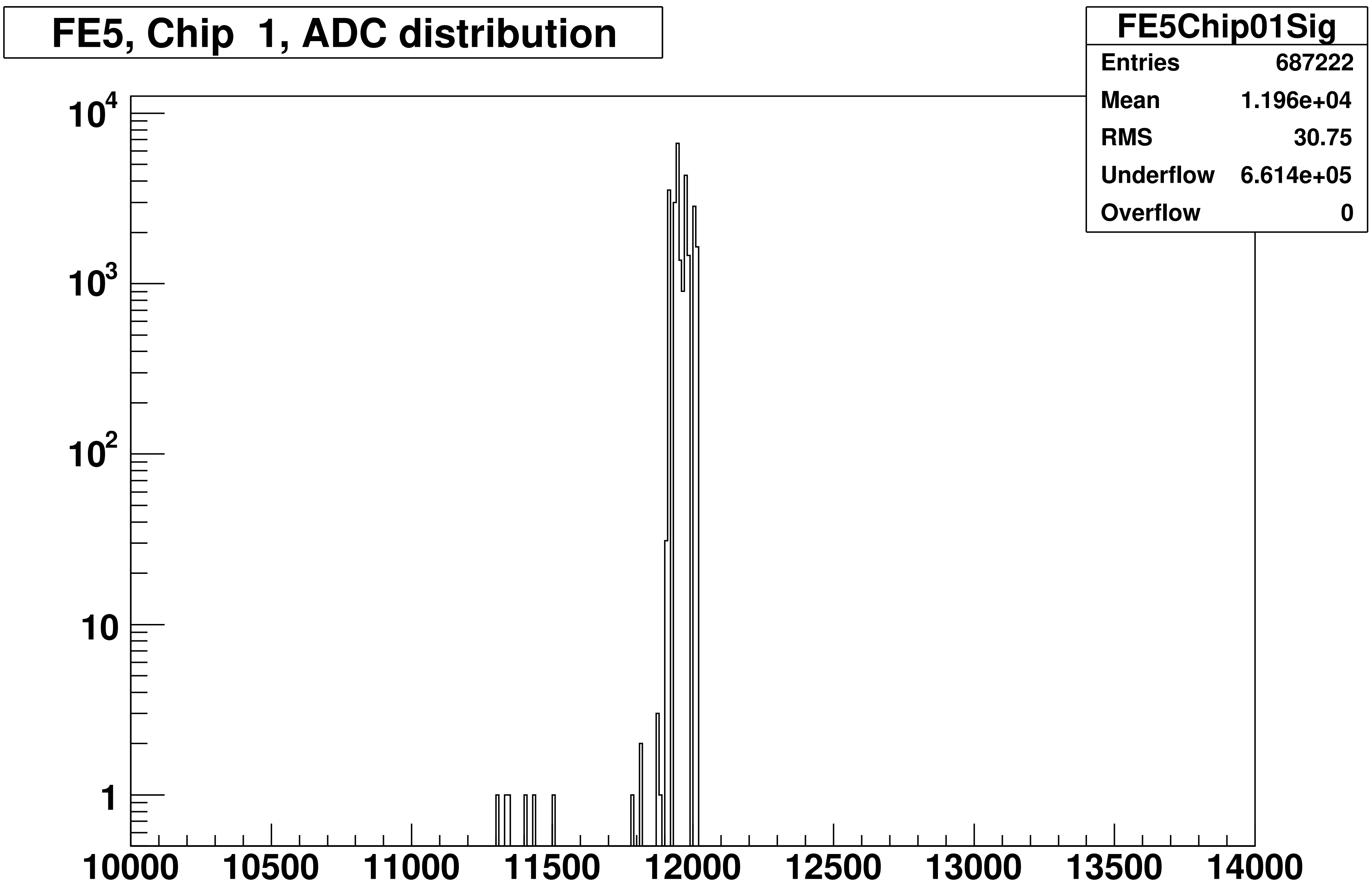

FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 687222
Mean 1.196e+04
RMS 30.75
Underflow 6.614e+05
Overflow 0
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10^3
10^2
10
1
10000
10500
11000
11500
12000
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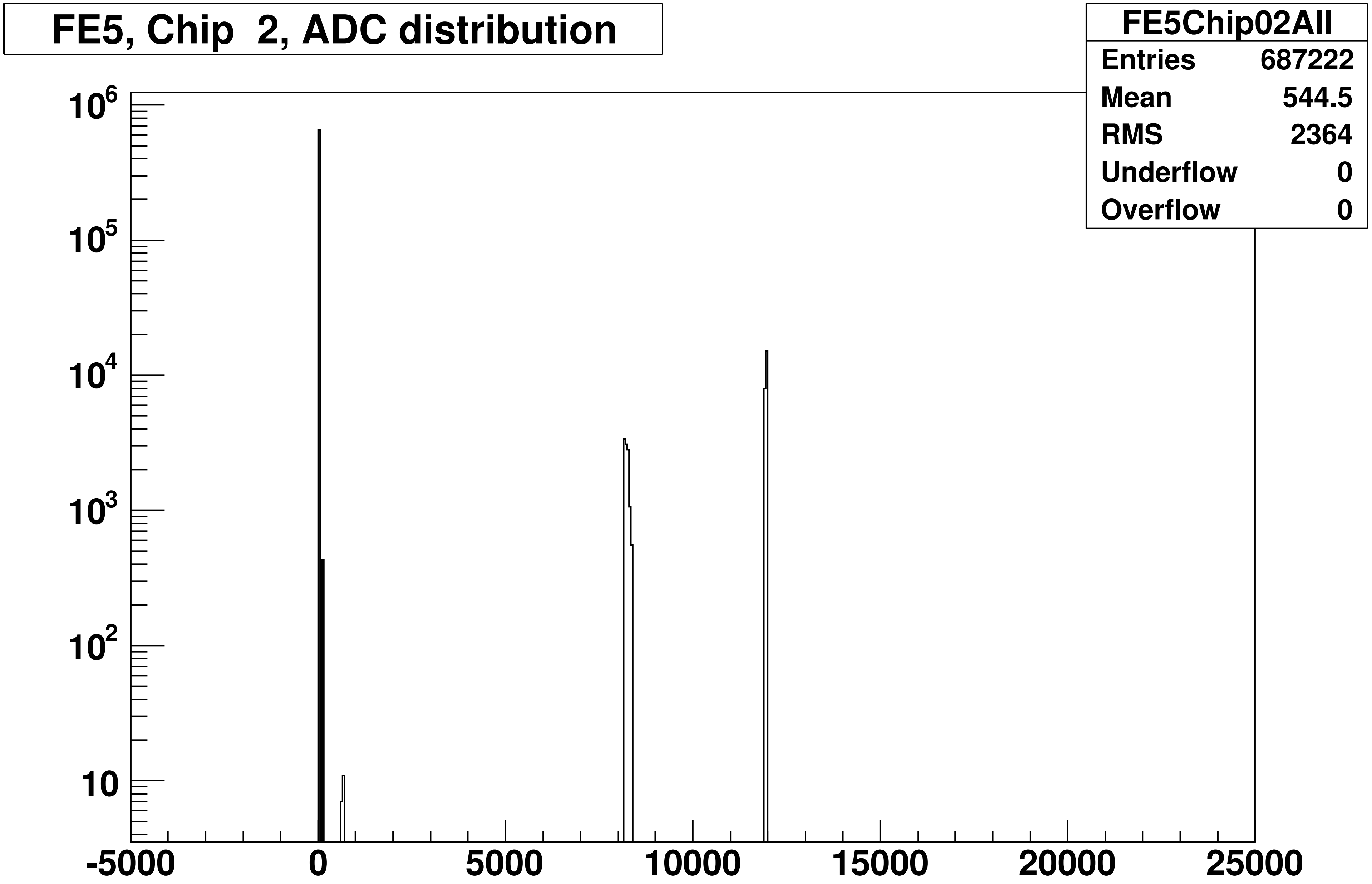
FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 687222
Mean 544.5
RMS 2364
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
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20000
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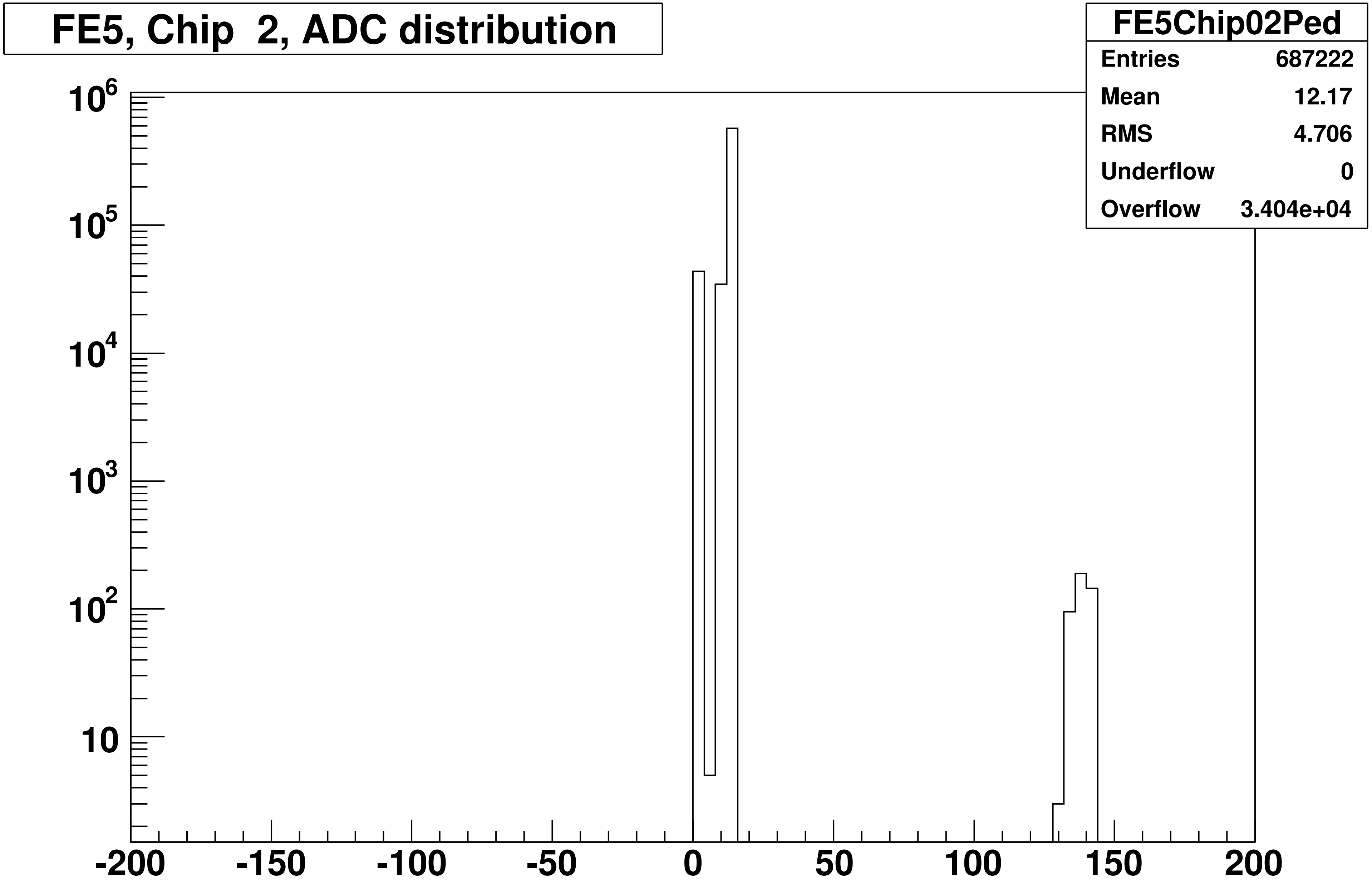

FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 687222
Mean 12.17
RMS 4.706
Underflow 0
Overflow 3.404e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
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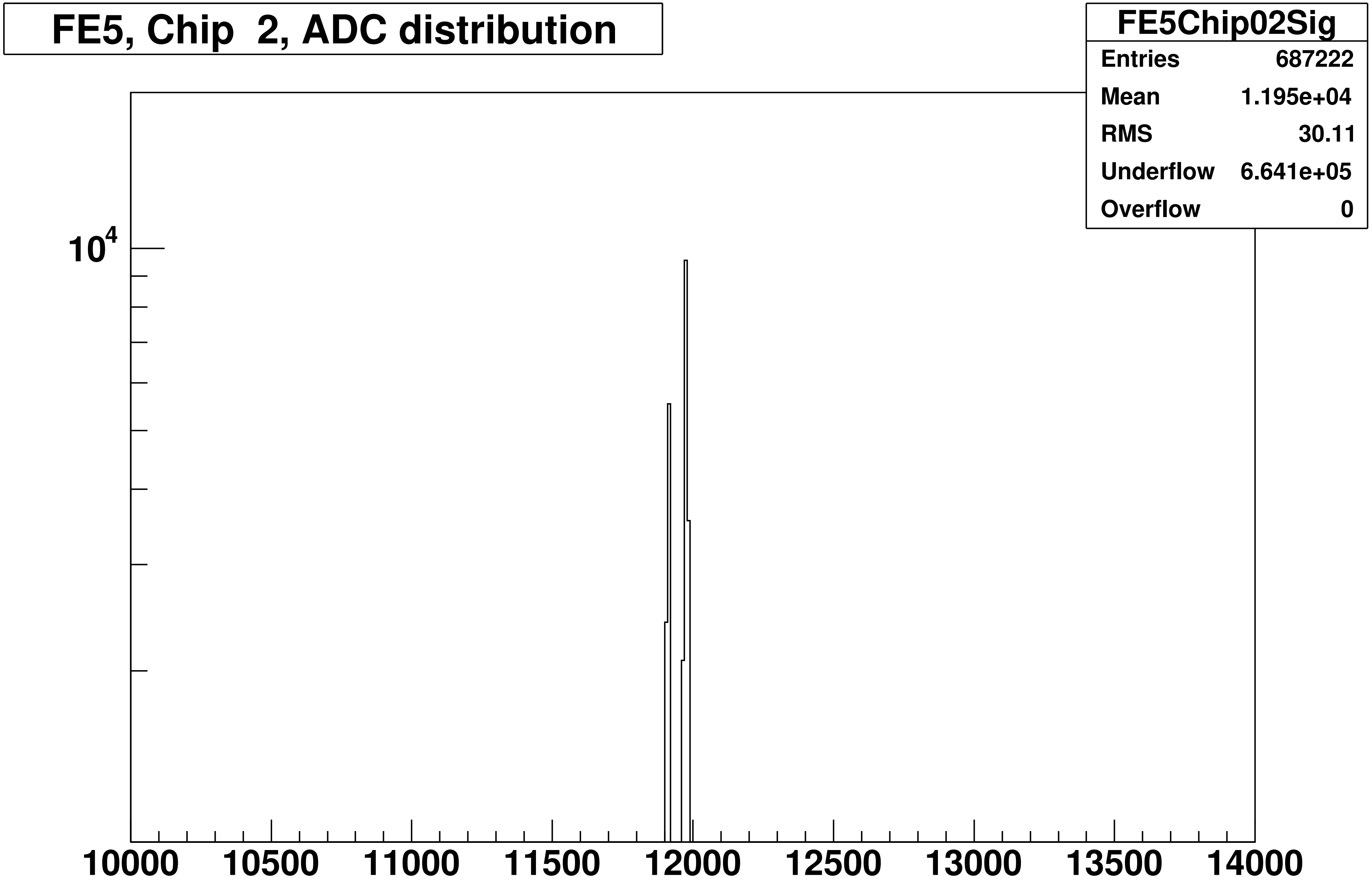
50
100
150
200



FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 687222
Mean 1.195e+04
RMS 30.11
Underflow 6.641e+05
Overflow 0
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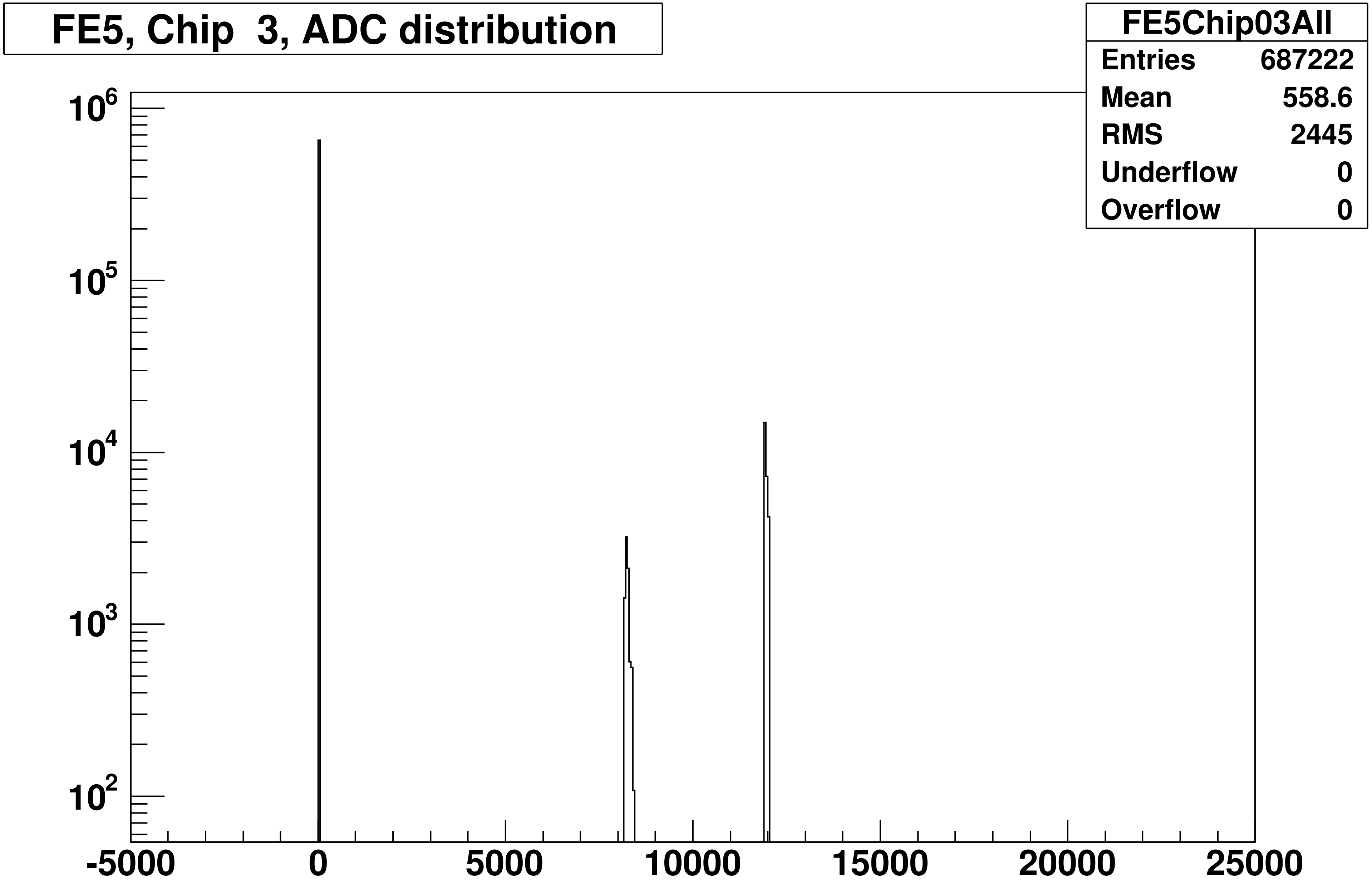
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 687222
Mean 558.6
RMS 2445
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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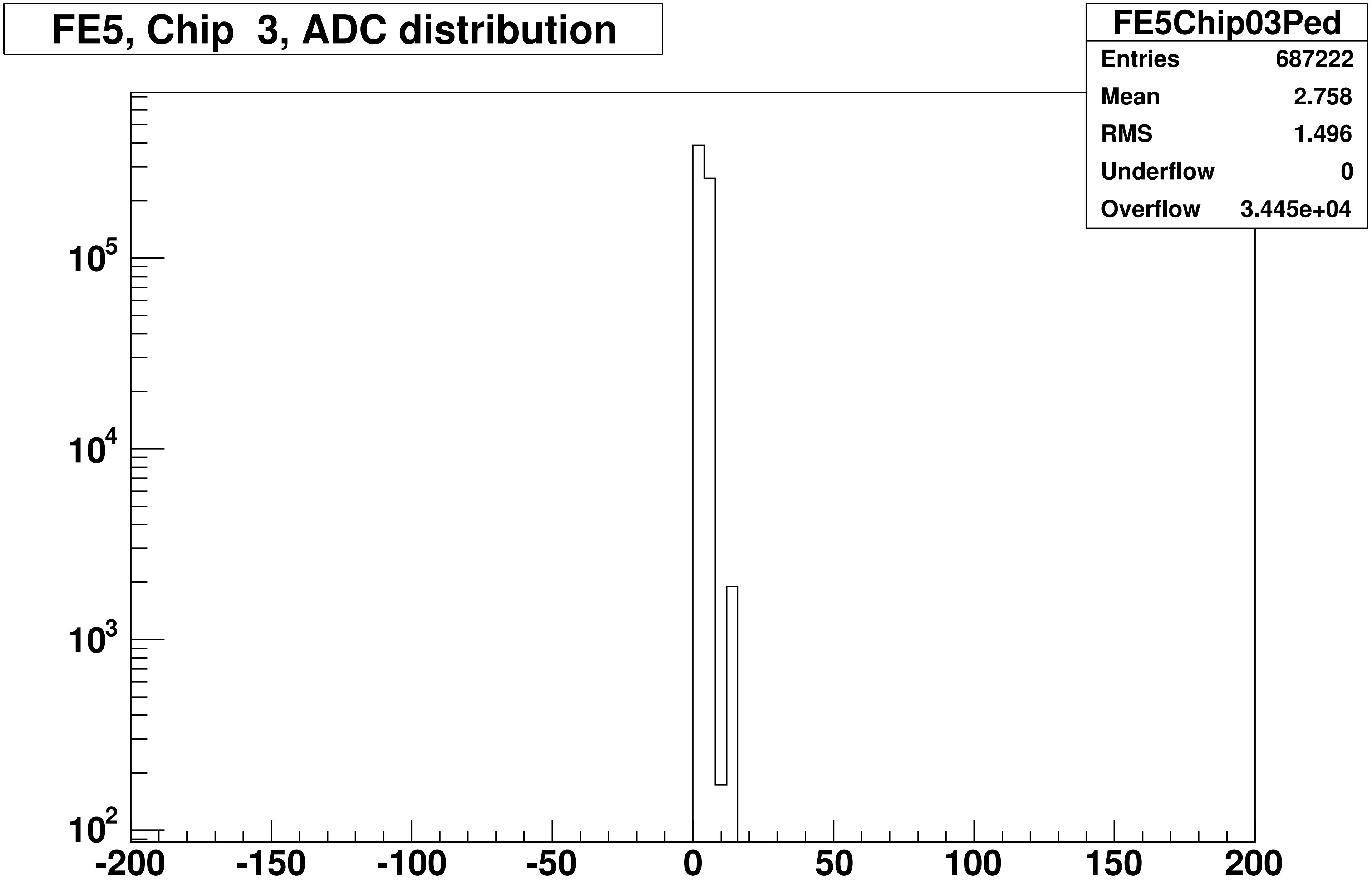
10000
15000
20000
25000



FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 687222
Mean 2.758
RMS 1.496
Underflow 0
Overflow 3.445e+04
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10^4
10^3
10^2
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-150
-100
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0
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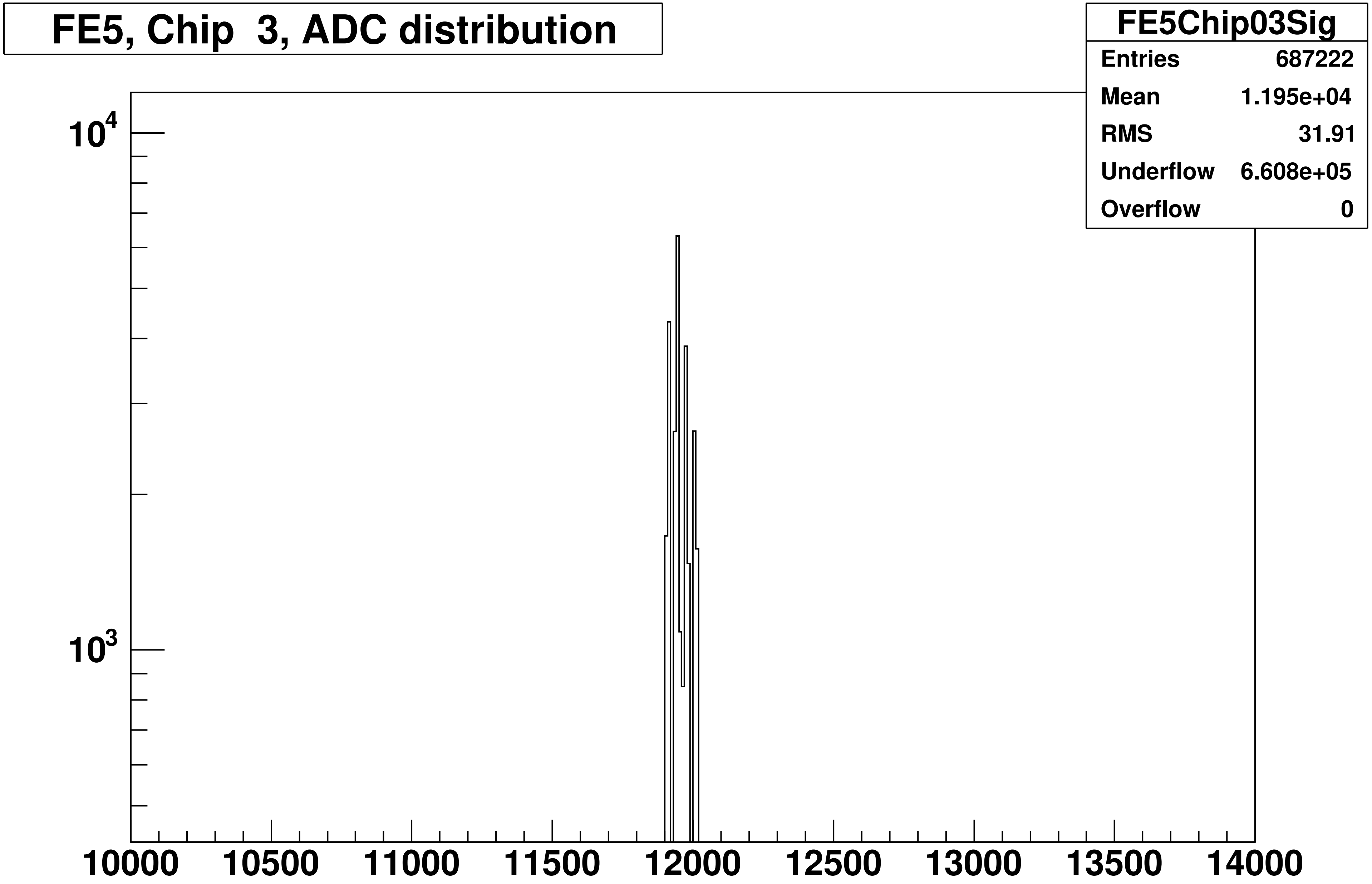
100
150
200


FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 687222
Mean 1.195e+04
RMS 31.91
Underflow 6.608e+05
Overflow 0
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10^3
10000
10500
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11500
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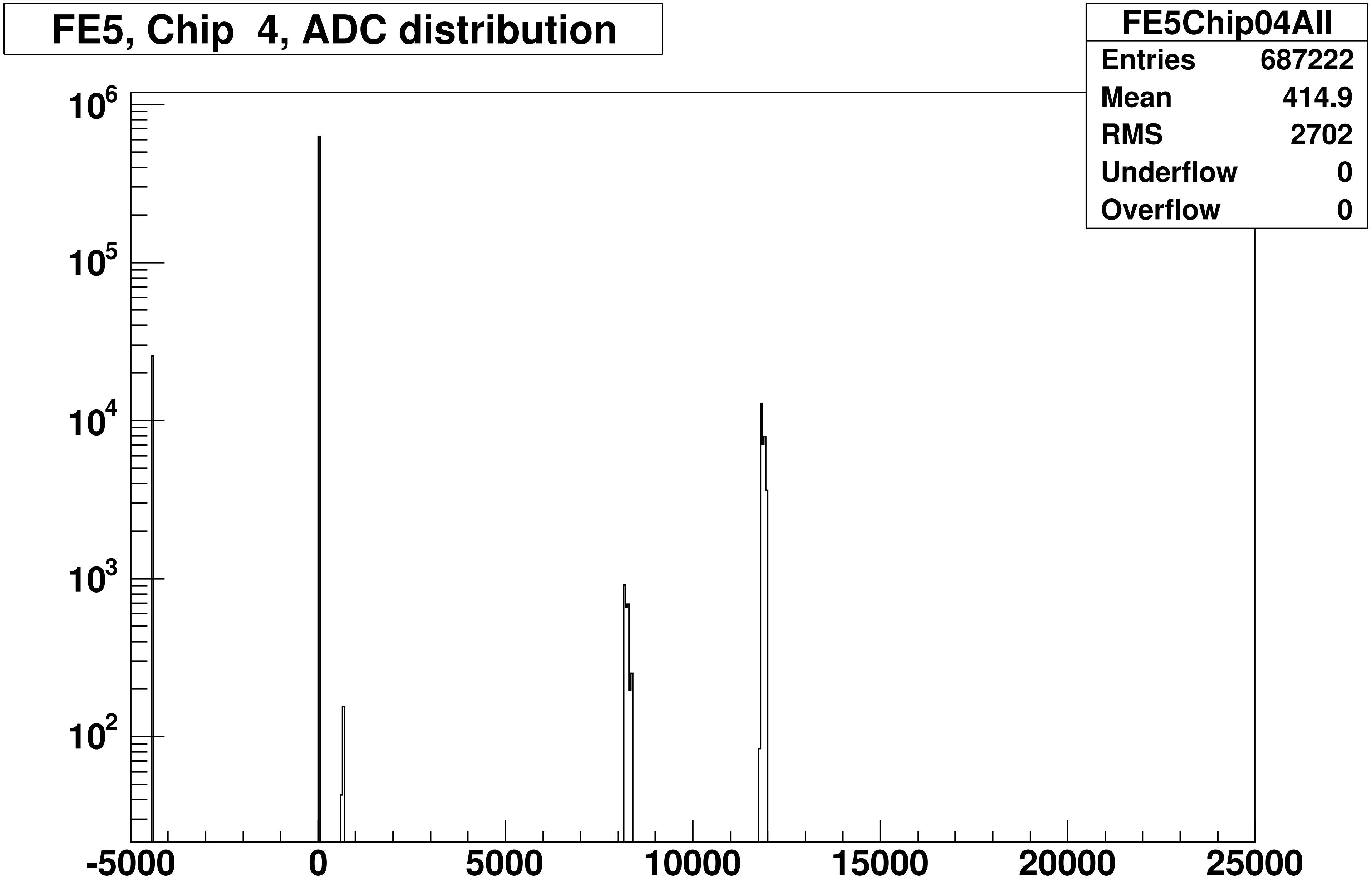
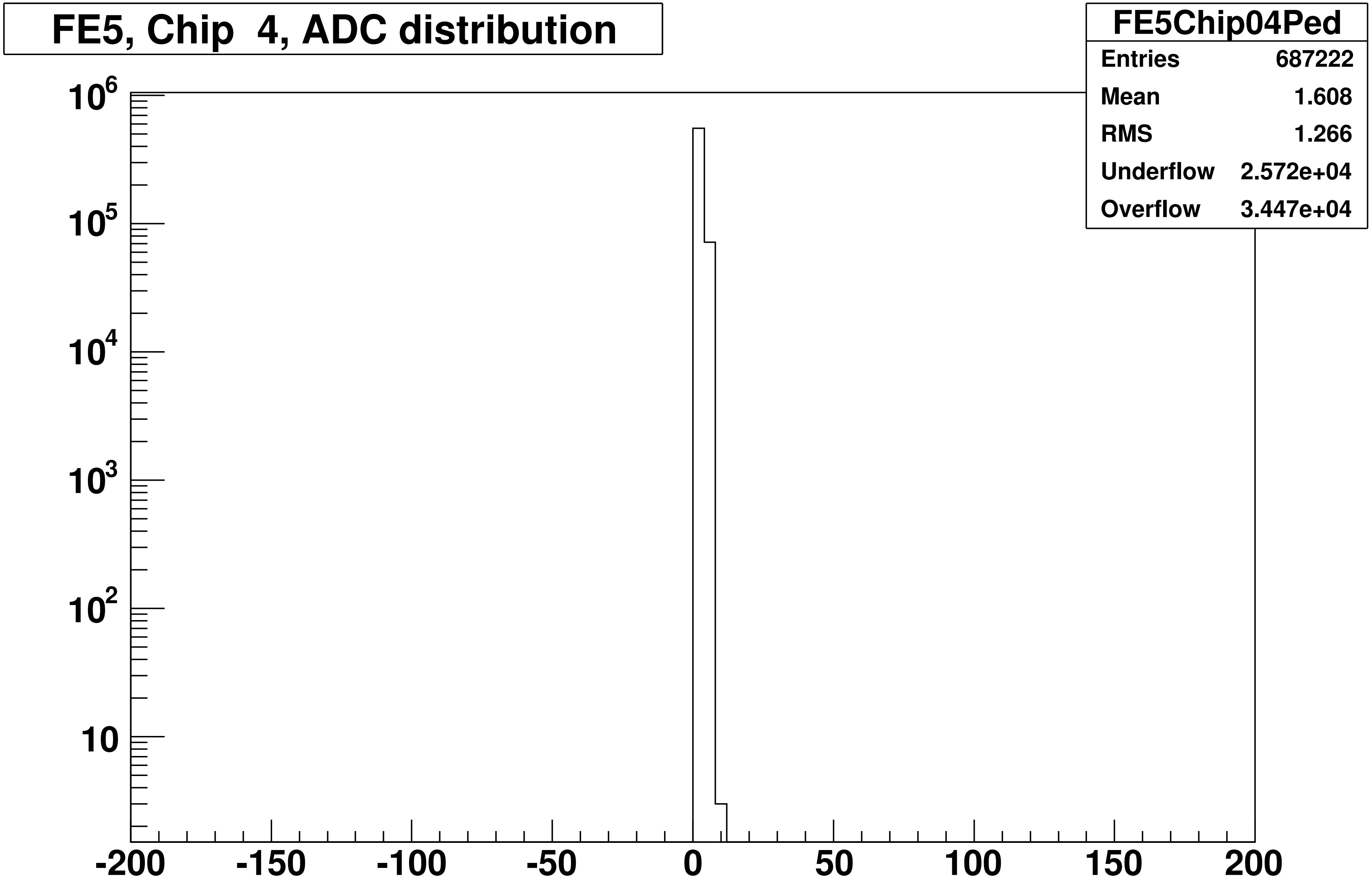
14000


FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 687222
Mean 414.9
RMS 2702
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 687222
Mean 1.608
RMS 1.266
Underflow 2.572e+04
Overflow 3.447e+04
10^6
10^5
10^4
10^3
10^2
10
-200
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-100
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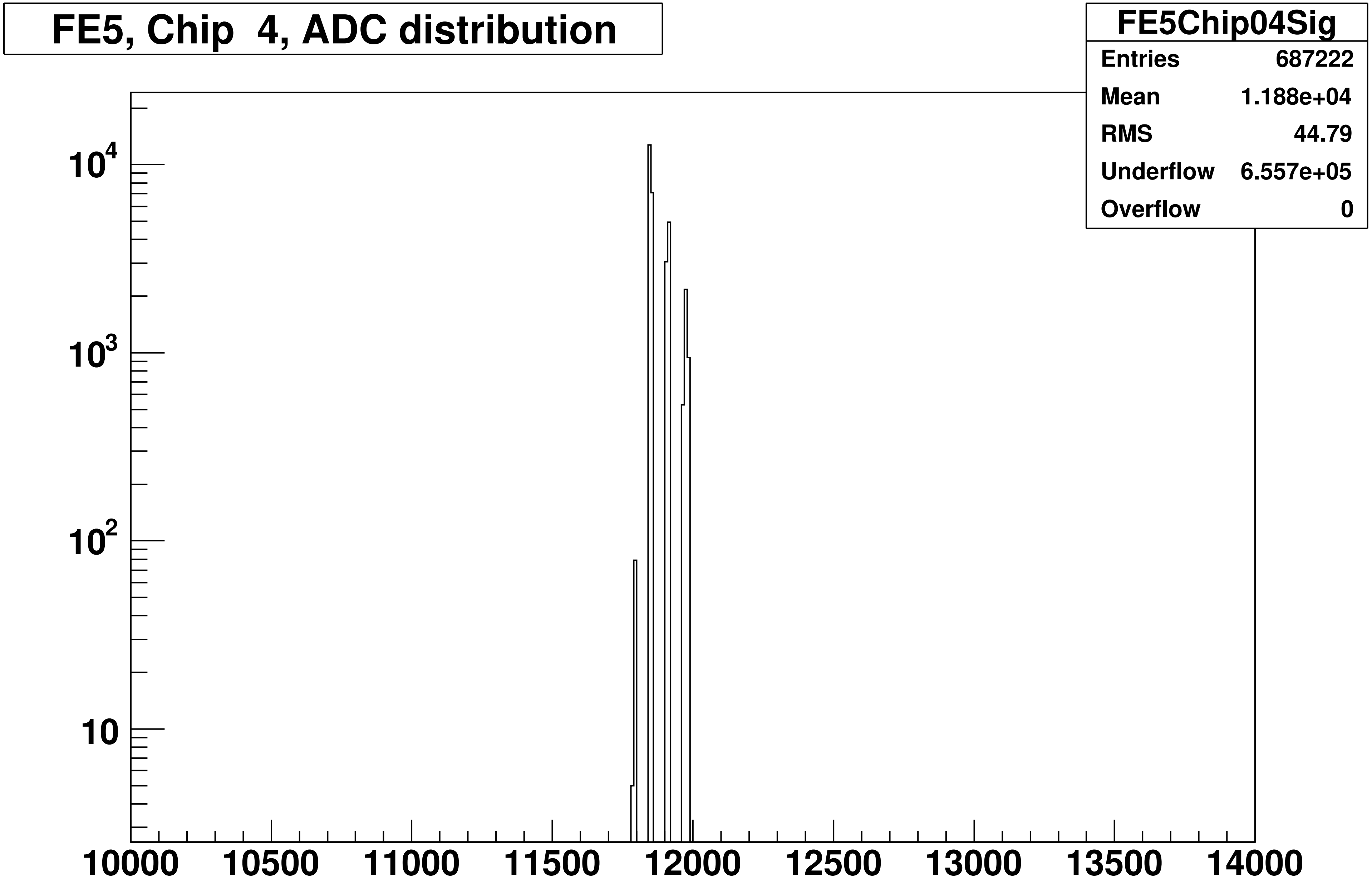
50
100
150
200


FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 687222
Mean 1.188e+04
RMS 44.79
Underflow 6.557e+05
Overflow 0
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10^3
10^2
10
10000
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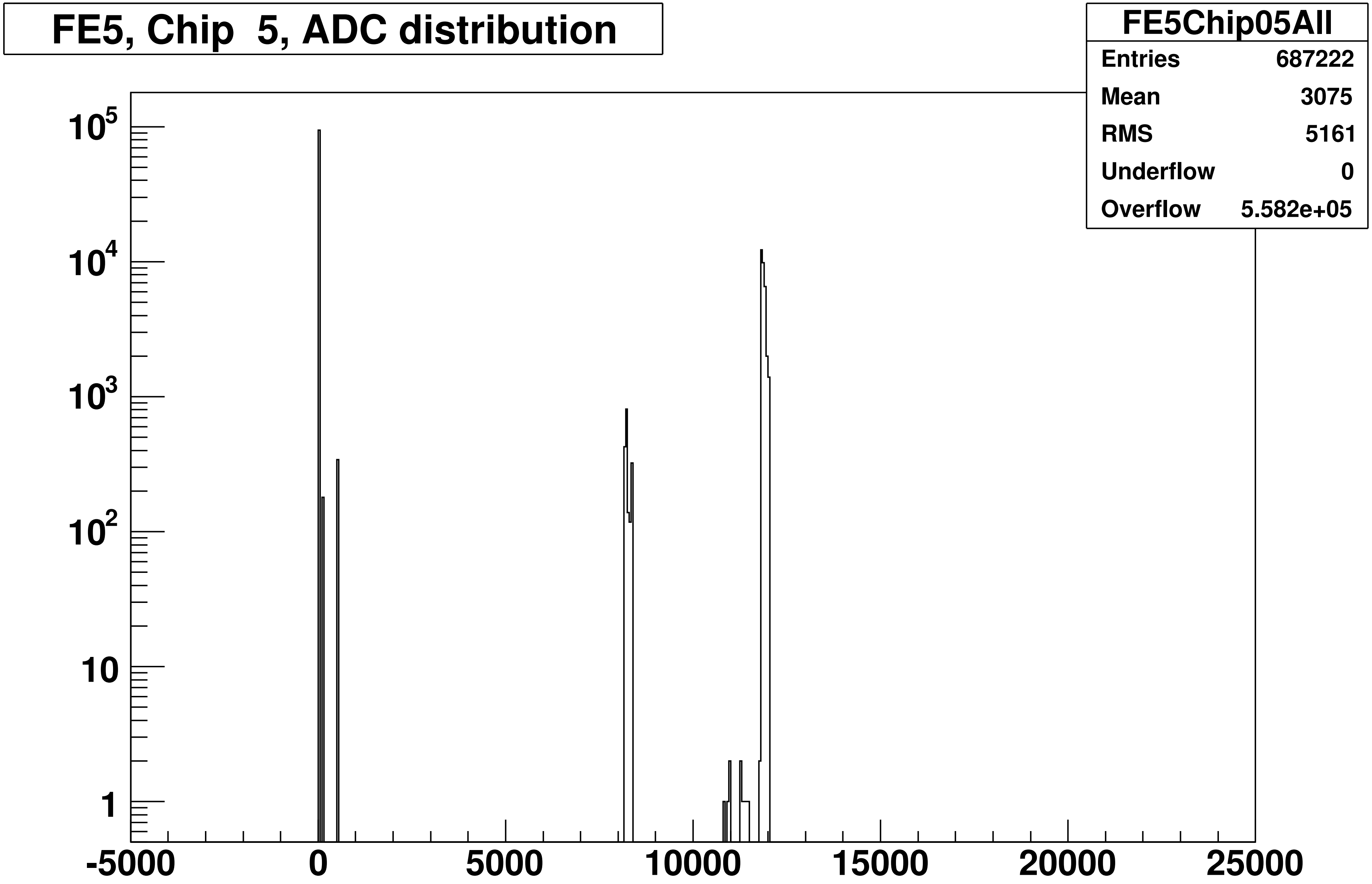

FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 687222
Mean 3075
RMS 5161
Underflow 0
Overflow 5.582e+05
10^5
10^4
10^3
10^2
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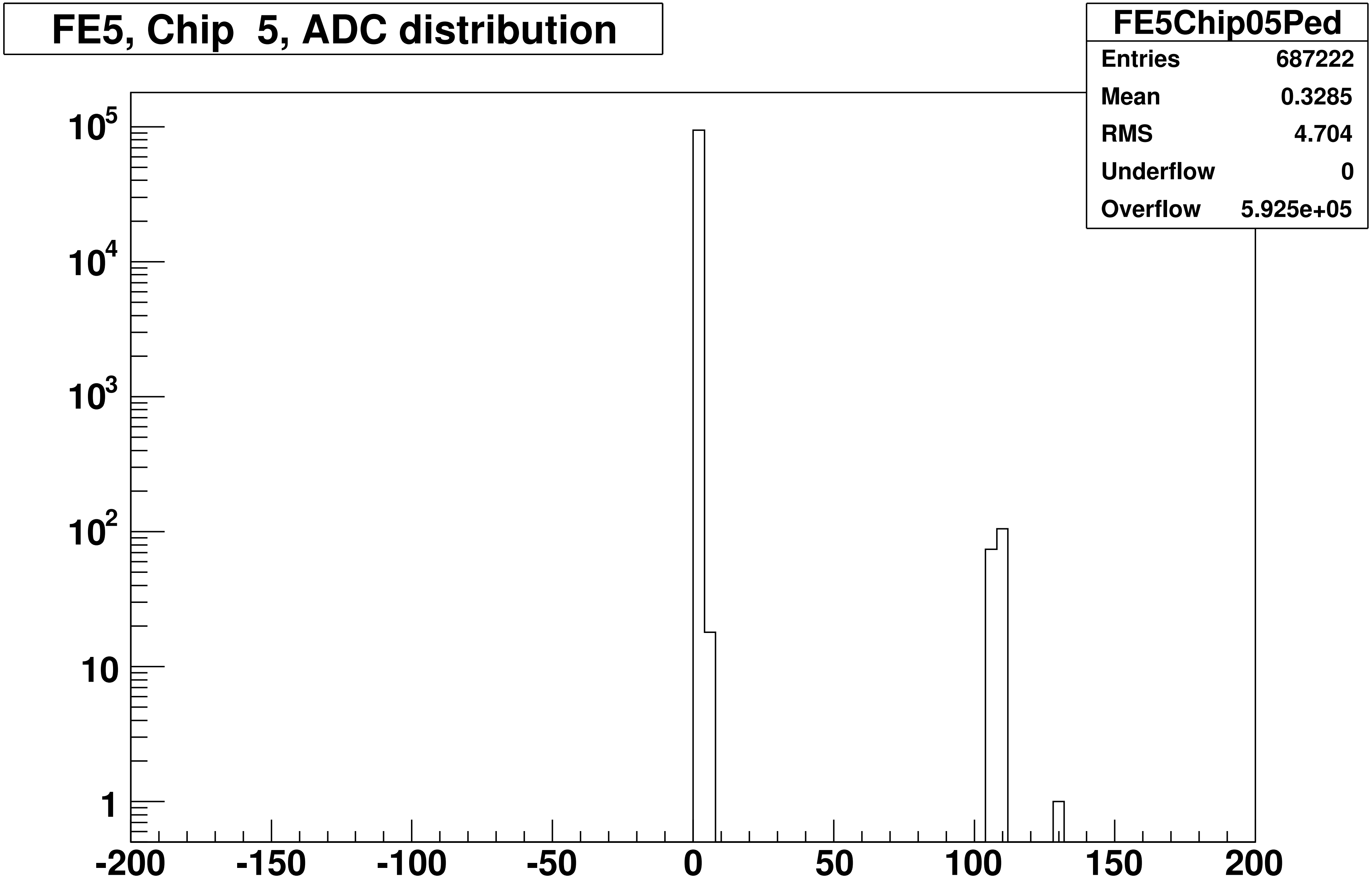
25000


FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 687222
Mean 0.3285
RMS 4.704
Underflow 0
Overflow 5.925e+05
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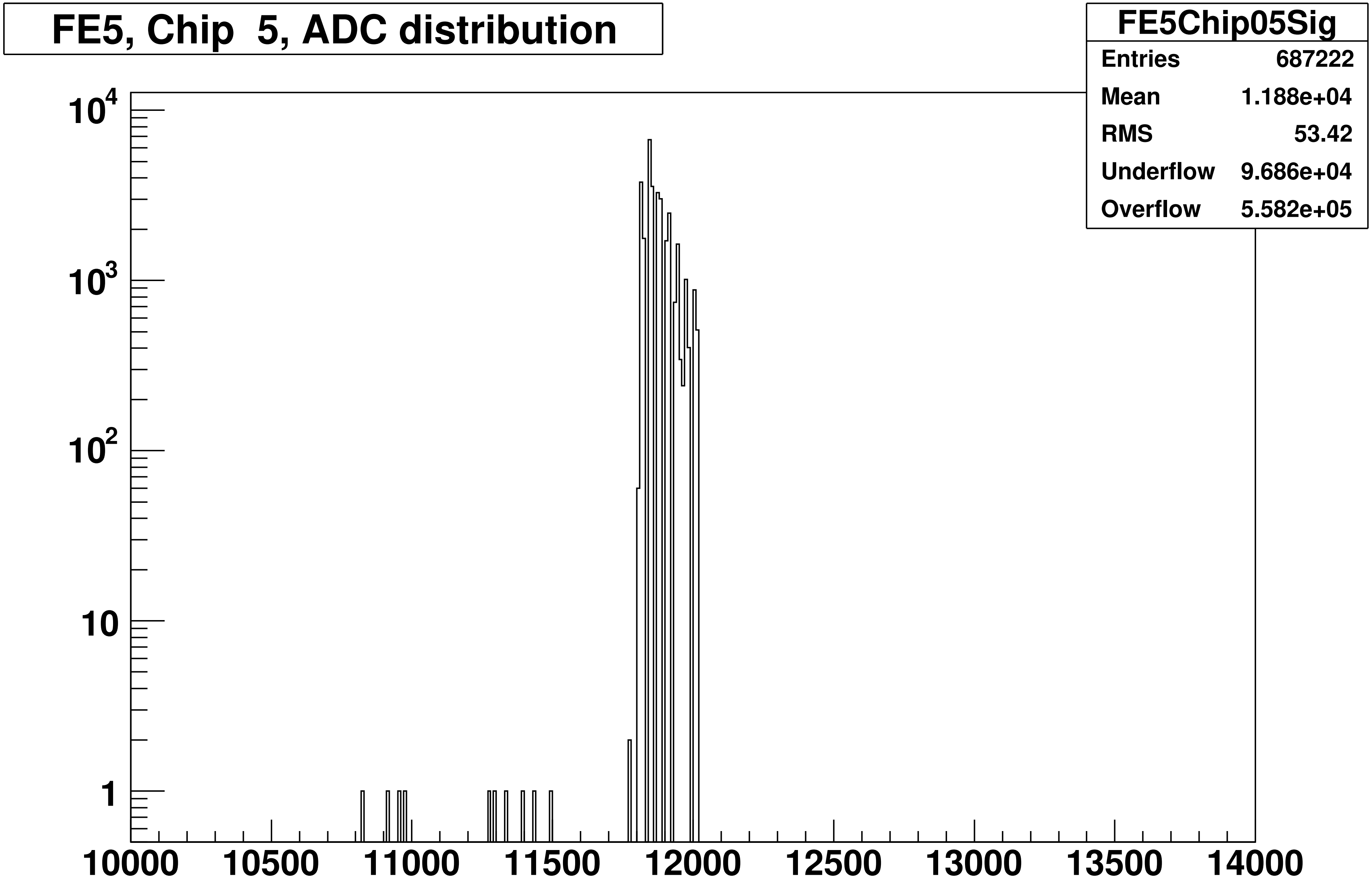
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 687222
Mean 1.188e+04
RMS 53.42
Underflow 9.686e+04
Overflow 5.582e+05
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10^2
10
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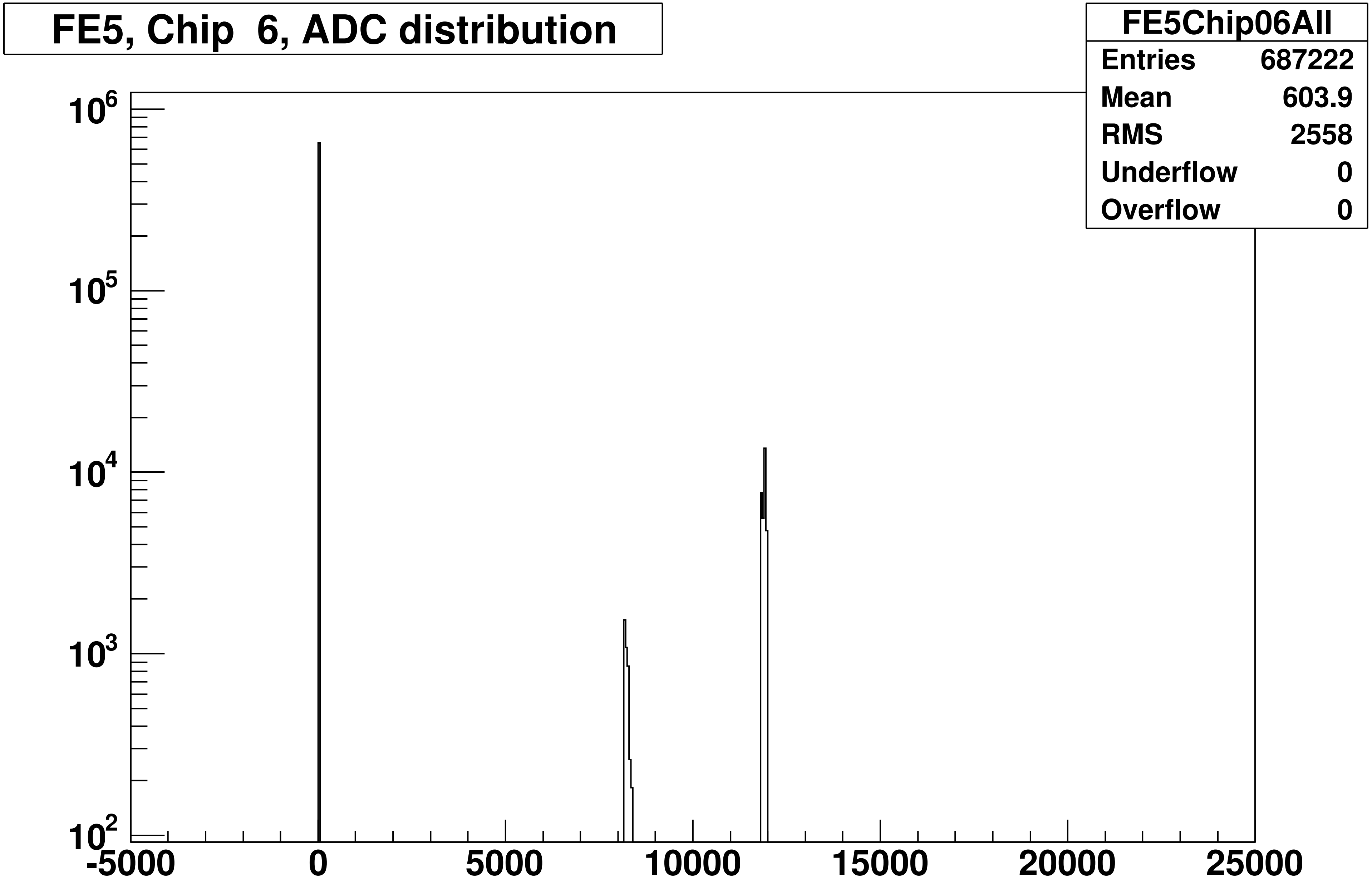

FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 687222
Mean 603.9
RMS 2558
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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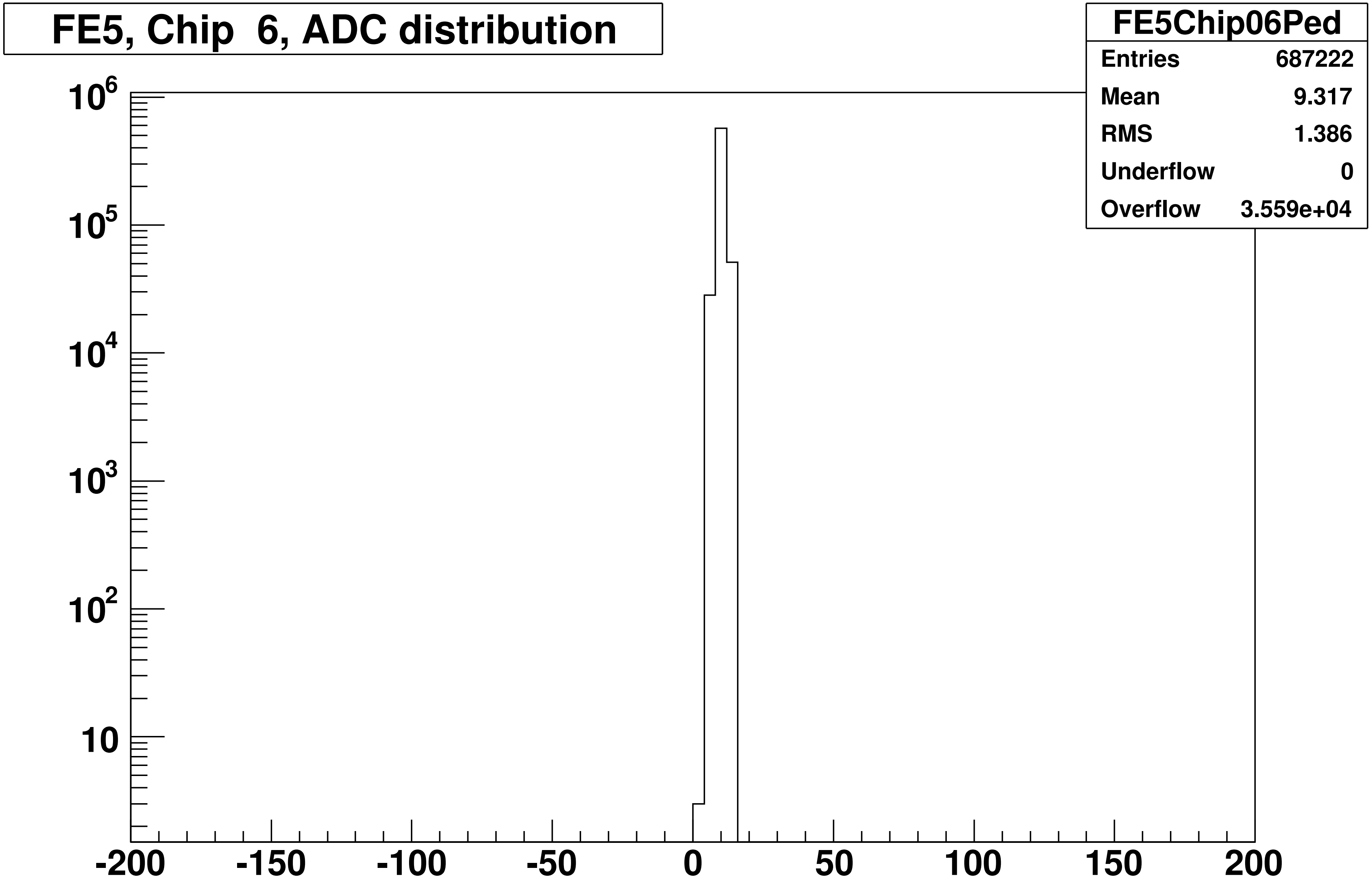
FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 687222
Mean 9.317
RMS 1.386
Underflow 0
Overflow 3.559e+04
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10^5
10^4
10^3
10^2
10
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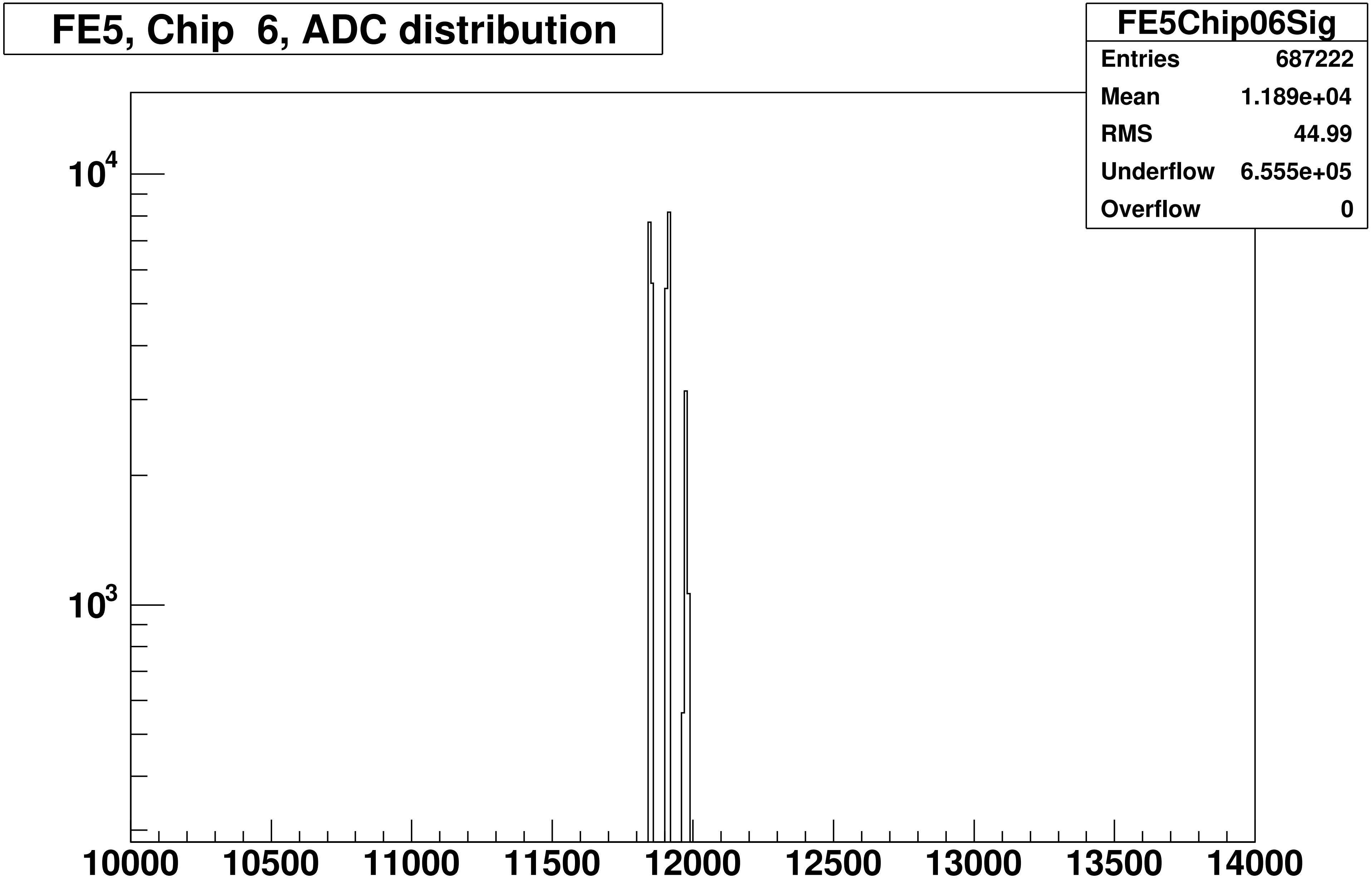
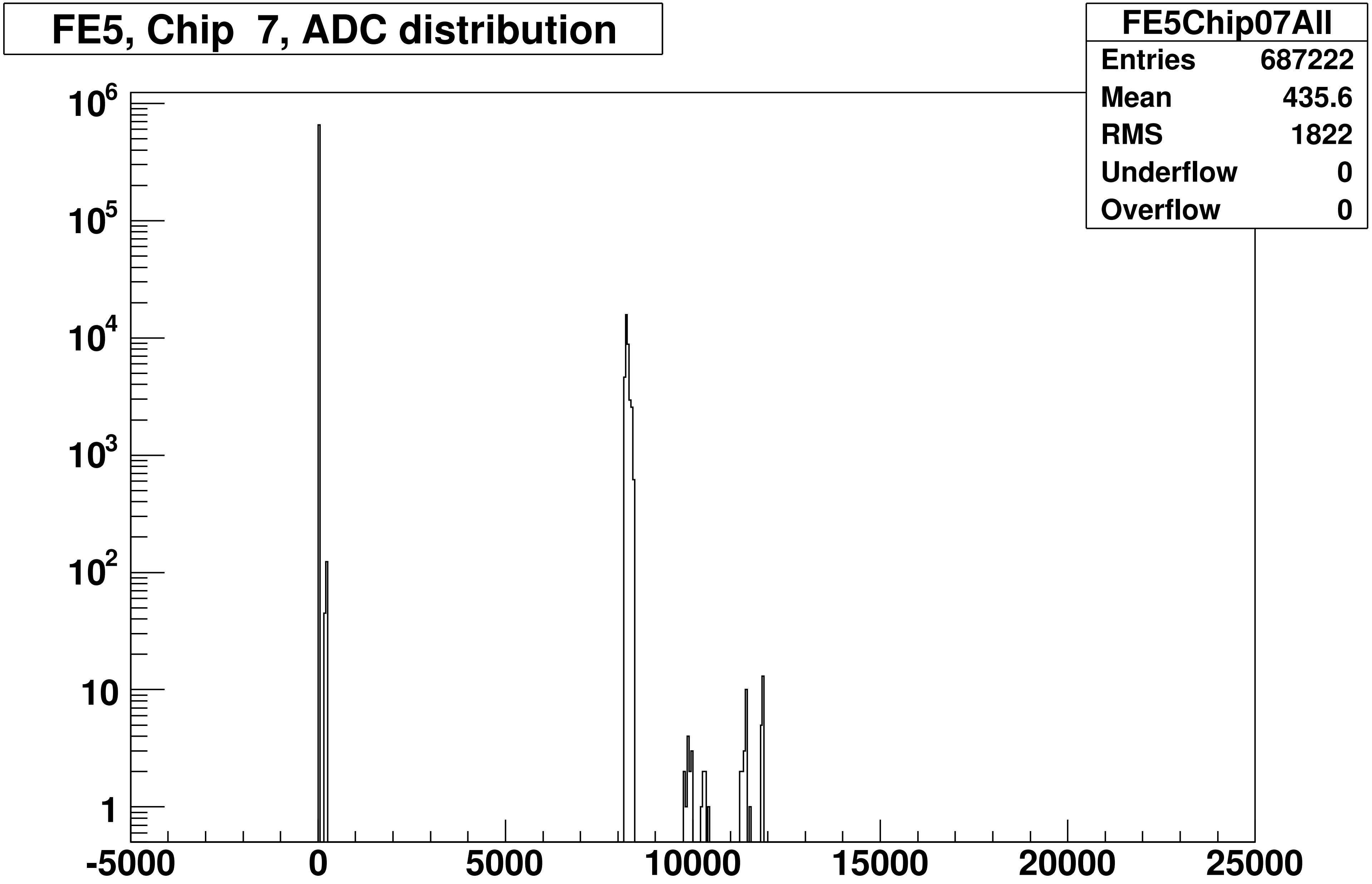
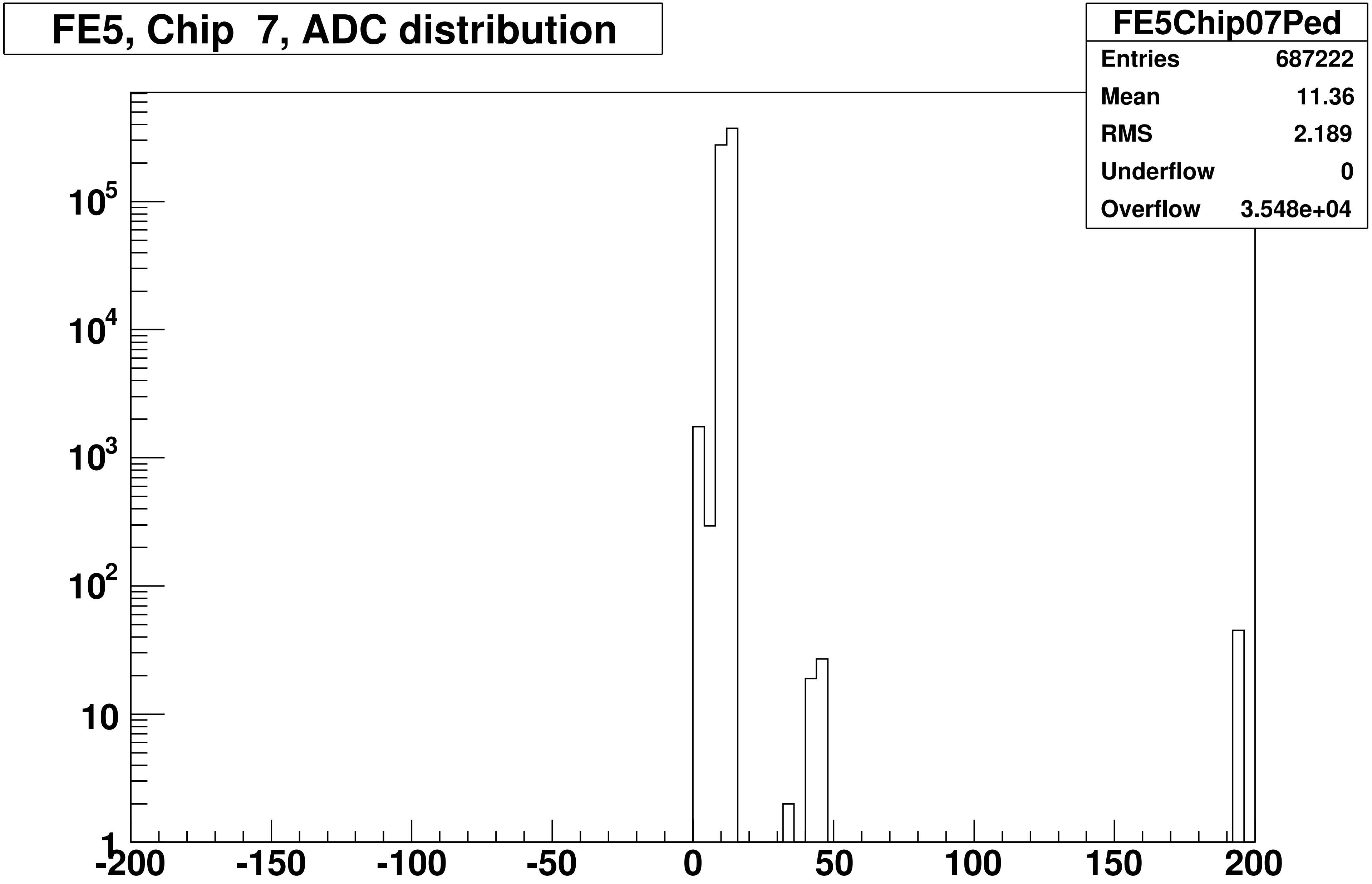
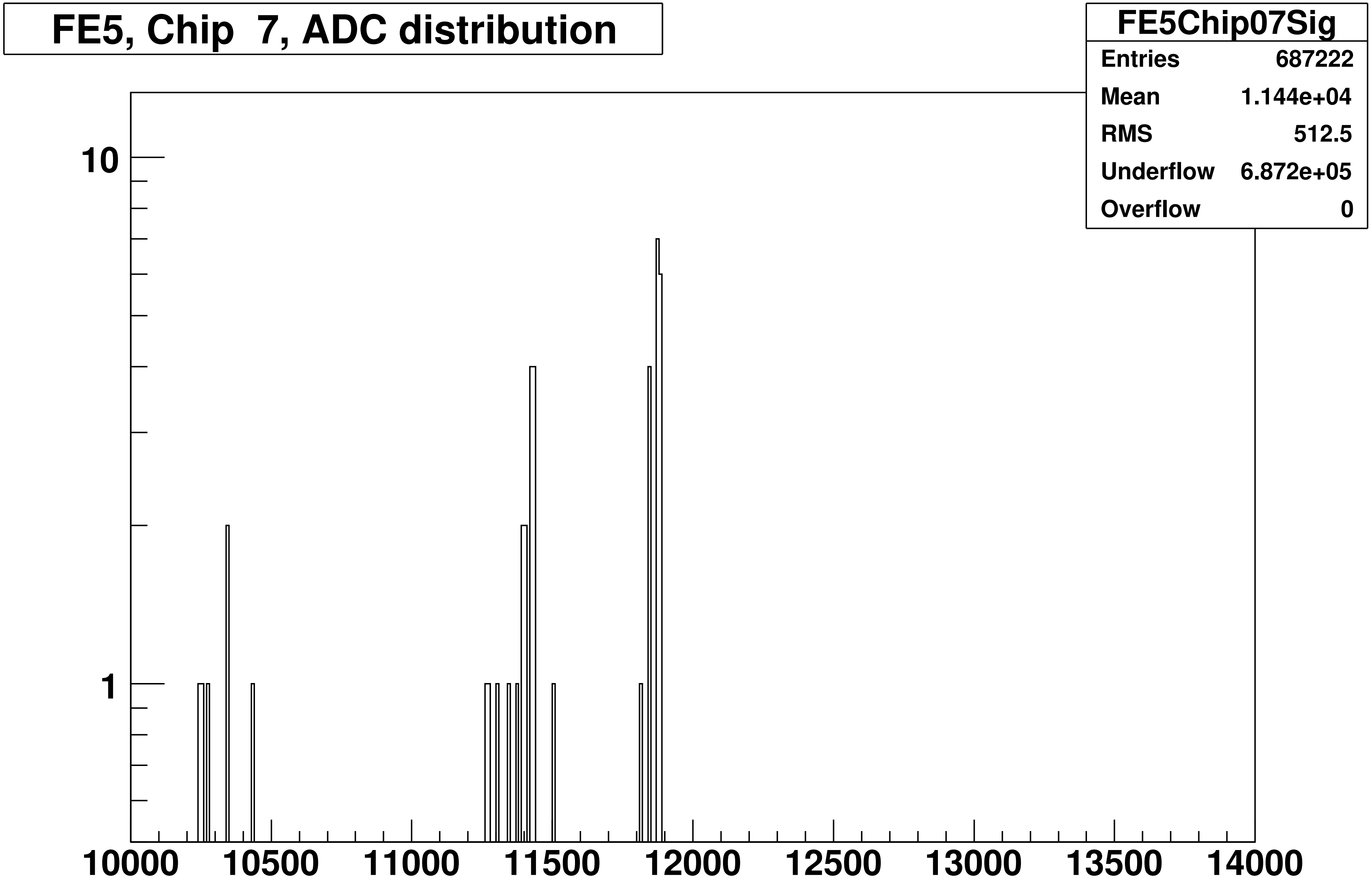
100
150
200


FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 687222
Mean 1.189e+04
RMS 44.99
Underflow 6.555e+05
Overflow 0
10^4
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 687222
Mean 435.6
RMS 1822
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 687222
Mean 11.36
RMS 2.189
Underflow 0
Overflow 3.548e+04
1
10
10^2
10^3
10^4
10^5
-200
-150
-100
-50
0
50
100
150
200



FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 687222
Mean 1.144e+04
RMS 512.5
Underflow 6.872e+05
Overflow 0
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1
10000
10500
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11500
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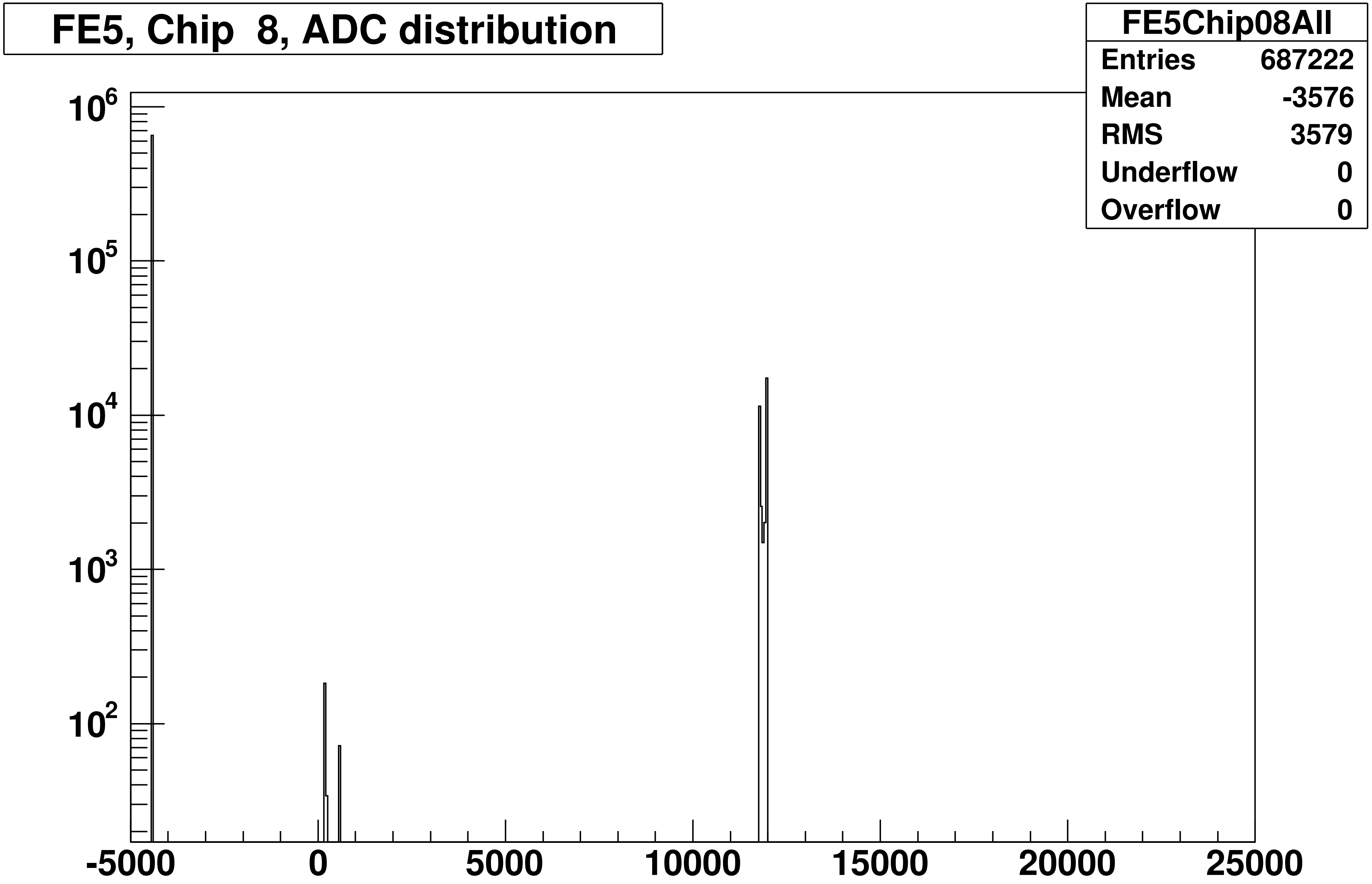
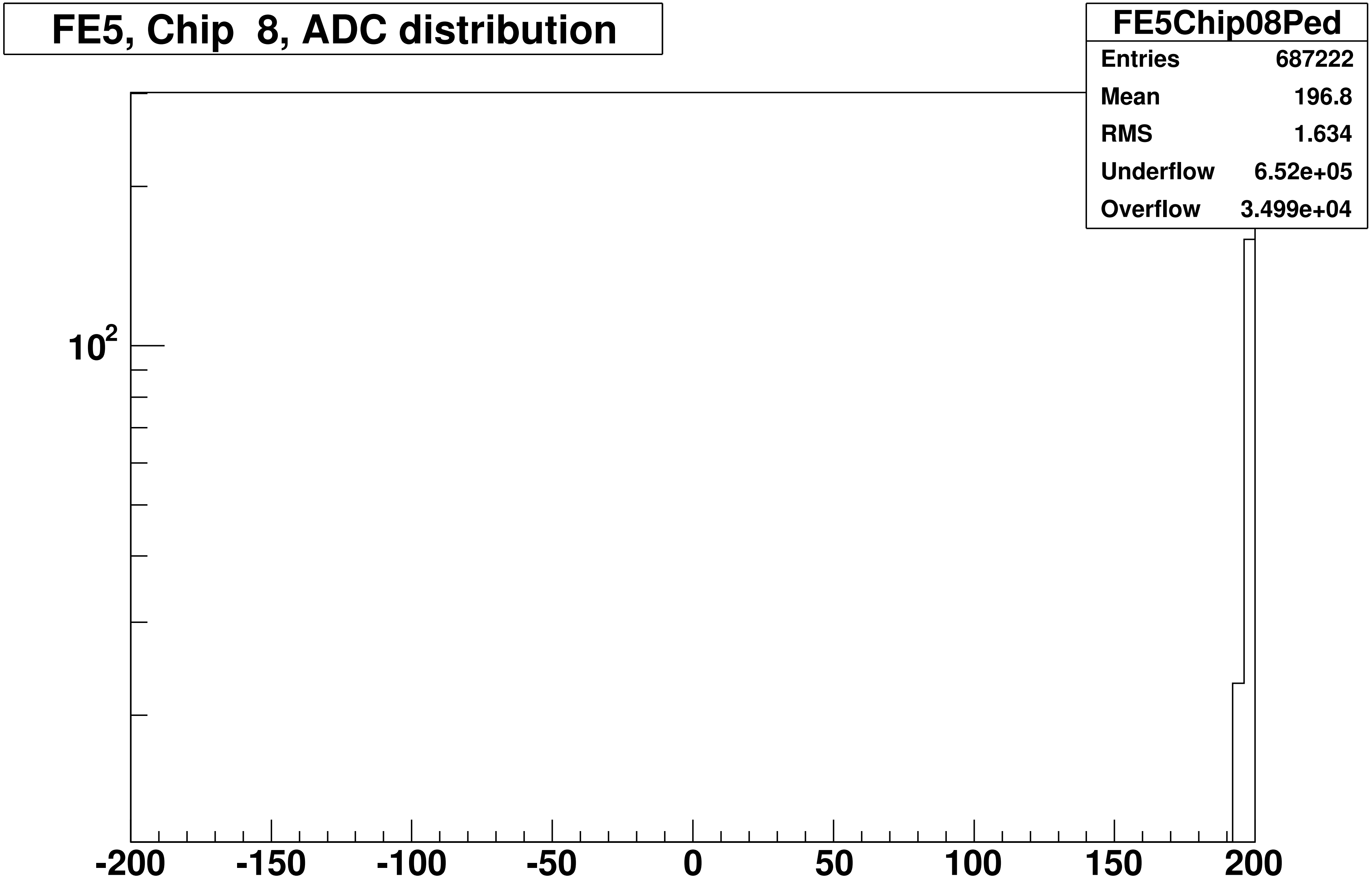
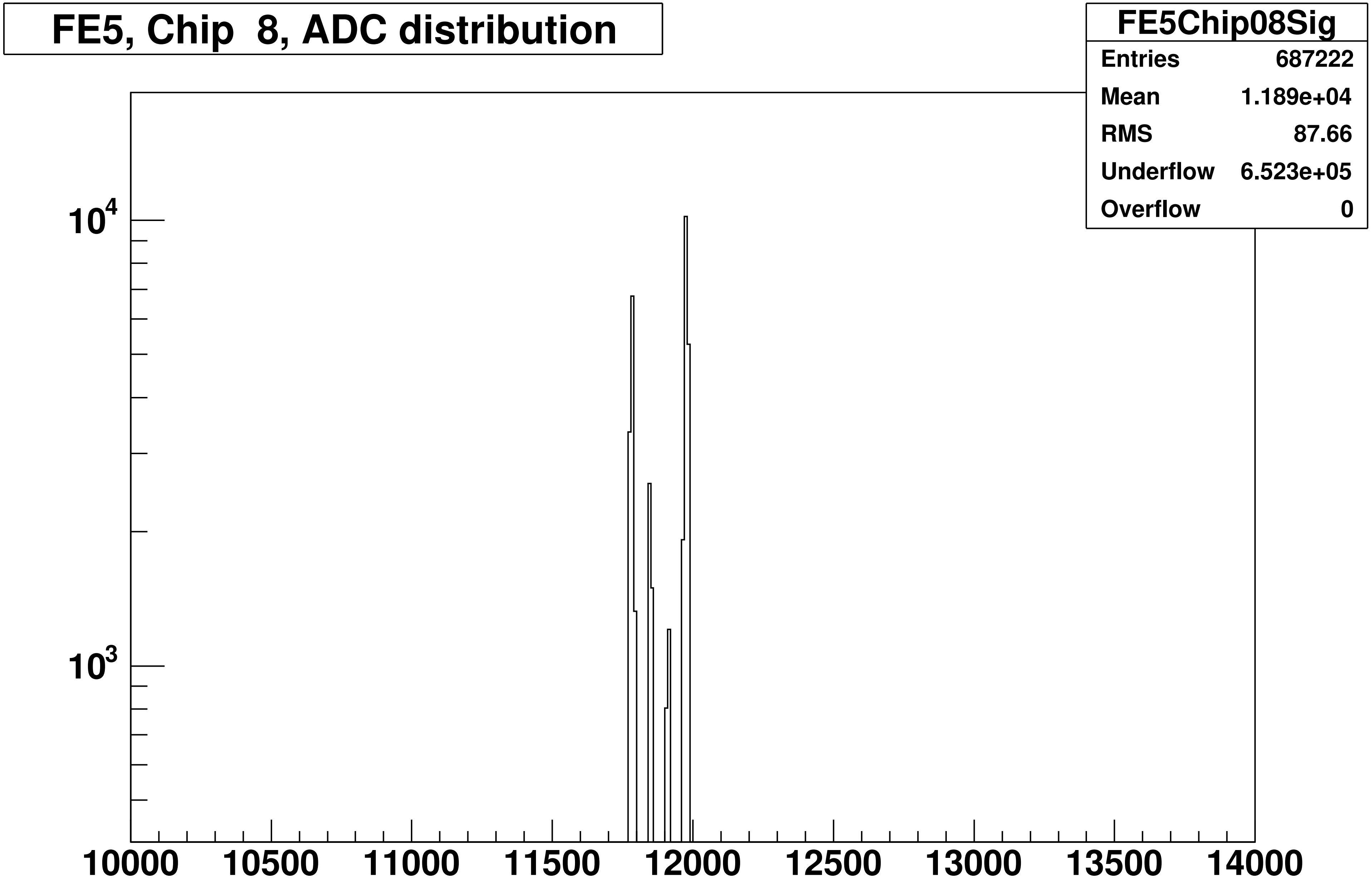
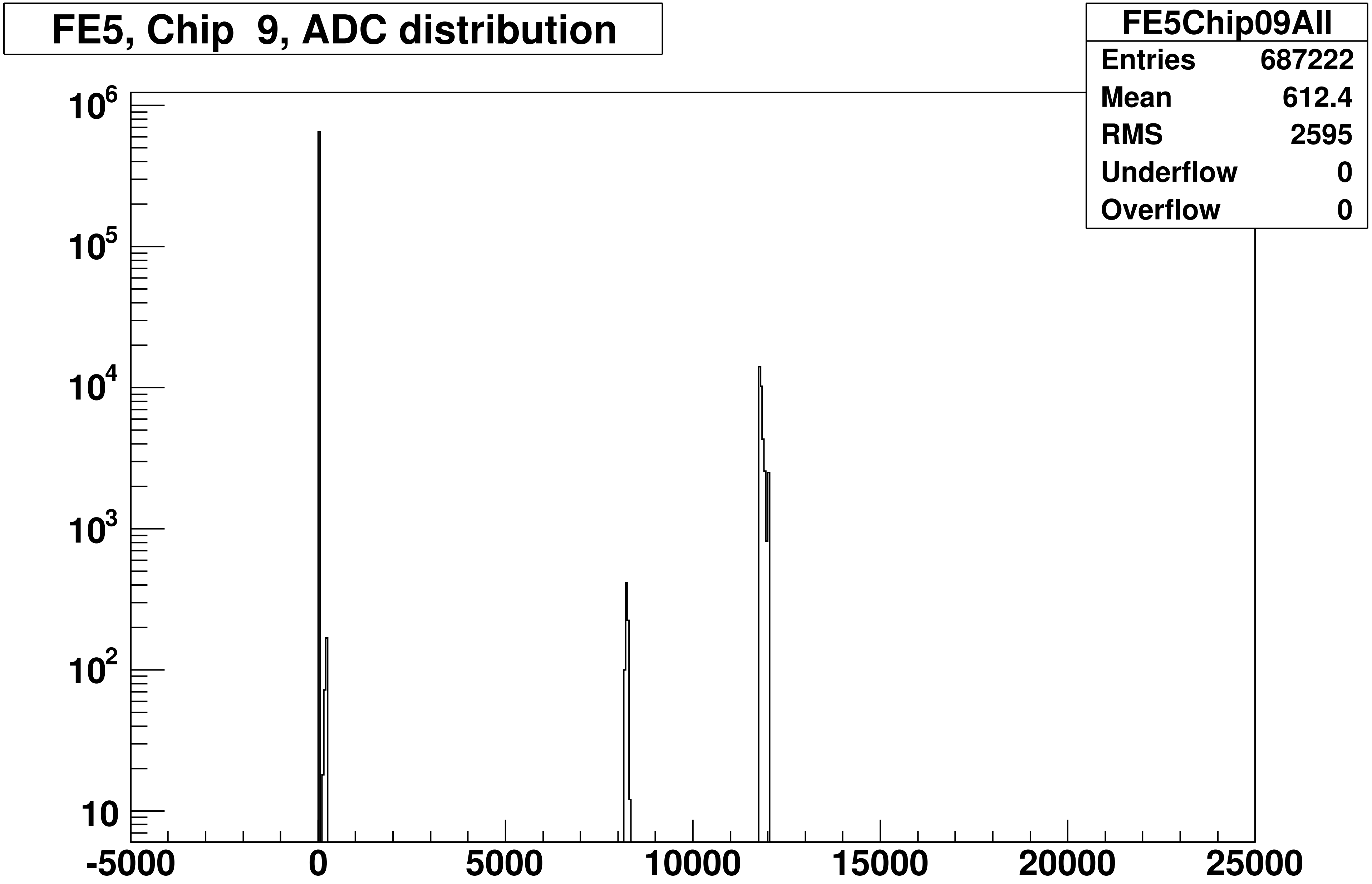
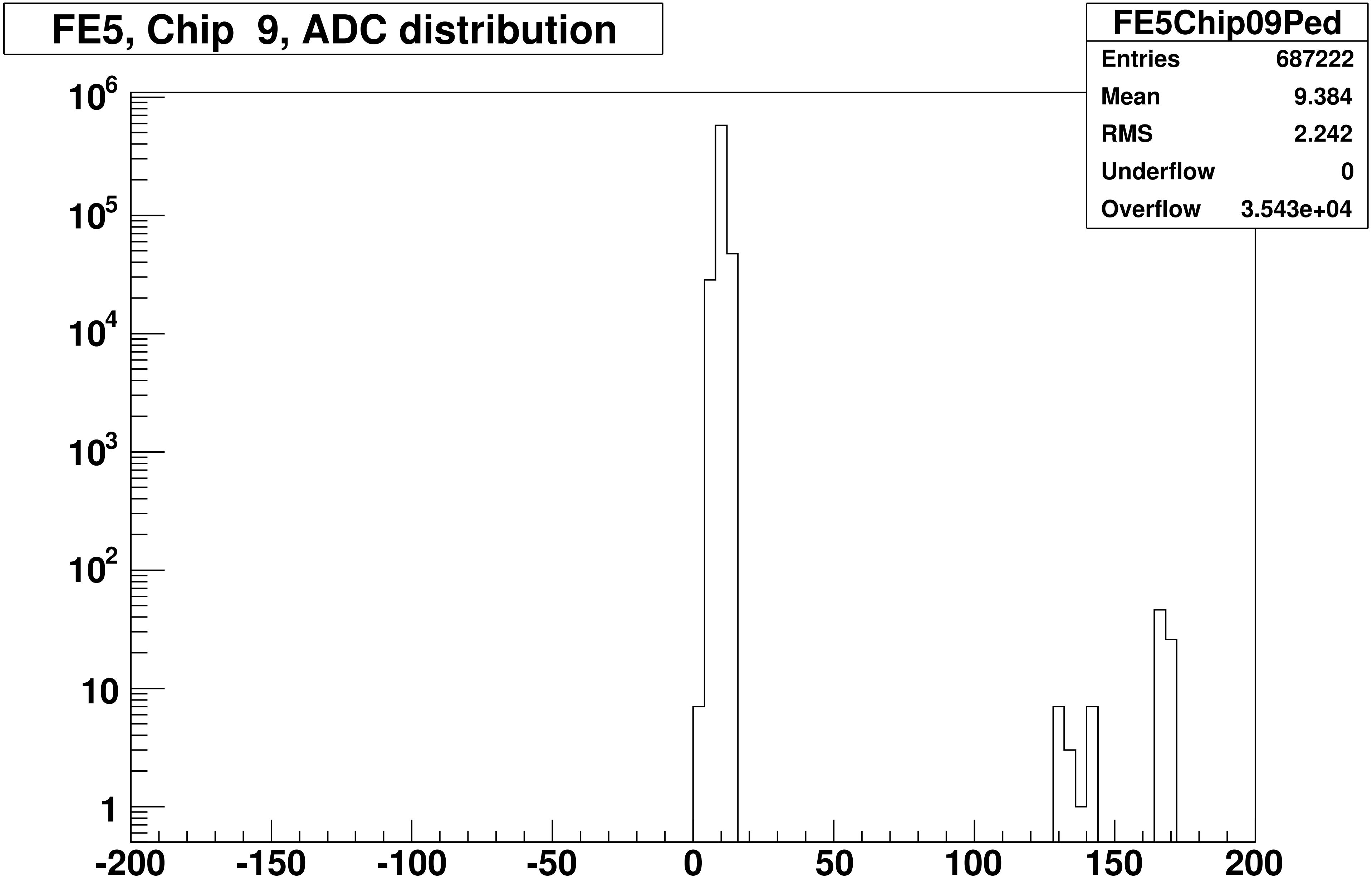
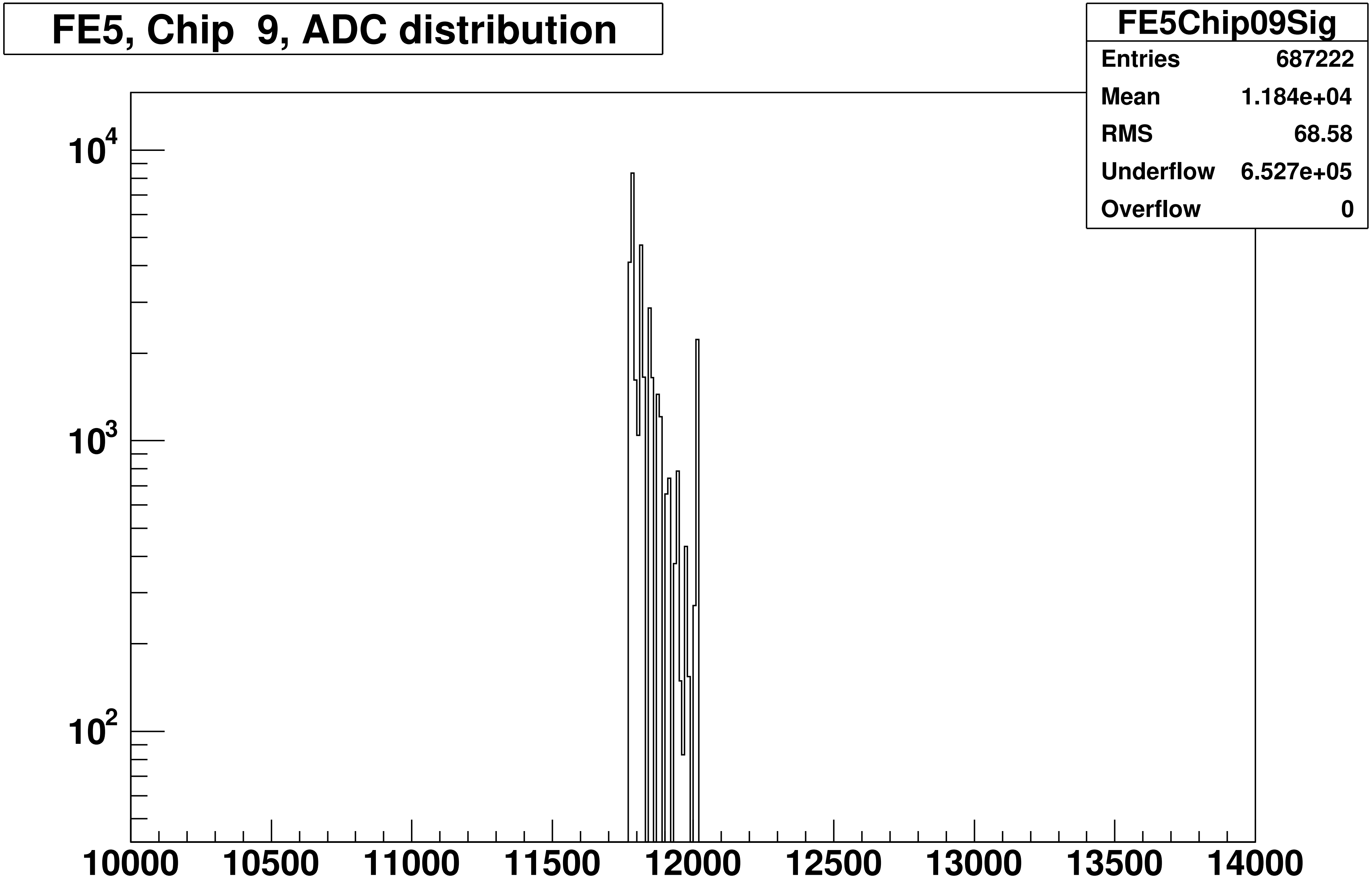

FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 687222
Mean -3576
RMS 3579
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

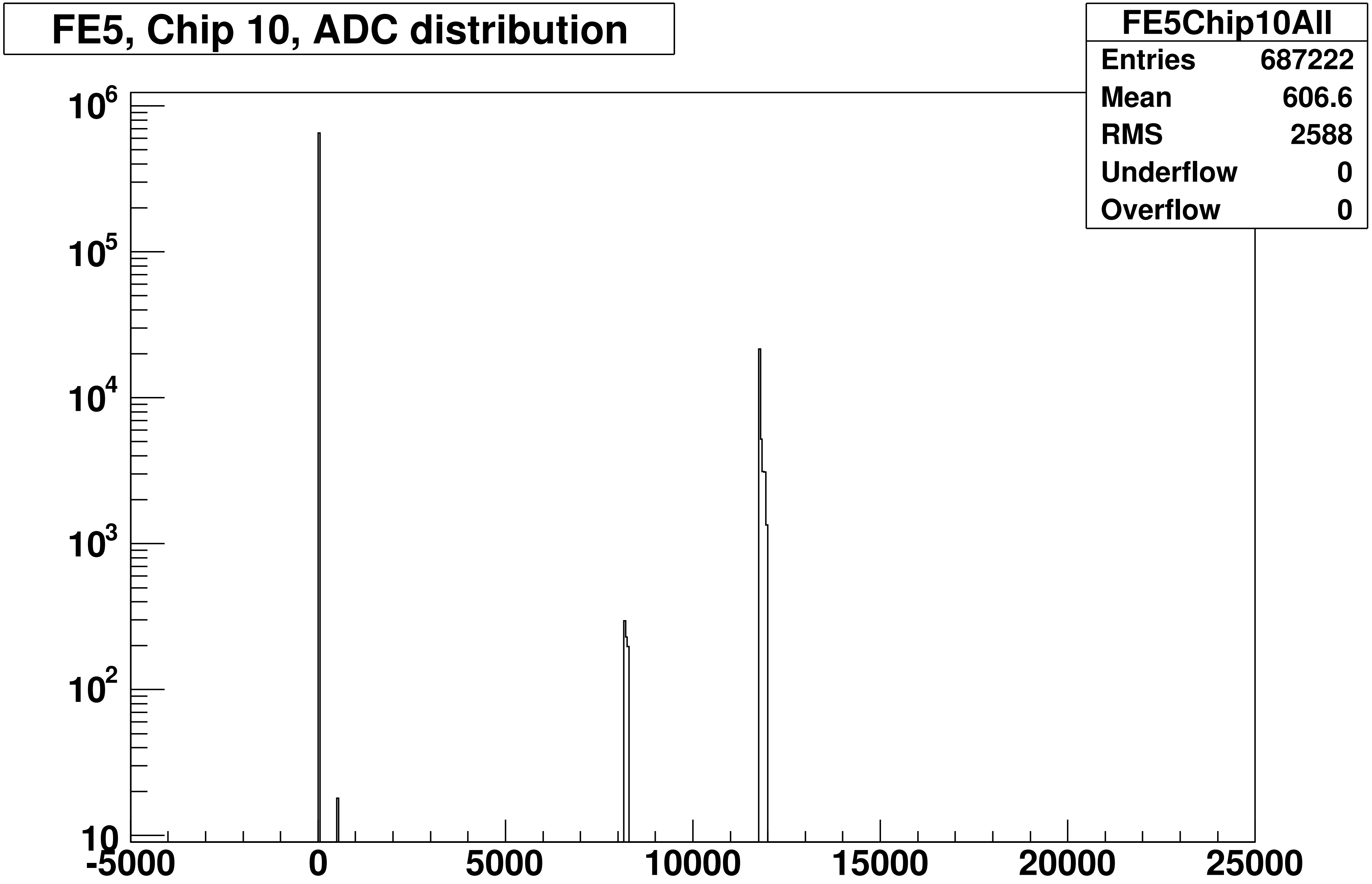

FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 687222
Mean 196.8
RMS 1.634
Underflow 6.52e+05
Overflow 3.499e+04
10^2
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 687222
Mean 1.189e+04
RMS 87.66
Underflow 6.523e+05
Overflow 0
10^4
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000

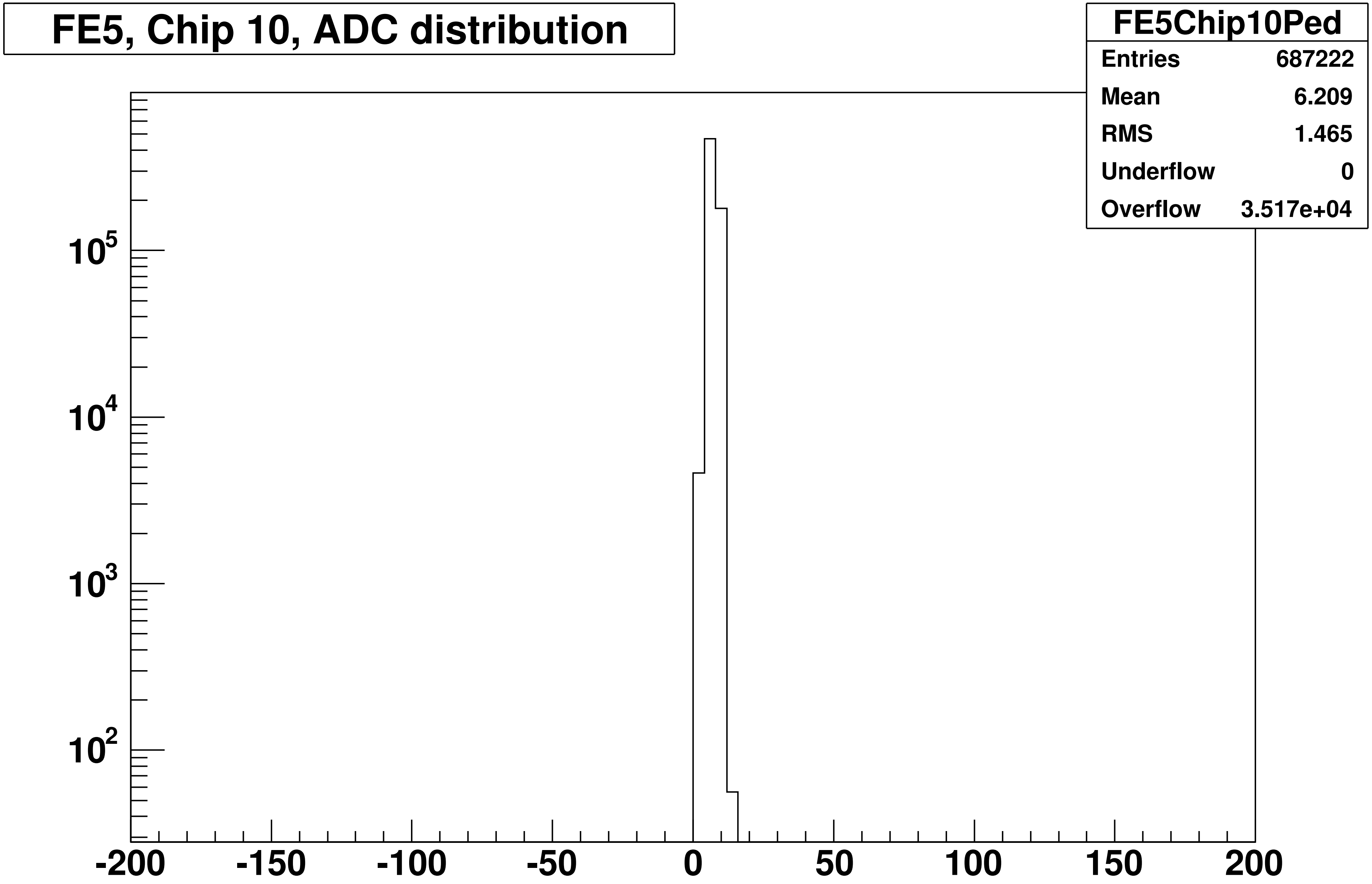

FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 687222
Mean 612.4
RMS 2595
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 687222
Mean 9.384
RMS 2.242
Underflow 0
Overflow 3.543e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

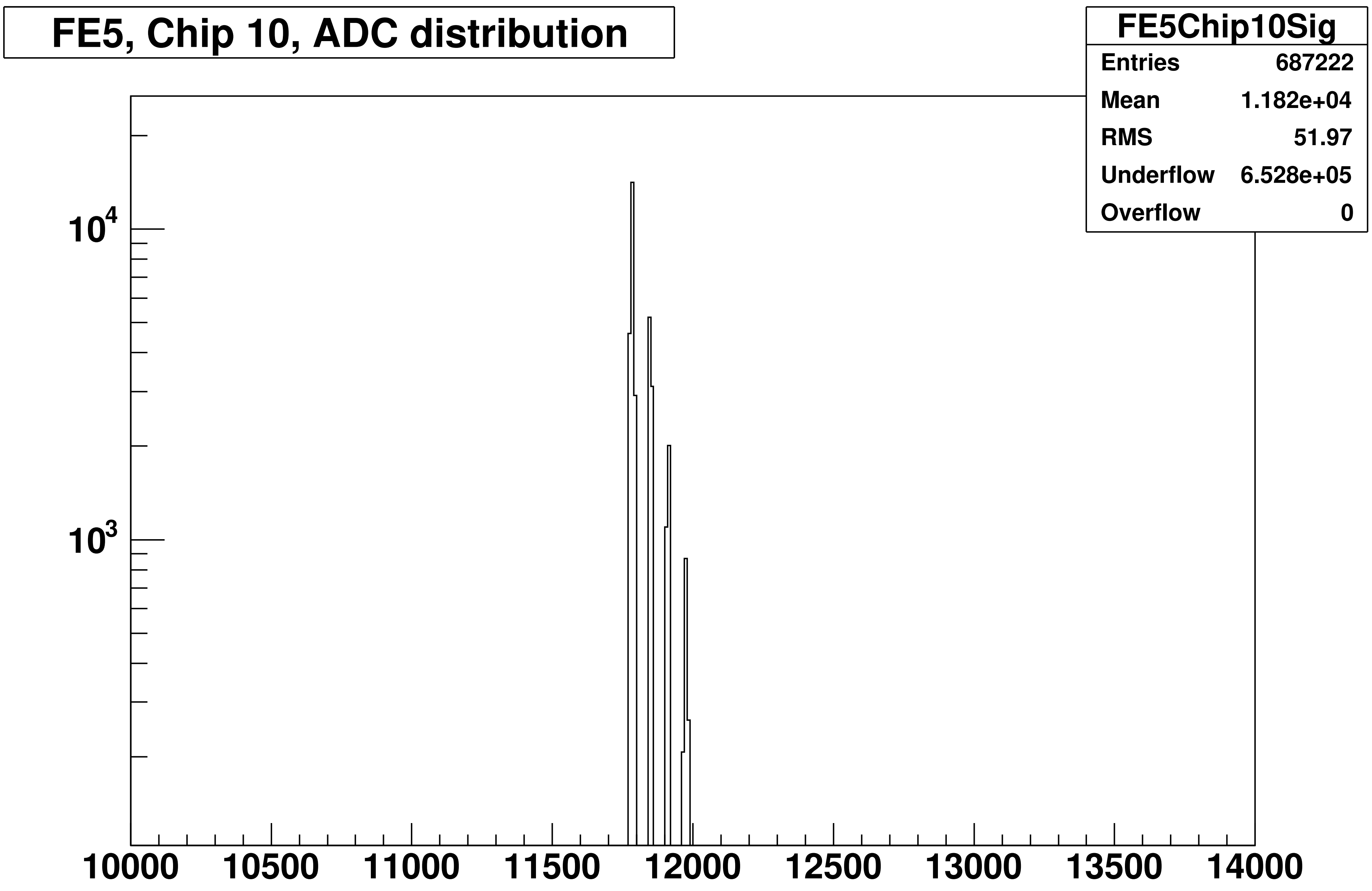

FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 687222
Mean 1.184e+04
RMS 68.58
Underflow 6.527e+05
Overflow 0
10^4
10^3
10^2
10000
10500
11000
11500
12000
12500
13000
13500
14000

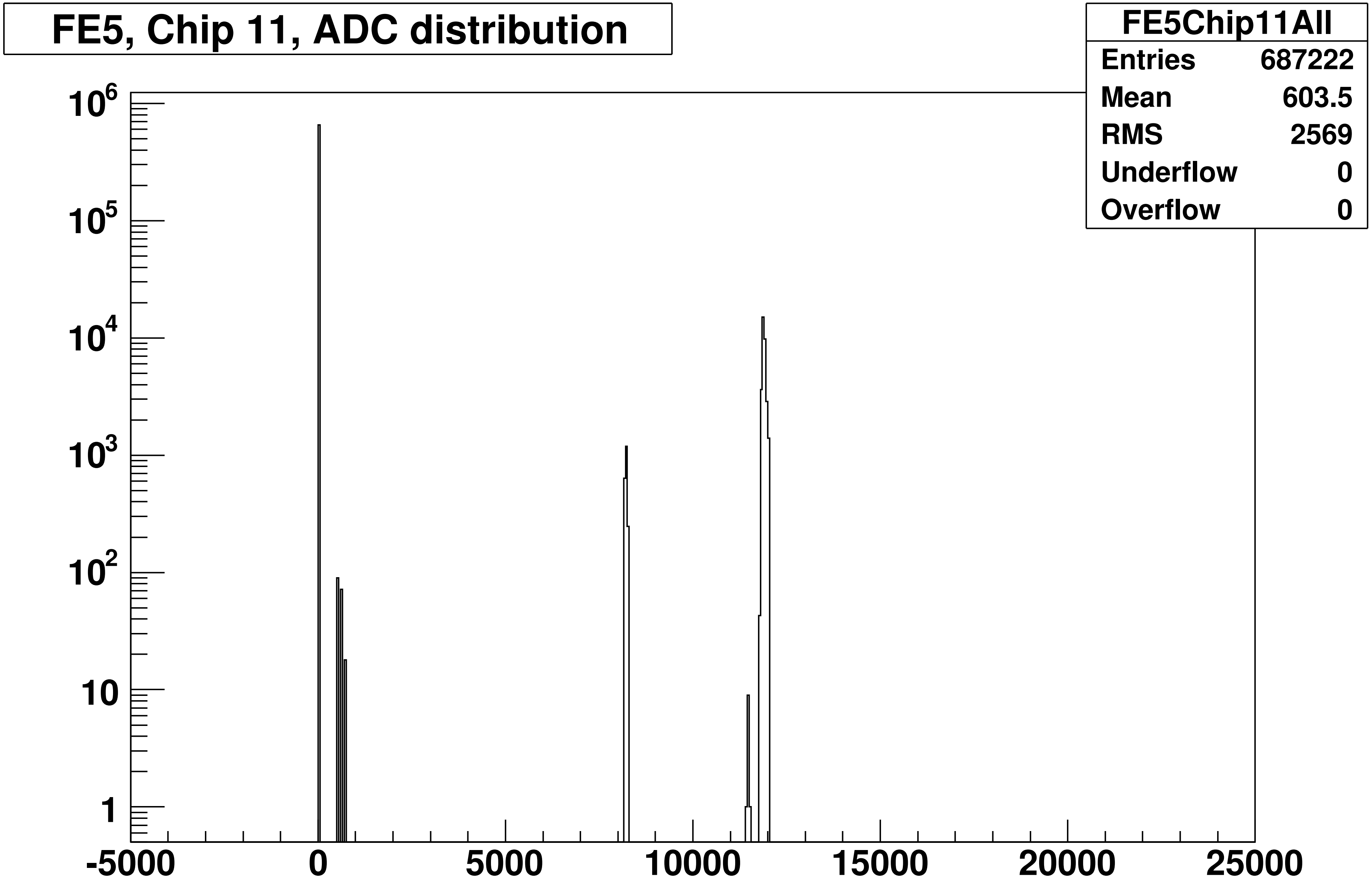


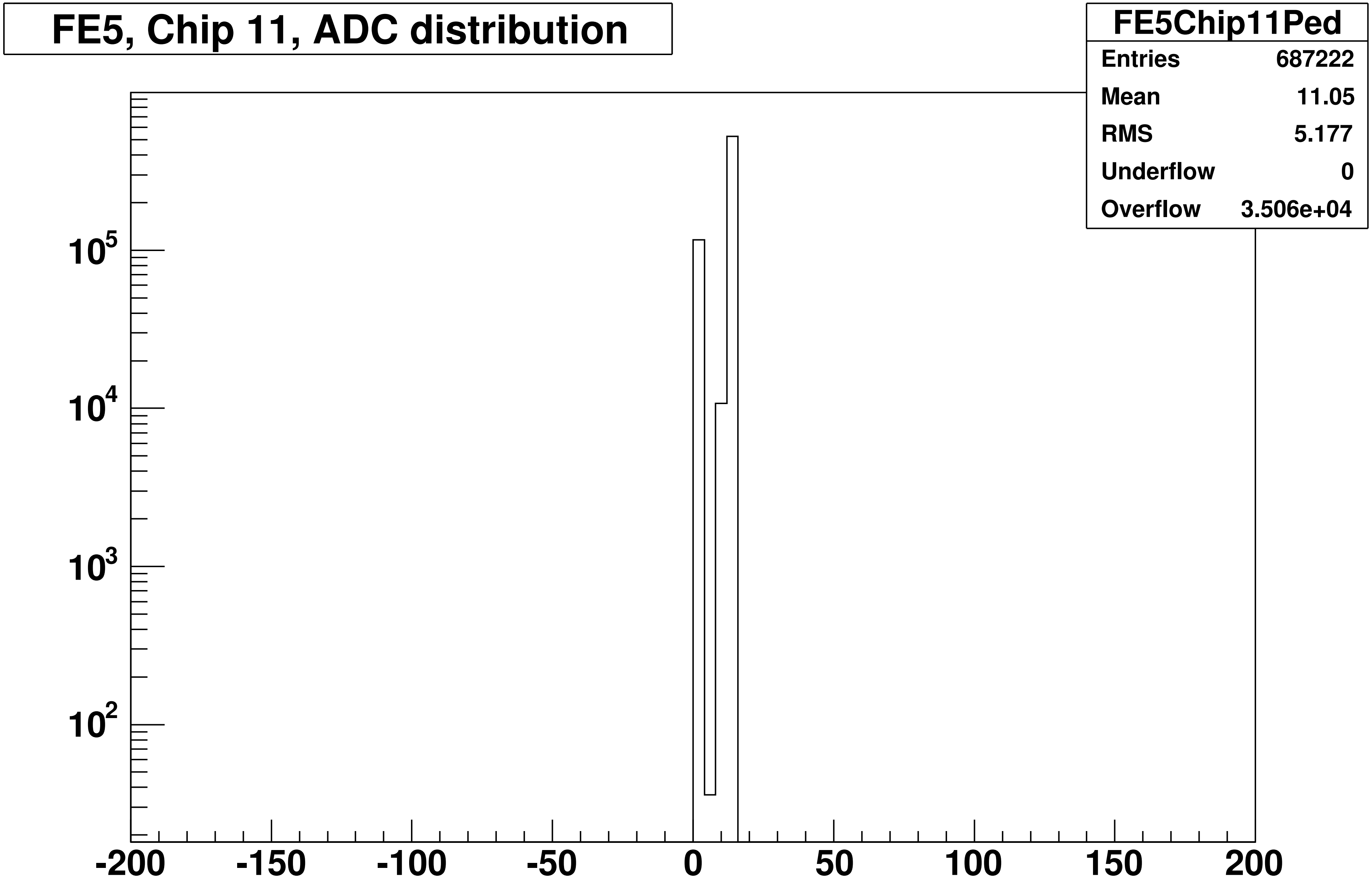
FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 687222
Mean 606.6
RMS 2588
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000



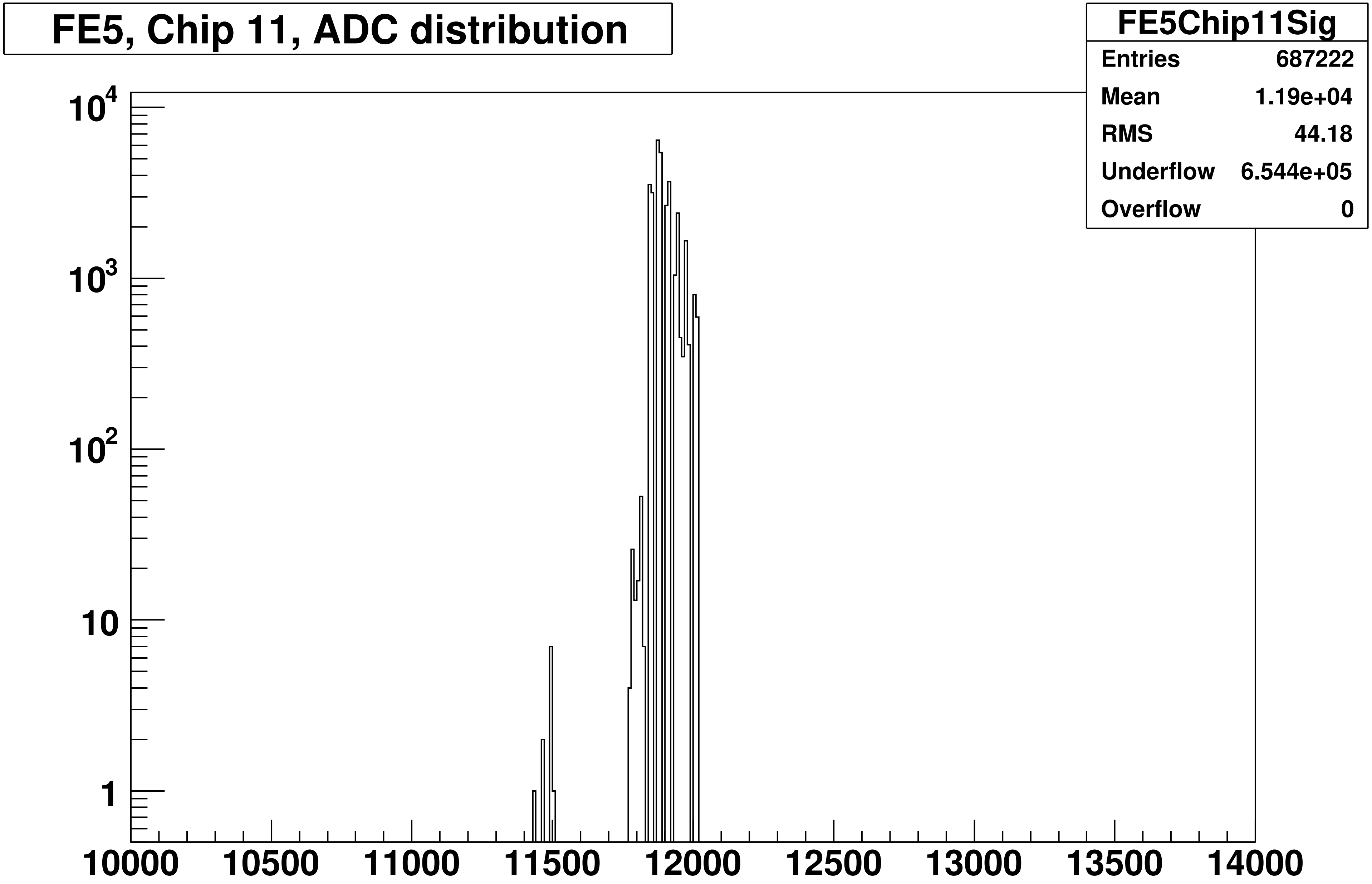
FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 687222
Mean 6.209
RMS 1.465
Underflow 0
Overflow 3.517e+04
10^5
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200


FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 687222
Mean 1.182e+04
RMS 51.97
Underflow 6.528e+05
Overflow 0
10^4
10^3
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 687222
Mean 603.5
RMS 2569
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 687222
Mean 11.05
RMS 5.177
Underflow 0
Overflow 3.506e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 687222
Mean 1.19e+04
RMS 44.18
Underflow 6.544e+05
Overflow 0
10^4
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
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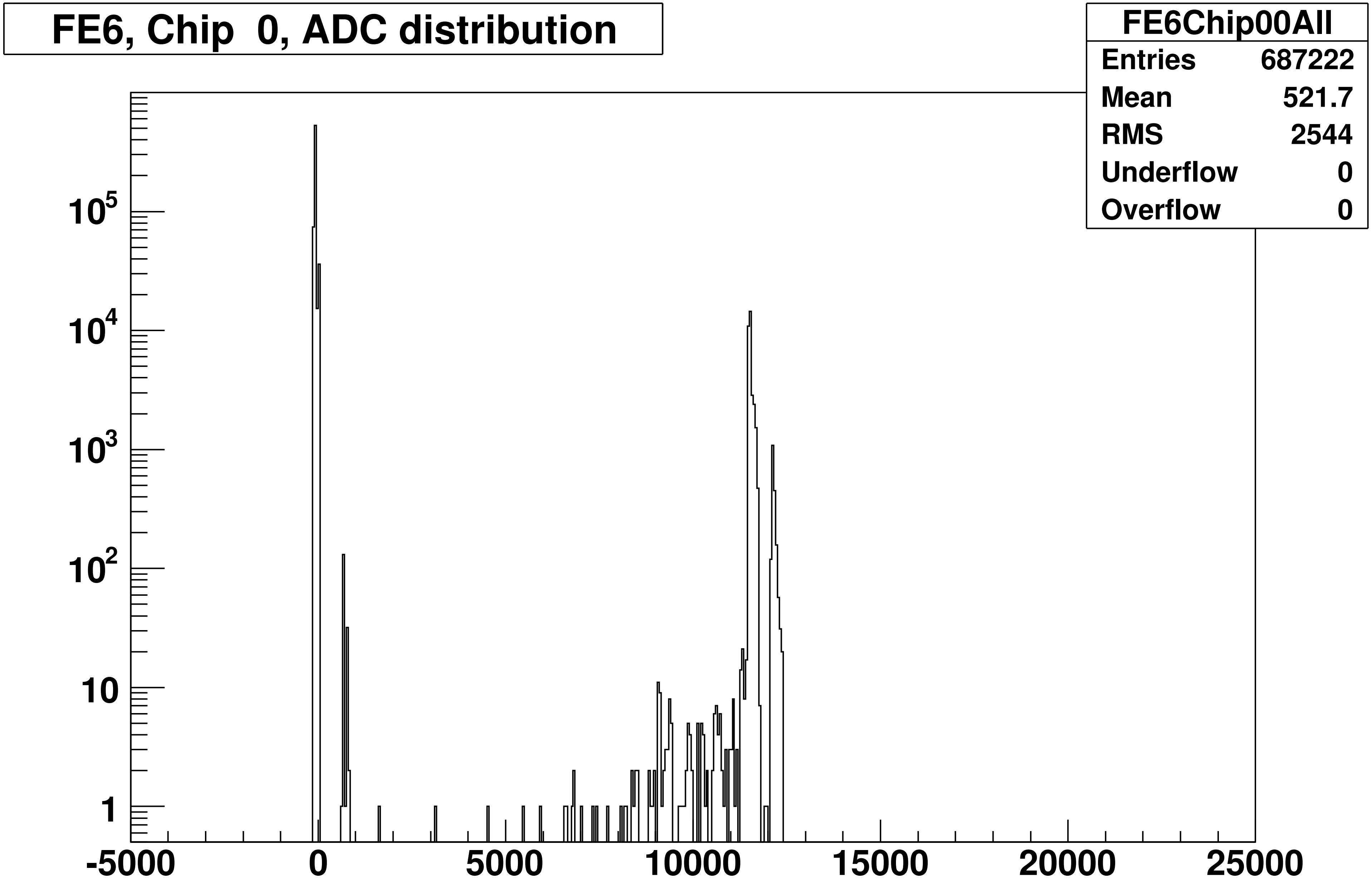
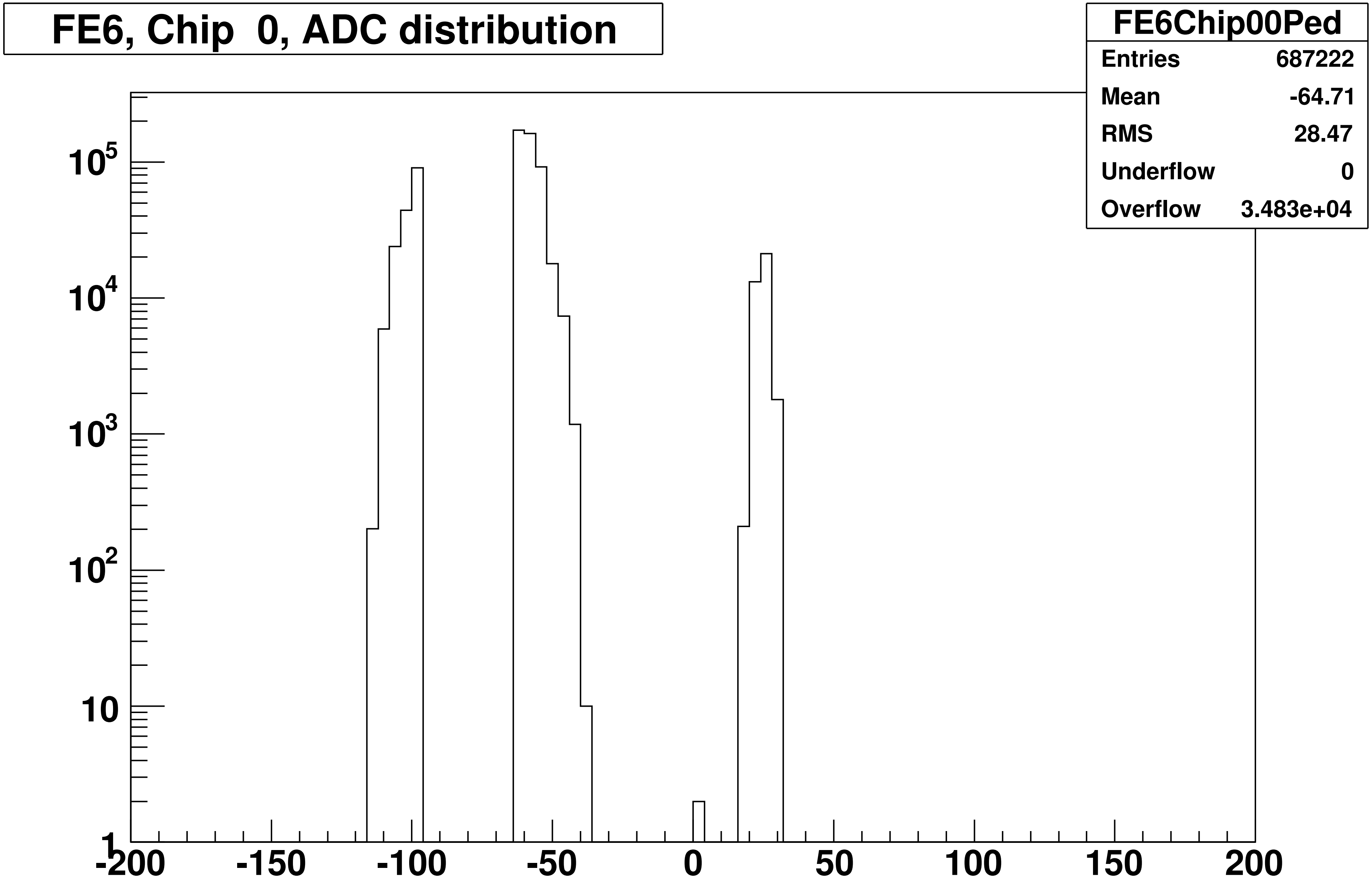
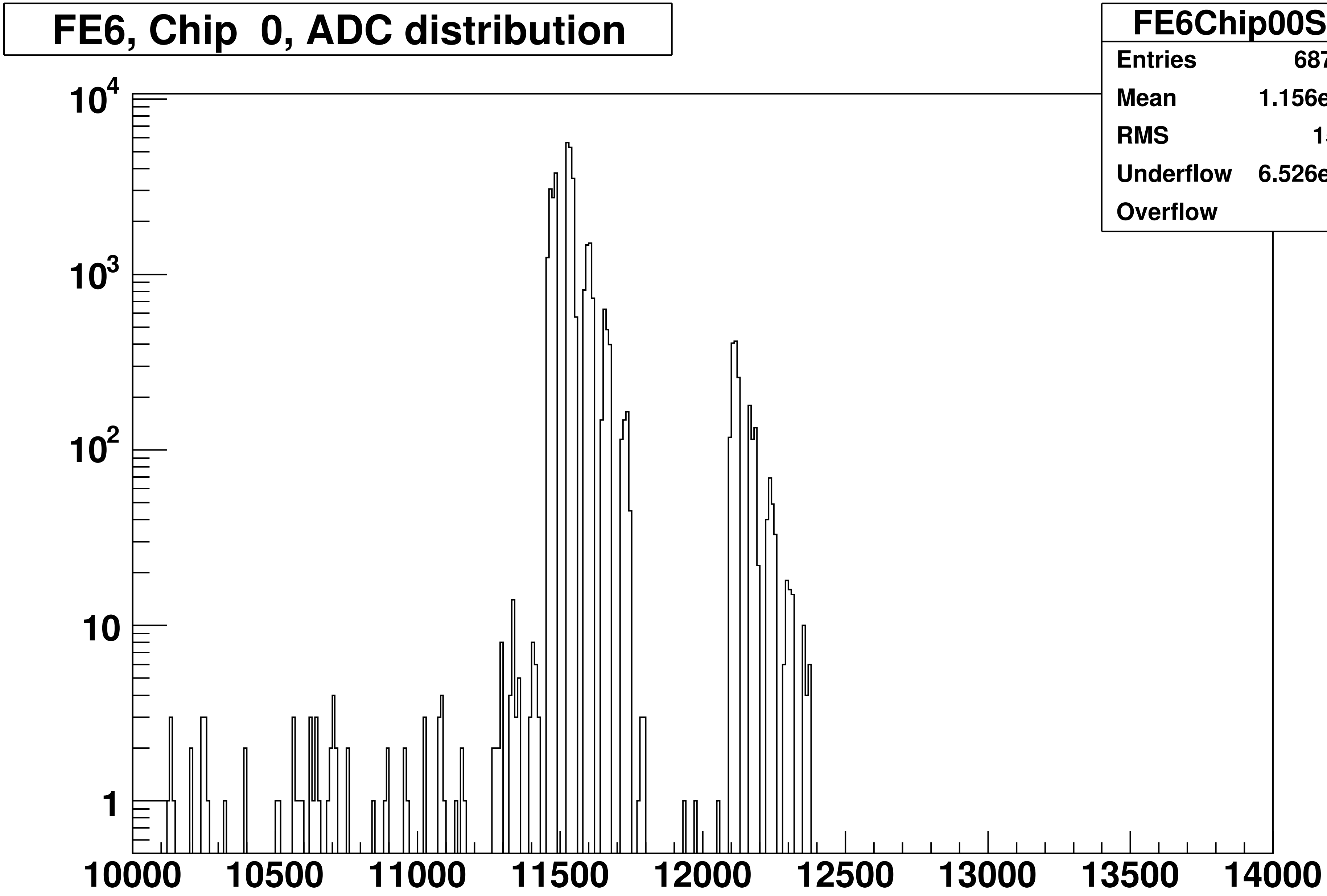
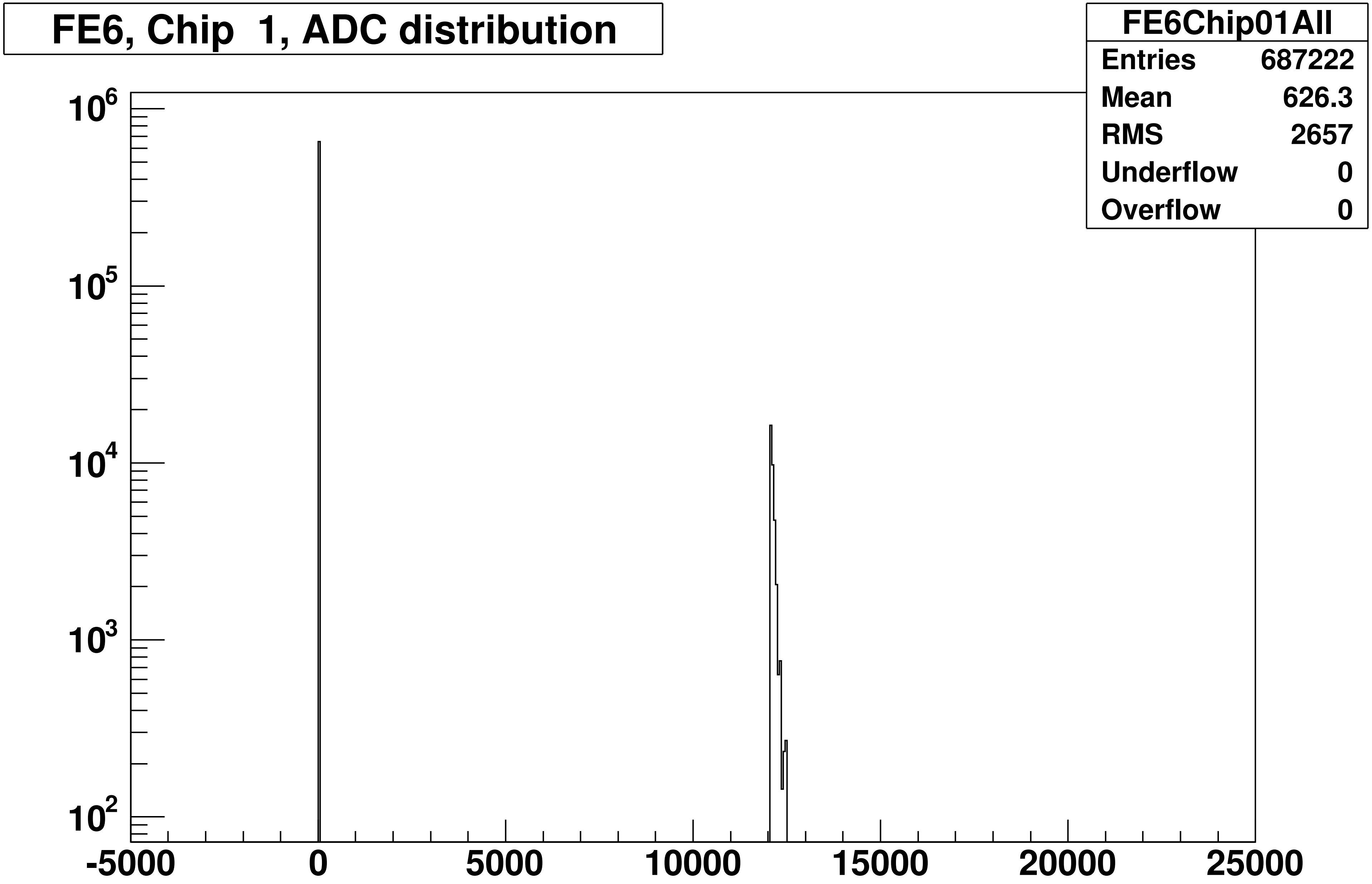
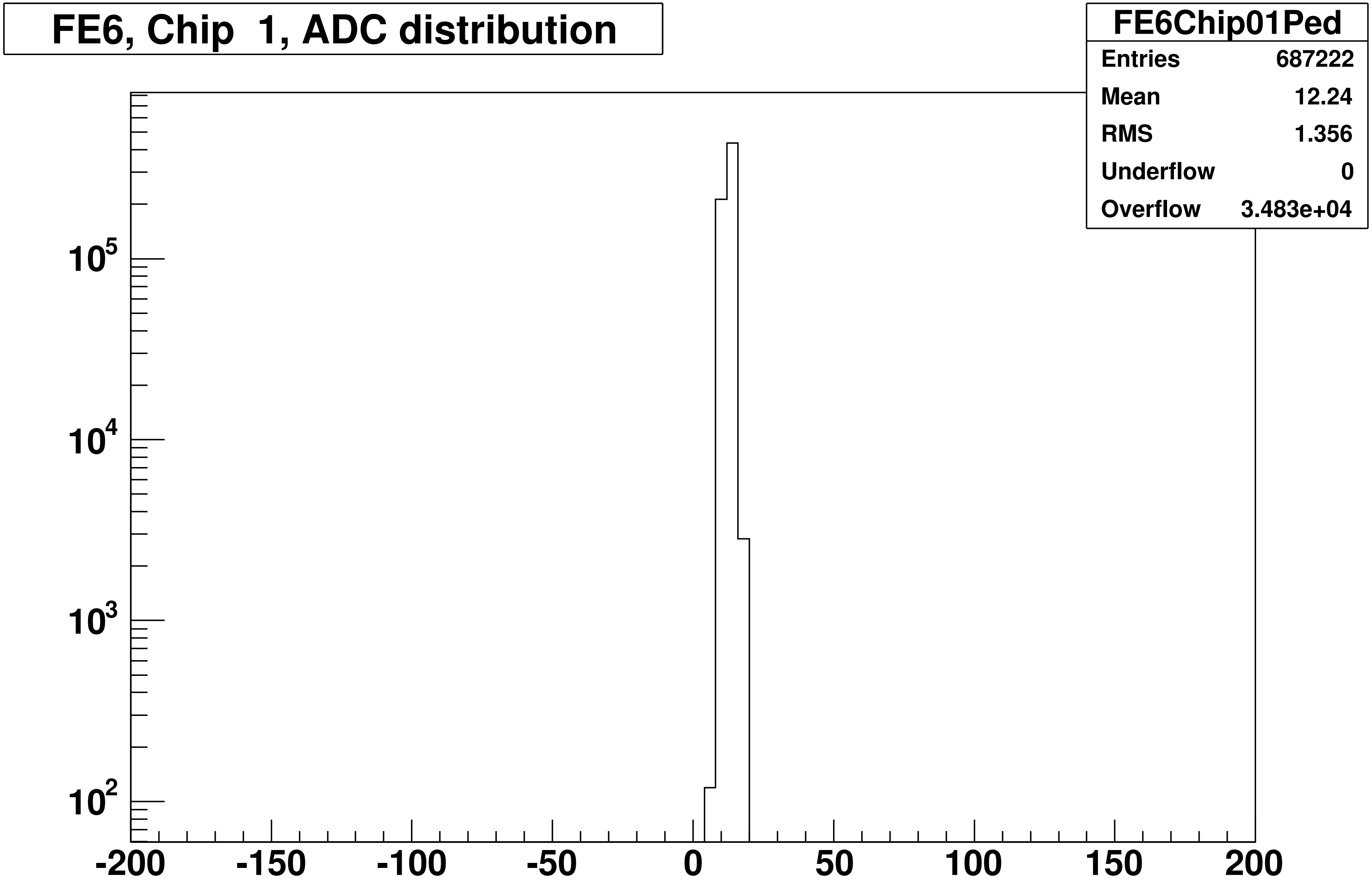
14000

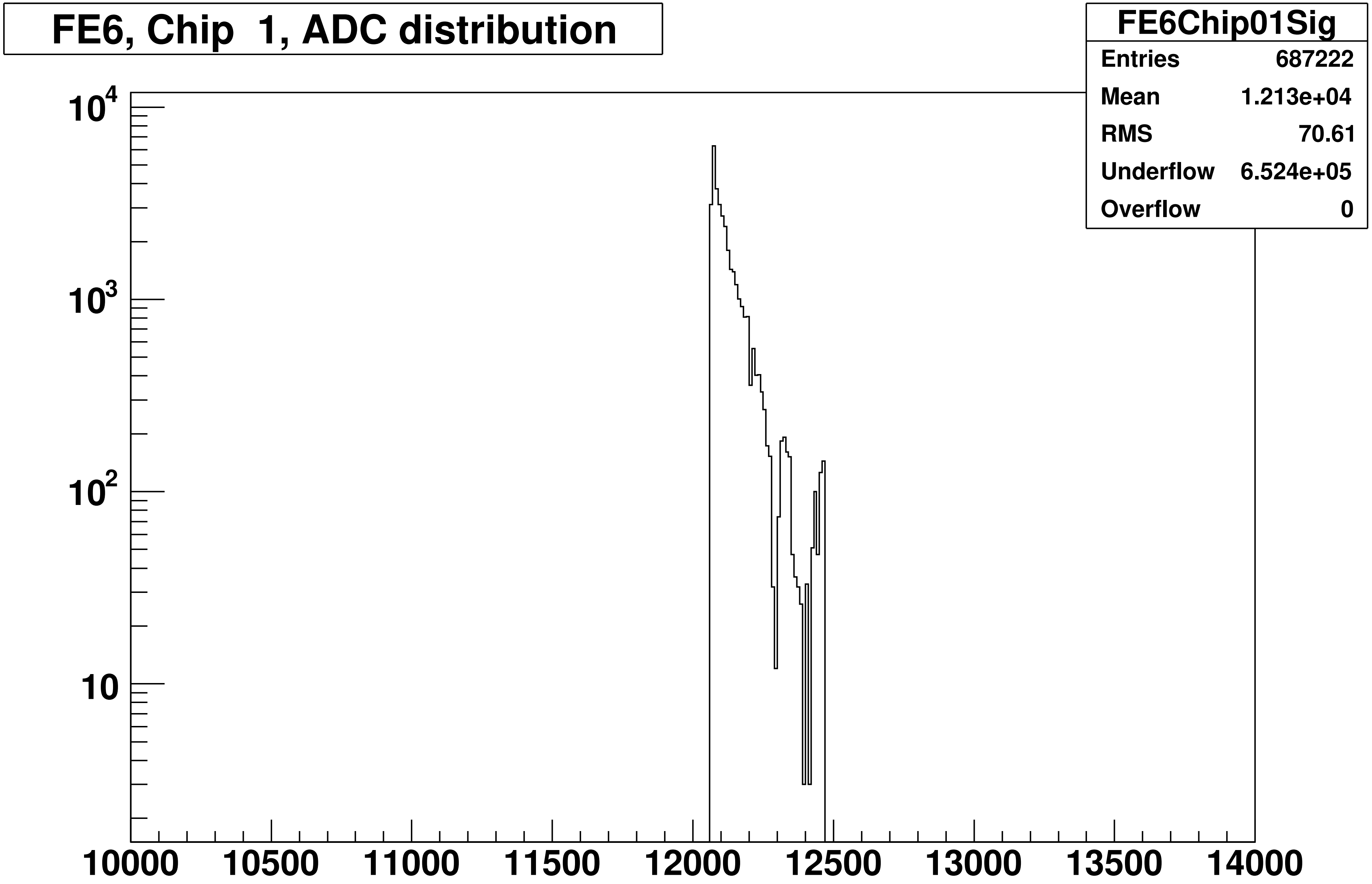

FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 687222
Mean 521.7
RMS 2544
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 687222
Mean -64.71
RMS 28.47
Underflow 0
Overflow 3.483e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 687222
Mean 1.156e+04
RMS 157.7
Underflow 6.526e+05
Overflow 0
10^4
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 687222
Mean 626.3
RMS 2657
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 687222
Mean 12.24
RMS 1.356
Underflow 0
Overflow 3.483e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 687222
Mean 1.213e+04
RMS 70.61
Underflow 6.524e+05
Overflow 0
10^4
10^3
10^2
10
10000
10500
11000
11500
12000
12500
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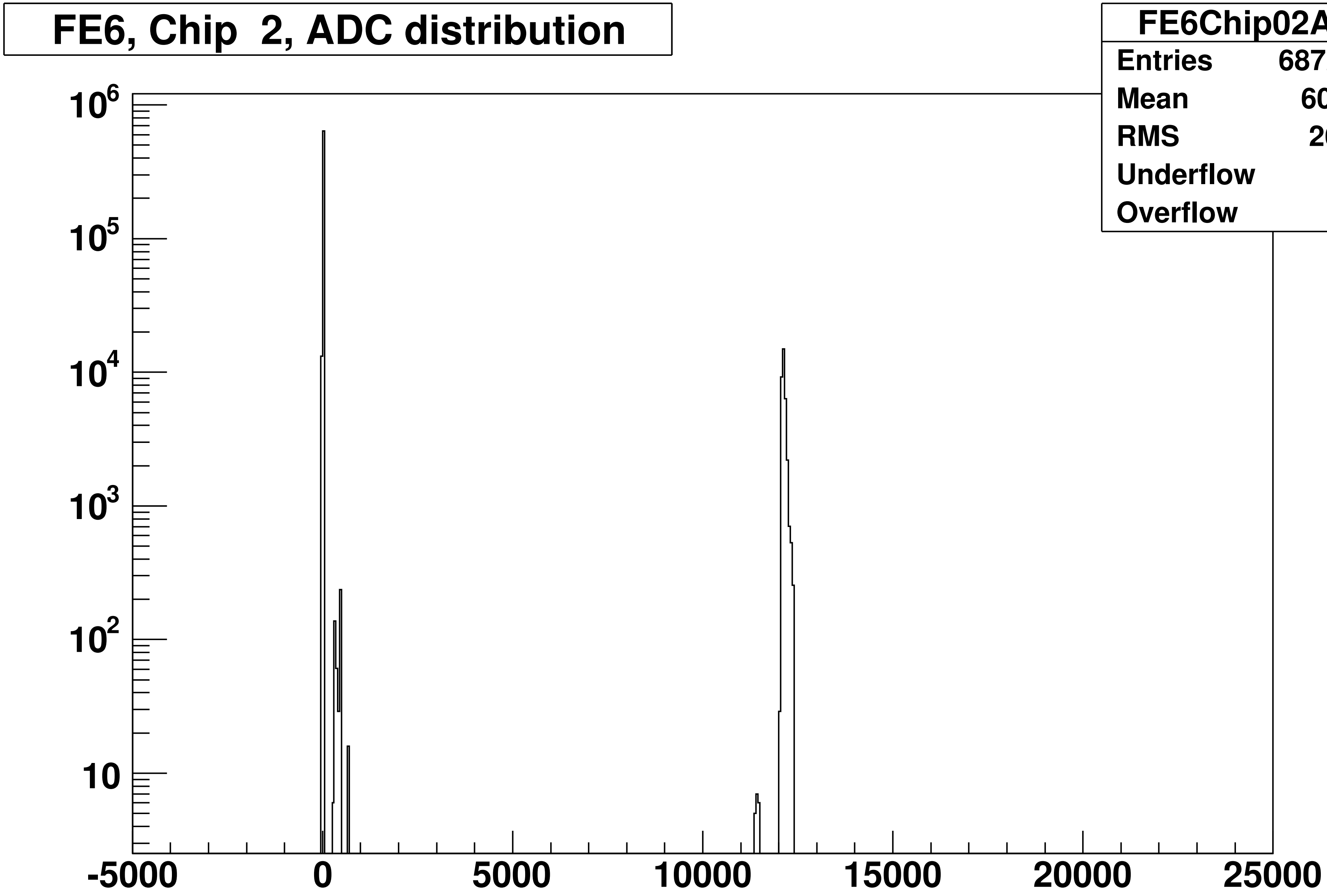
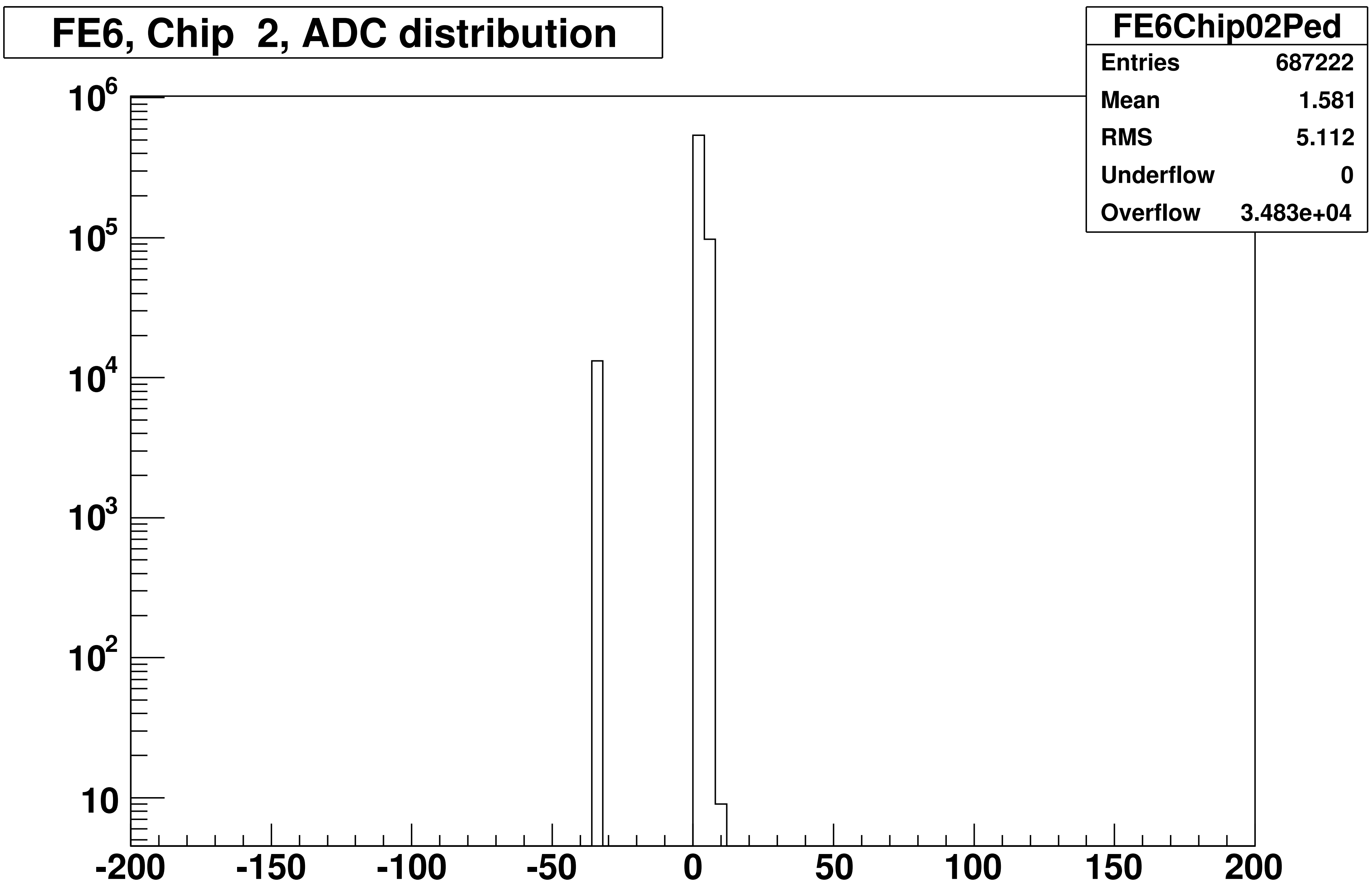
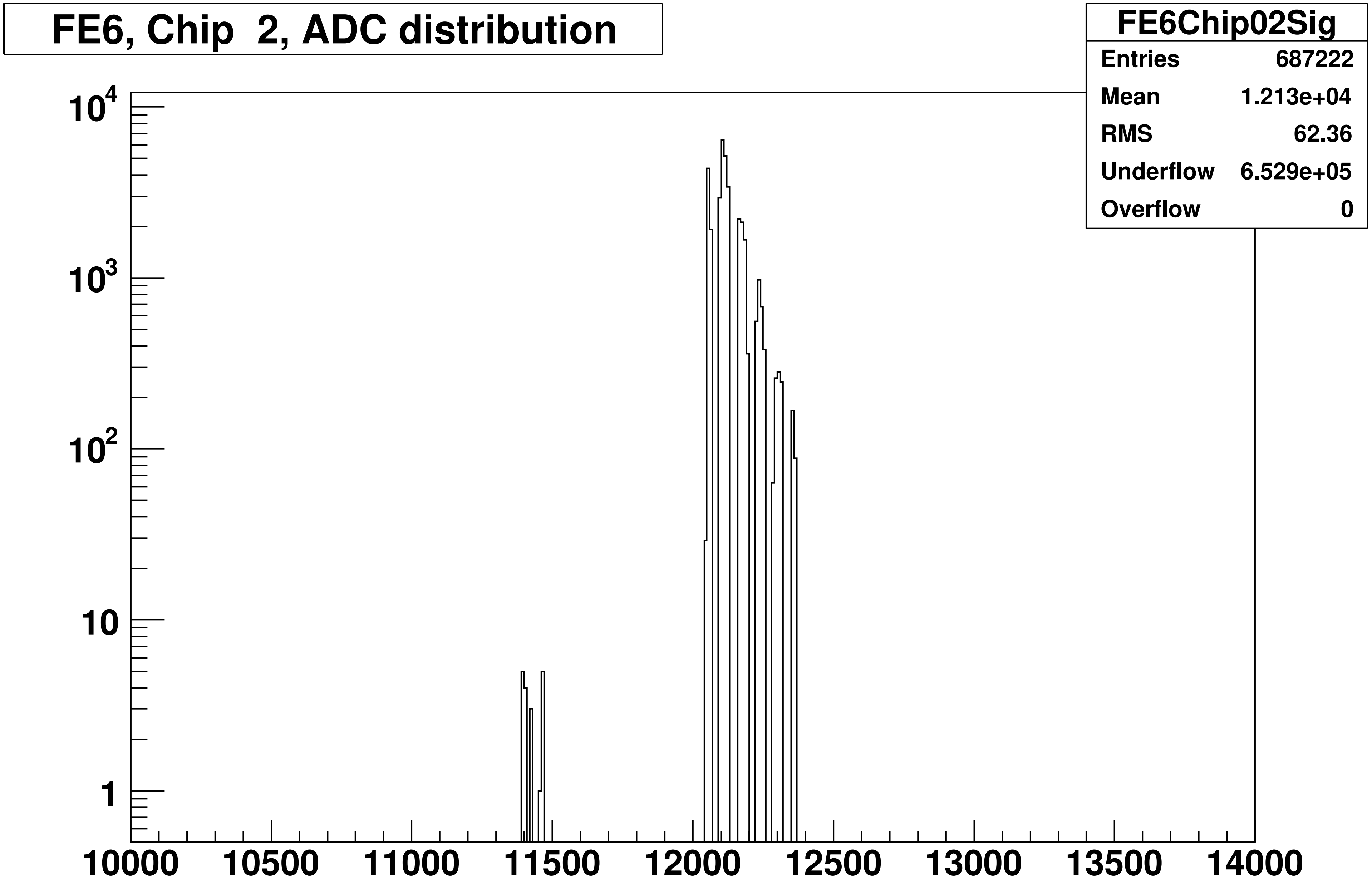
13500
14000



FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 687222
Mean 607.9
RMS 2642
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 687222
Mean 1.581
RMS 5.112
Underflow 0
Overflow 3.483e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 687222
Mean 1.213e+04
RMS 62.36
Underflow 6.529e+05
Overflow 0
10^4
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
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13500
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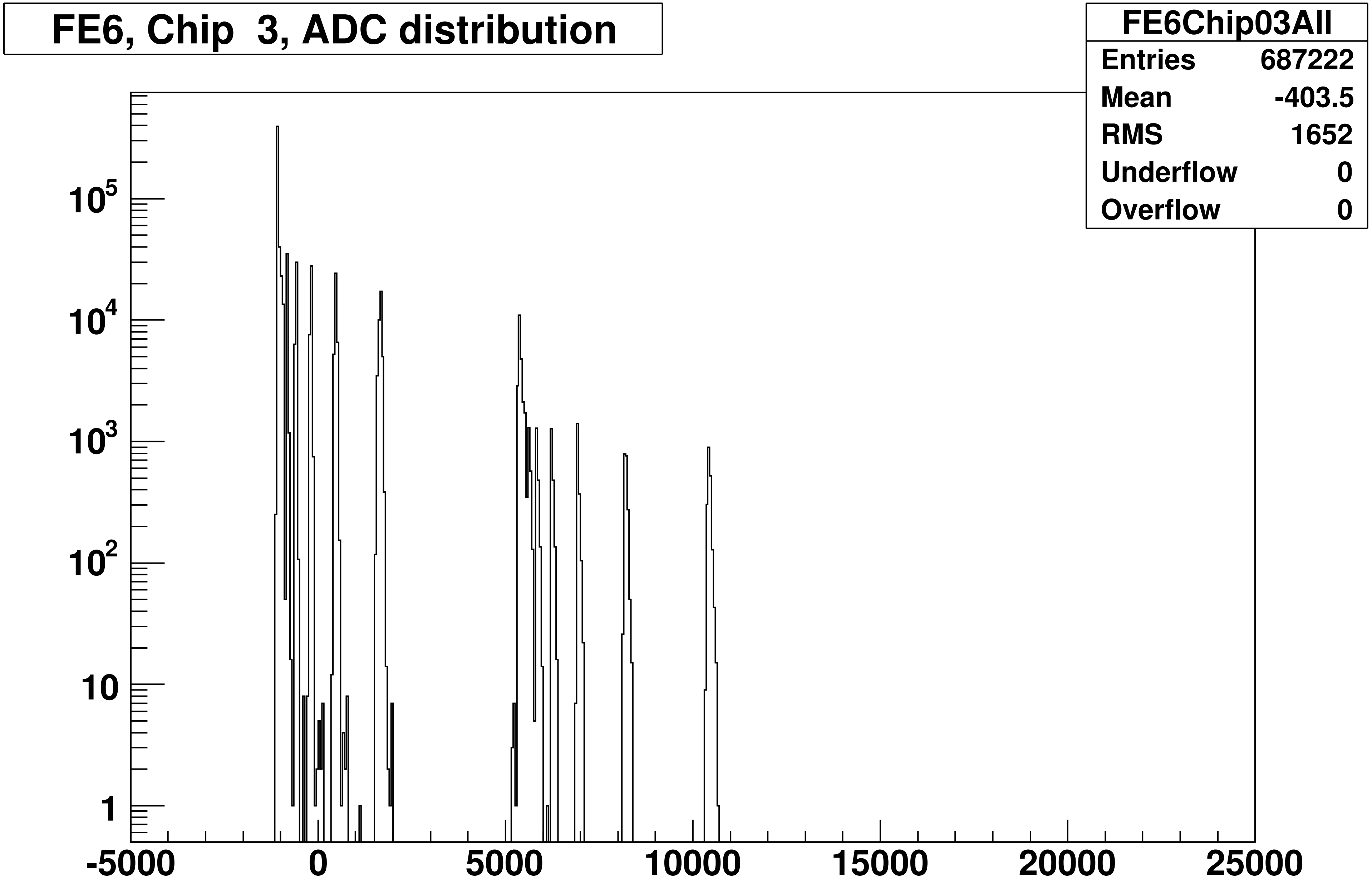
FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 687222
Mean -403.5
RMS 1652
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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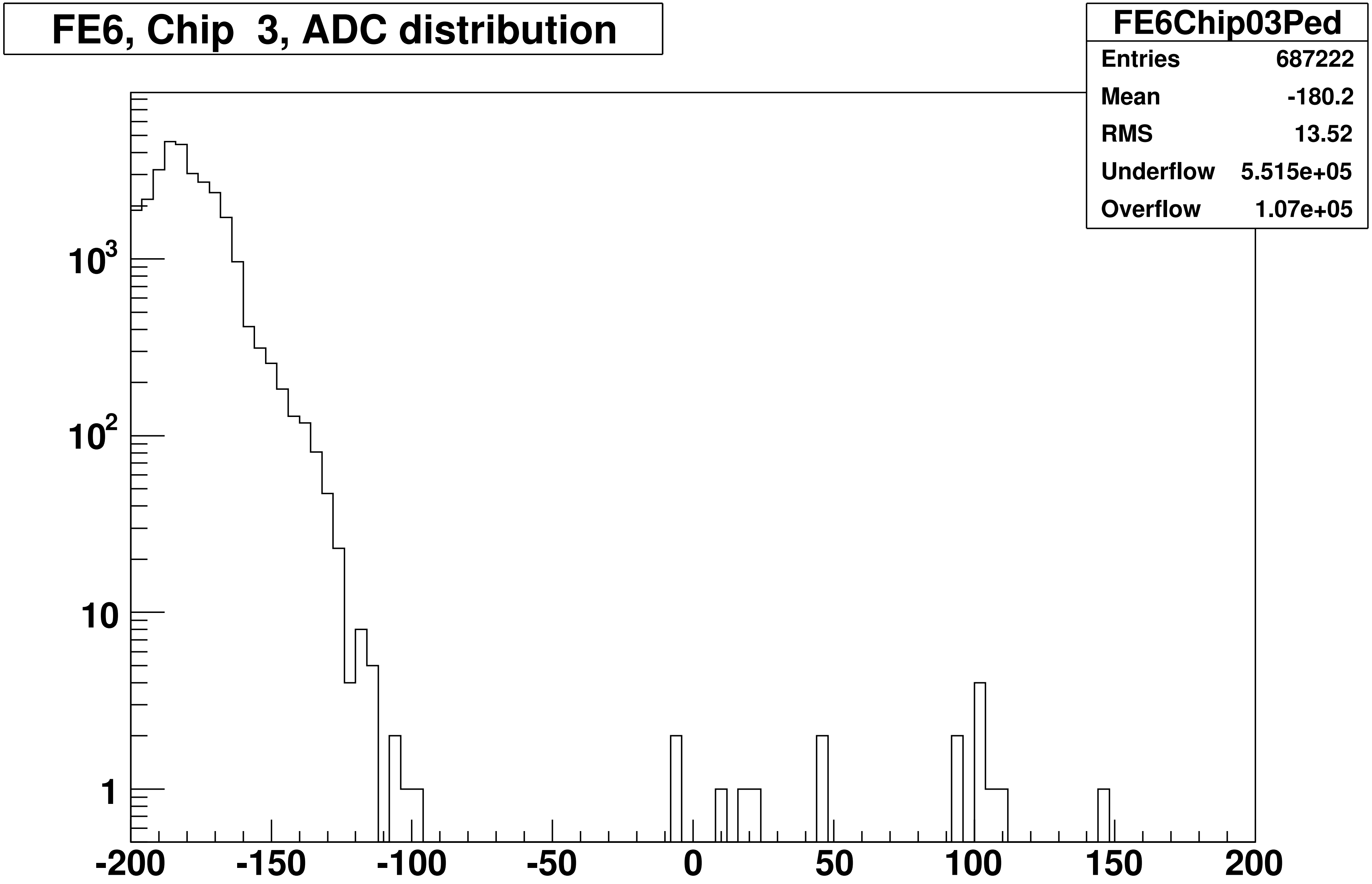
10000
15000
20000
25000


FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 687222
Mean -180.2
RMS 13.52
Underflow 5.515e+05
Overflow 1.07e+05
10^3
10^2
10
1
-200
-150
-100
-50
0
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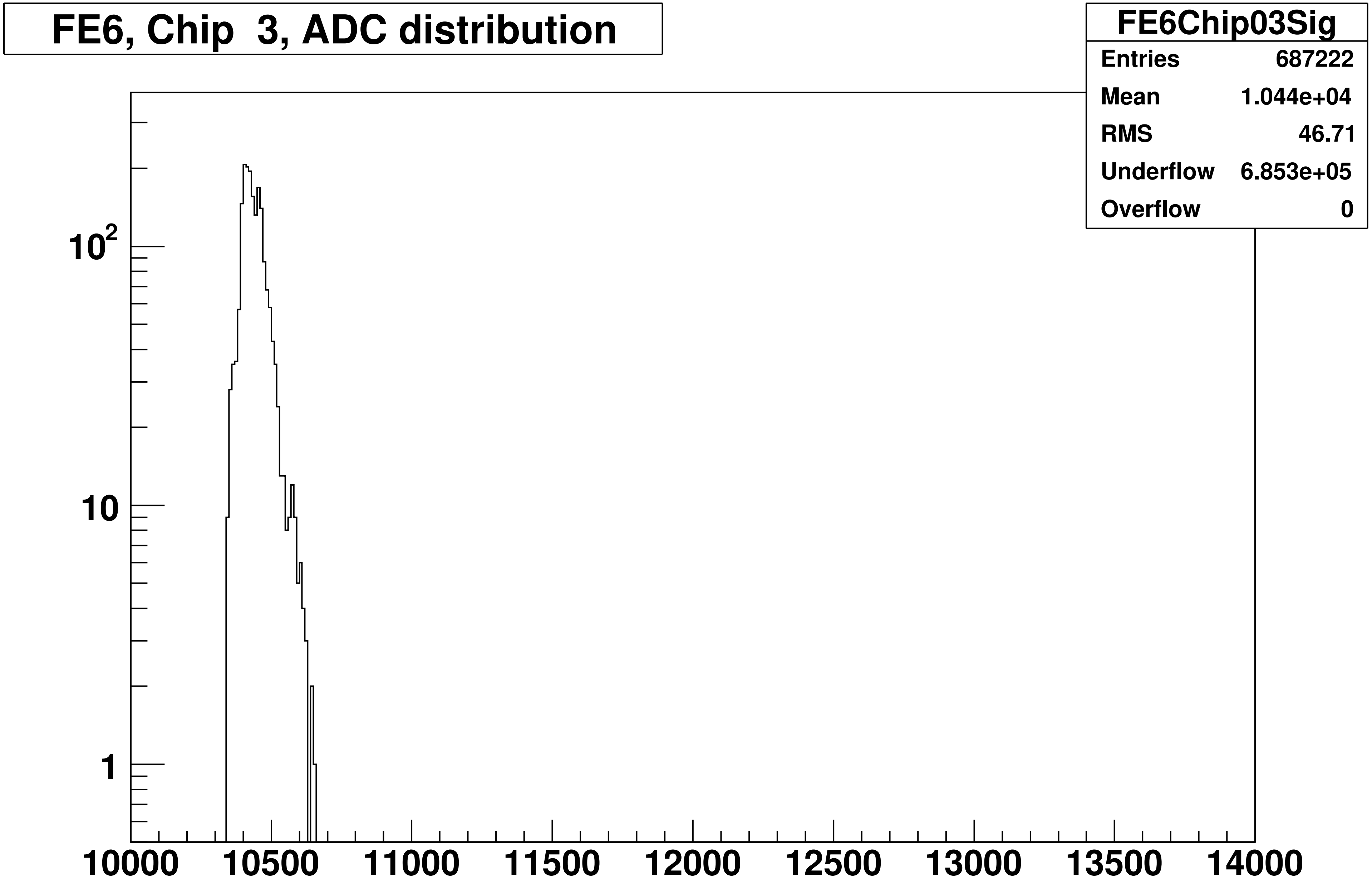
100
150
200



FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 687222
Mean 1.044e+04
RMS 46.71
Underflow 6.853e+05
Overflow 0
10^2
10
1
10000
10500
11000
11500
12000
12500
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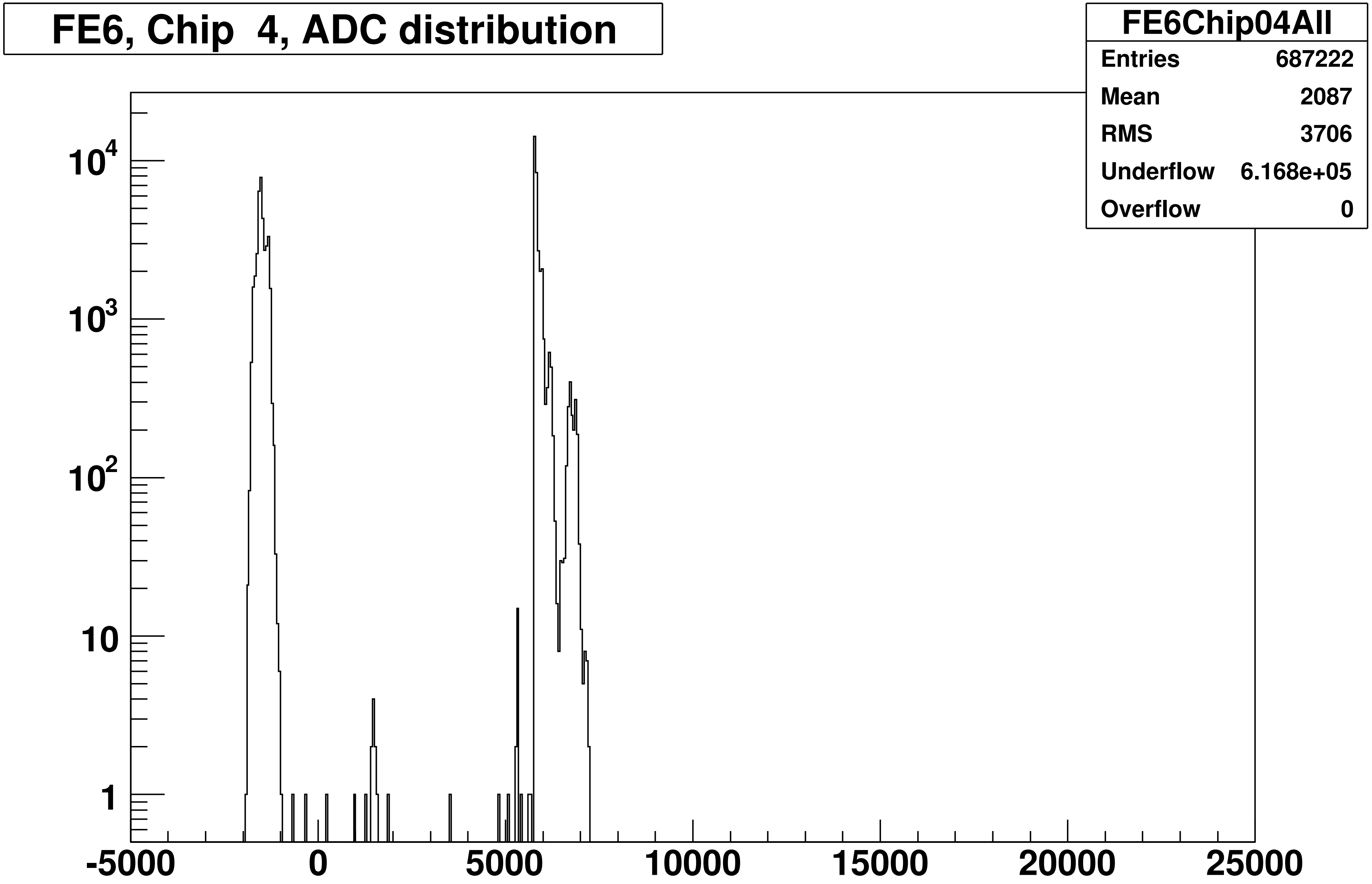
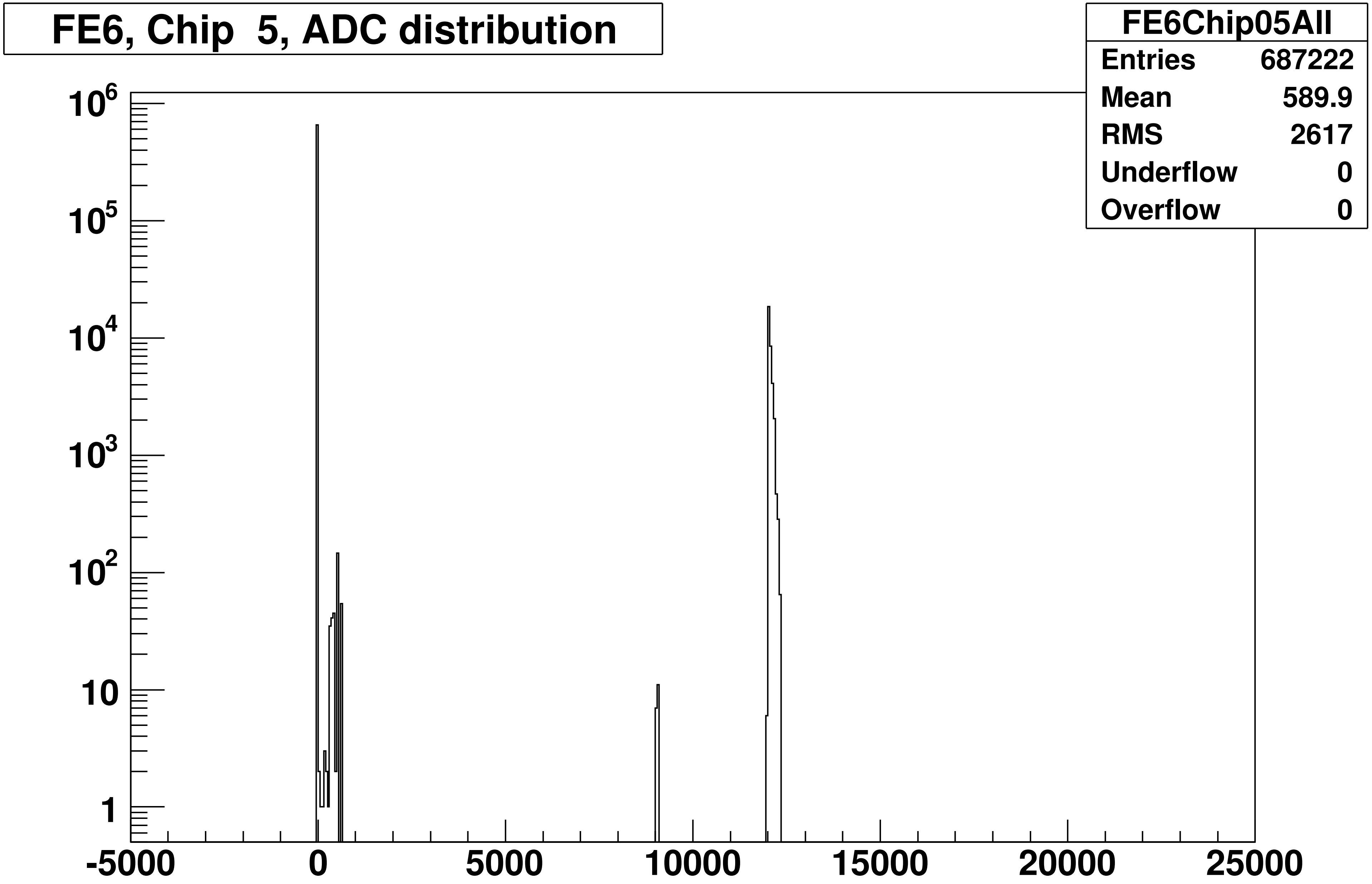
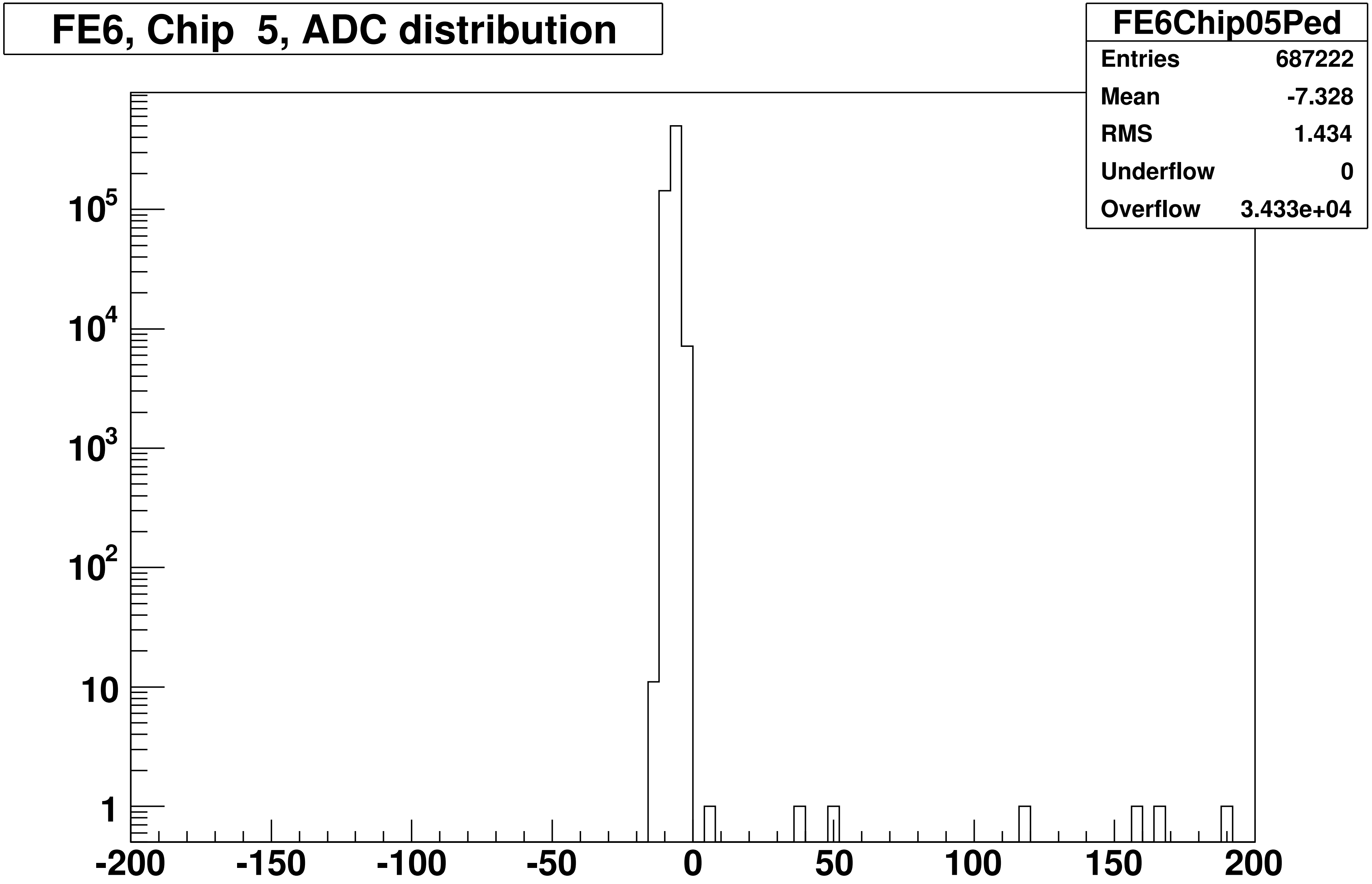
13500
14000



FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 687222
Mean 2087
RMS 3706
Underflow 6.168e+05
Overflow 0
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 687222
Mean 589.9
RMS 2617
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 687222
Mean -7.328
RMS 1.434
Underflow 0
Overflow 3.433e+04
10^5
10^4
10^3
10^2
10
1
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-100
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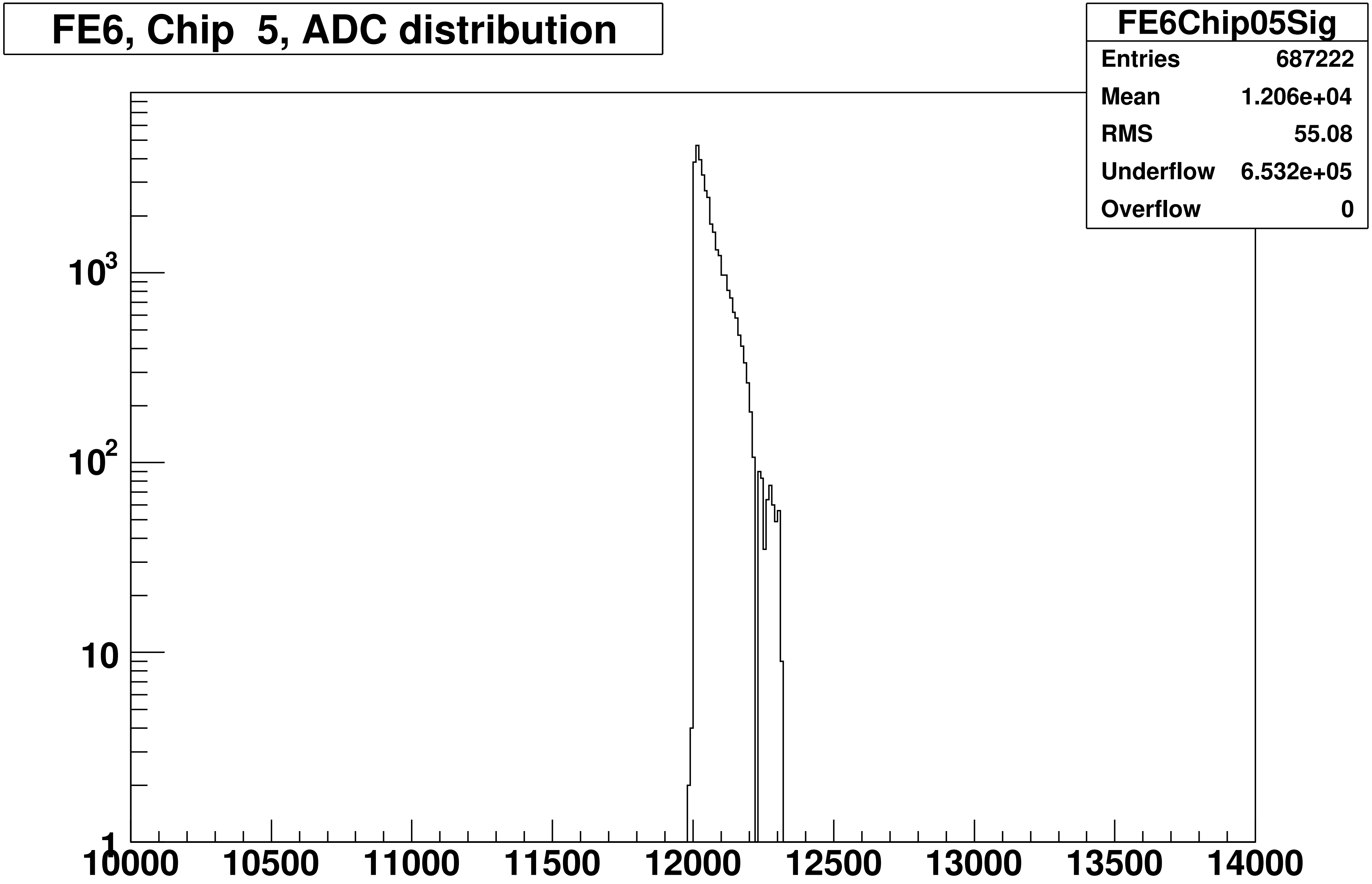
100
150
200


FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 687222
Mean 1.206e+04
RMS 55.08
Underflow 6.532e+05
Overflow 0
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10^2
10
1
10000
10500
11000
11500
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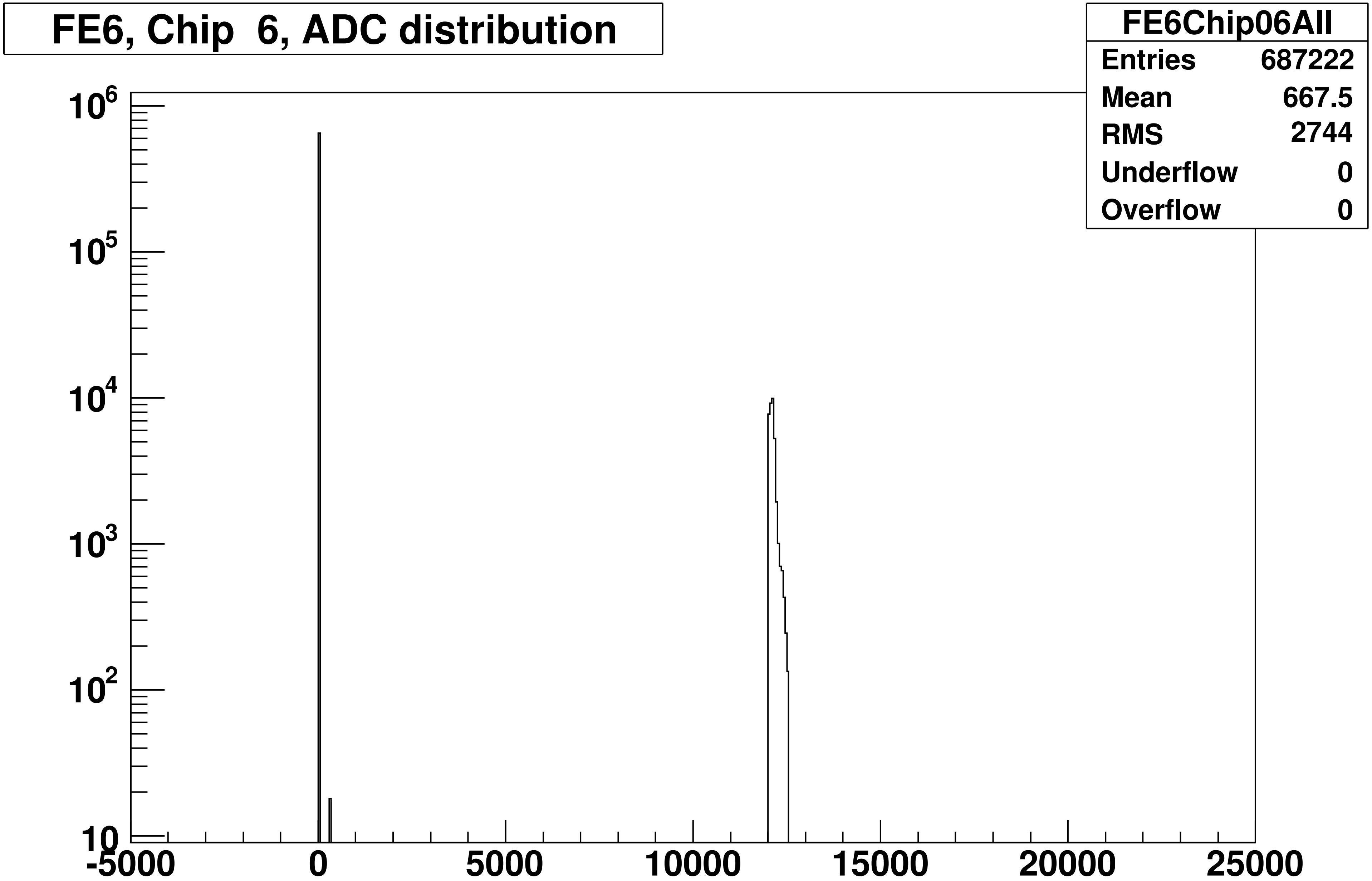

FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 687222
Mean 667.5
RMS 2744
Underflow 0
Overflow 0
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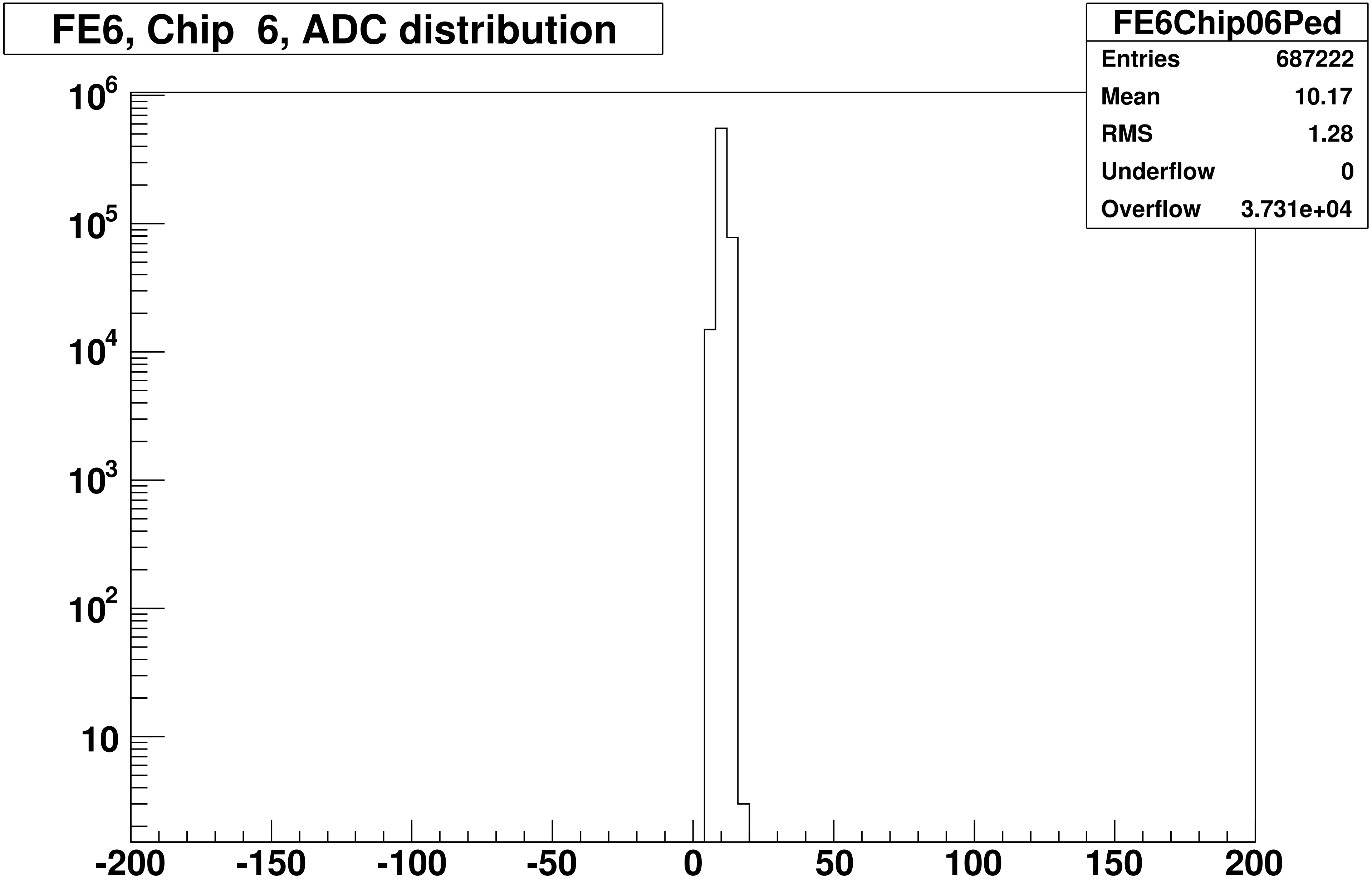
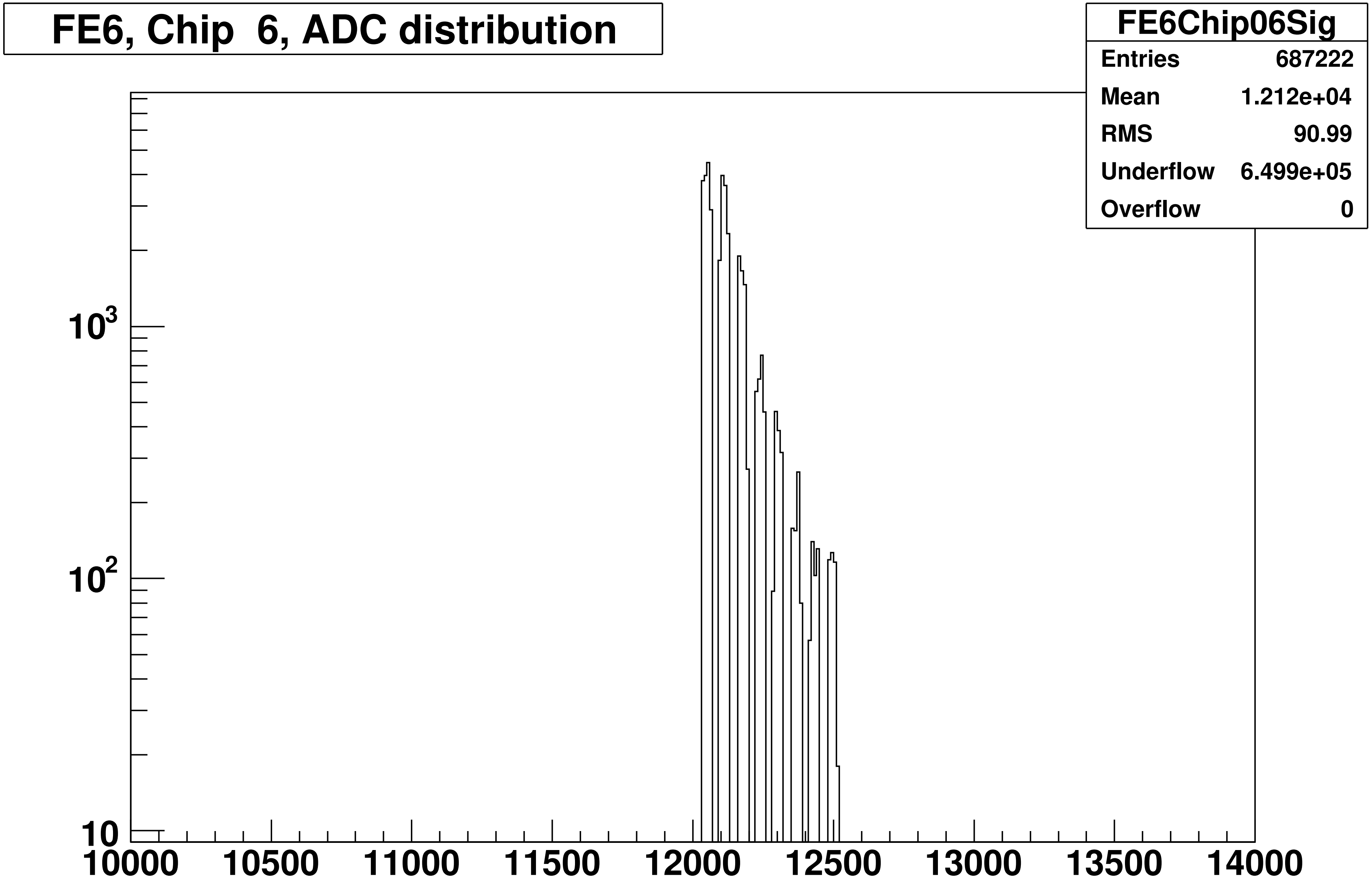
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 687222
Mean 10.17
RMS 1.28
Underflow 0
Overflow 3.731e+04
10^6
10^5
10^4
10^3
10^2
10
-200 -150 -100 -50 0 50 100 150 200


FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 687222
Mean 1.212e+04
RMS 90.99
Underflow 6.499e+05
Overflow 0
10^3
10^2
10
10000
10500
11000
11500
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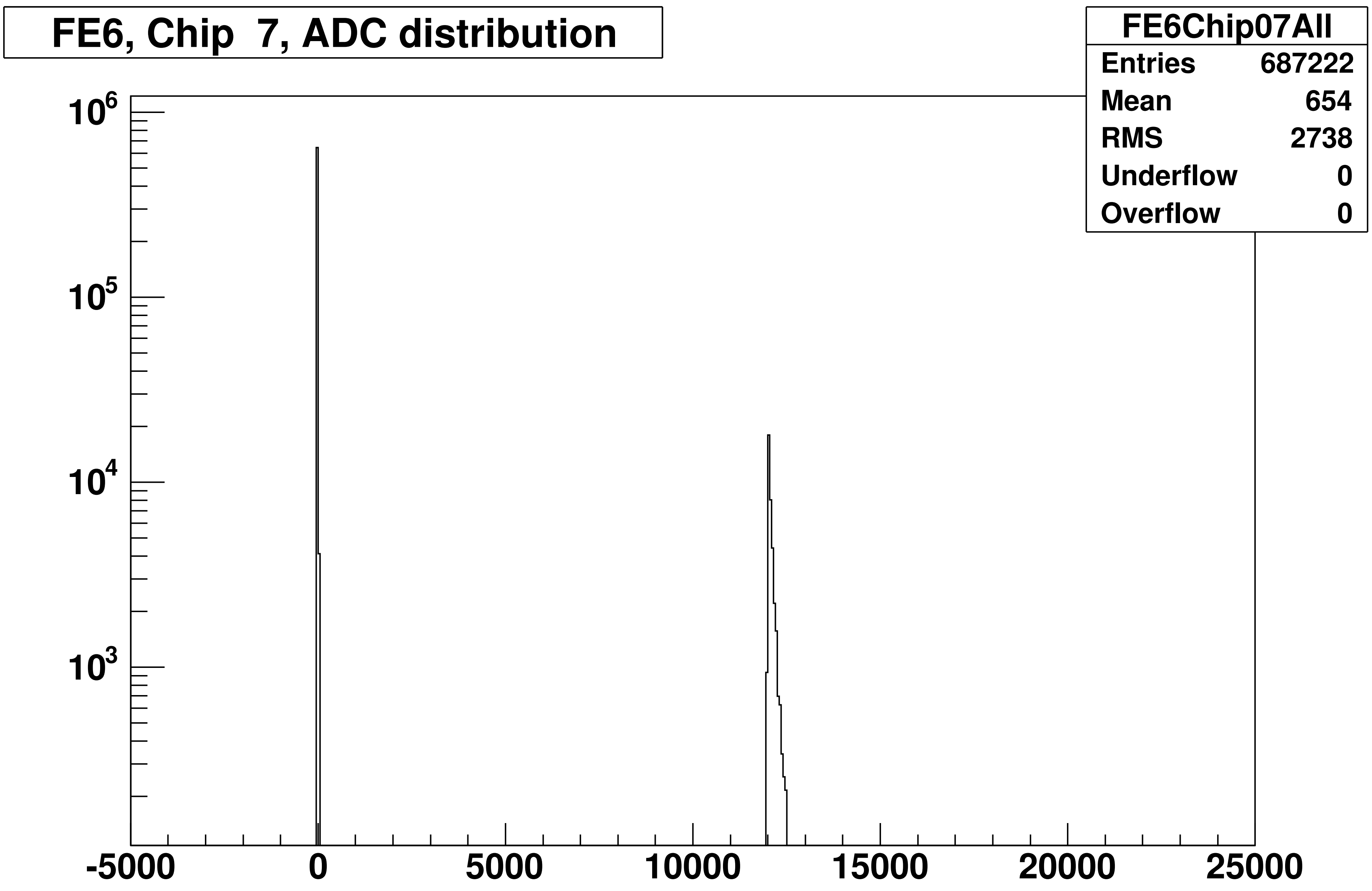
13500
14000


FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 687222
Mean 654
RMS 2738
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
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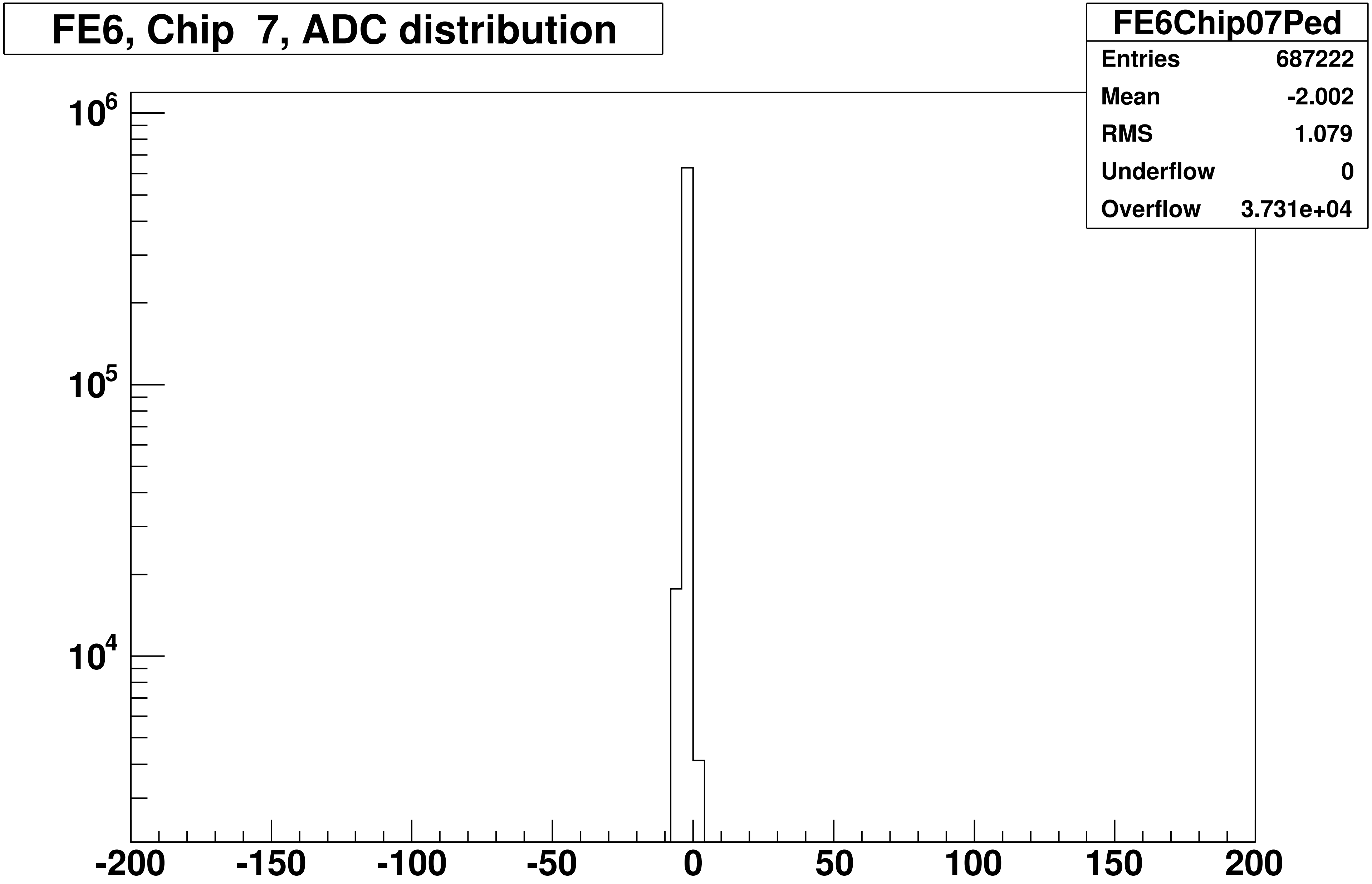
10000
15000
20000
25000


FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 687222
Mean -2.002
RMS 1.079
Underflow 0
Overflow 3.731e+04
10^6
10^5
10^4
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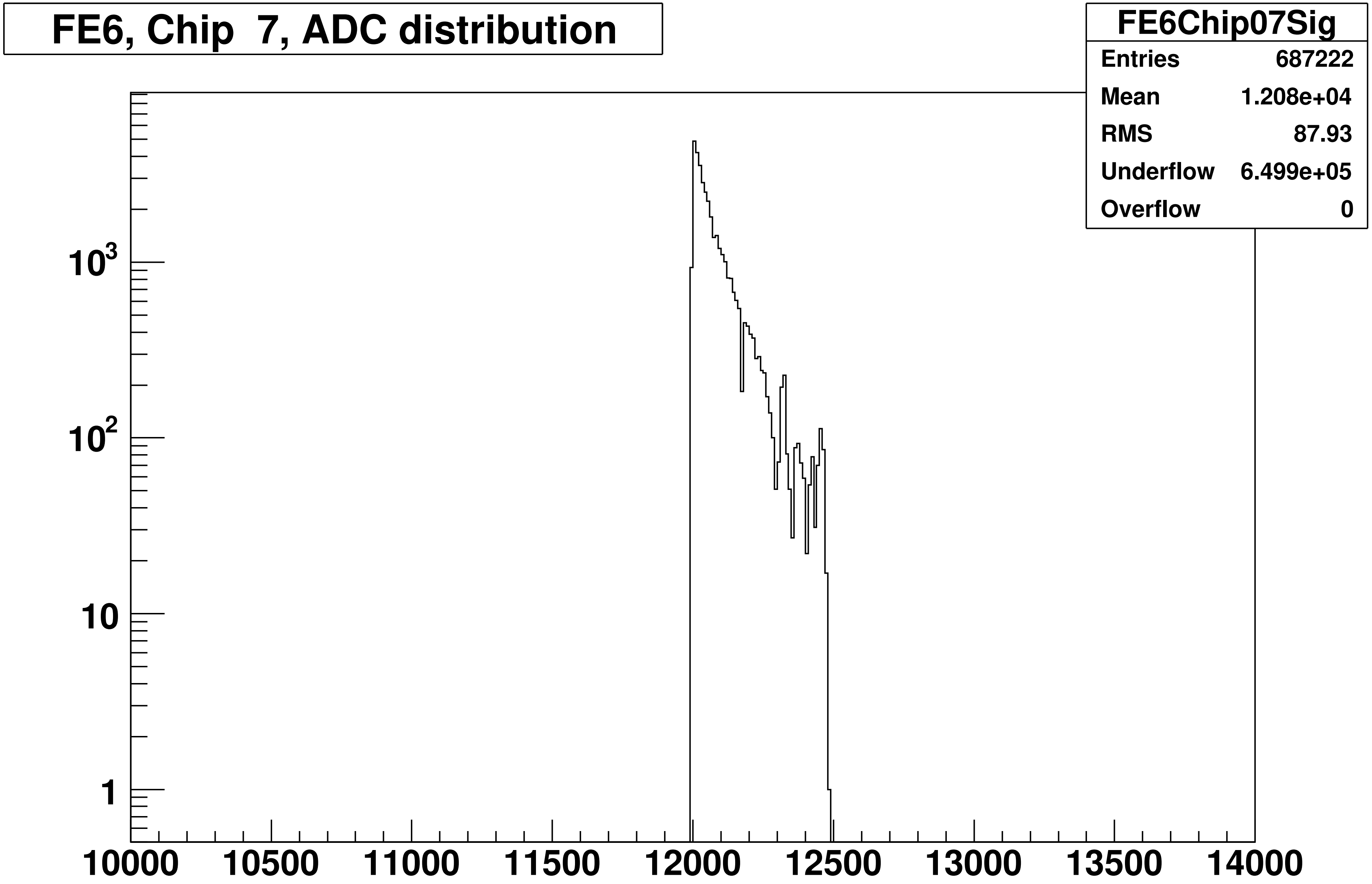
-100
-50
0
50
100
150
200



FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 687222
Mean 1.208e+04
RMS 87.93
Underflow 6.499e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
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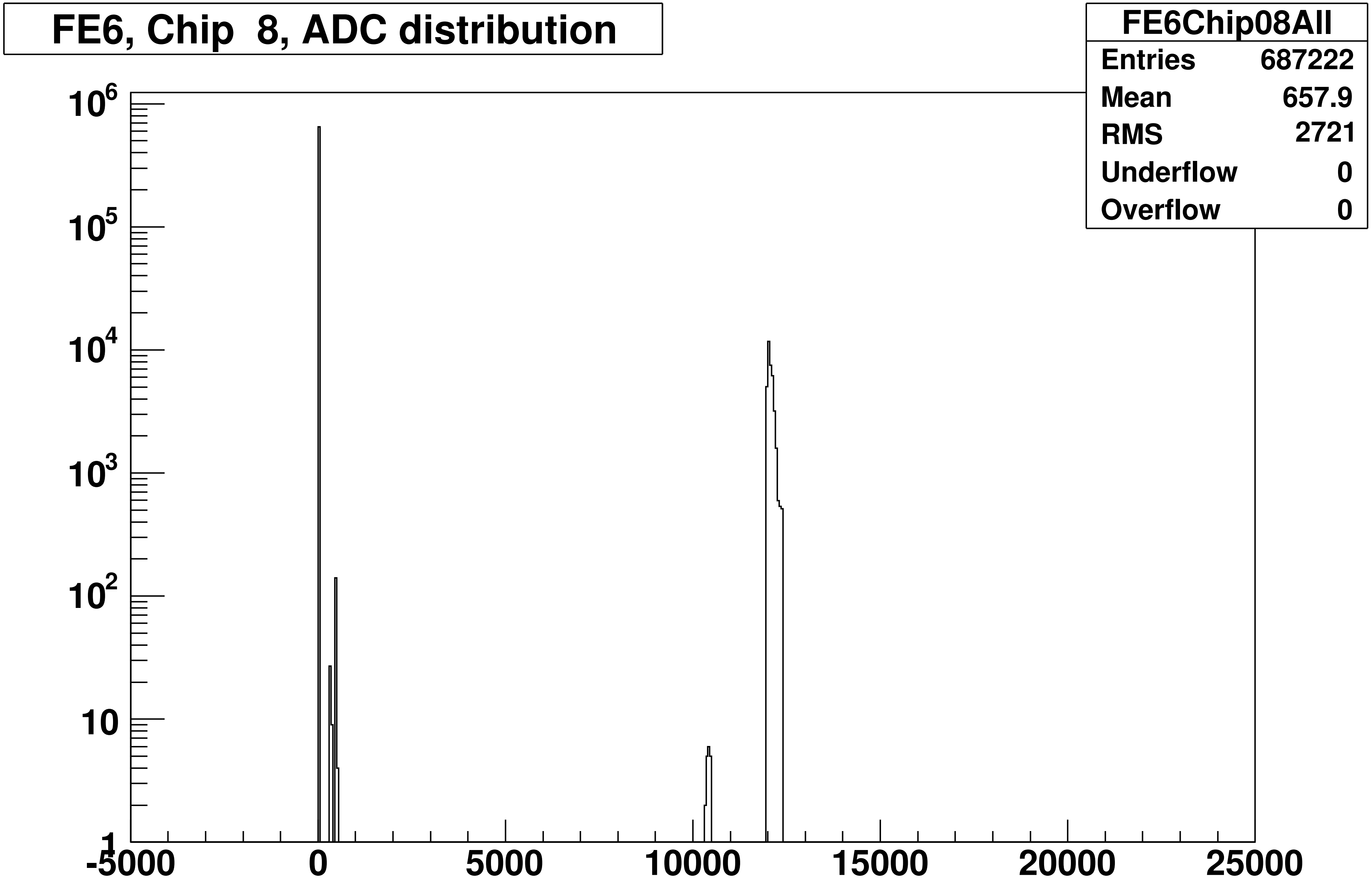
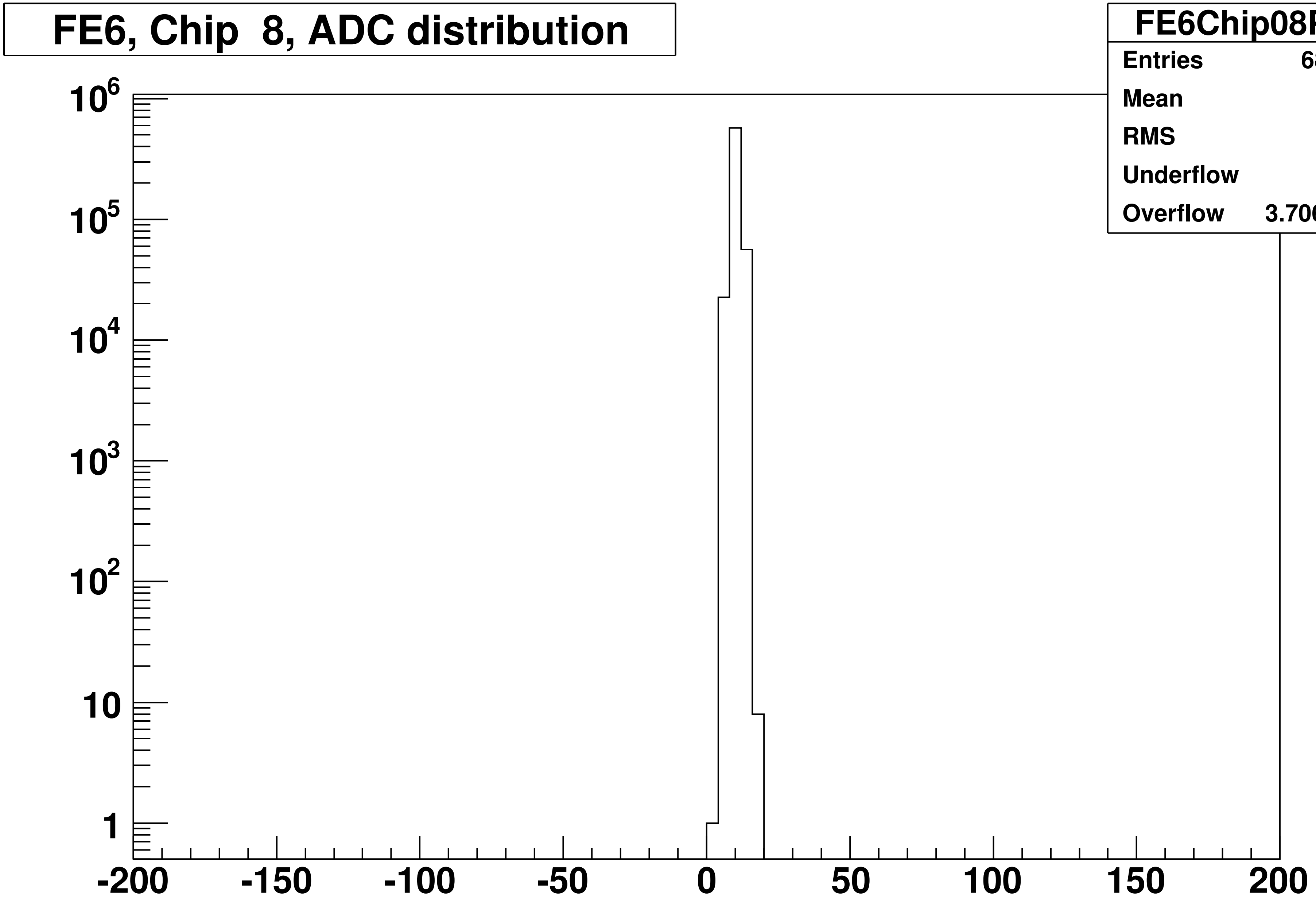
14000



FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 687222
Mean 657.9
RMS 2721
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 687222
Mean 9.929
RMS 1.287
Underflow 0
Overflow 3.706e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
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-100
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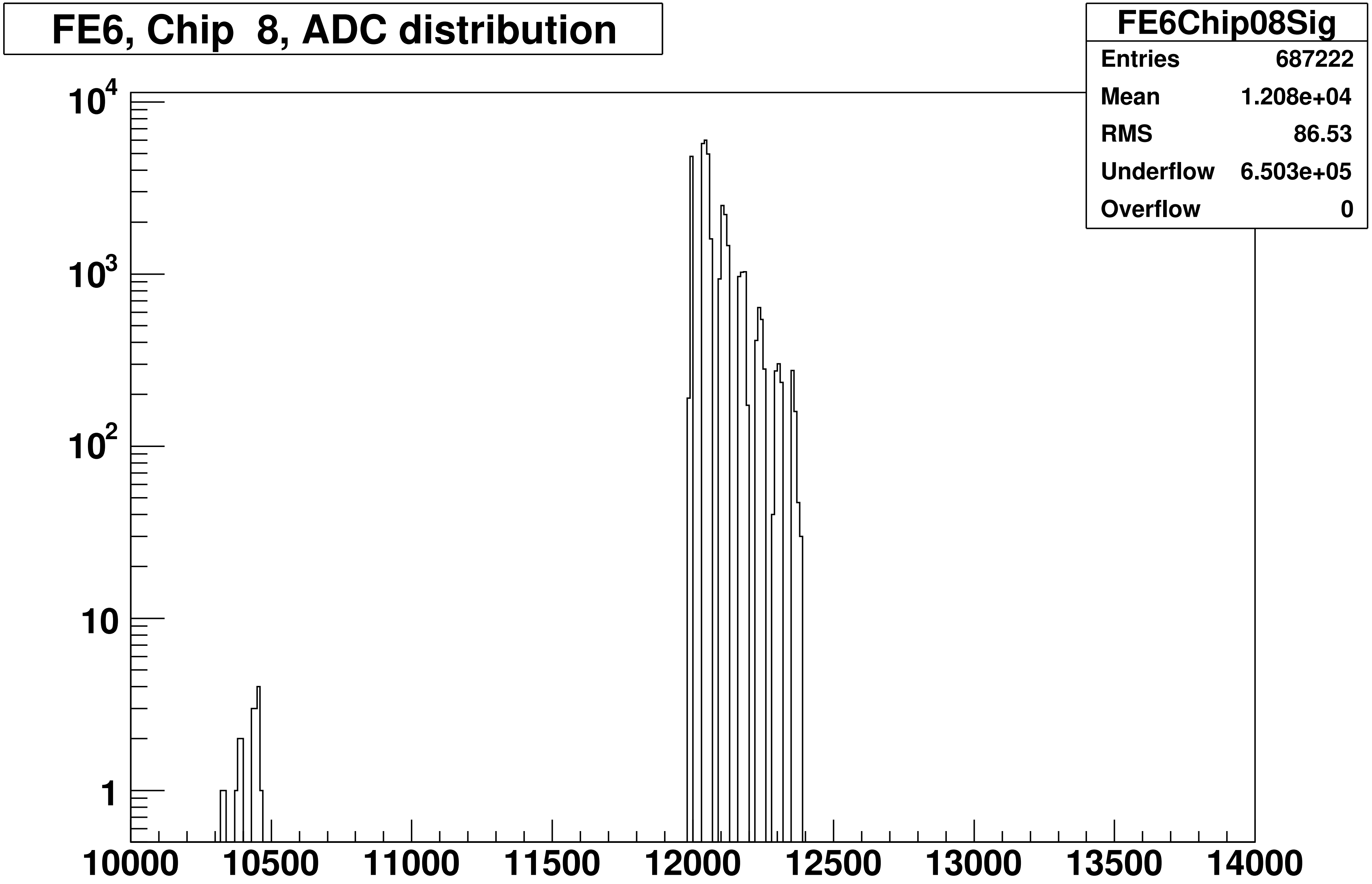
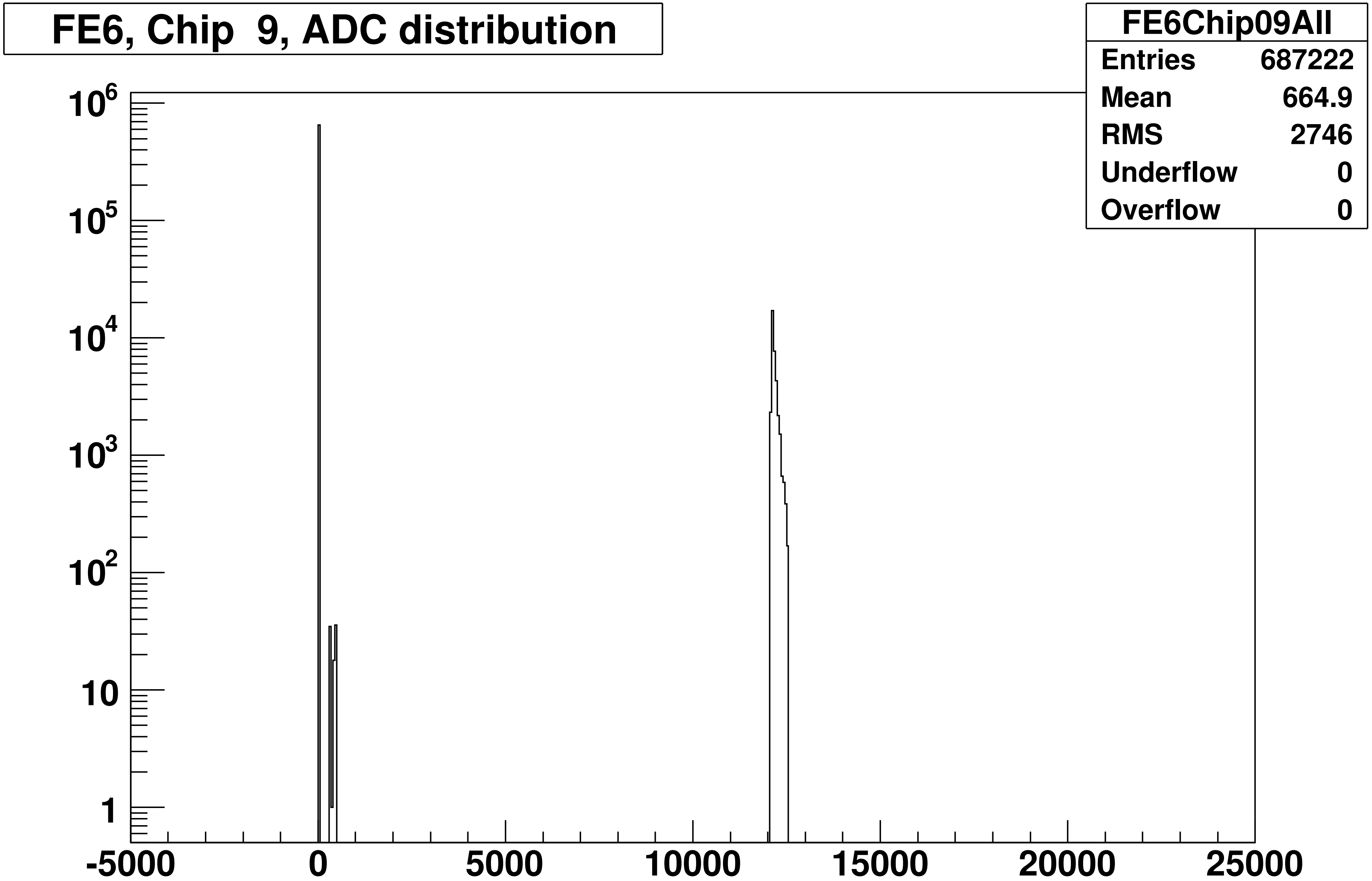
0
50
100
150
200


FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 687222
Mean 1.208e+04
RMS 86.53
Underflow 6.503e+05
Overflow 0
10^4
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000


FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 687222
Mean 664.9
RMS 2746
Underflow 0
Overflow 0
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10^4
10^3
10^2
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1
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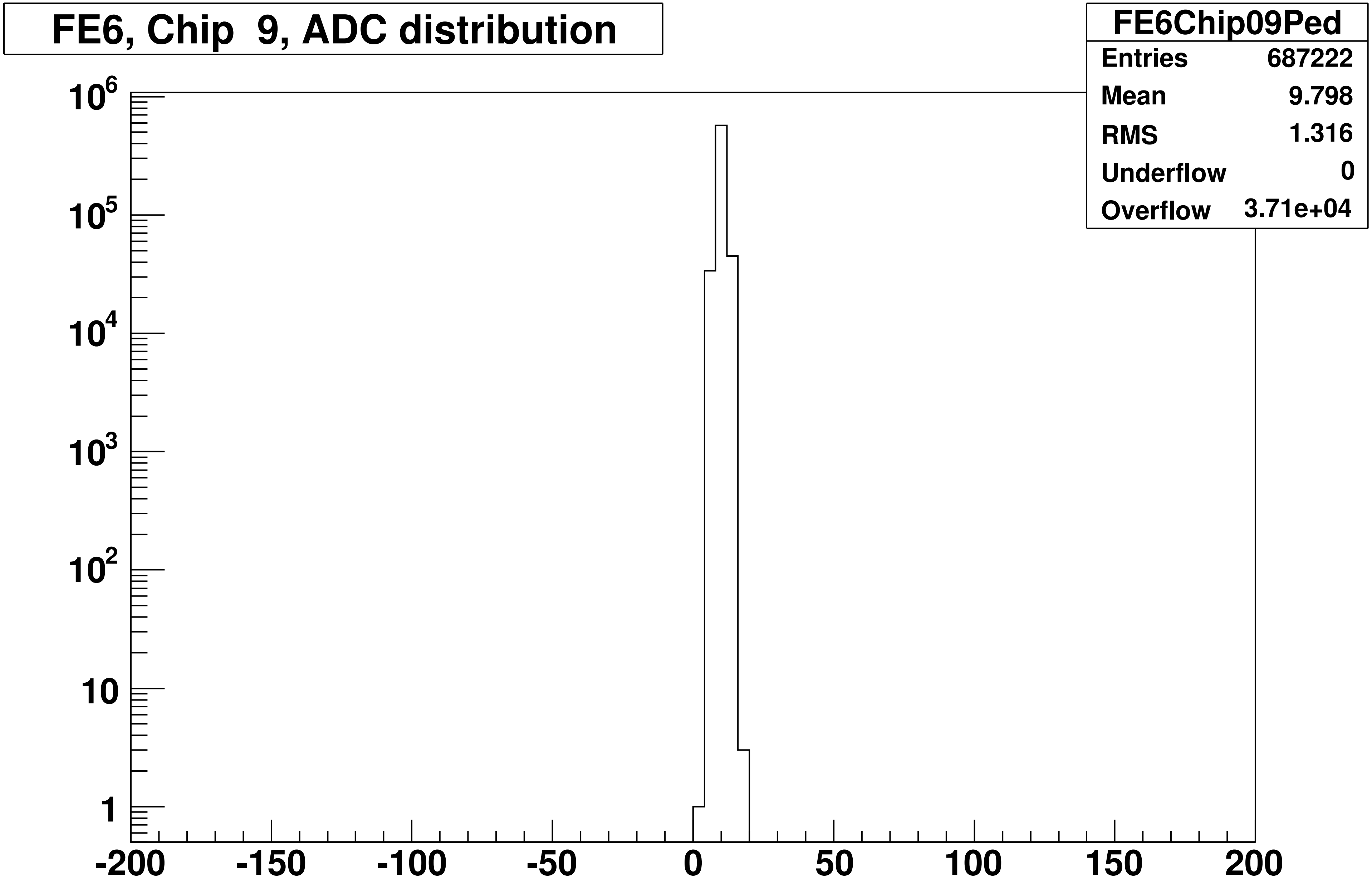

FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 687222
Mean 9.798
RMS 1.316
Underflow 0
Overflow 3.71e+04
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10^5
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10^3
10^2
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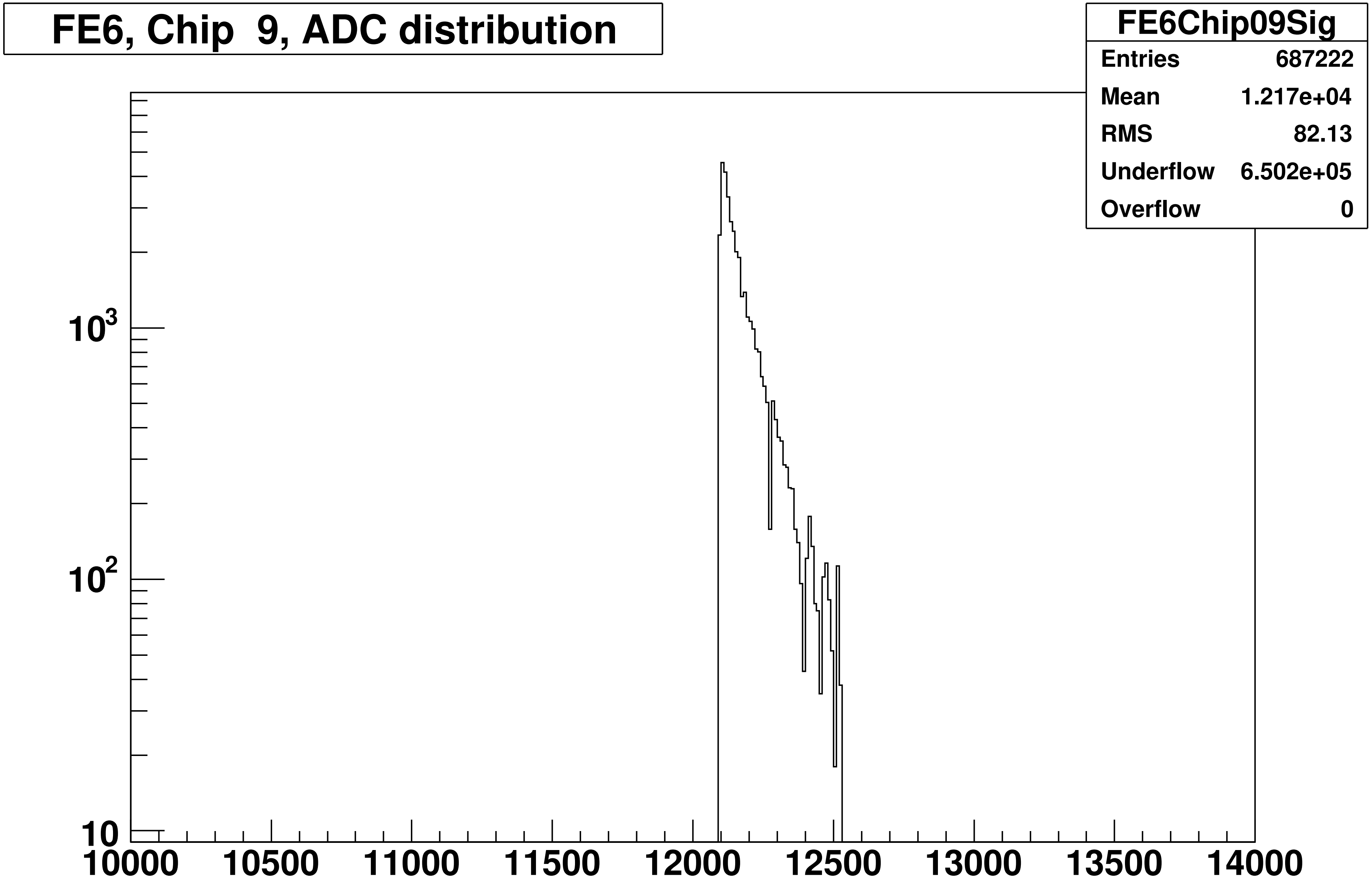
FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 687222
Mean 1.217e+04
RMS 82.13
Underflow 6.502e+05
Overflow 0
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10^2
10^3
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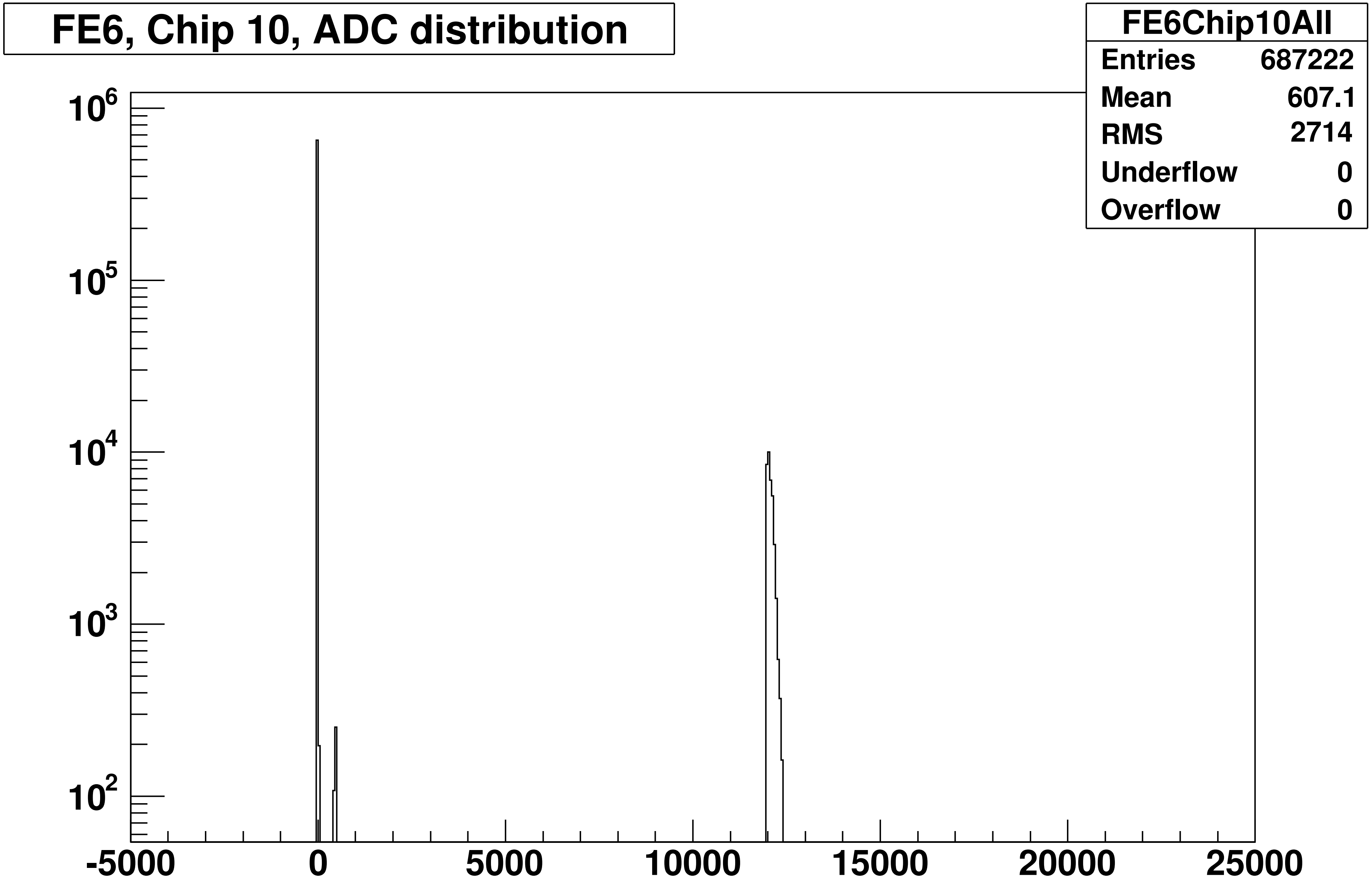
FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 687222
Mean 607.1
RMS 2714
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
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10000
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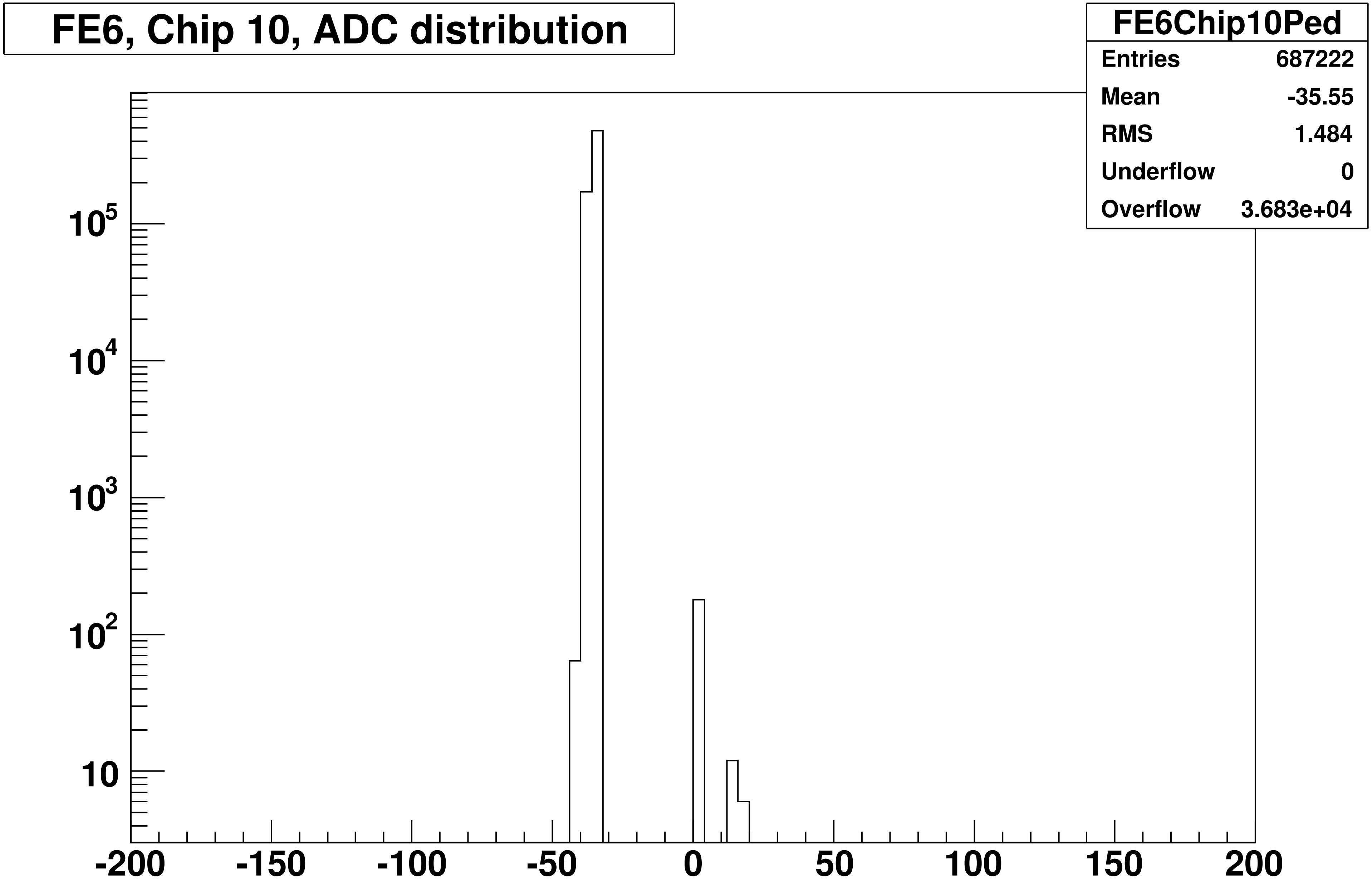
20000
25000


FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 687222
Mean -35.55
RMS 1.484
Underflow 0
Overflow 3.683e+04
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10^4
10^3
10^2
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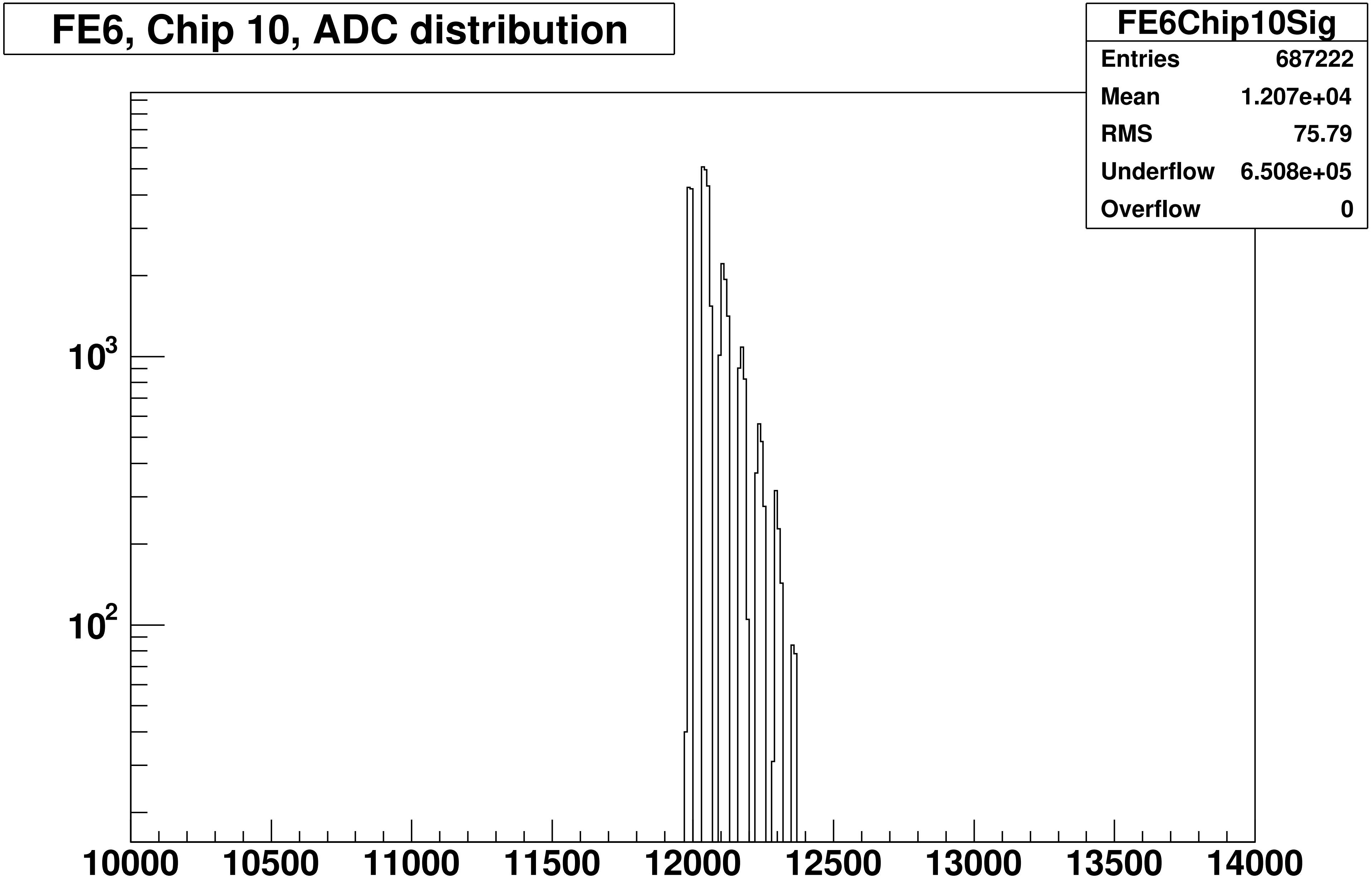
FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 687222
Mean 1.207e+04
RMS 75.79
Underflow 6.508e+05
Overflow 0
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10^2
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11500
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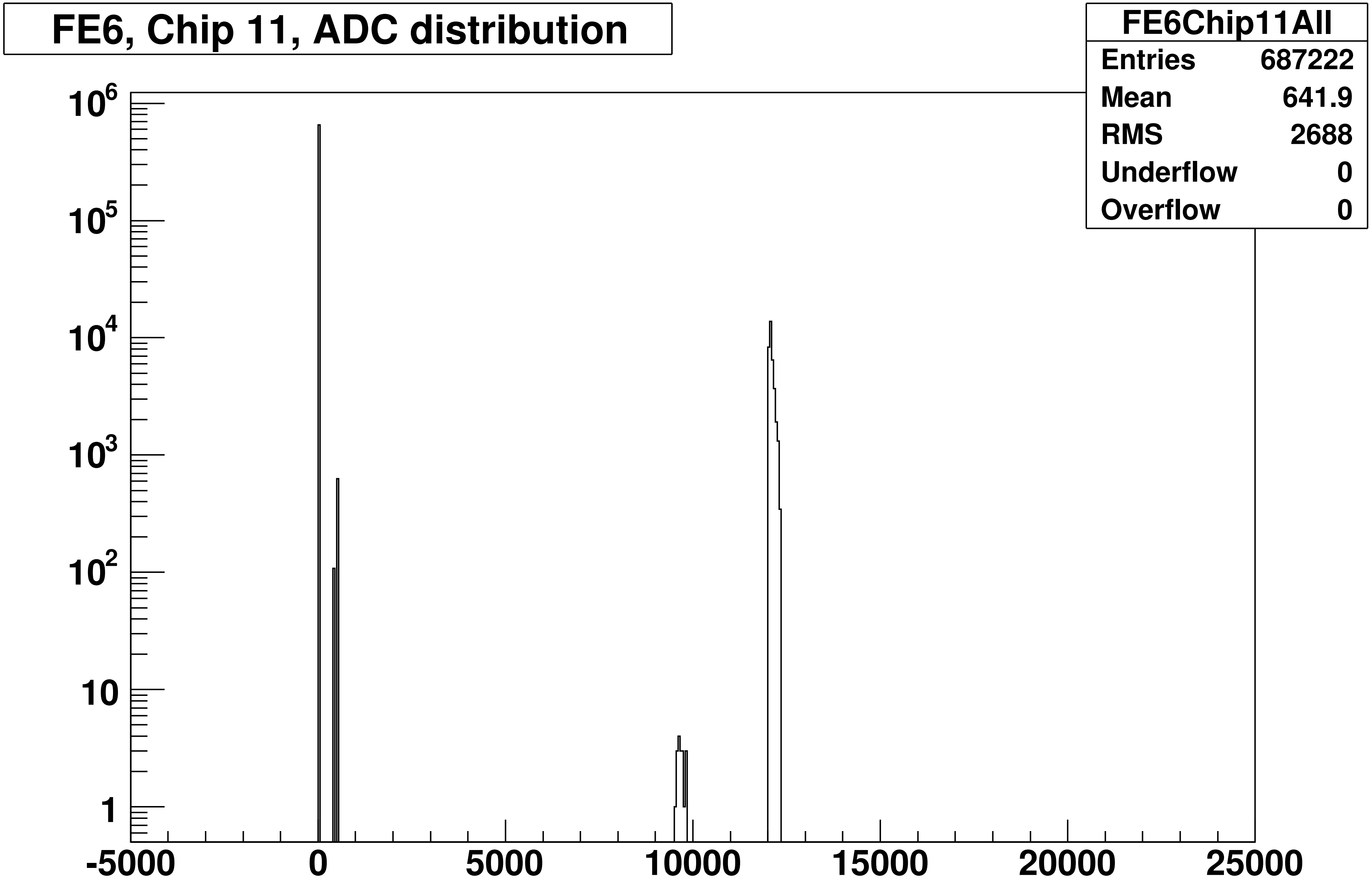
FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 687222
Mean 641.9
RMS 2688
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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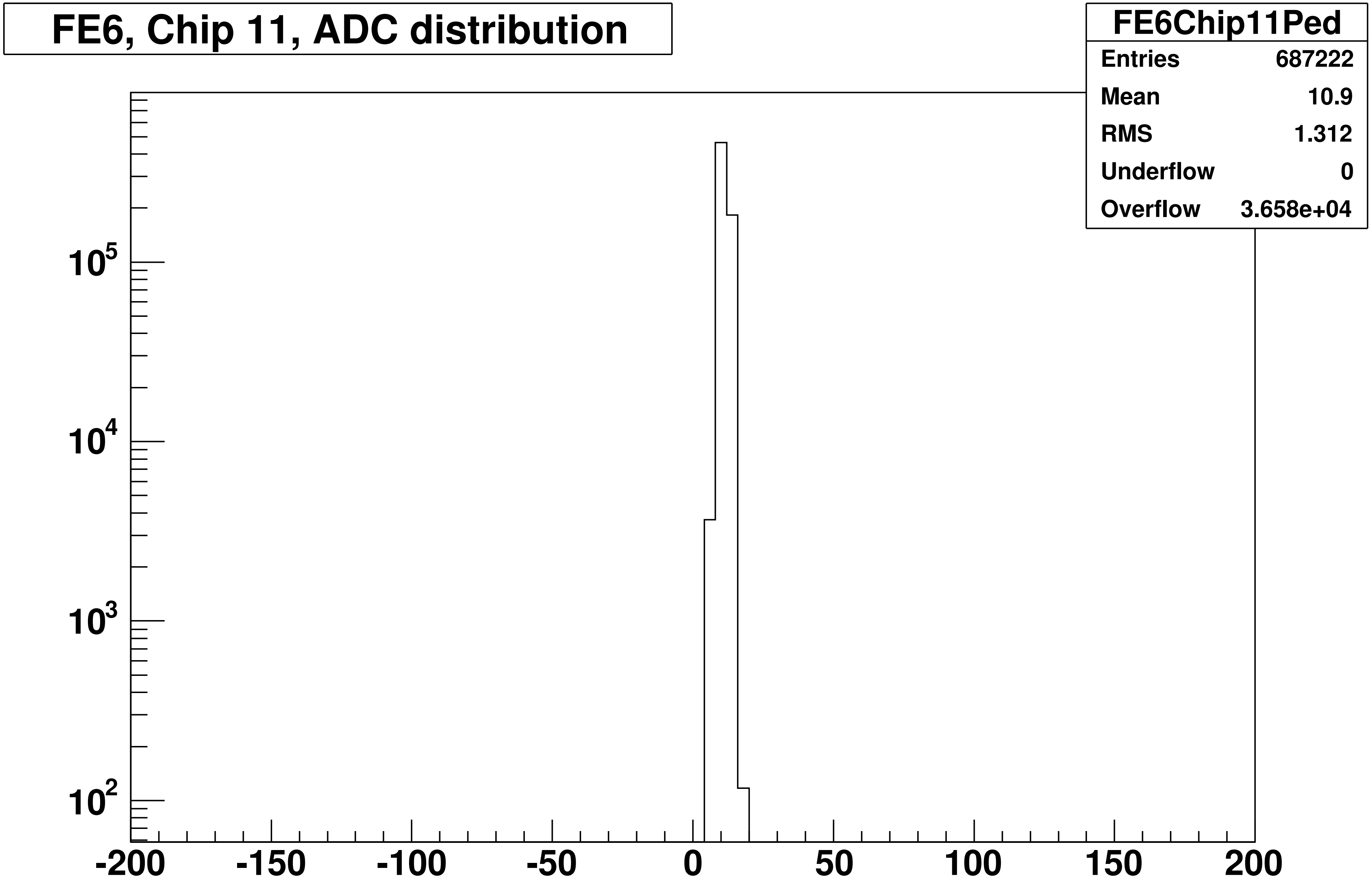
25000


FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 687222
Mean 10.9
RMS 1.312
Underflow 0
Overflow 3.658e+04
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10^4
10^3
10^2
-200
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0
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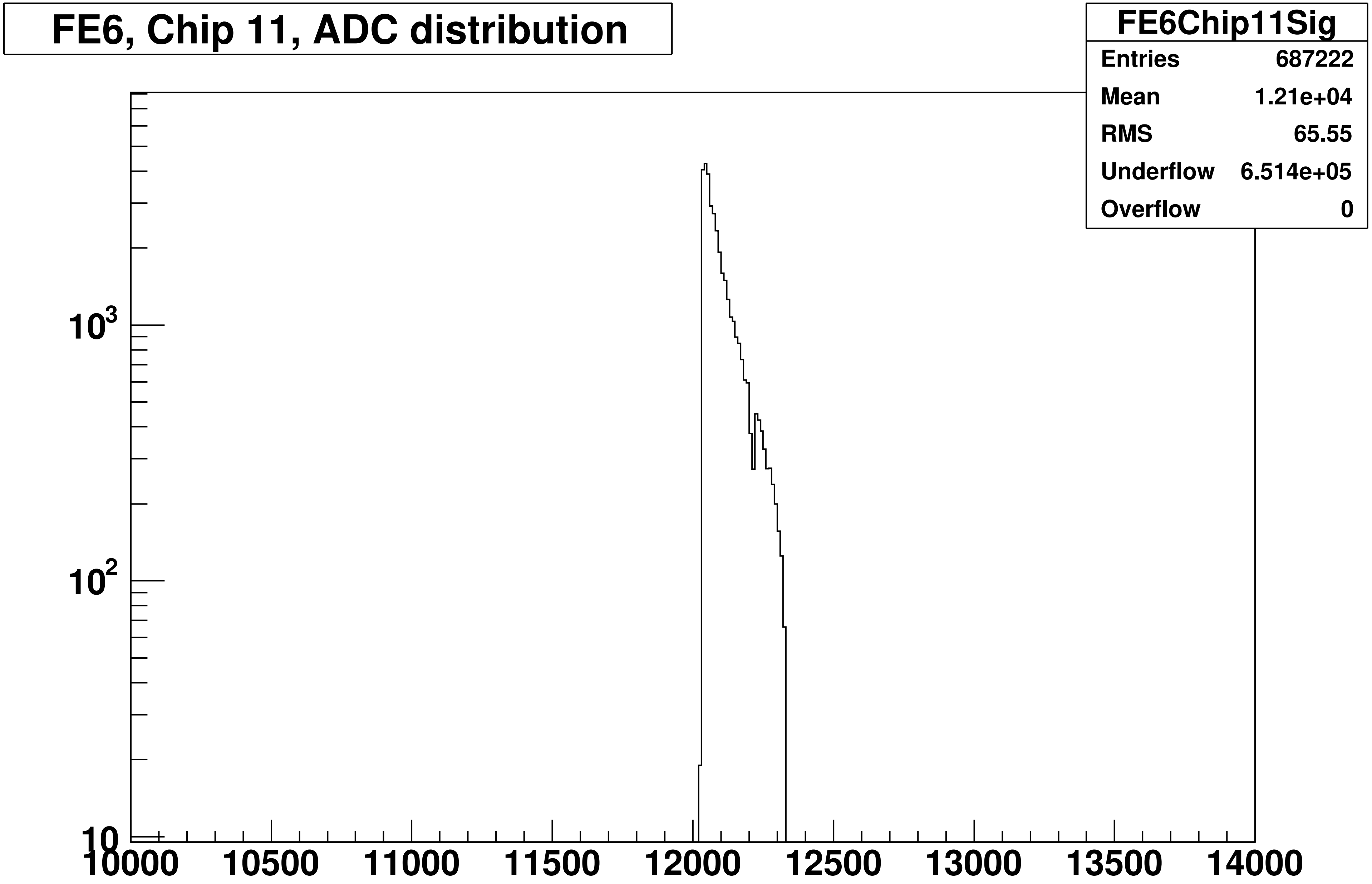
100
150
200



FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 687222
Mean 1.21e+04
RMS 65.55
Underflow 6.514e+05
Overflow 0
10
10^2
10^3
10000
10500
11000
11500
12000
12500
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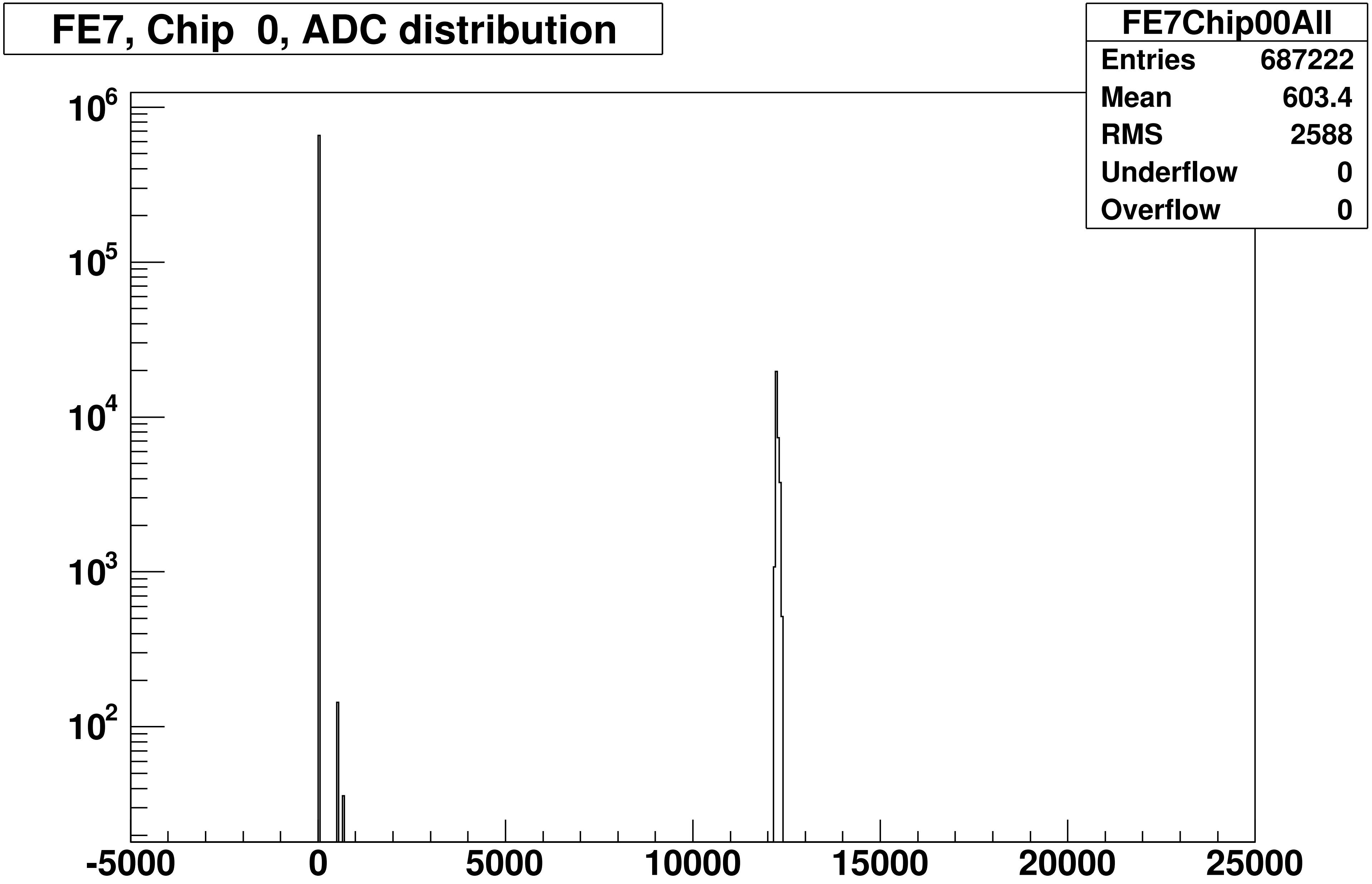
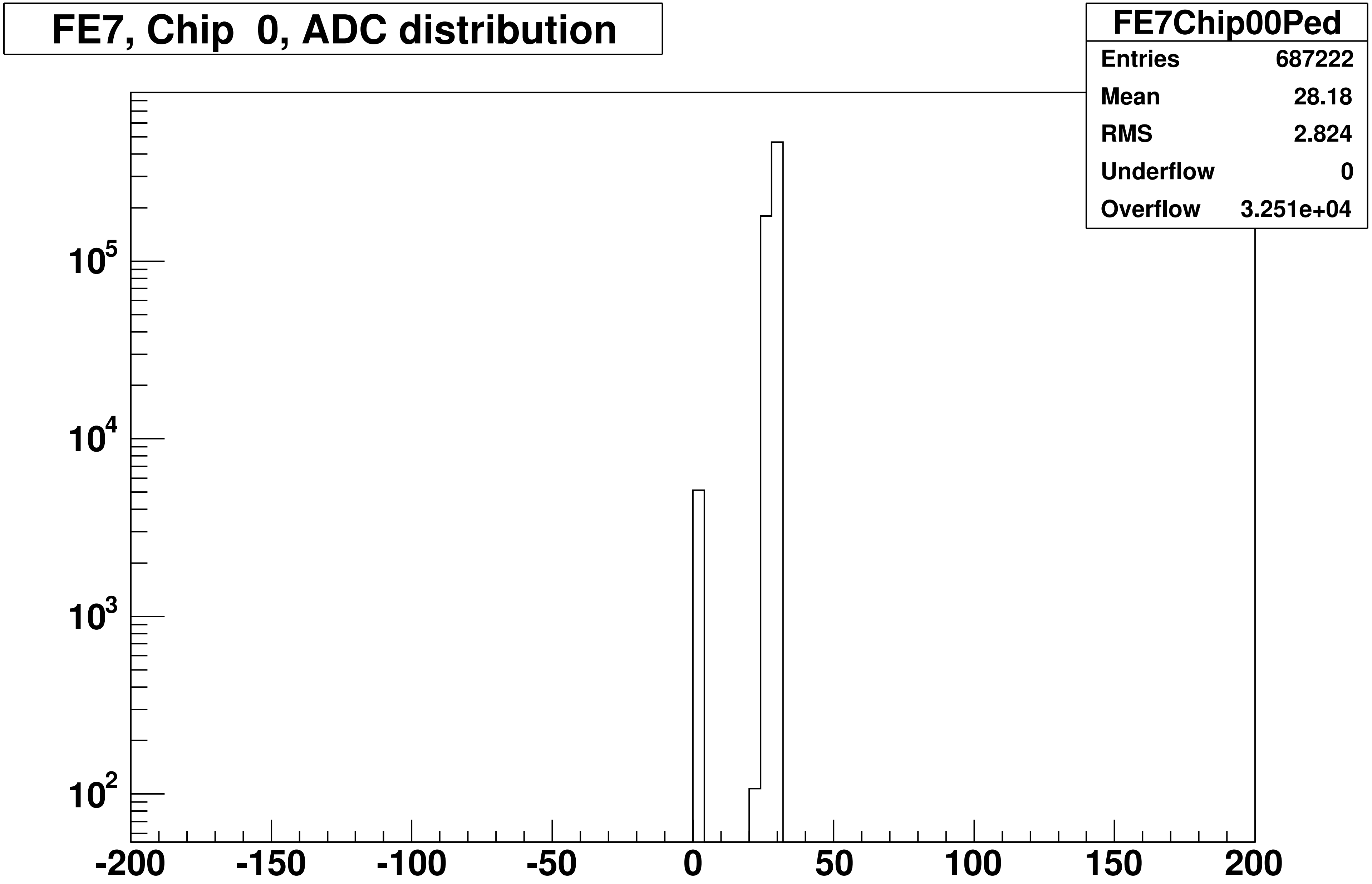

FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 687222
Mean 603.4
RMS 2588
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 687222
Mean 28.18
RMS 2.824
Underflow 0
Overflow 3.251e+04
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10^4
10^3
10^2
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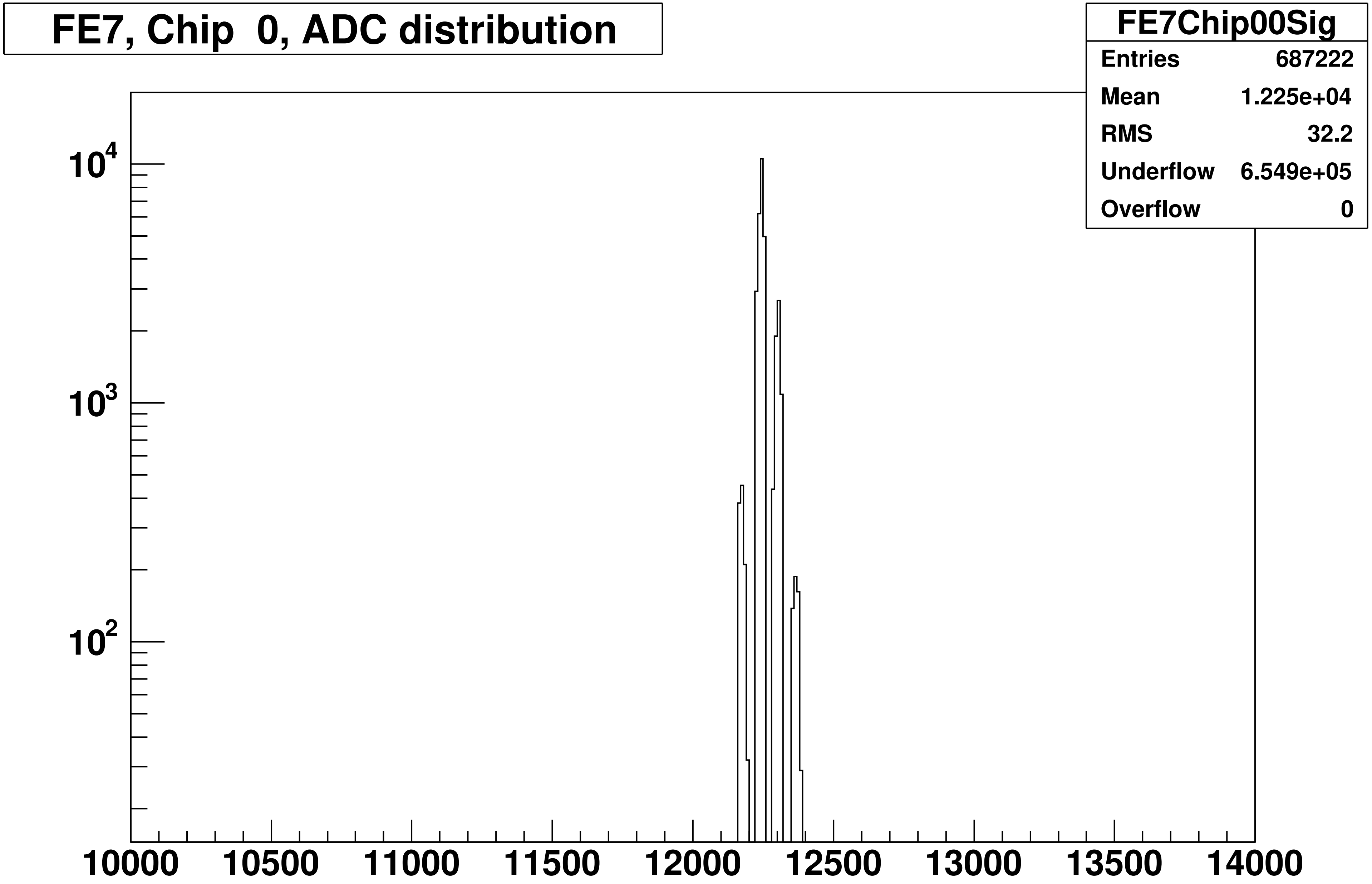
-150
-100
-50
0
50
100
150
200



FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 687222
Mean 1.225e+04
RMS 32.2
Underflow 6.549e+05
Overflow 0
10^4
10^3
10^2
10000
10500
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11500
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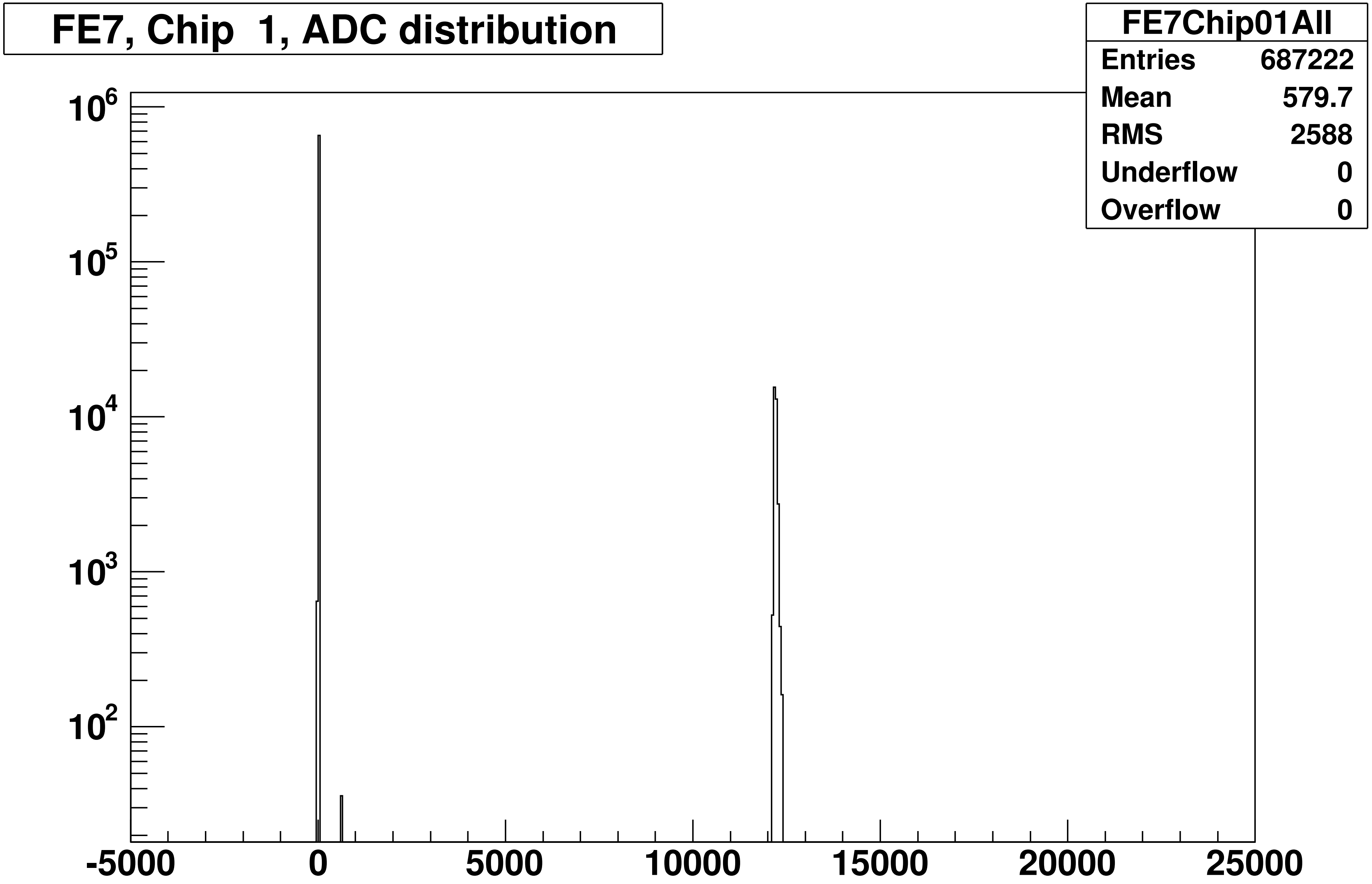
FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 687222
Mean 579.7
RMS 2588
Underflow 0
Overflow 0
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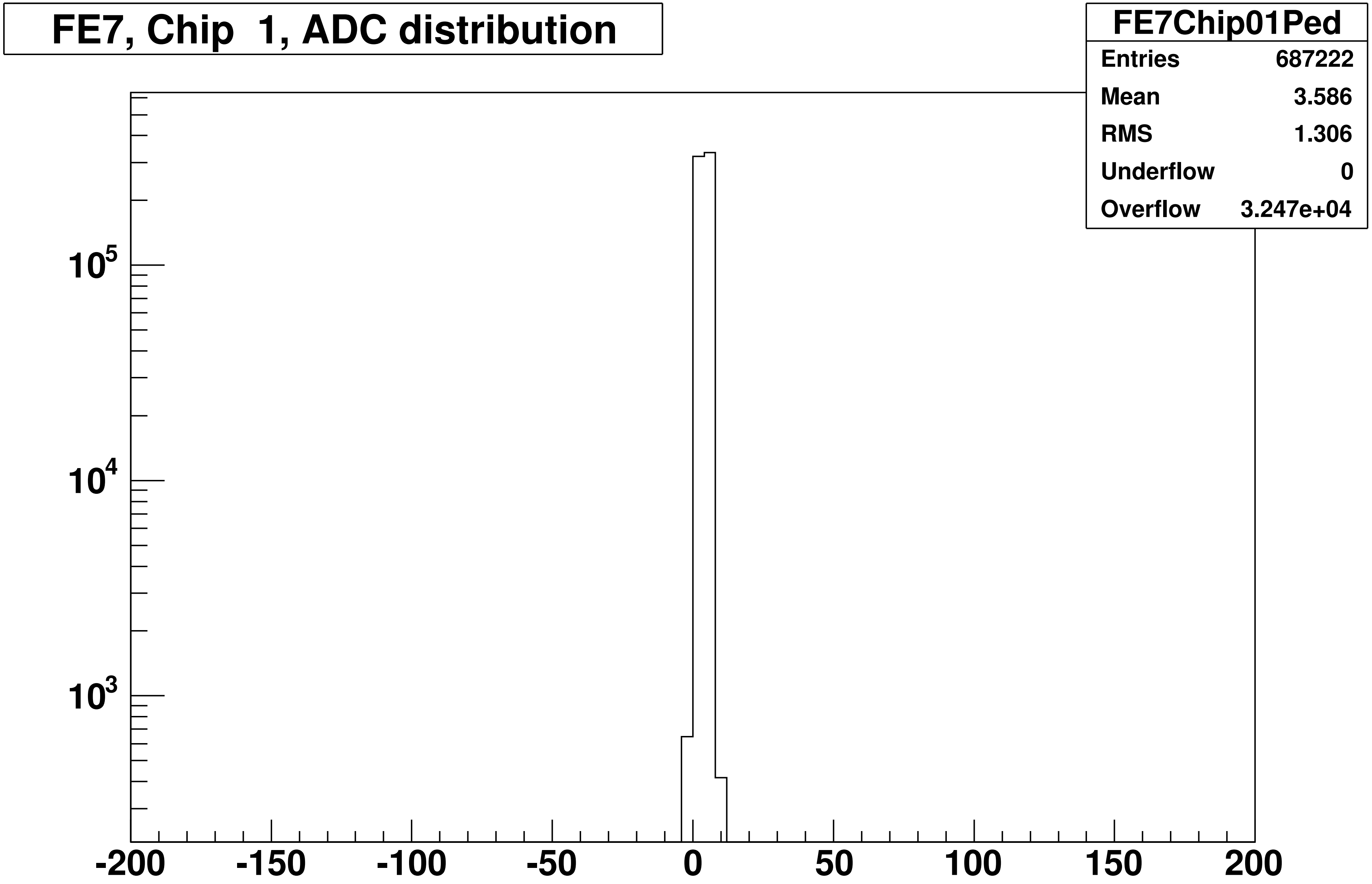
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 687222
Mean 3.586
RMS 1.306
Underflow 0
Overflow 3.247e+04
10^5
10^4
10^3
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-100
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0
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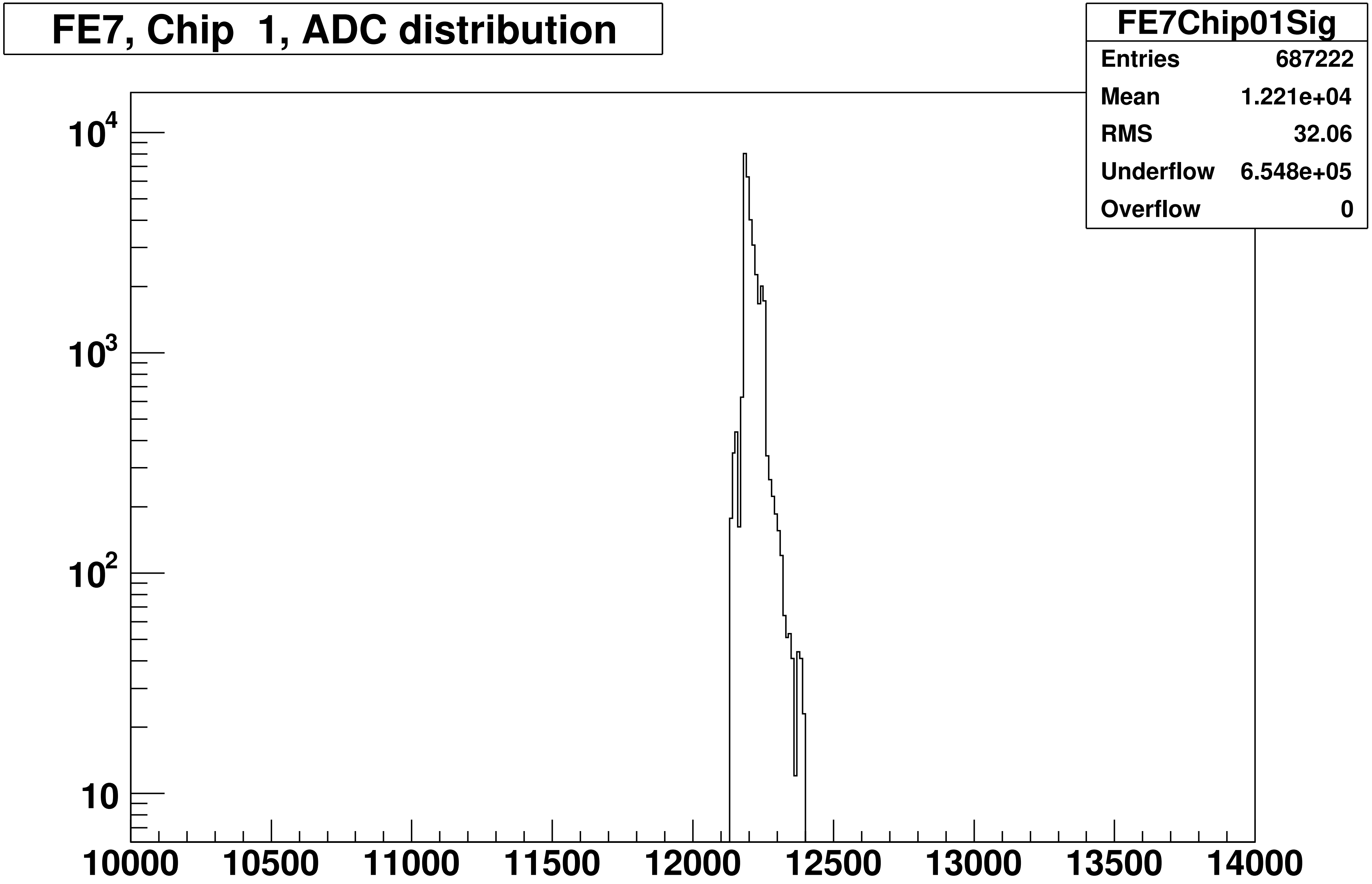
100
150
200


FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 687222
Mean 1.221e+04
RMS 32.06
Underflow 6.548e+05
Overflow 0
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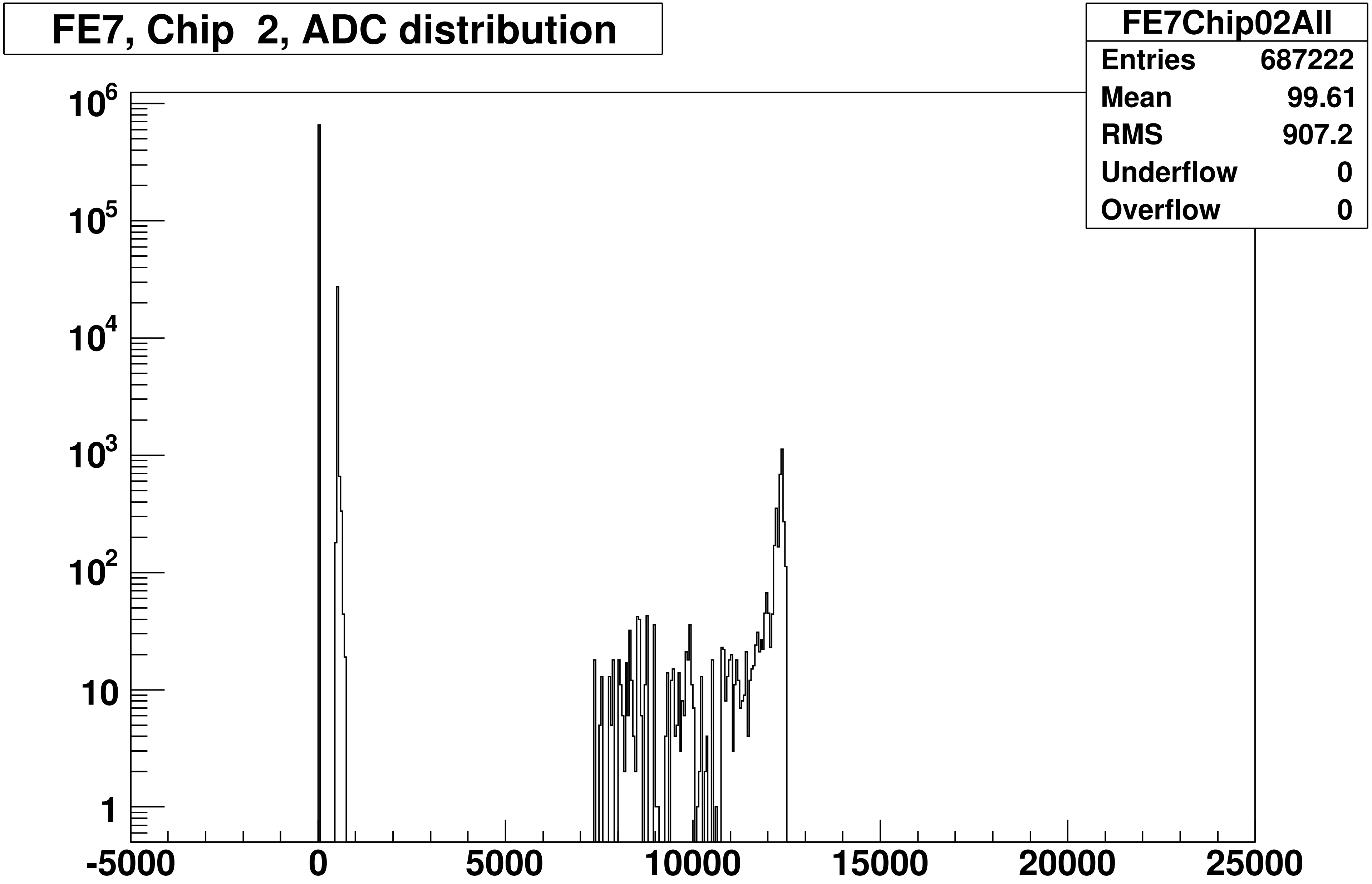
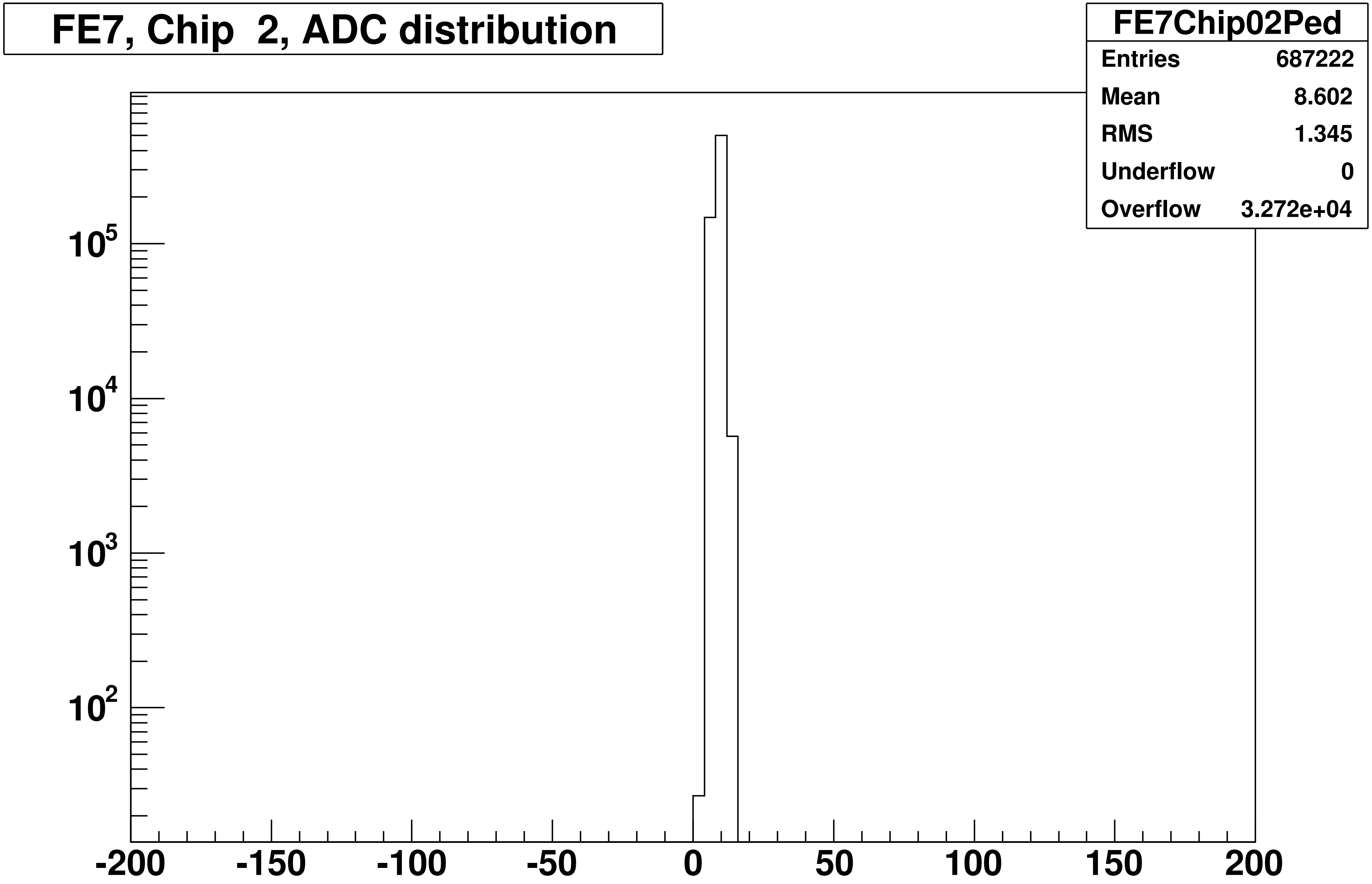
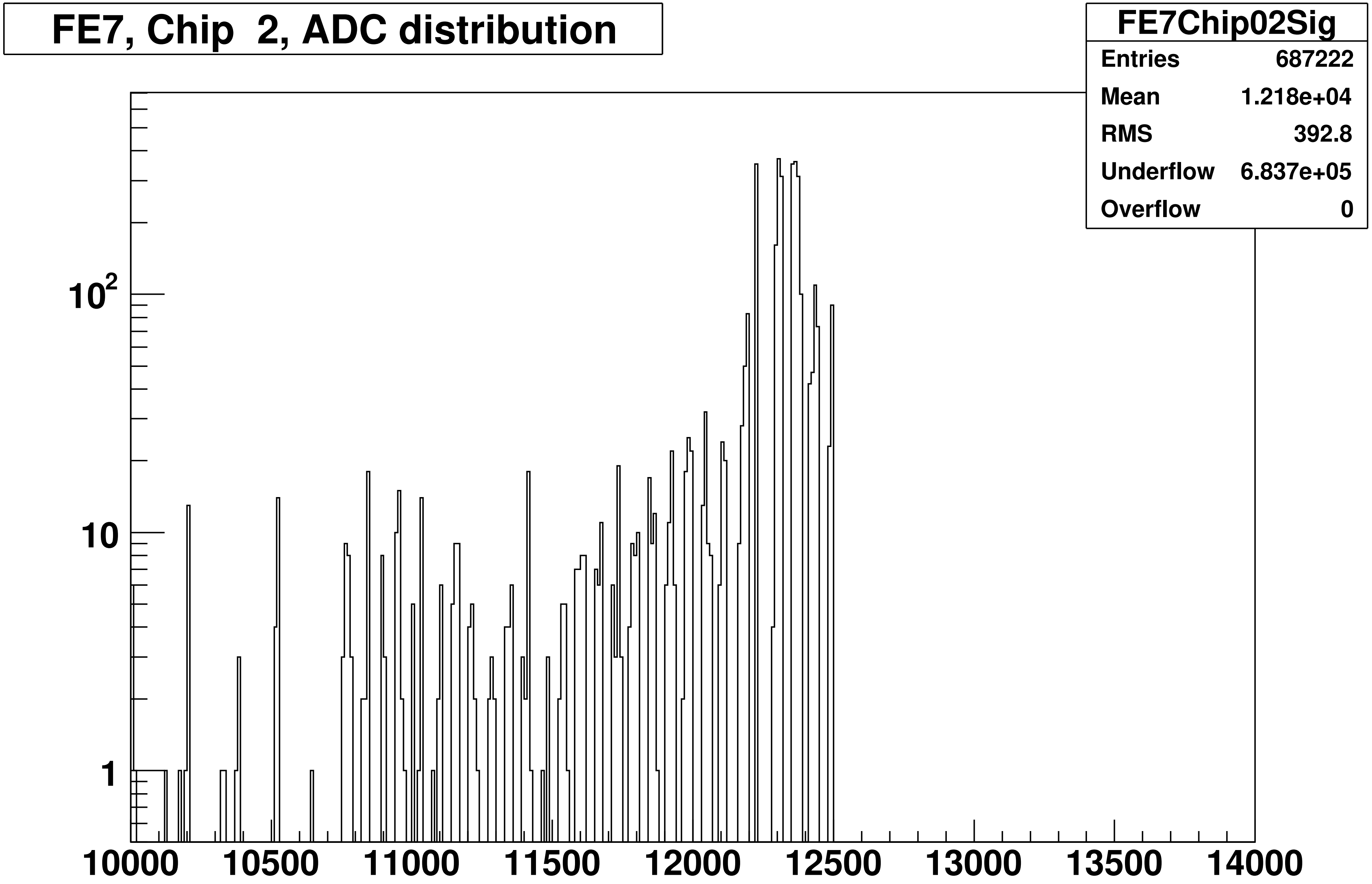
10^3
10^2
10
10000
10500
11000
11500
12000
12500
13000
13500
14000



FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 687222
Mean 99.61
RMS 907.2
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 687222
Mean 8.602
RMS 1.345
Underflow 0
Overflow 3.272e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 687222
Mean 1.218e+04
RMS 392.8
Underflow 6.837e+05
Overflow 0
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10
1
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11500
12000
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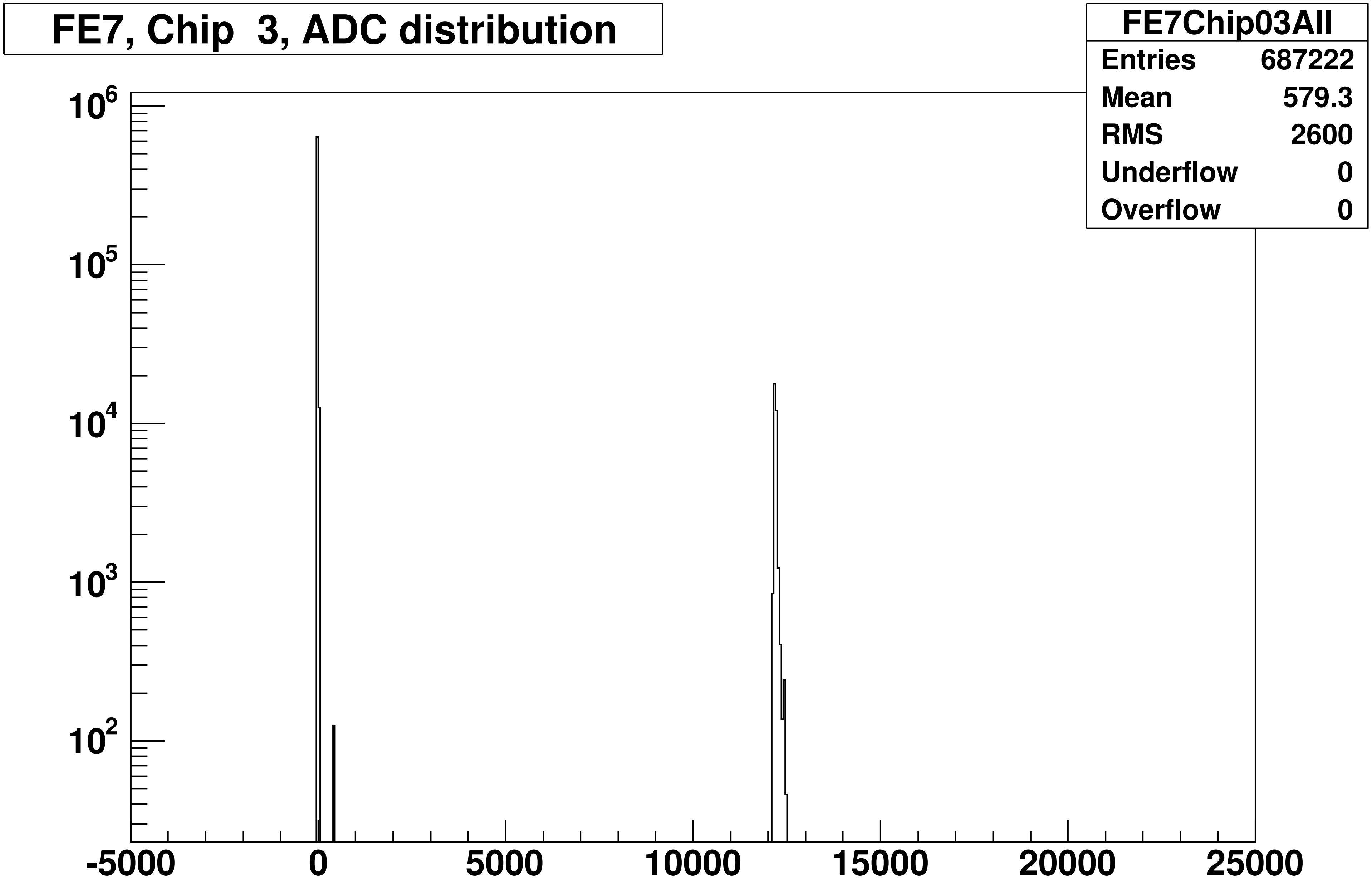
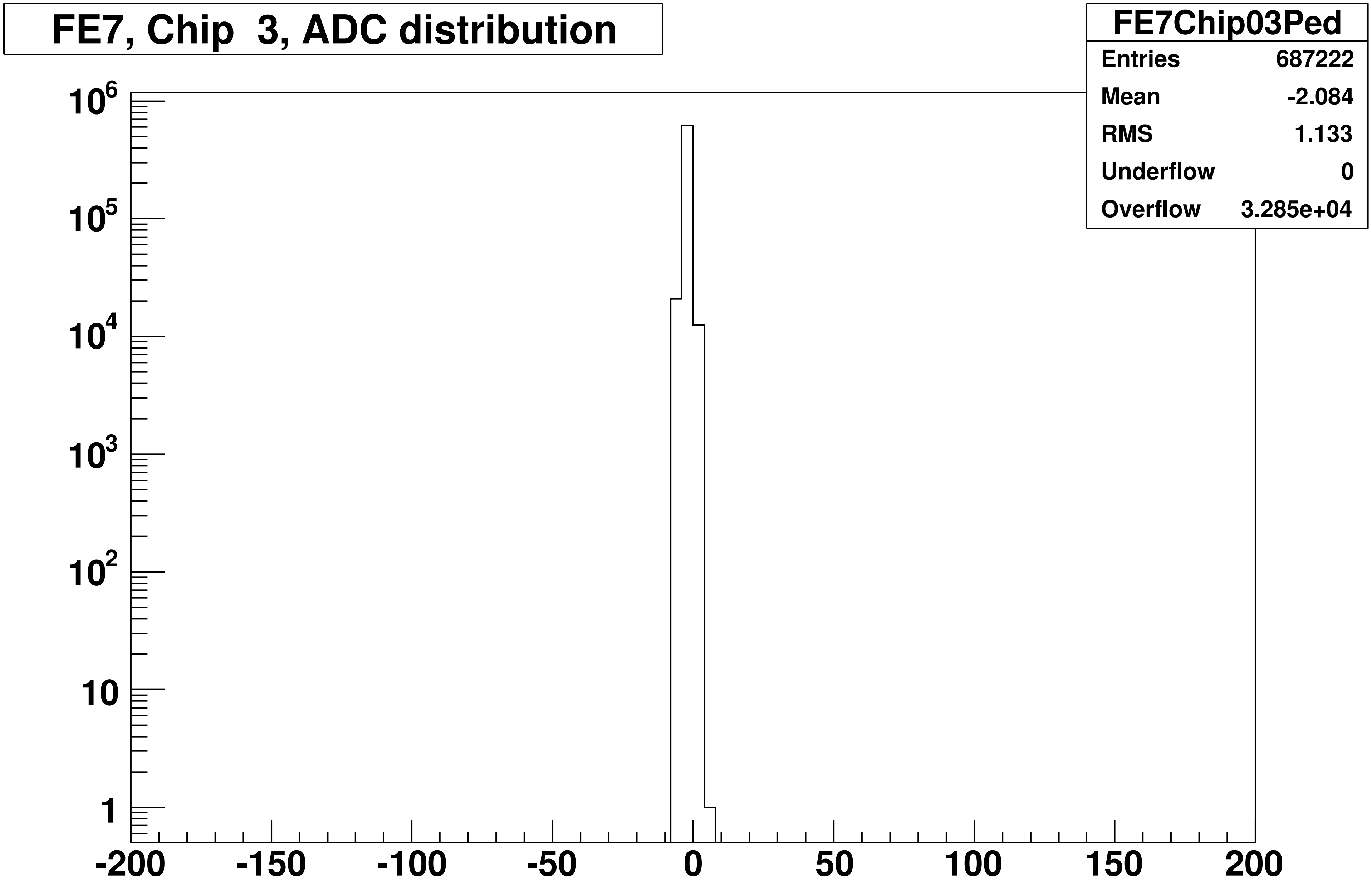

FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 687222
Mean 579.3
RMS 2600
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 687222
Mean -2.084
RMS 1.133
Underflow 0
Overflow 3.285e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
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-100
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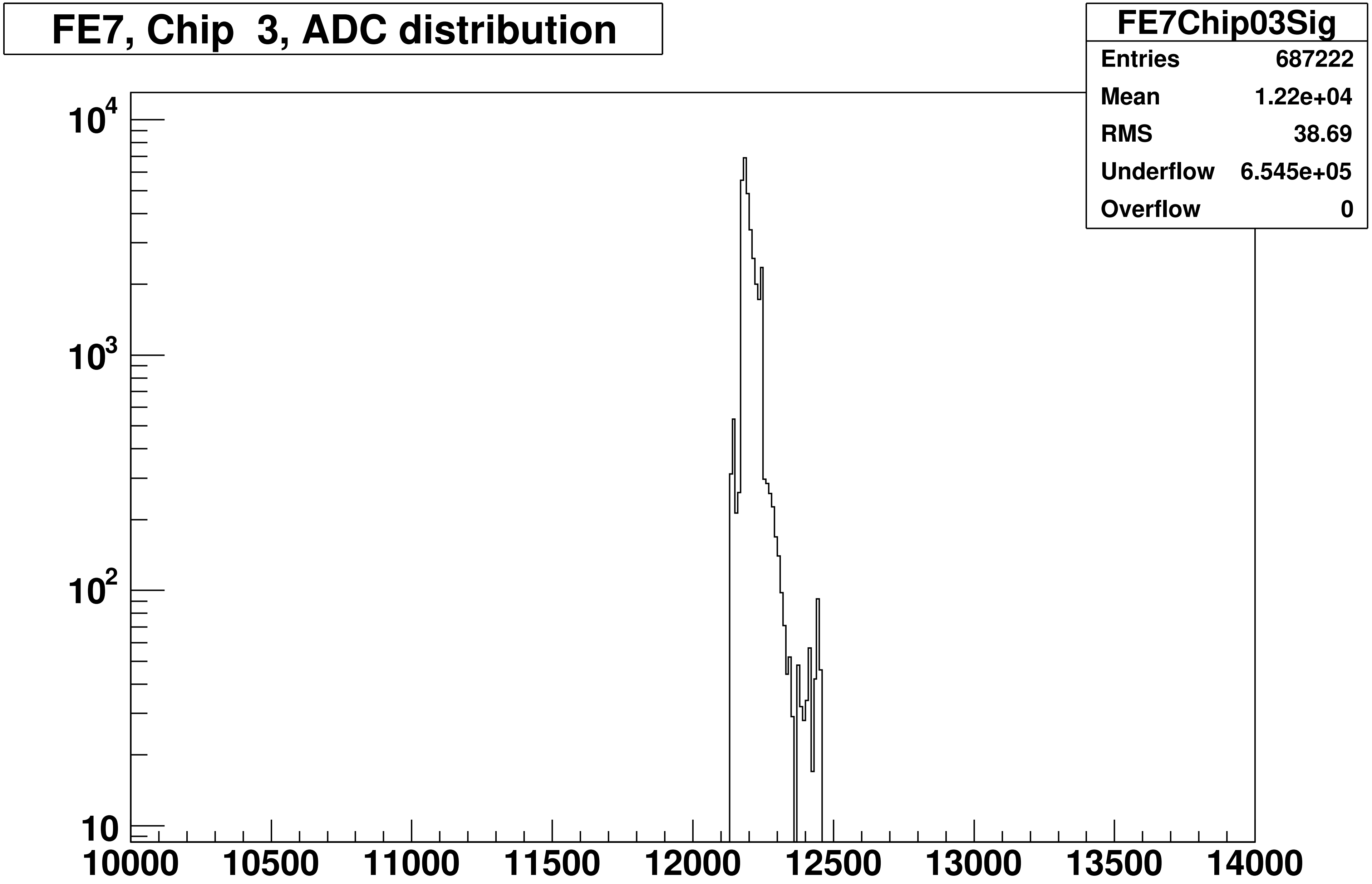
50
100
150
200


FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 687222
Mean 1.22e+04
RMS 38.69
Underflow 6.545e+05
Overflow 0
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10^3
10^2
10
10000
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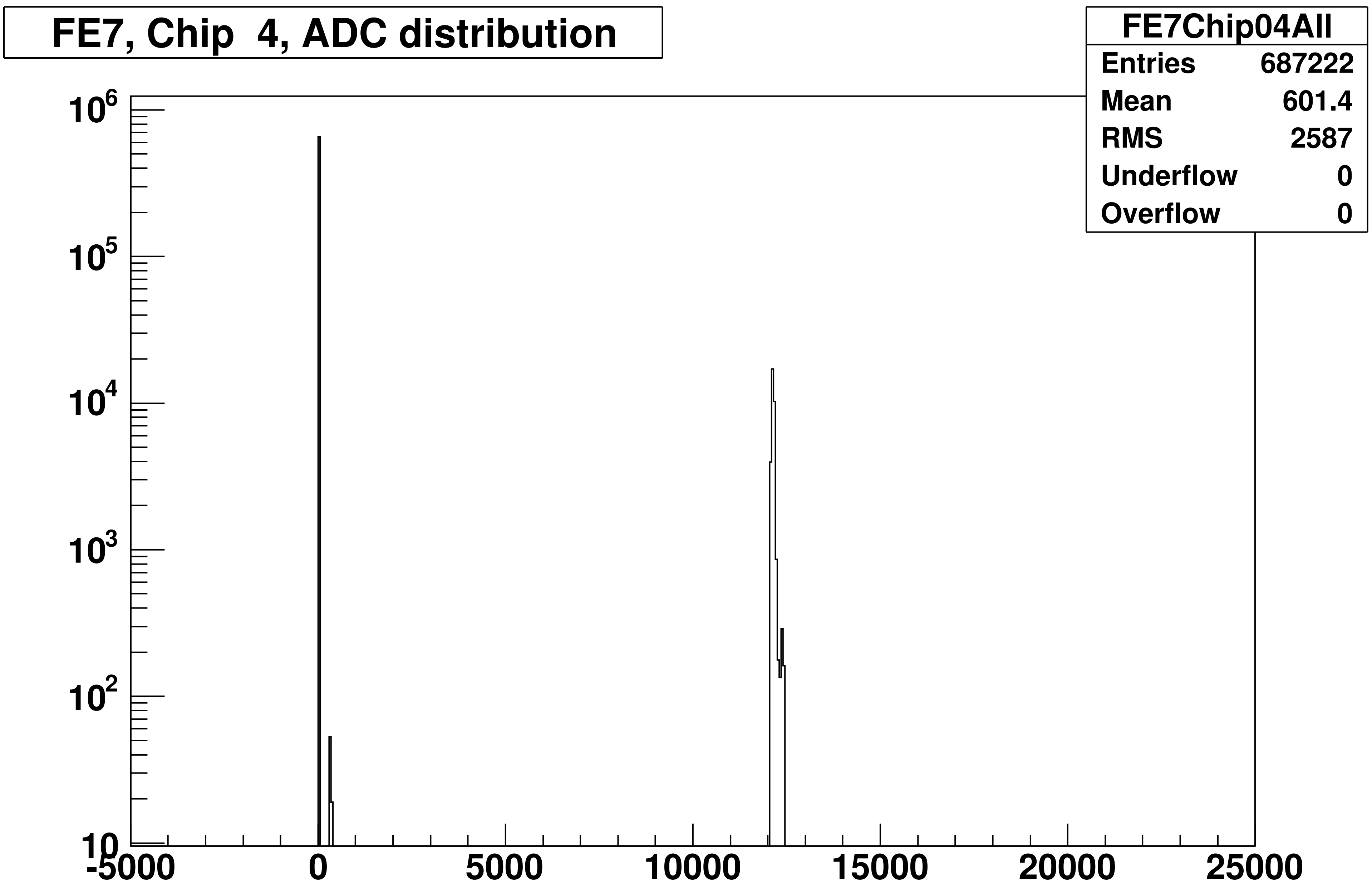
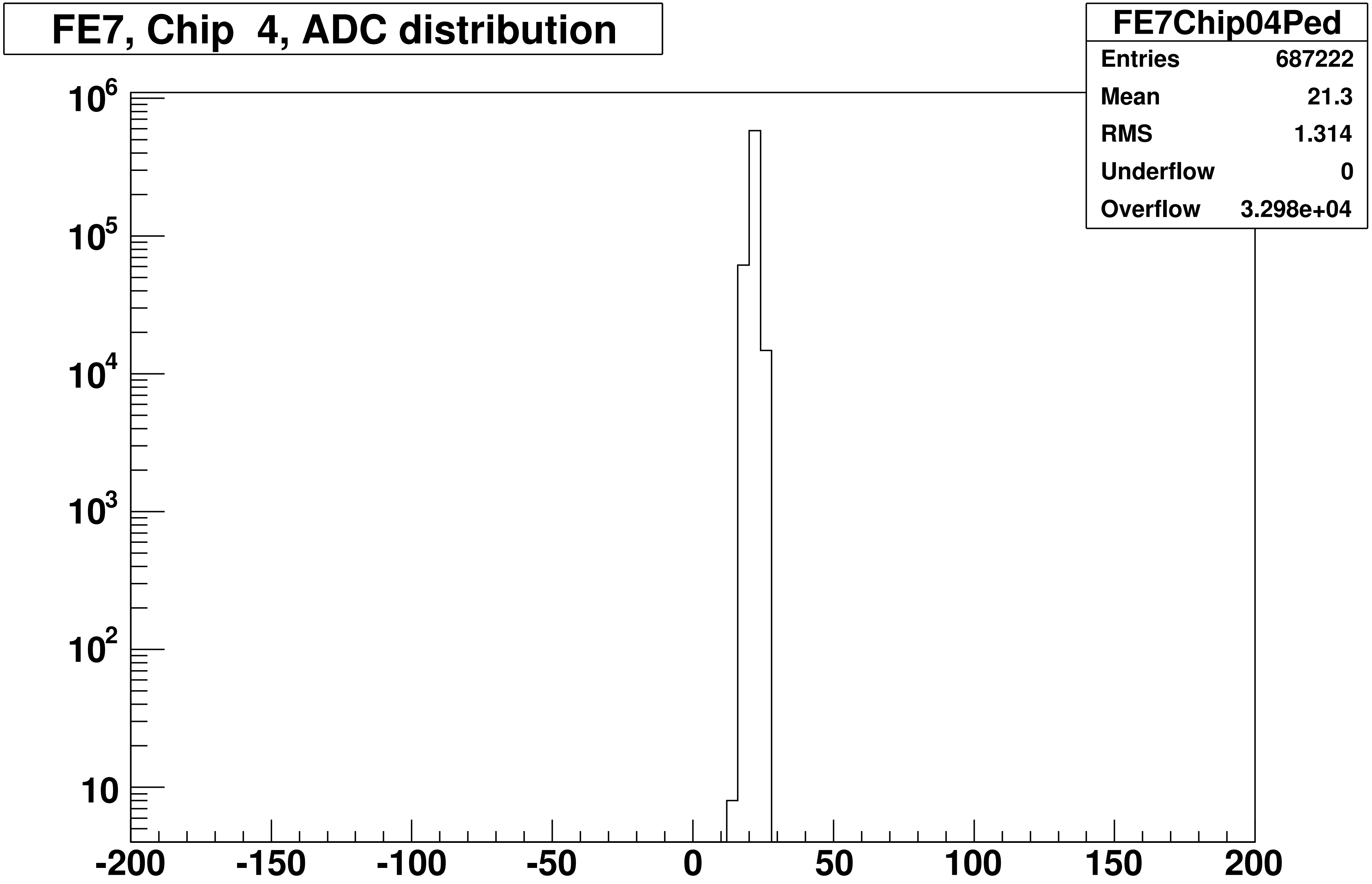

FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 687222
Mean 601.4
RMS 2587
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 687222
Mean 21.3
RMS 1.314
Underflow 0
Overflow 3.298e+04
10
10^2
10^3
10^4
10^5
10^6
-200
-150
-100
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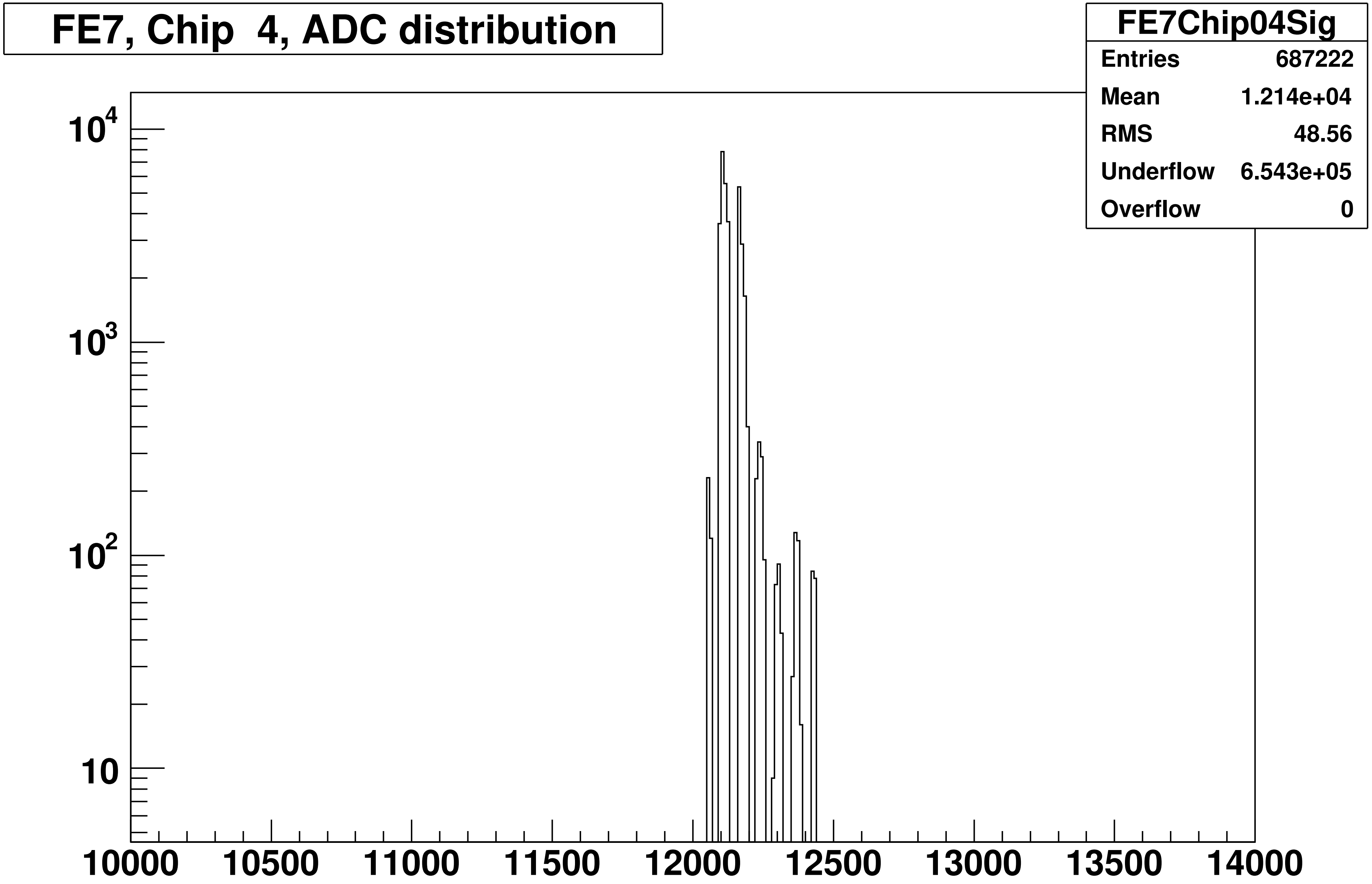
0
50
100
150
200


FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 687222
Mean 1.214e+04
RMS 48.56
Underflow 6.543e+05
Overflow 0
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10^3
10^2
10
10000
10500
11000
11500
12000
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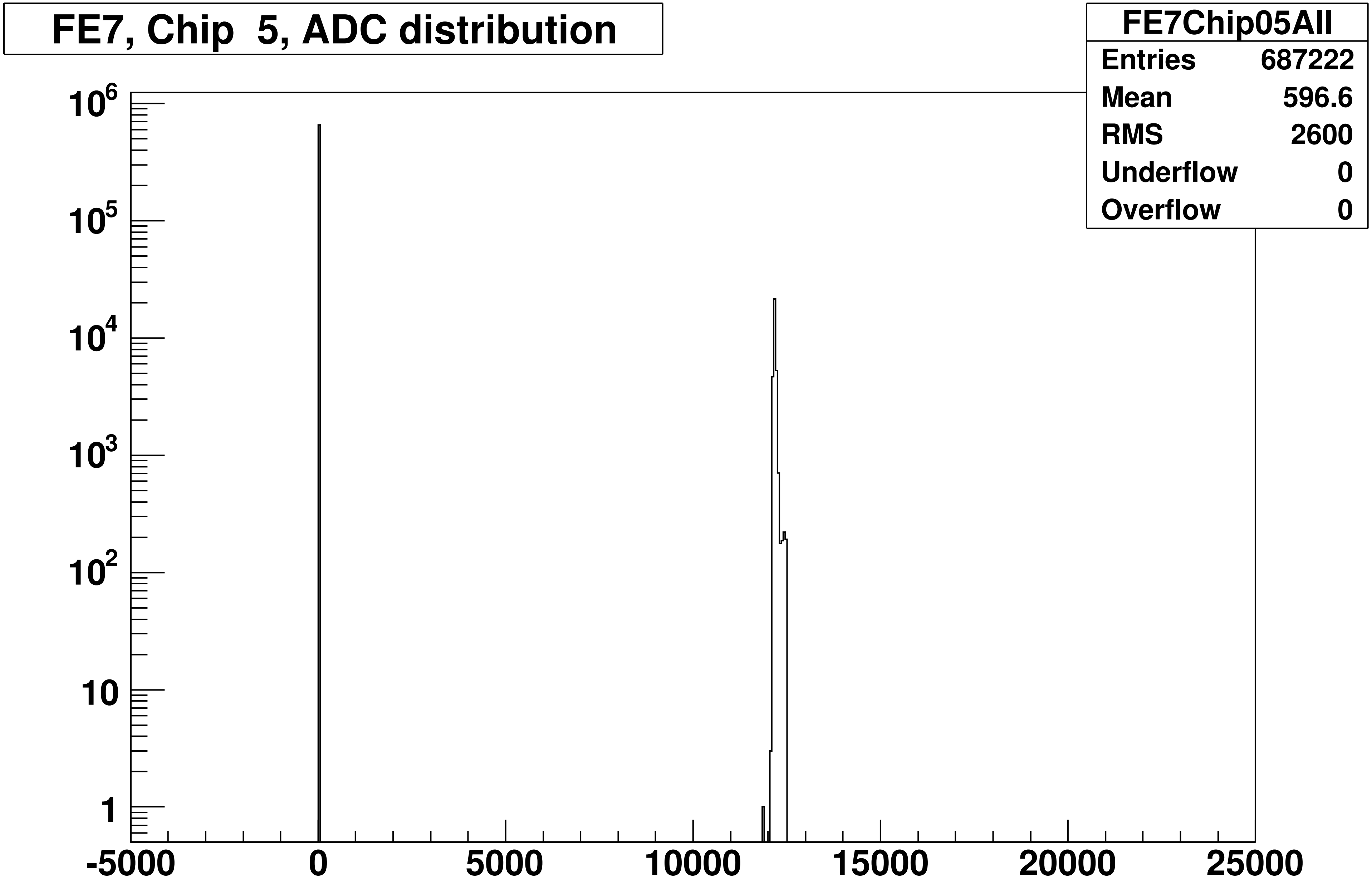
14000


FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 687222
Mean 596.6
RMS 2600
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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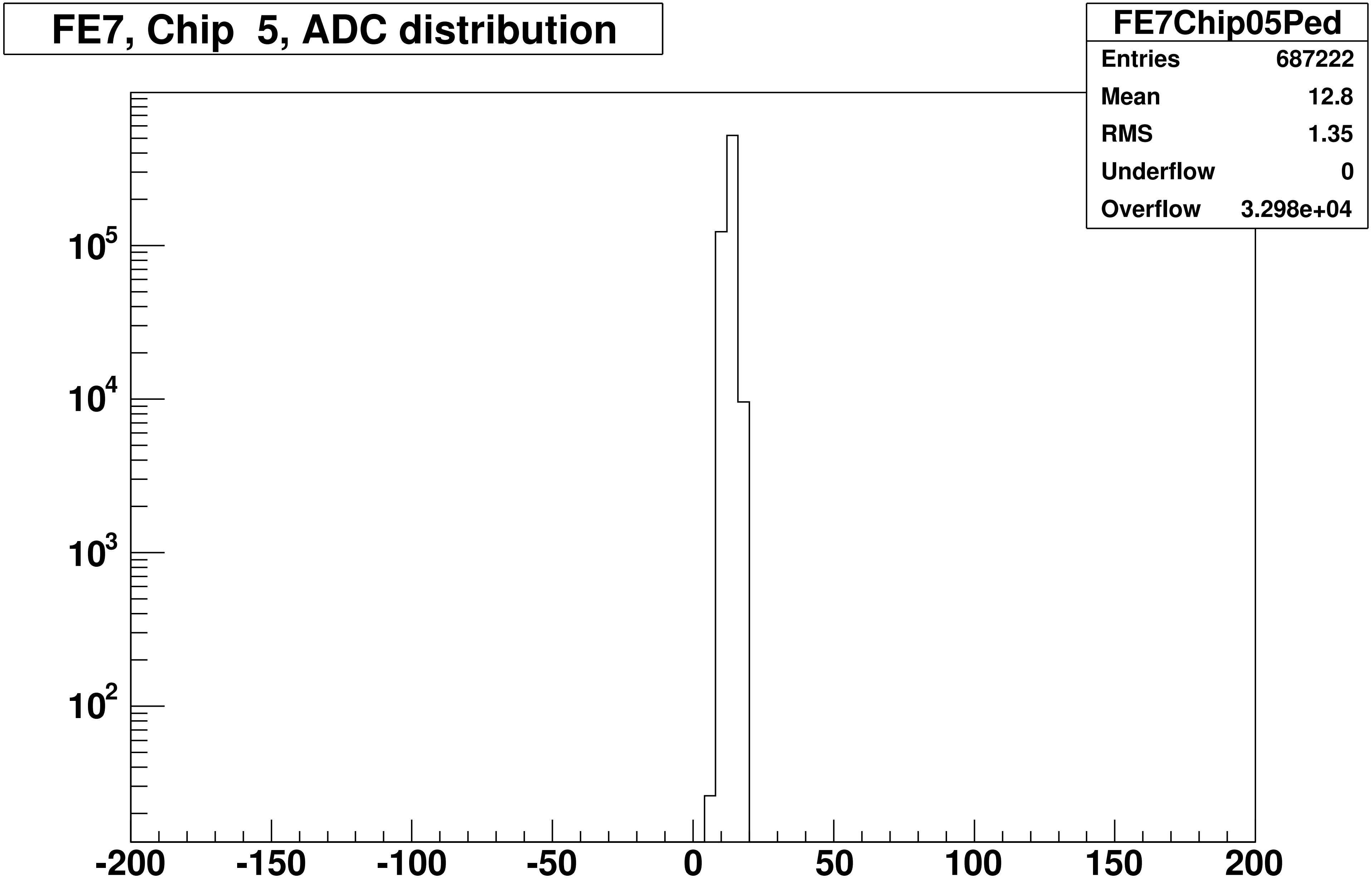
25000


FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 687222
Mean 12.8
RMS 1.35
Underflow 0
Overflow 3.298e+04
10^5
10^4
10^3
10^2
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-100
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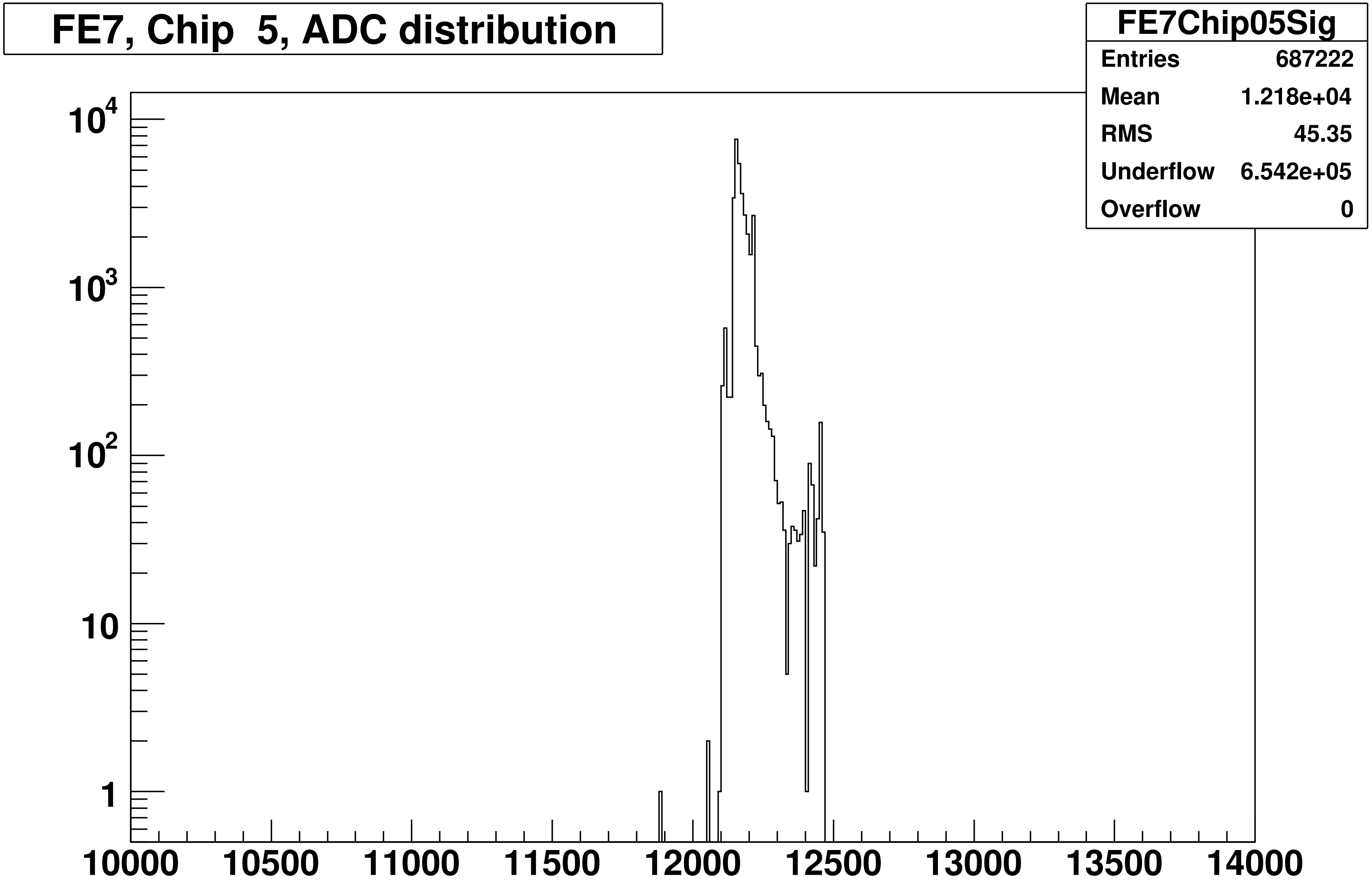
100
150
200


FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 687222
Mean 1.218e+04
RMS 45.35
Underflow 6.542e+05
Overflow 0
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10^3
10^2
10
1
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11500
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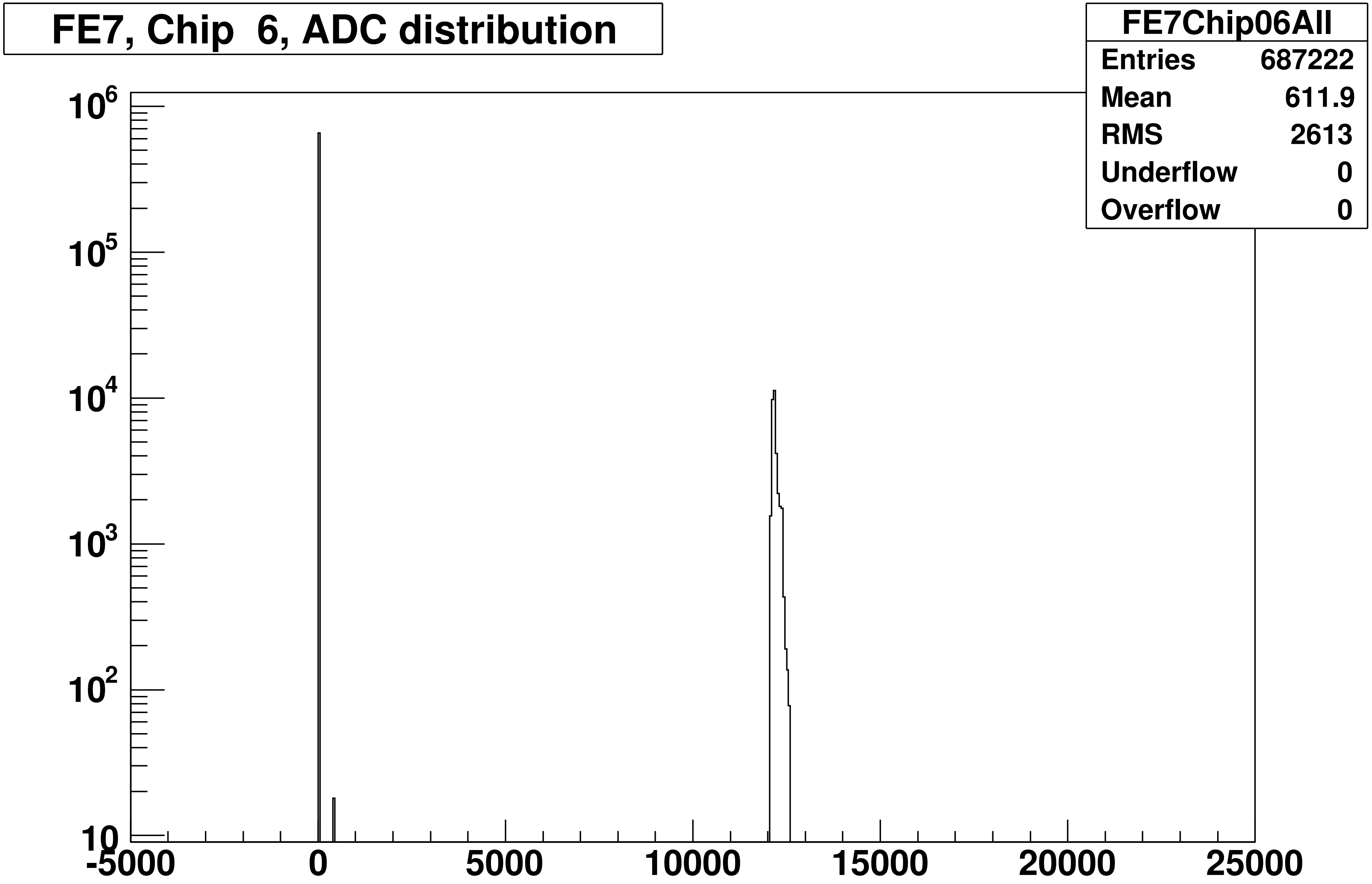
14000


FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 687222
Mean 611.9
RMS 2613
Underflow 0
Overflow 0
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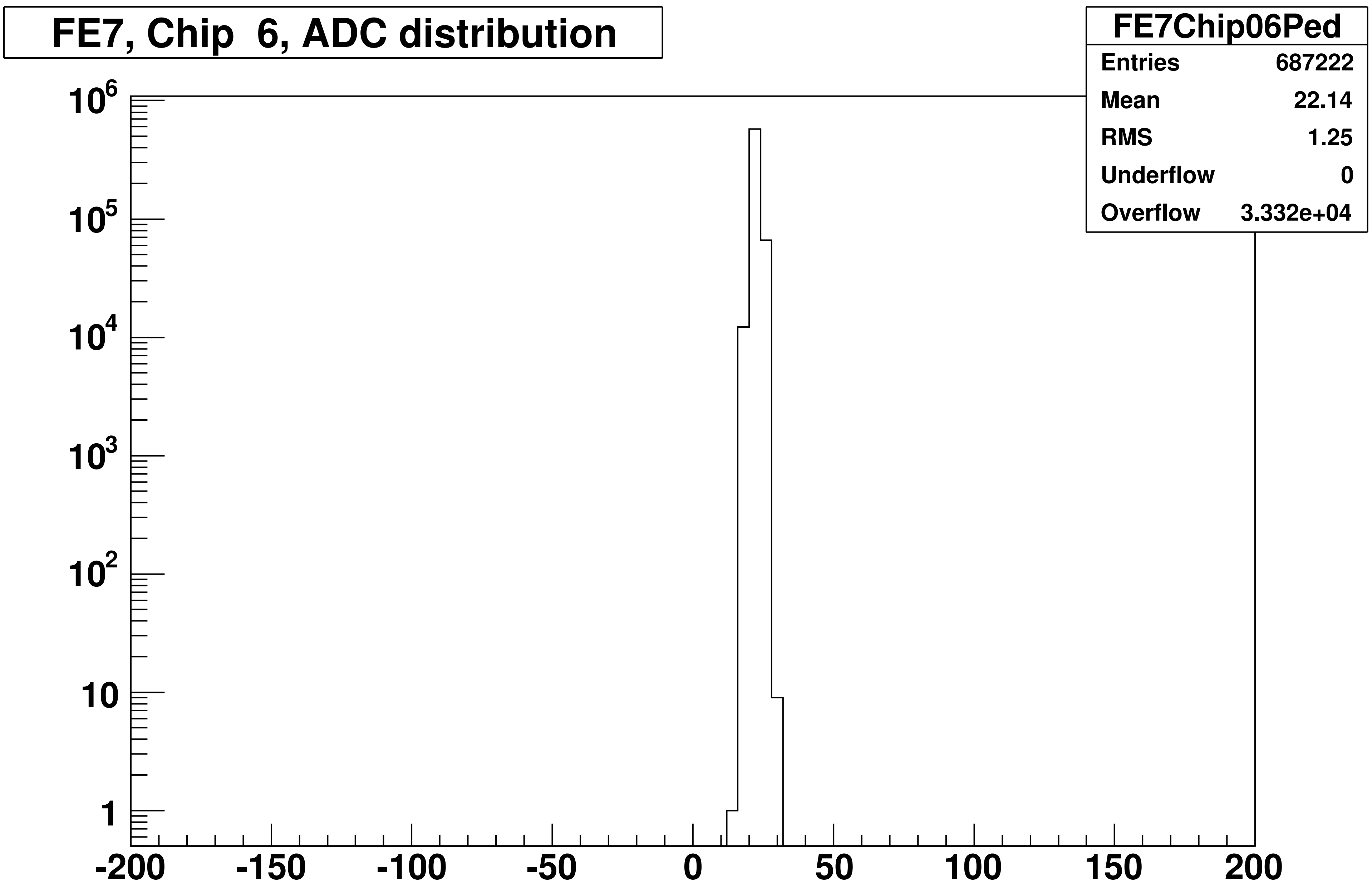
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 687222
Mean 22.14
RMS 1.25
Underflow 0
Overflow 3.332e+04
1
10
10^2
10^3
10^4
10^5
10^6
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0
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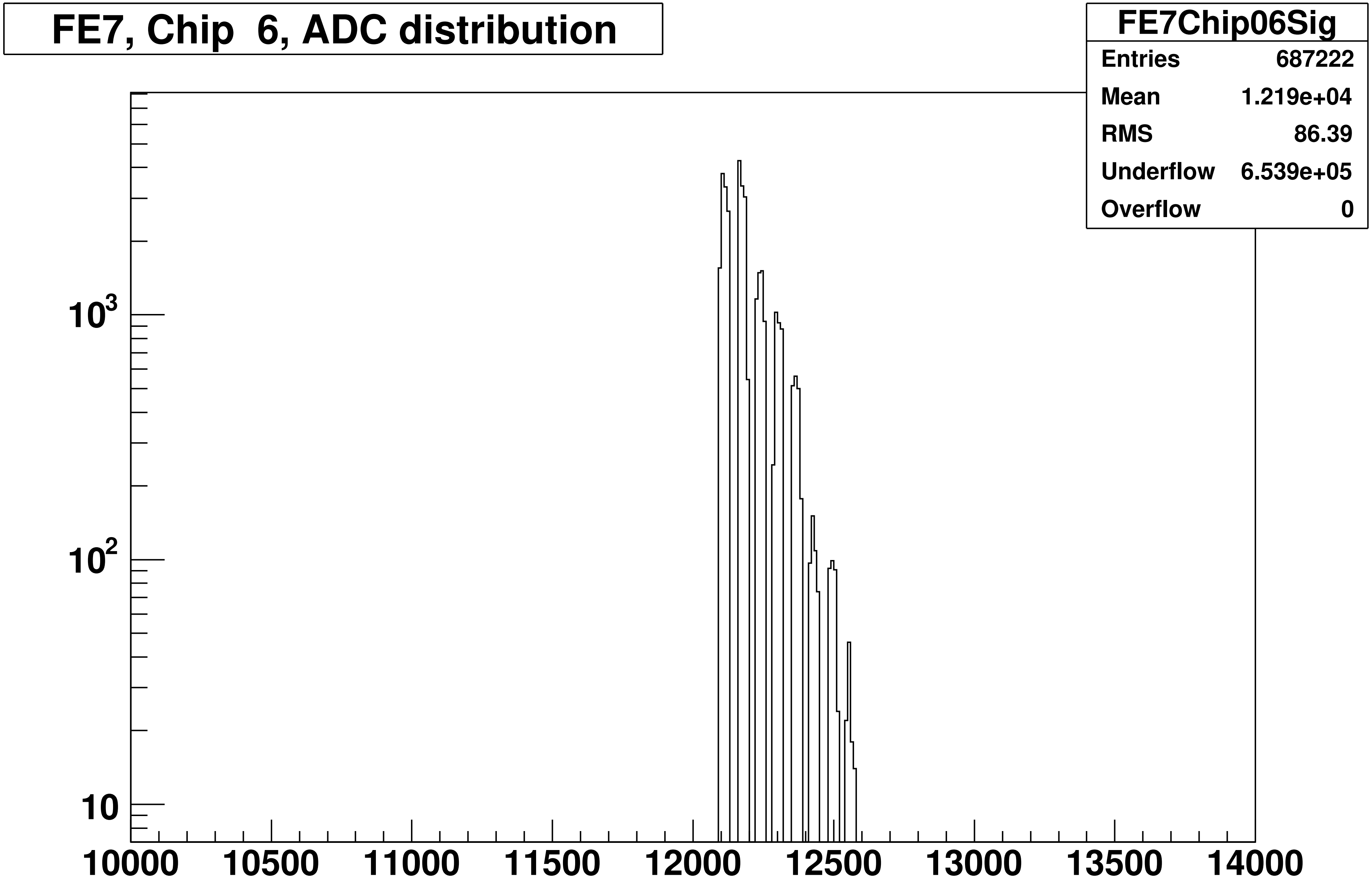
100
150
200



FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 687222
Mean 1.219e+04
RMS 86.39
Underflow 6.539e+05
Overflow 0
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10^2
10
10000
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11000
11500
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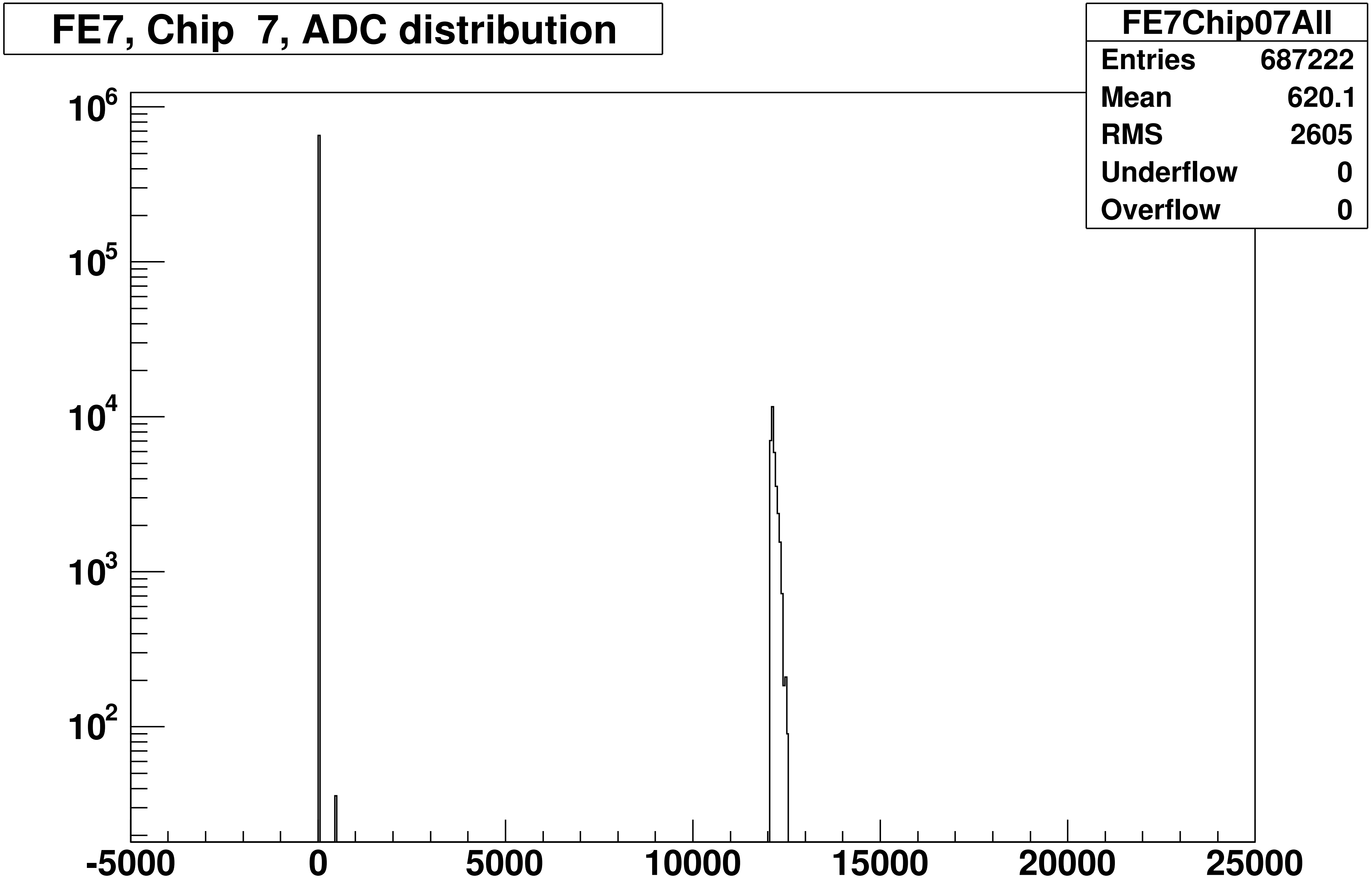
13000
13500
14000


FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 687222
Mean 620.1
RMS 2605
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
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0
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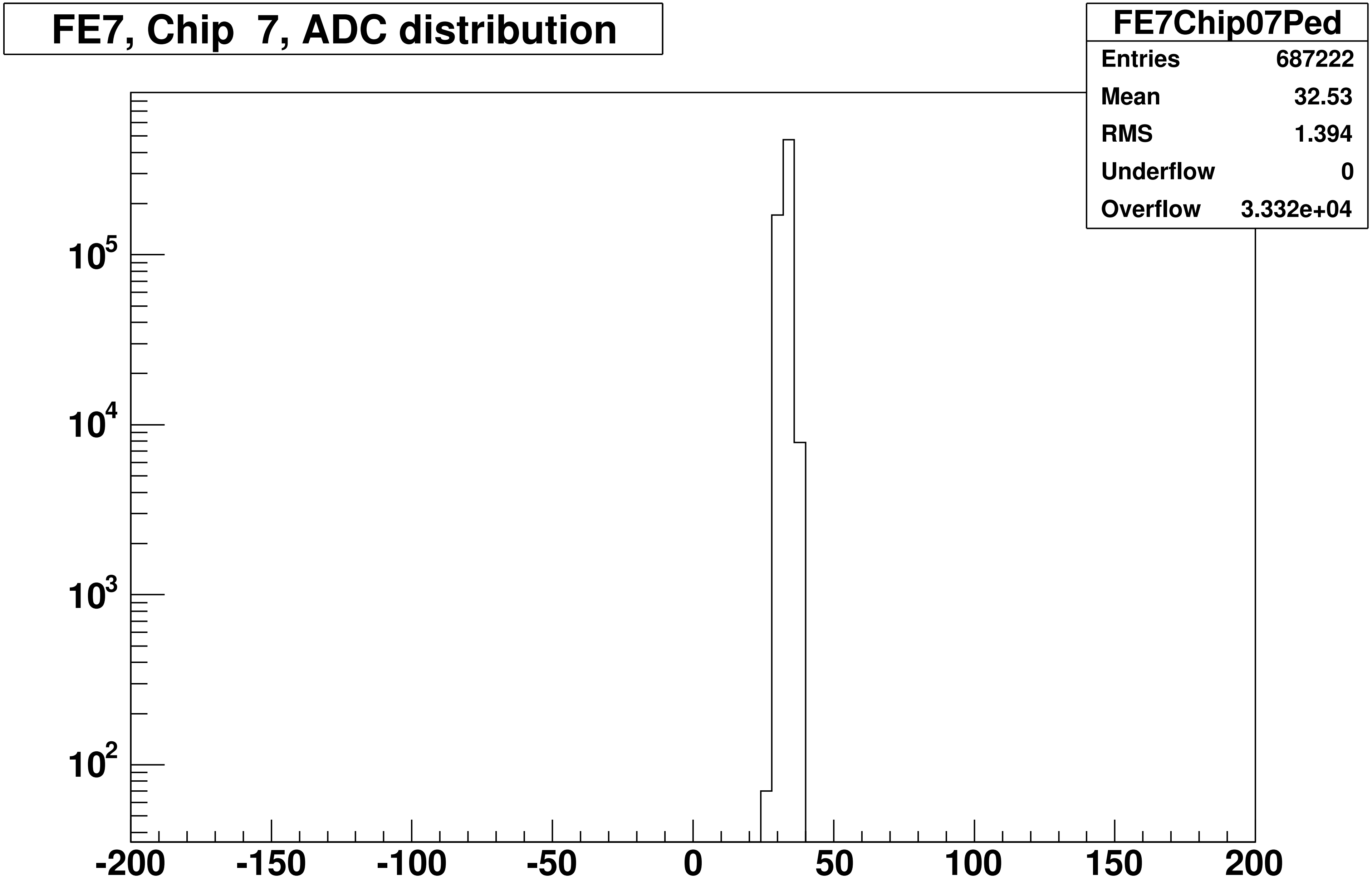
10000
15000
20000
25000



FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 687222
Mean 32.53
RMS 1.394
Underflow 0
Overflow 3.332e+04
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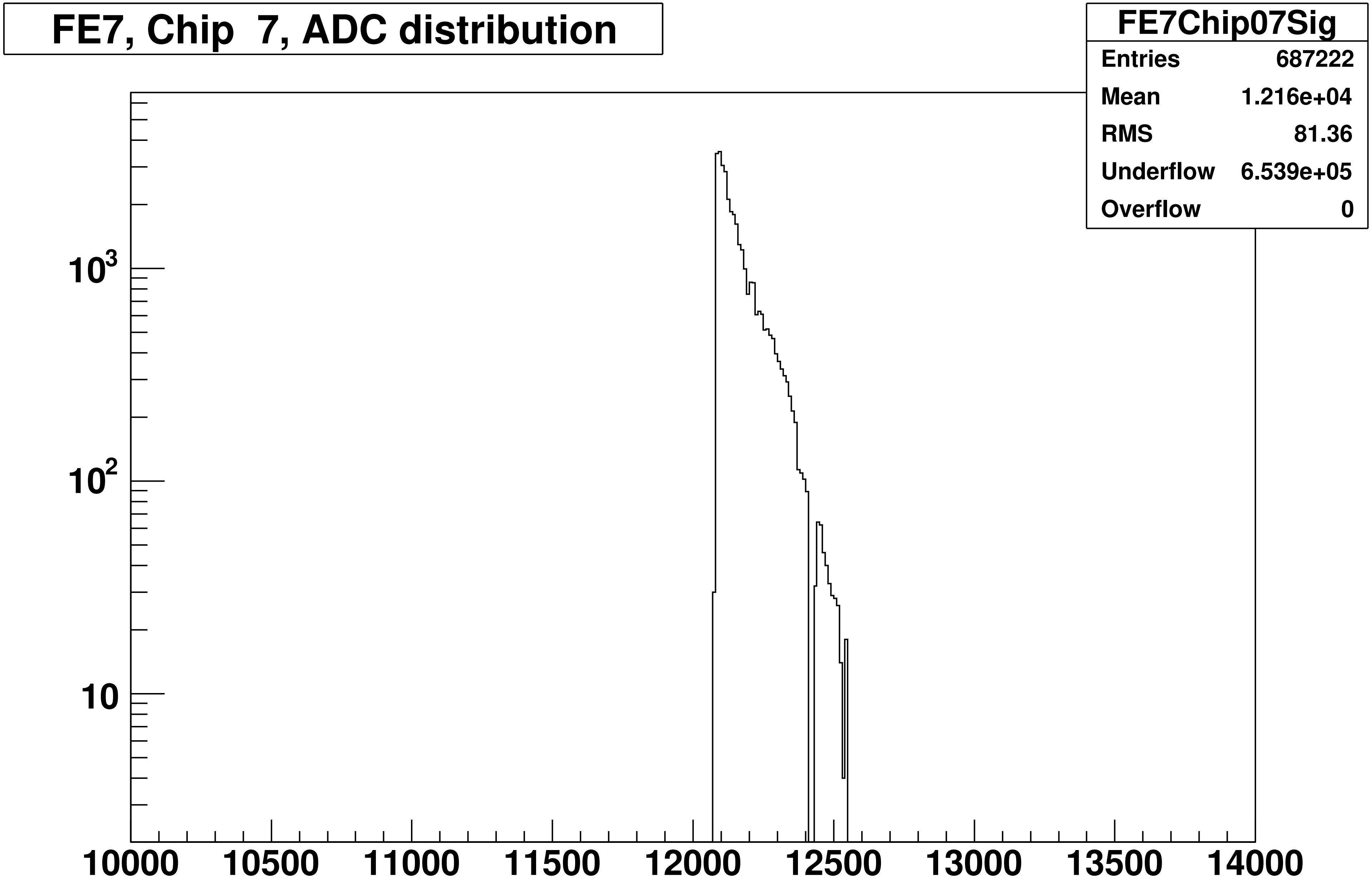
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 687222
Mean 1.216e+04
RMS 81.36
Underflow 6.539e+05
Overflow 0
10^3
10^2
10
10000
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11500
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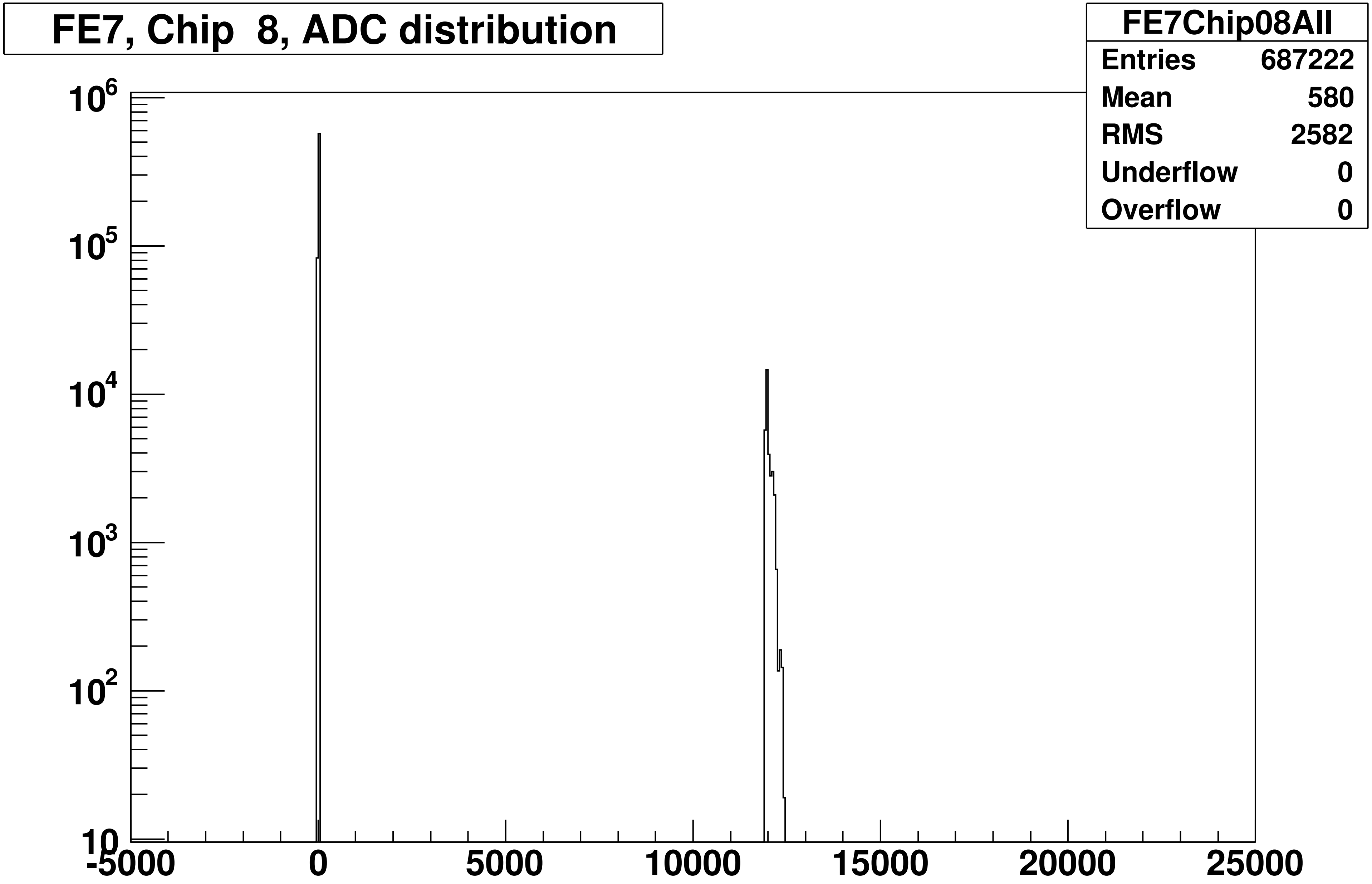

FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 687222
Mean 580
RMS 2582
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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10000
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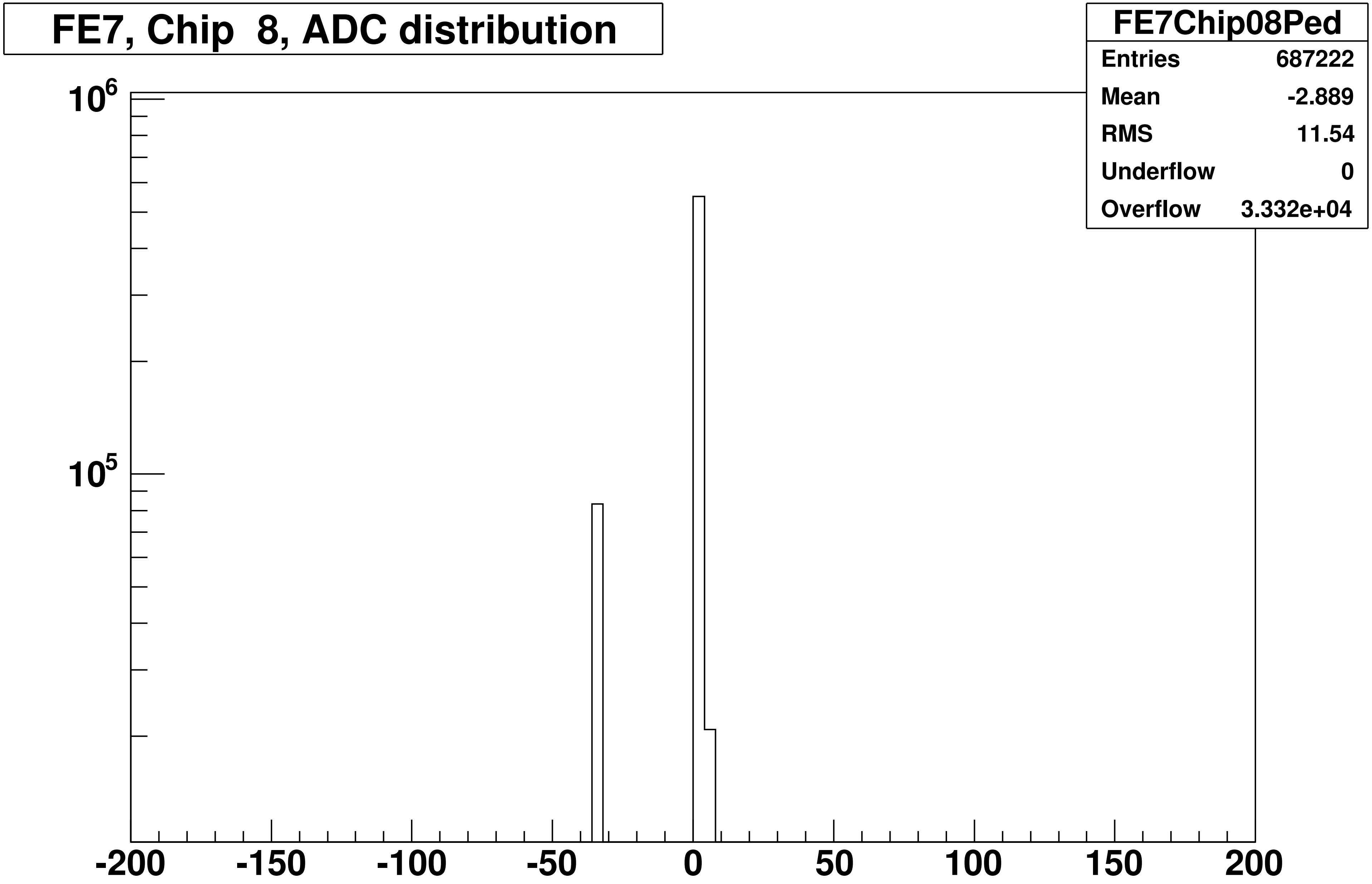
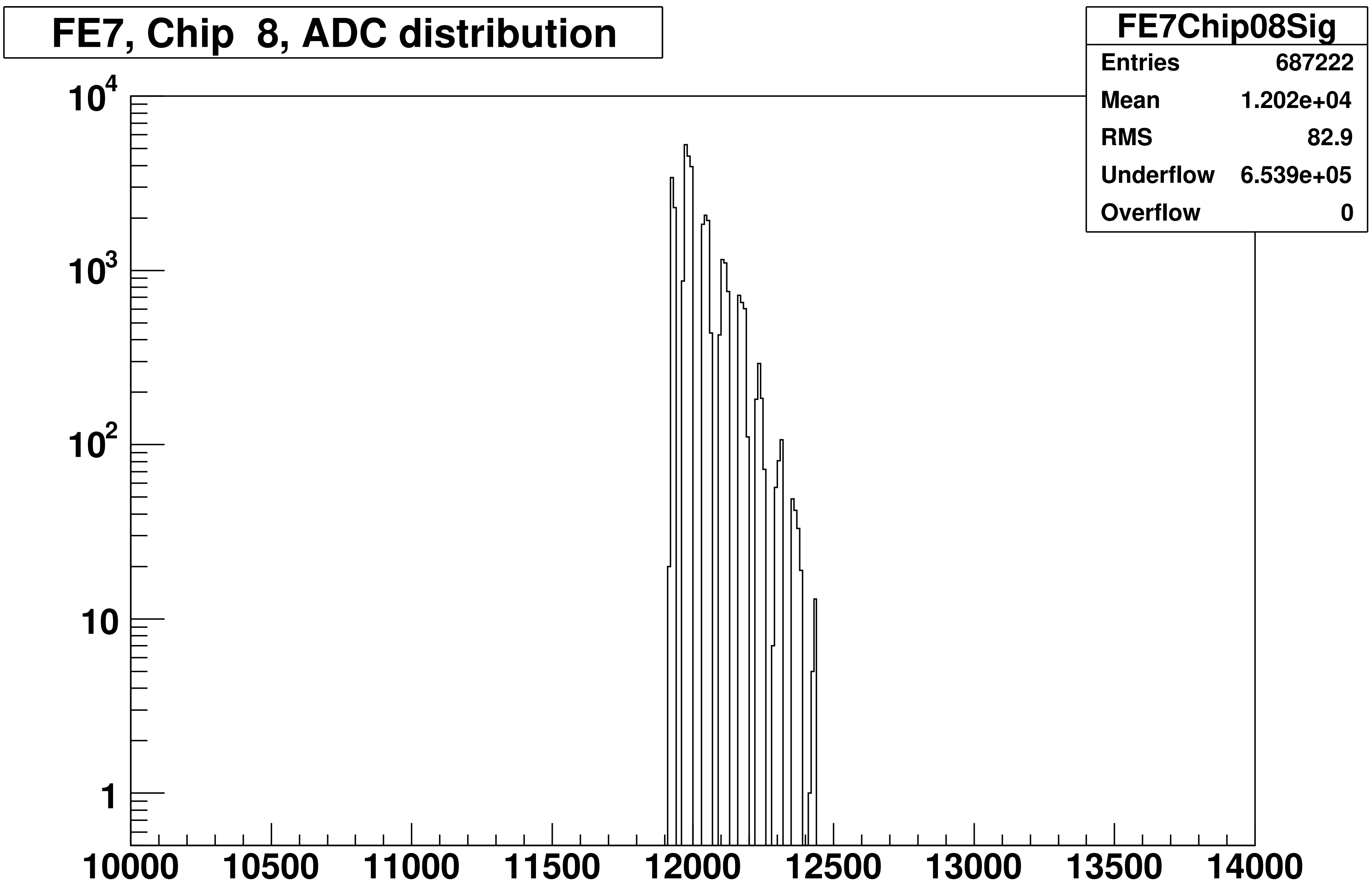
20000
25000


FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 687222
Mean -2.889
RMS 11.54
Underflow 0
Overflow 3.332e+04
10^6
10^5
-200
-150
-100
-50
0
50
100
150
200



FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 687222
Mean 1.202e+04
RMS 82.9
Underflow 6.539e+05
Overflow 0
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10^3
10^2
10
1
10000
10500
11000
11500
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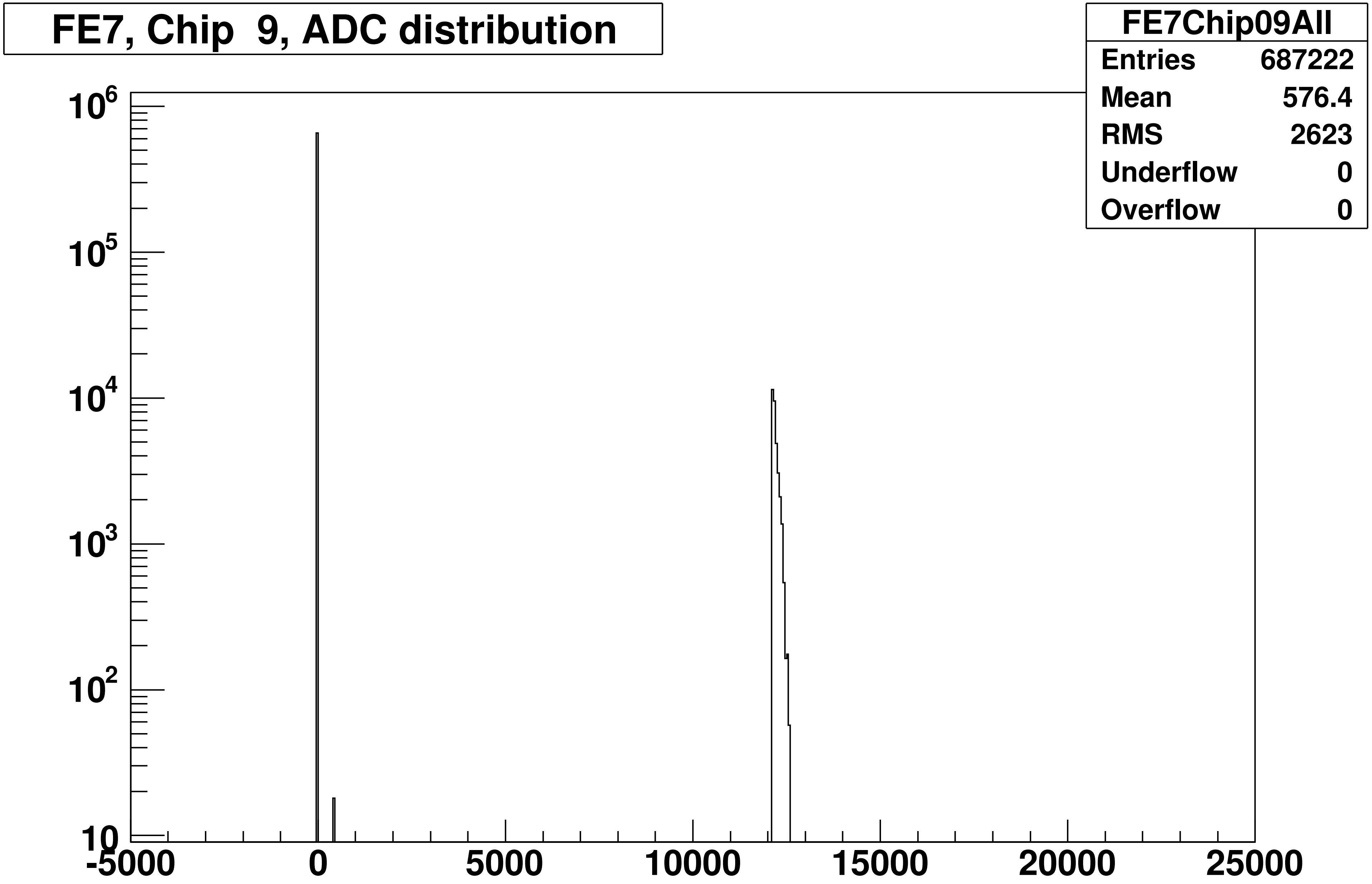
14000


FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 687222
Mean 576.4
RMS 2623
Underflow 0
Overflow 0
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10^3
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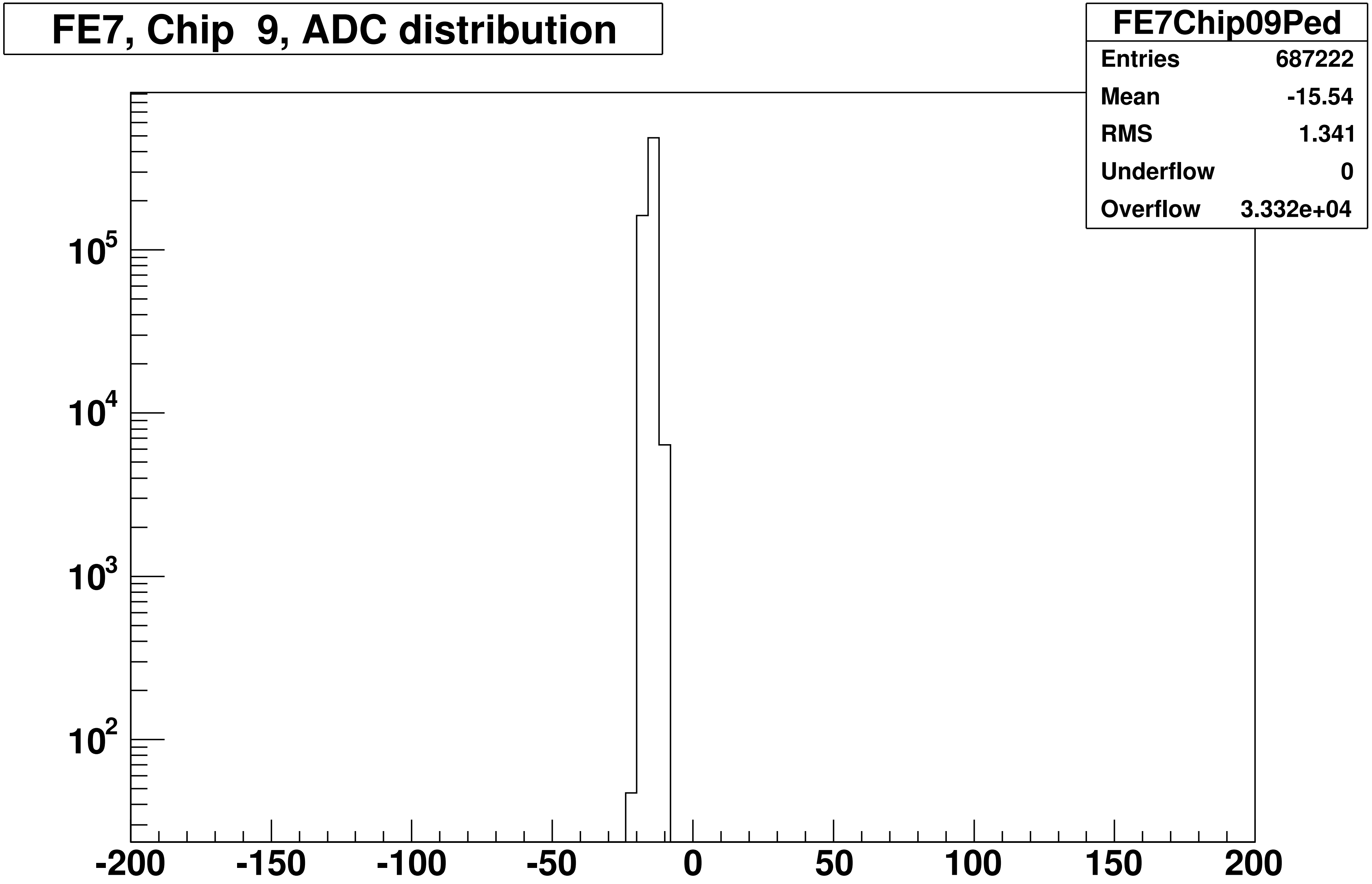
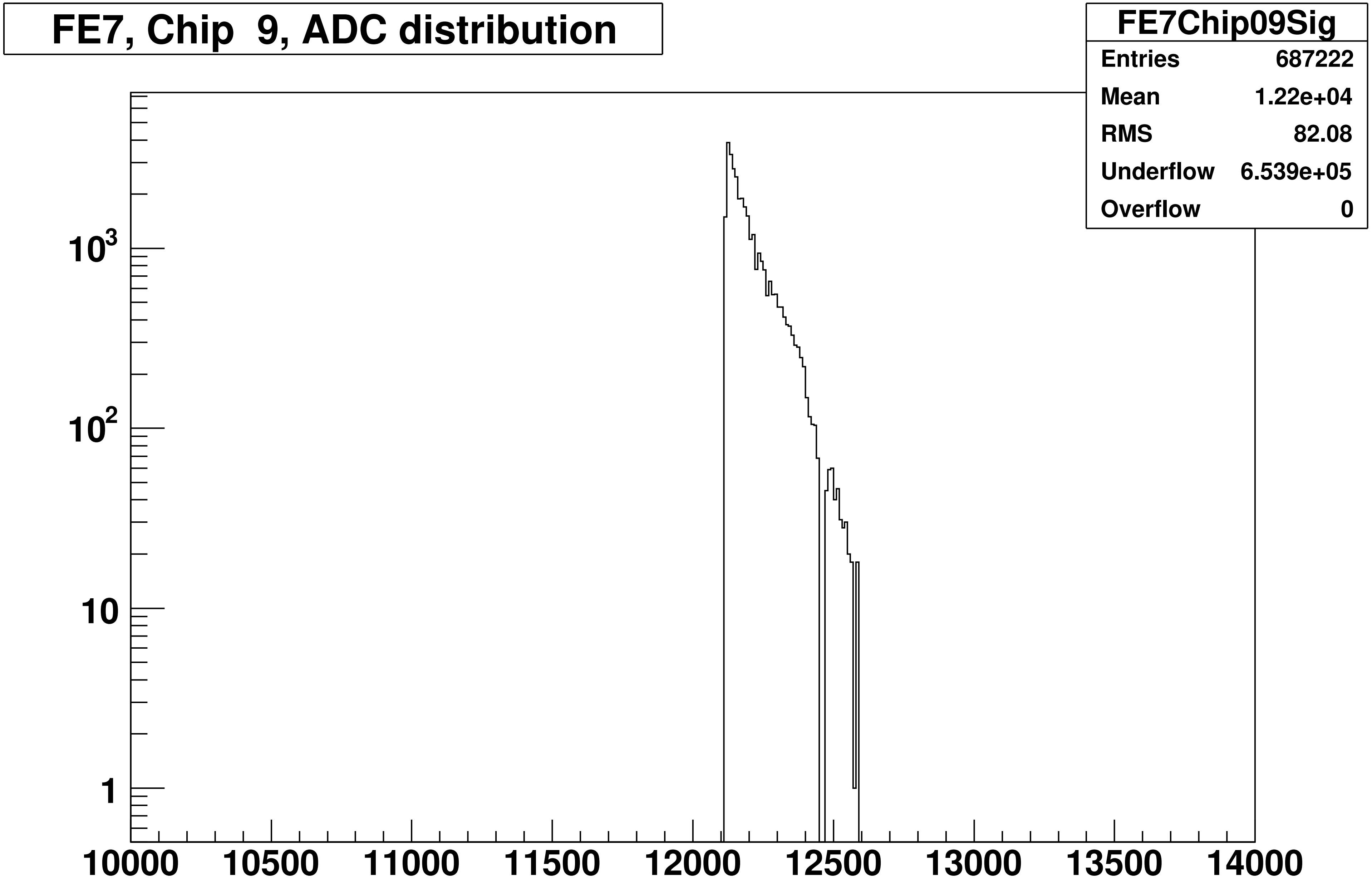
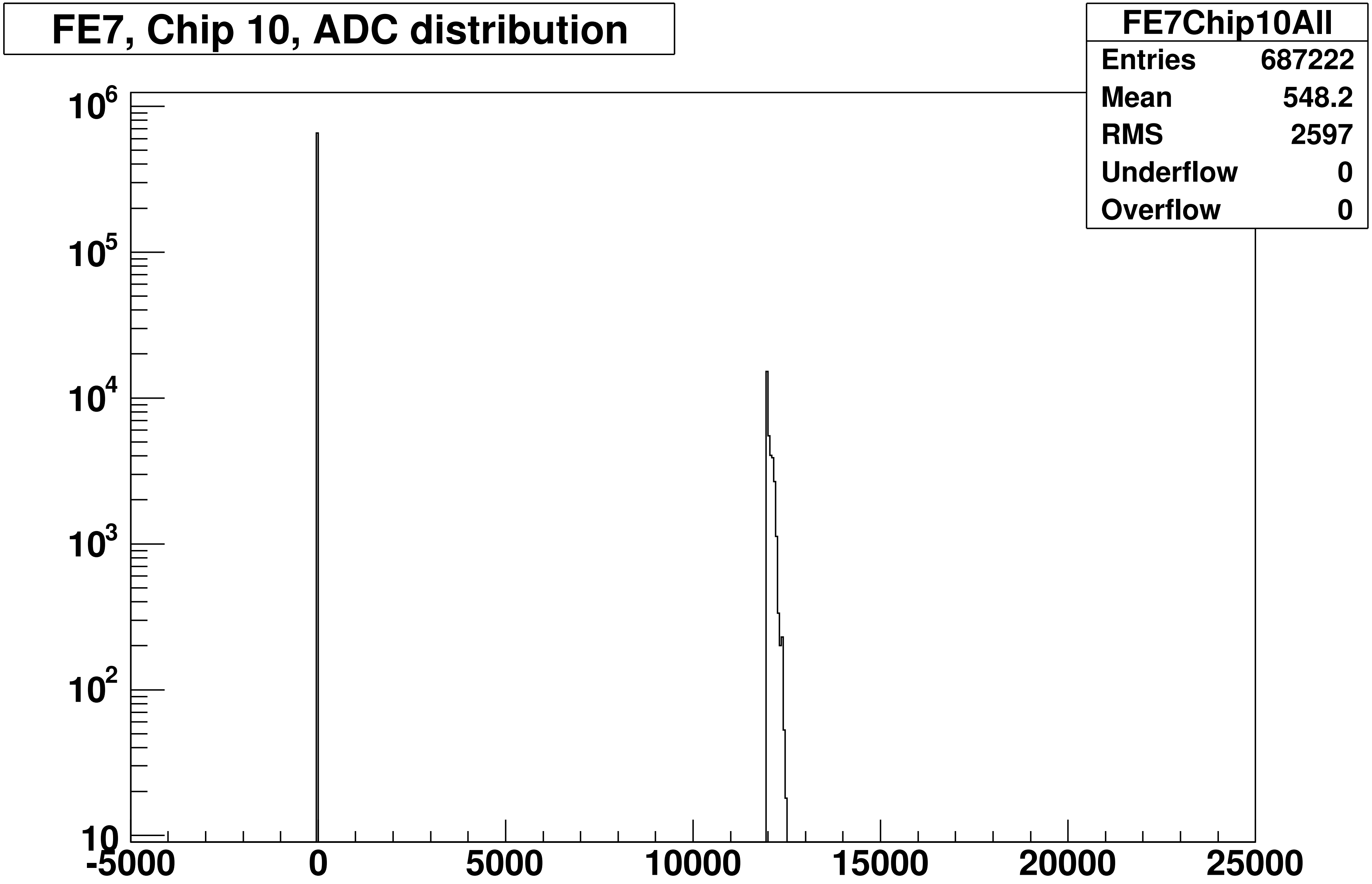
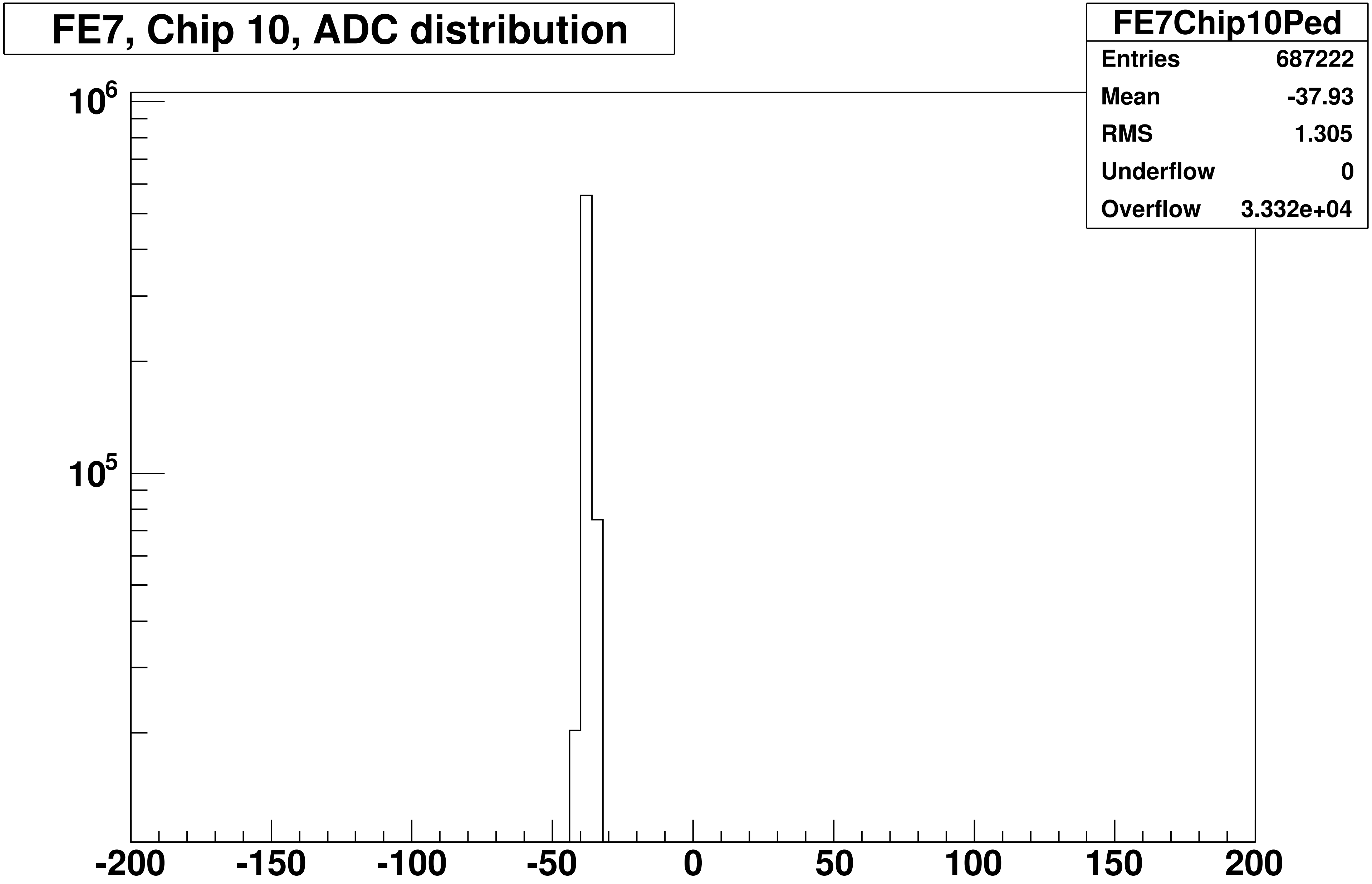
25000

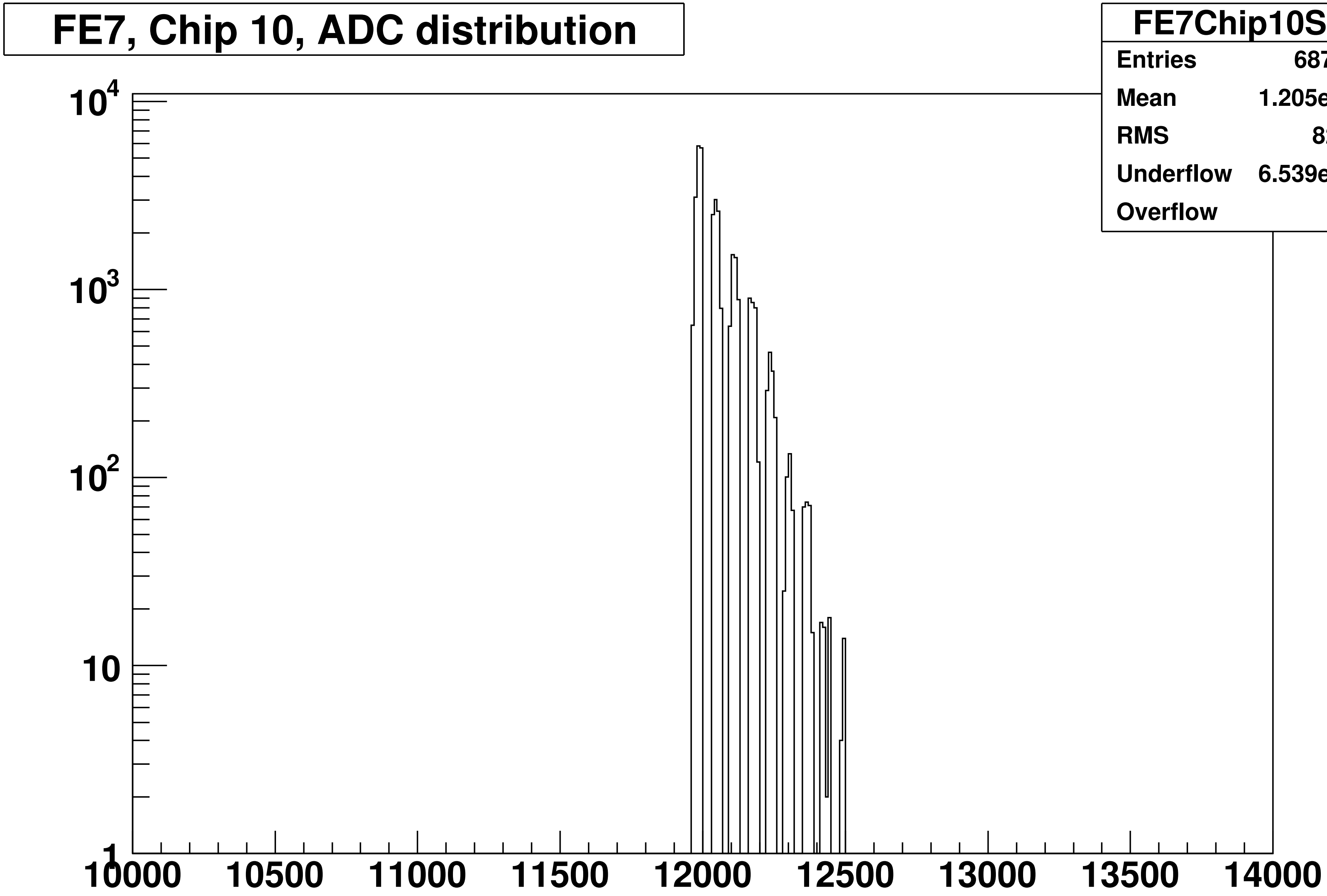


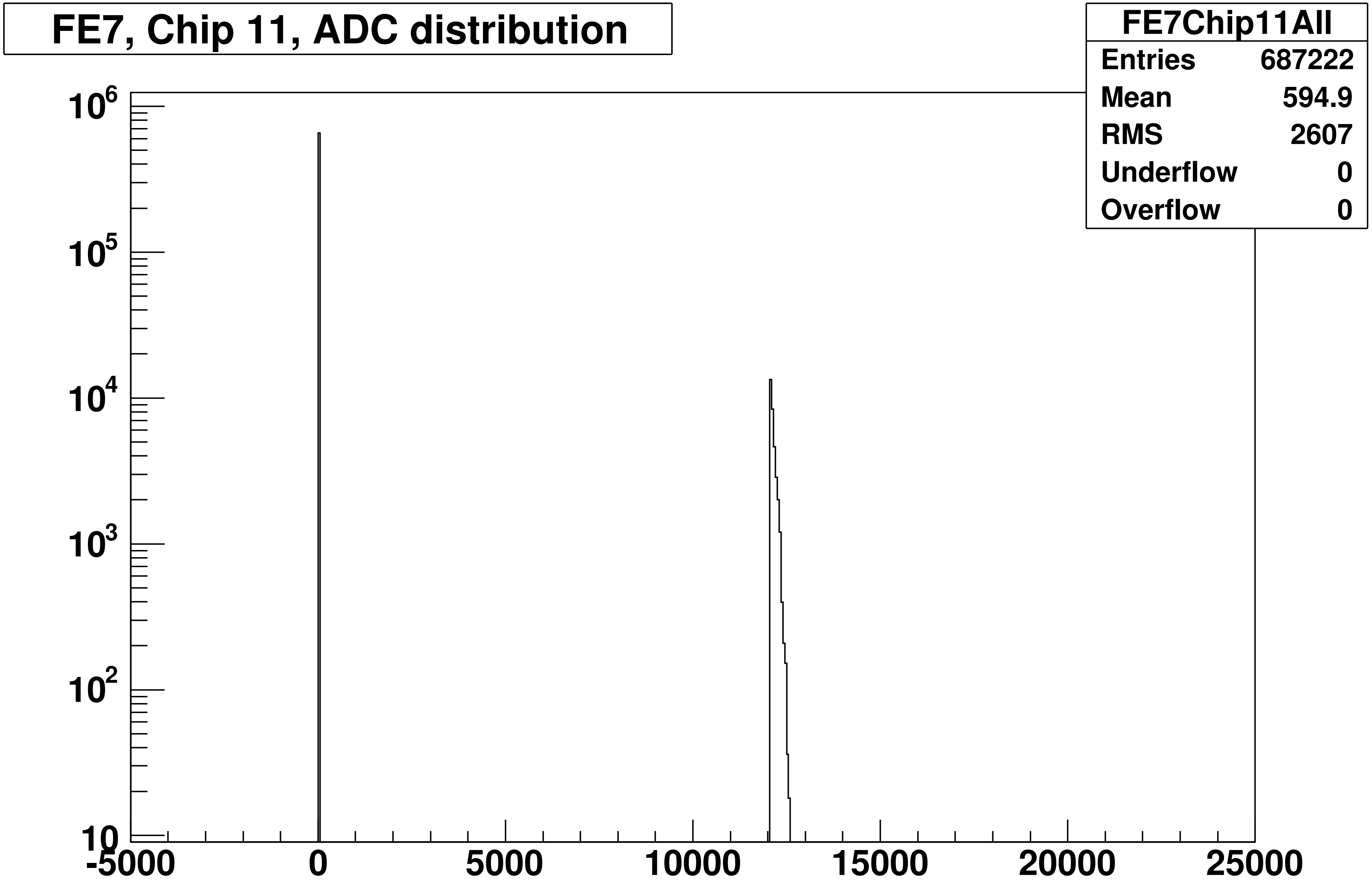
FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 687222
Mean -15.54
RMS 1.341
Underflow 0
Overflow 3.332e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200

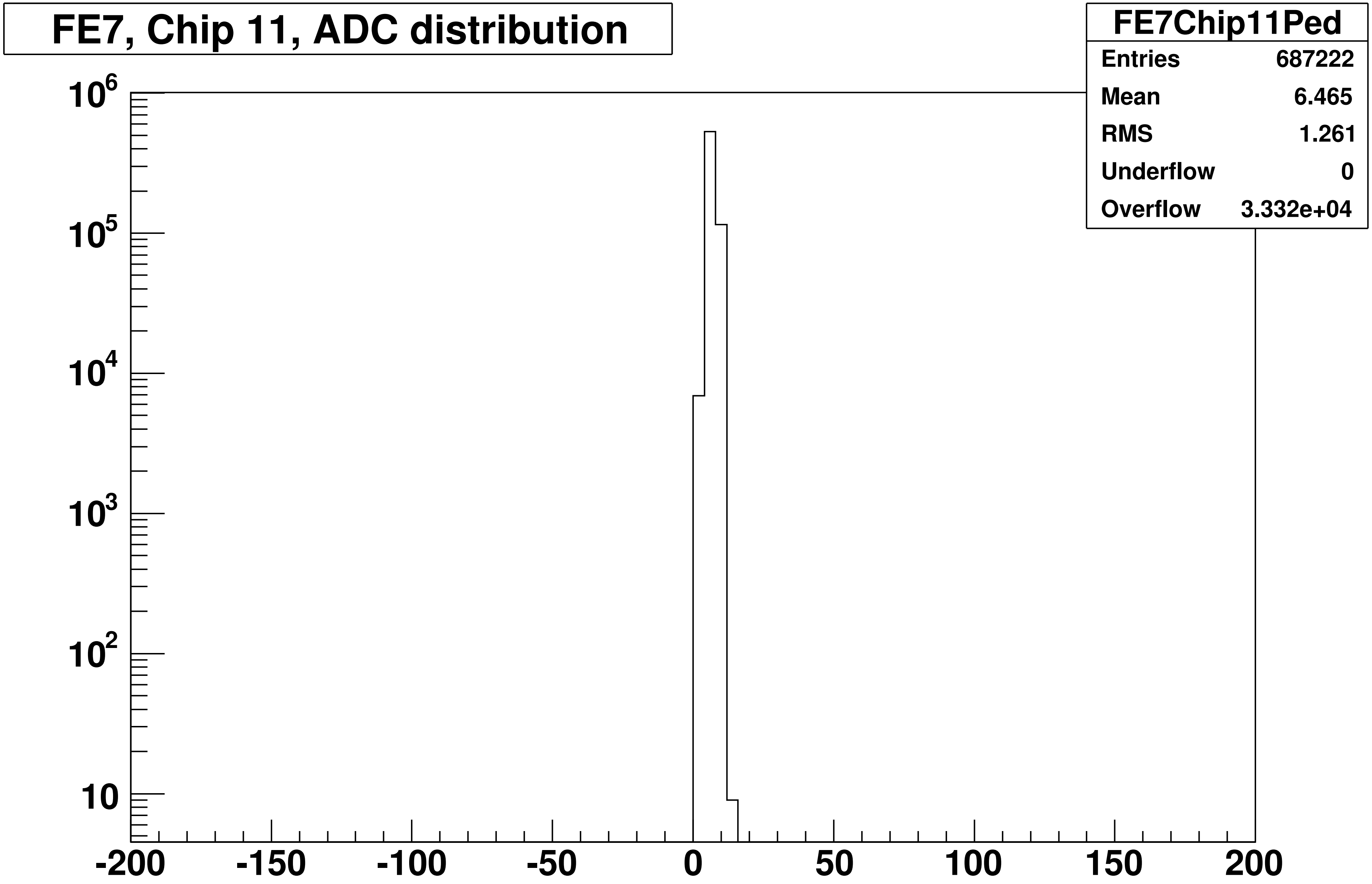

FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 687222
Mean 1.22e+04
RMS 82.08
Underflow 6.539e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

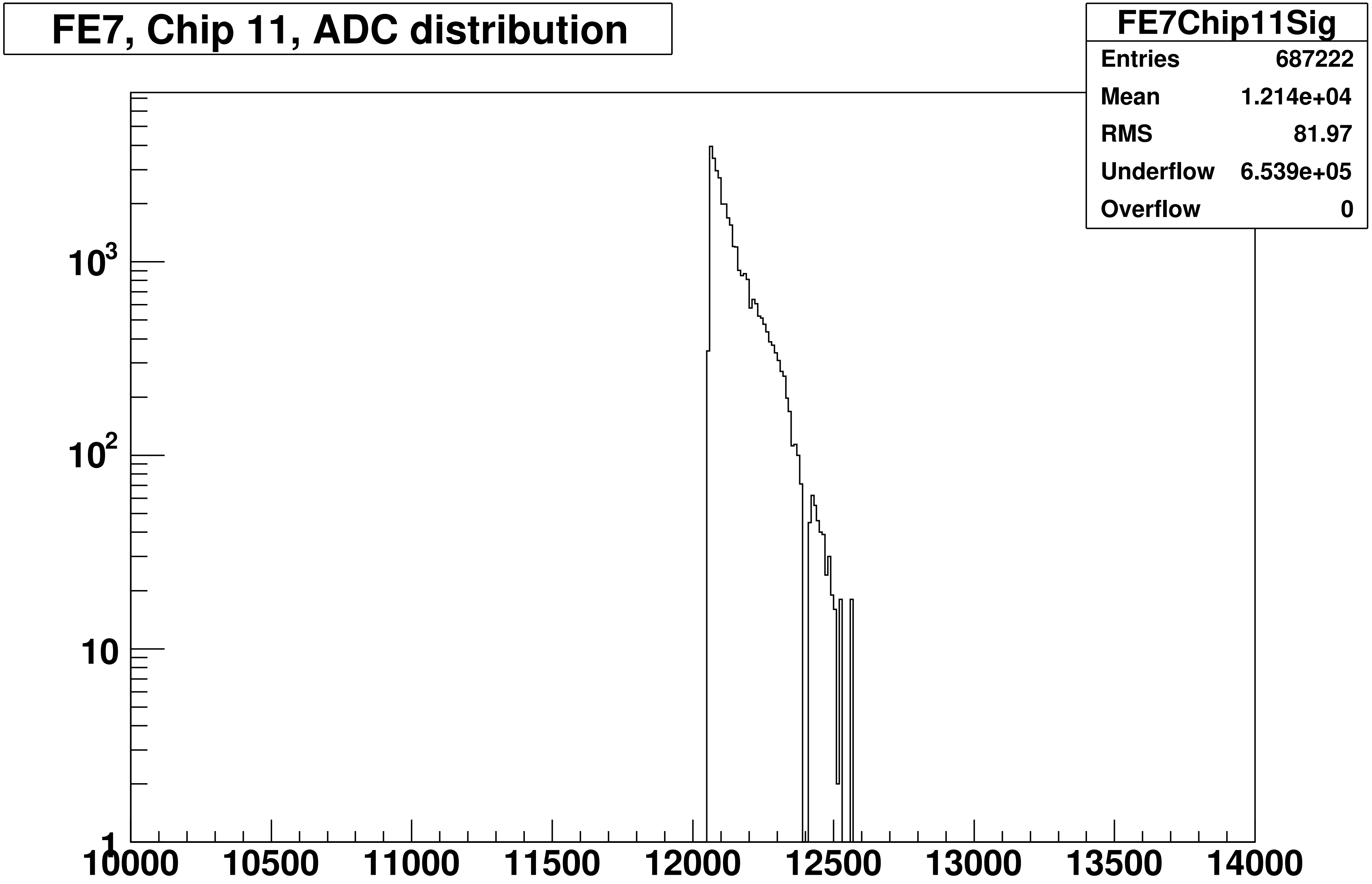

FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 687222
Mean 548.2
RMS 2597
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 687222
Mean -37.93
RMS 1.305
Underflow 0
Overflow 3.332e+04
10^6
10^5
-200
-150
-100
-50
0
50
100
150
200

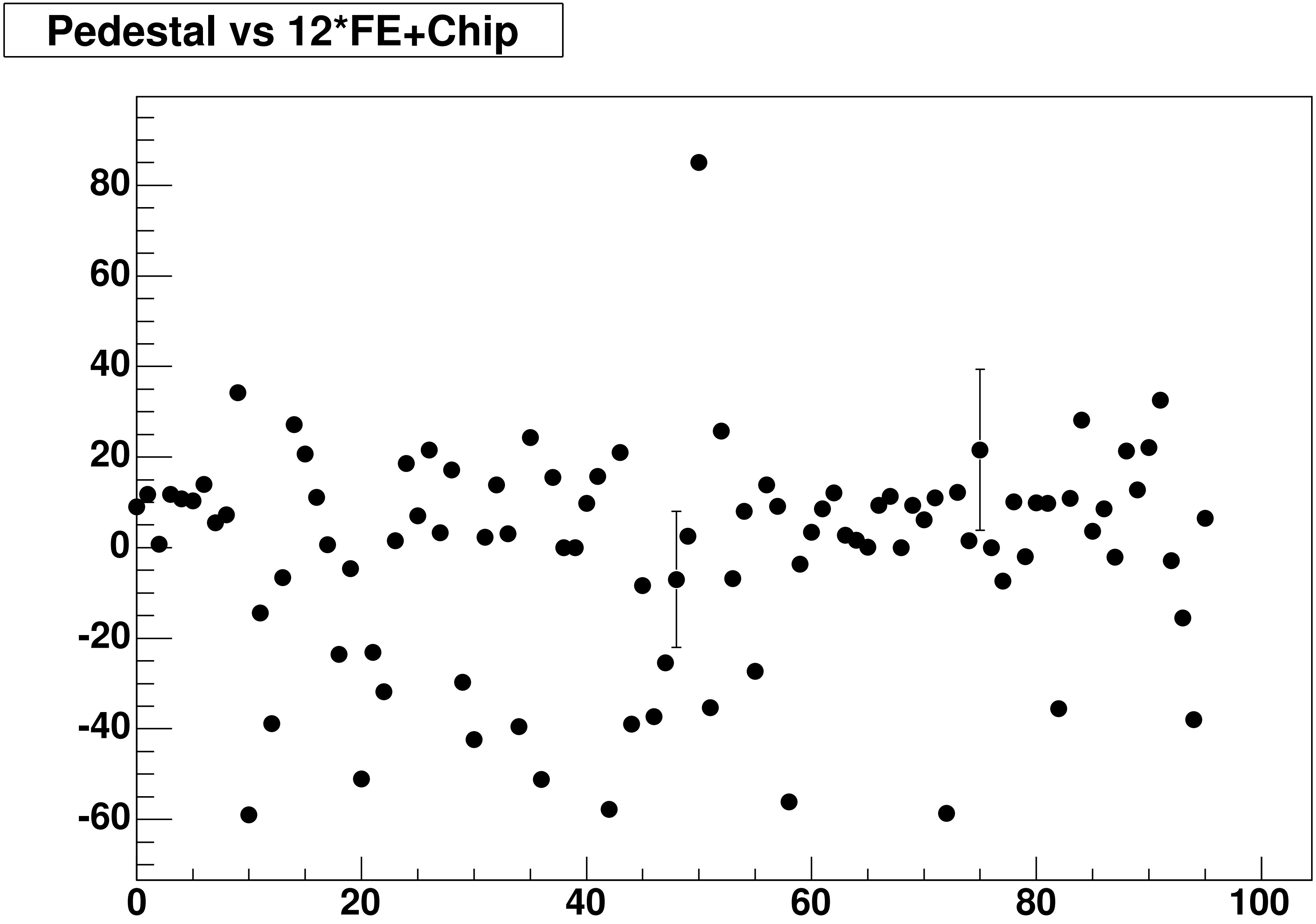

FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 687222
Mean 1.205e+04
RMS 82.43
Underflow 6.539e+05
Overflow 0
10^4
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000

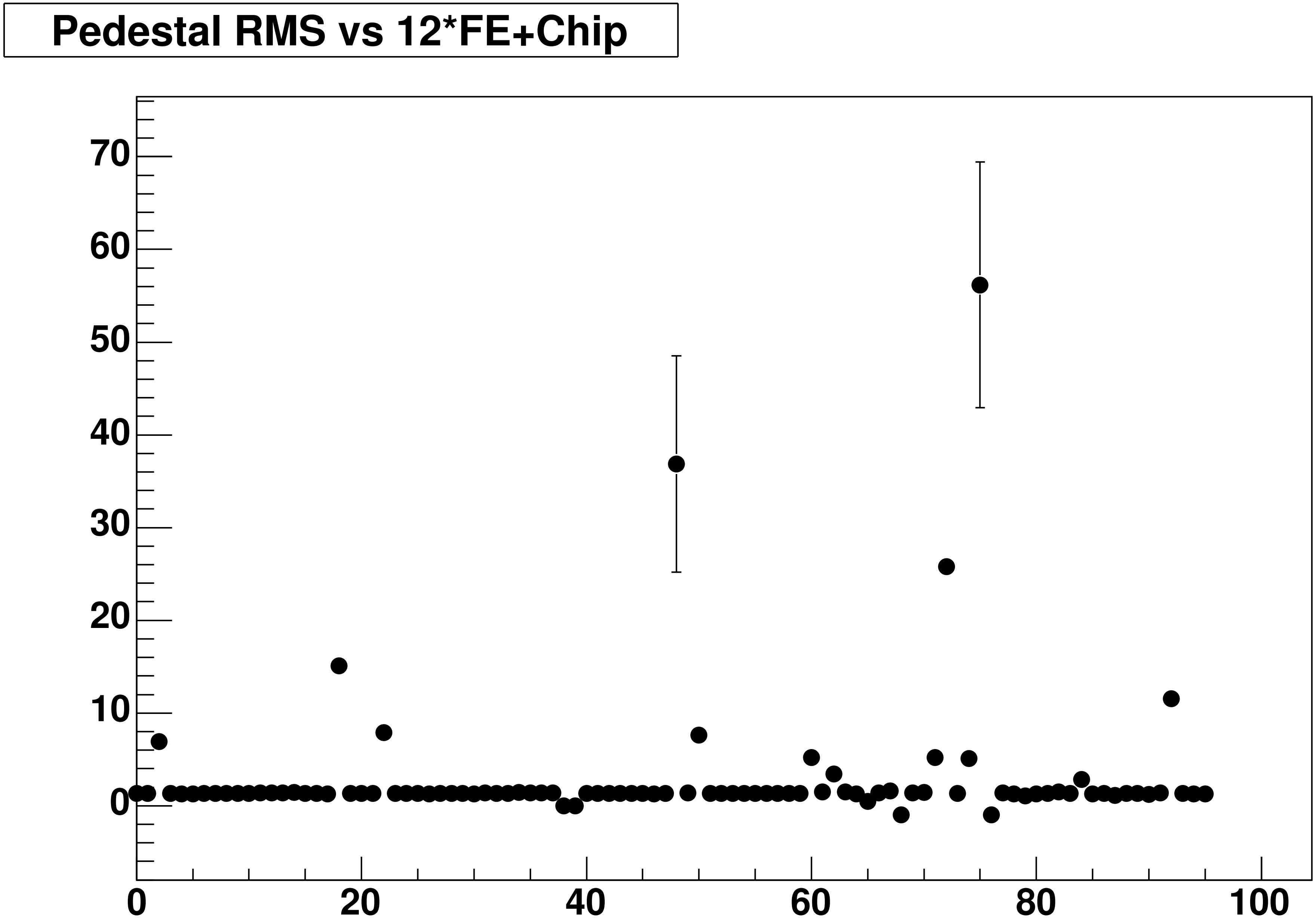

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 687222
Mean 594.9
RMS 2607
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

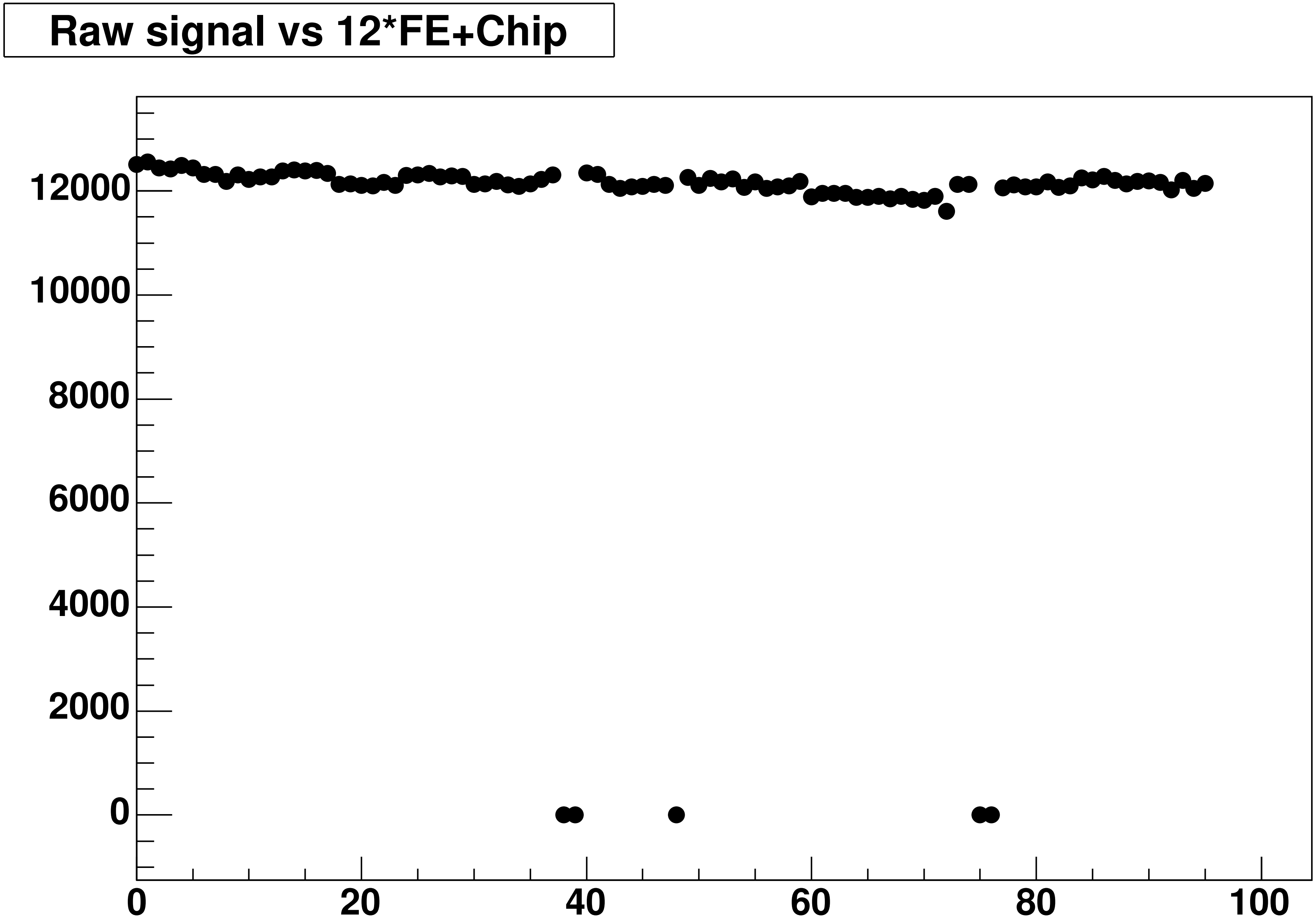

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 687222
Mean 6.465
RMS 1.261
Underflow 0
Overflow 3.332e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200



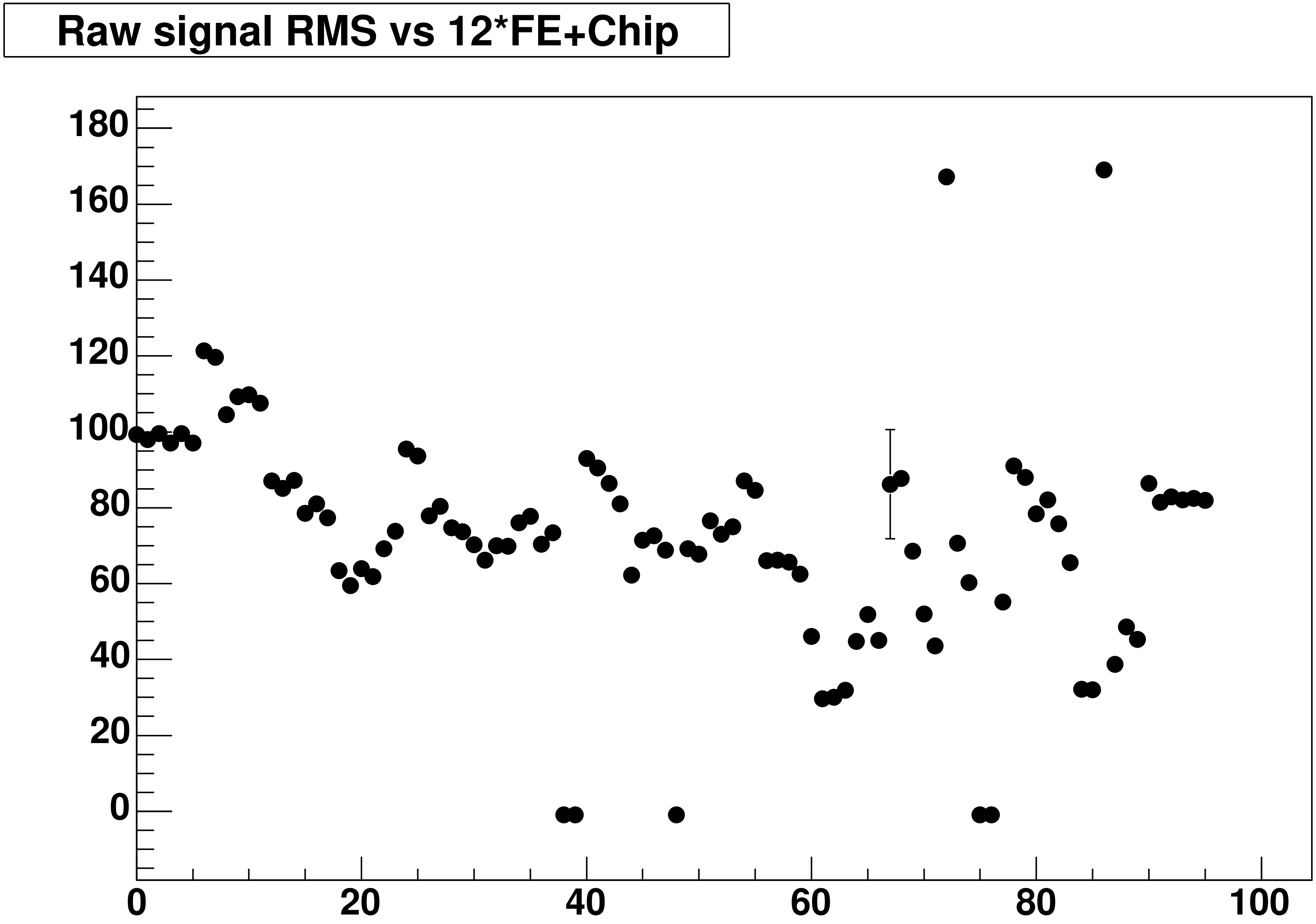
FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 687222
Mean 1.214e+04
RMS 81.97
Underflow 6.539e+05
Overflow 0
10^3
10^2
10
1
10000
10500
11000
11500
12000
12500
13000
13500
14000


Pedestal vs 12*FE+Chip
80
60
40
20
0
-20
-40
-60
0
20
40
60
80
100

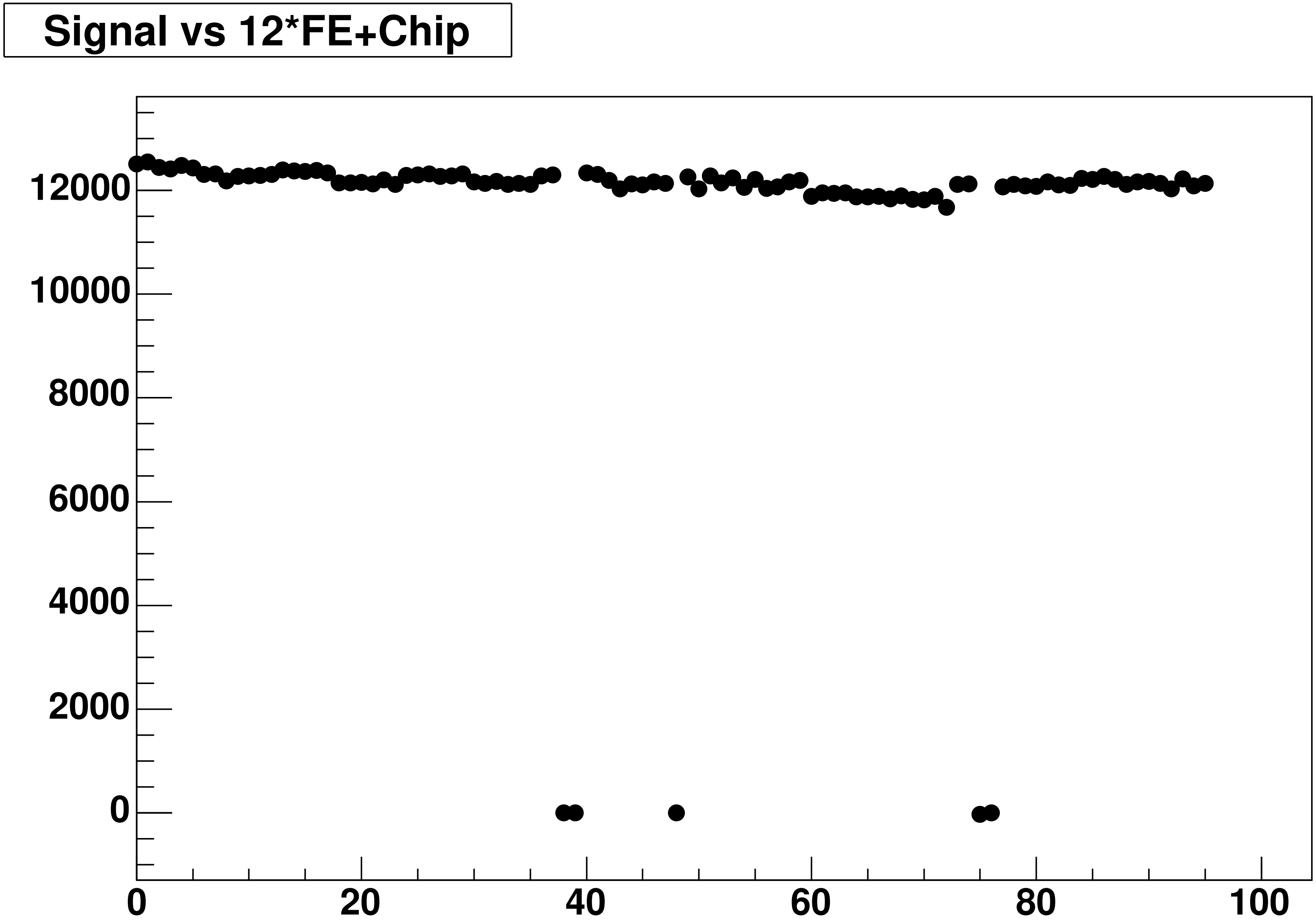

Pedestal RMS vs 12*FE+Chip
70
60
50
40
30
20
10
0
0
20
40
60
80
100

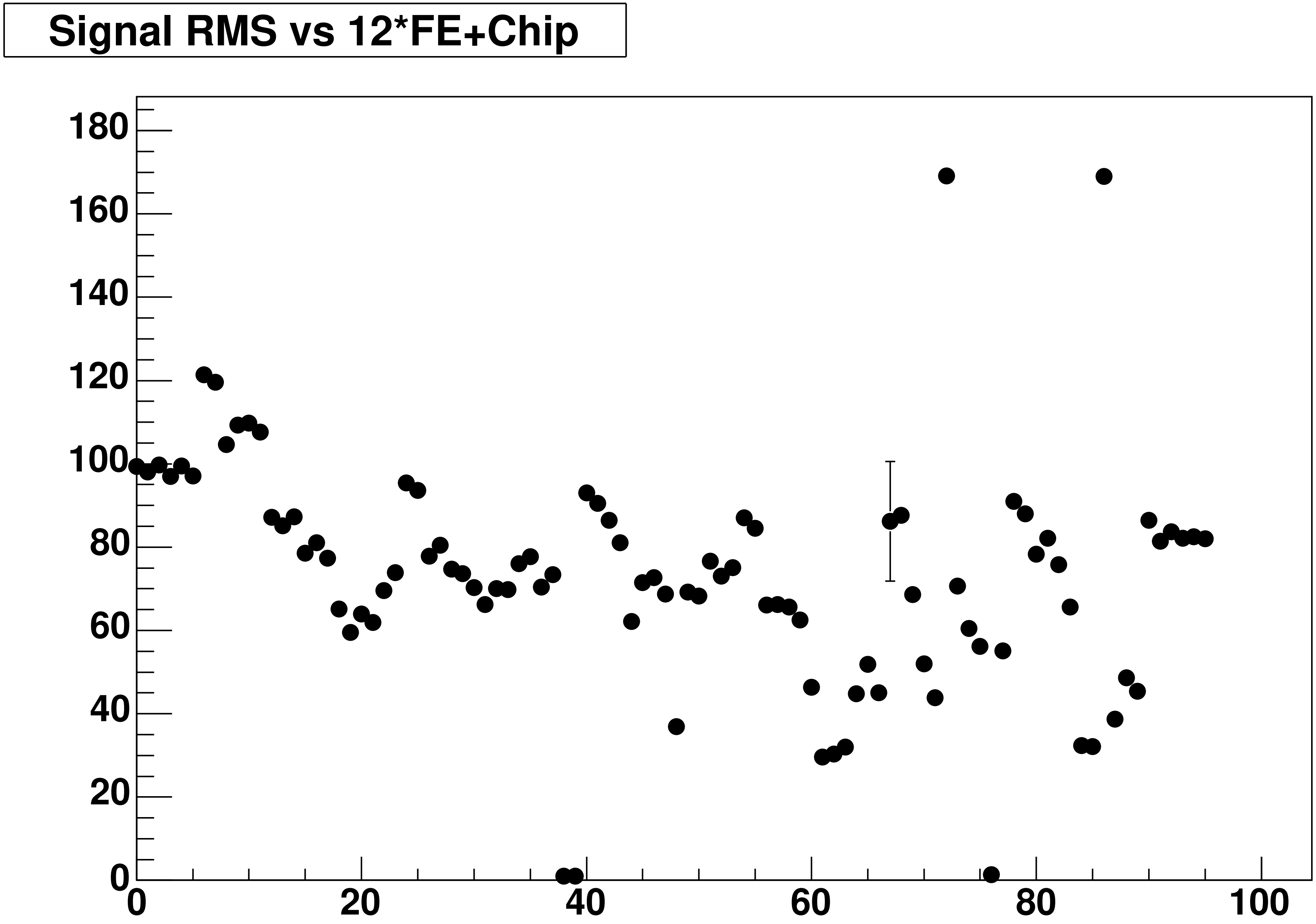


Raw signal vs 12*FE+Chip
12000
10000
8000
6000
4000
2000
0
0
20
40
60
80
100



Raw signal RMS vs 12*FE+Chip
180
160
140
120
100
80
60
40
20
0
0
20
40
60
80
100


Signal vs 12*FE+Chip
12000
10000
8000
6000
4000
2000
0
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
180
160
140
120
100
80
60
40
20
0
0
20
40
60
80
100