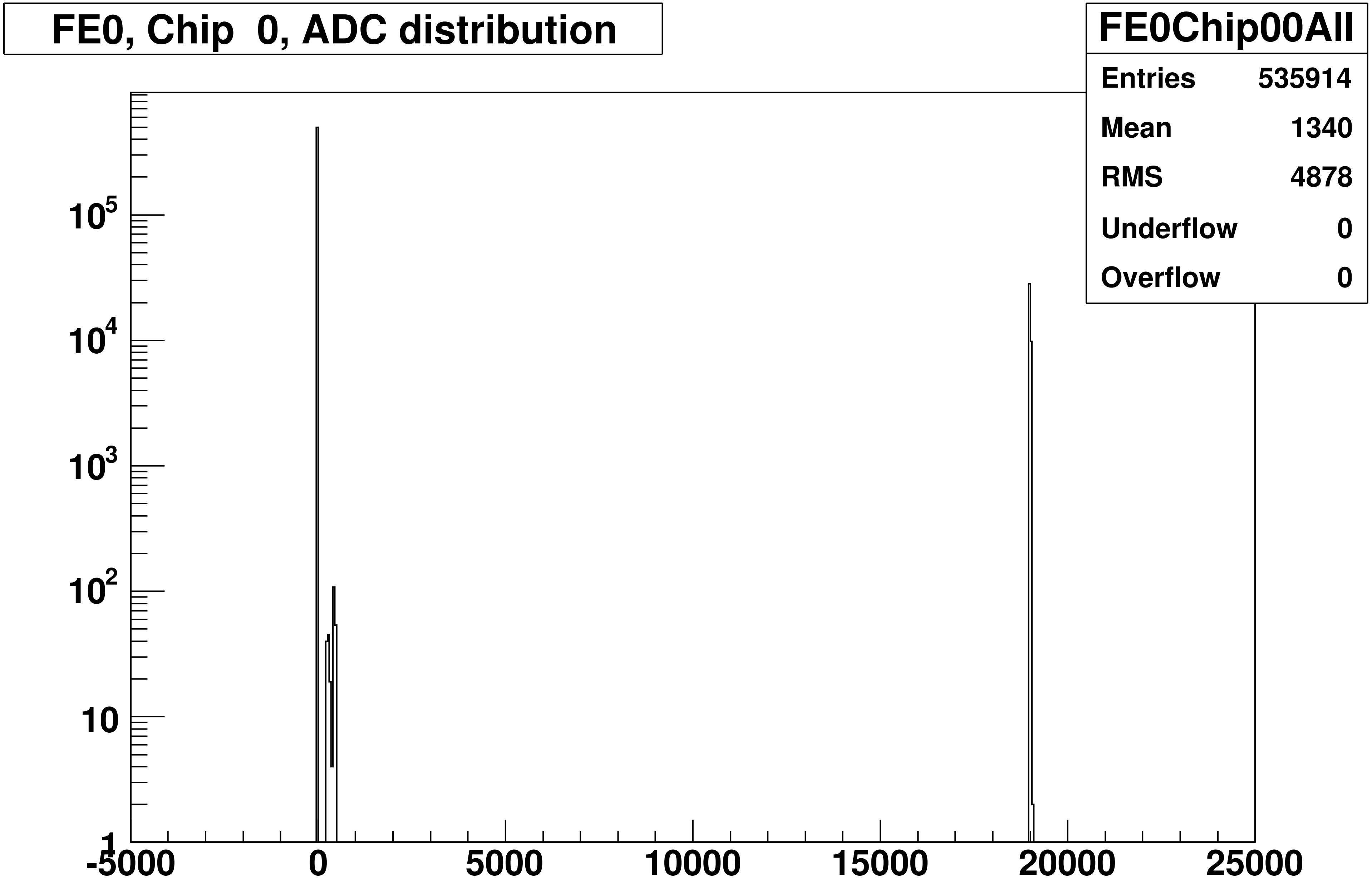
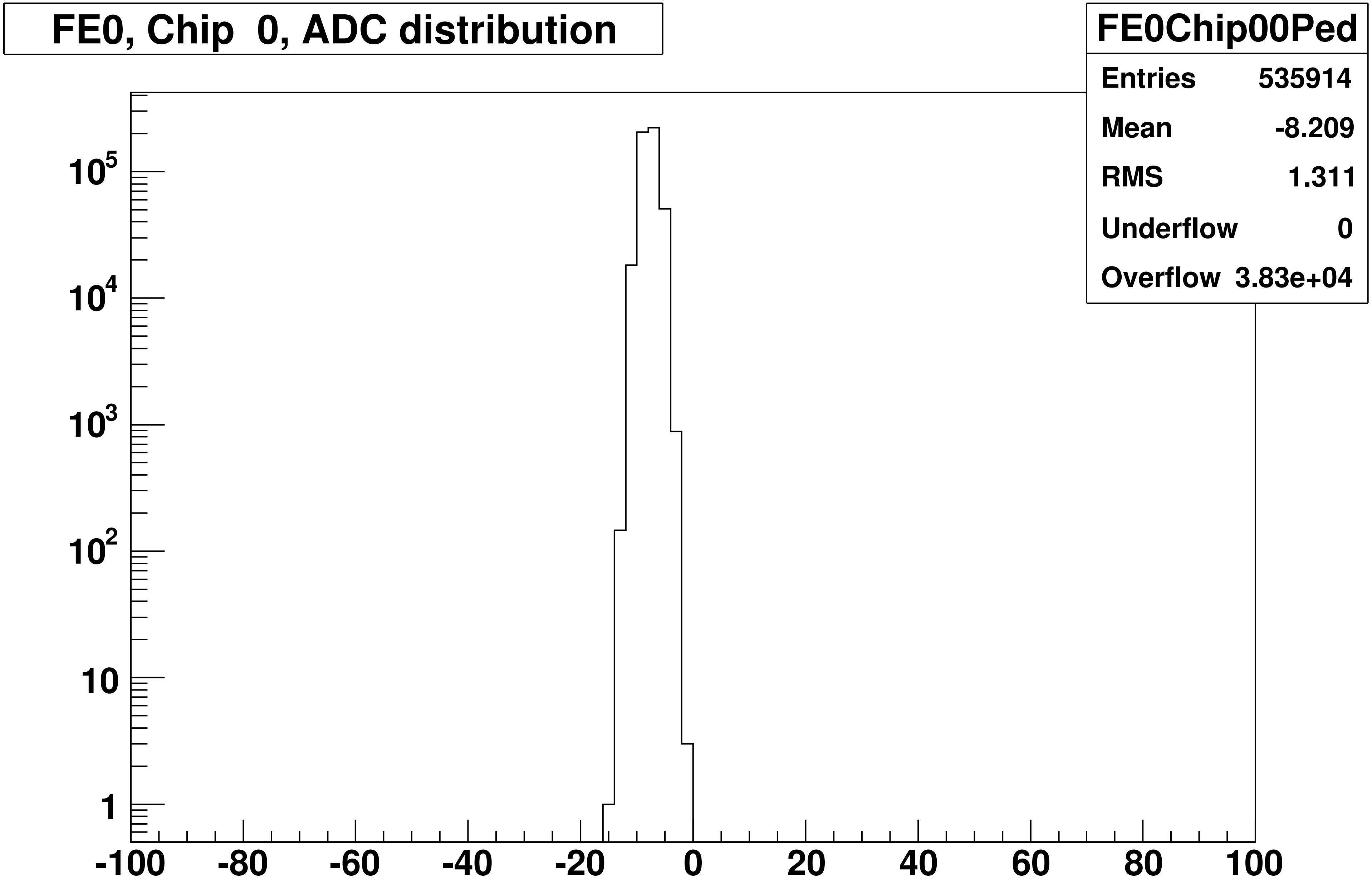
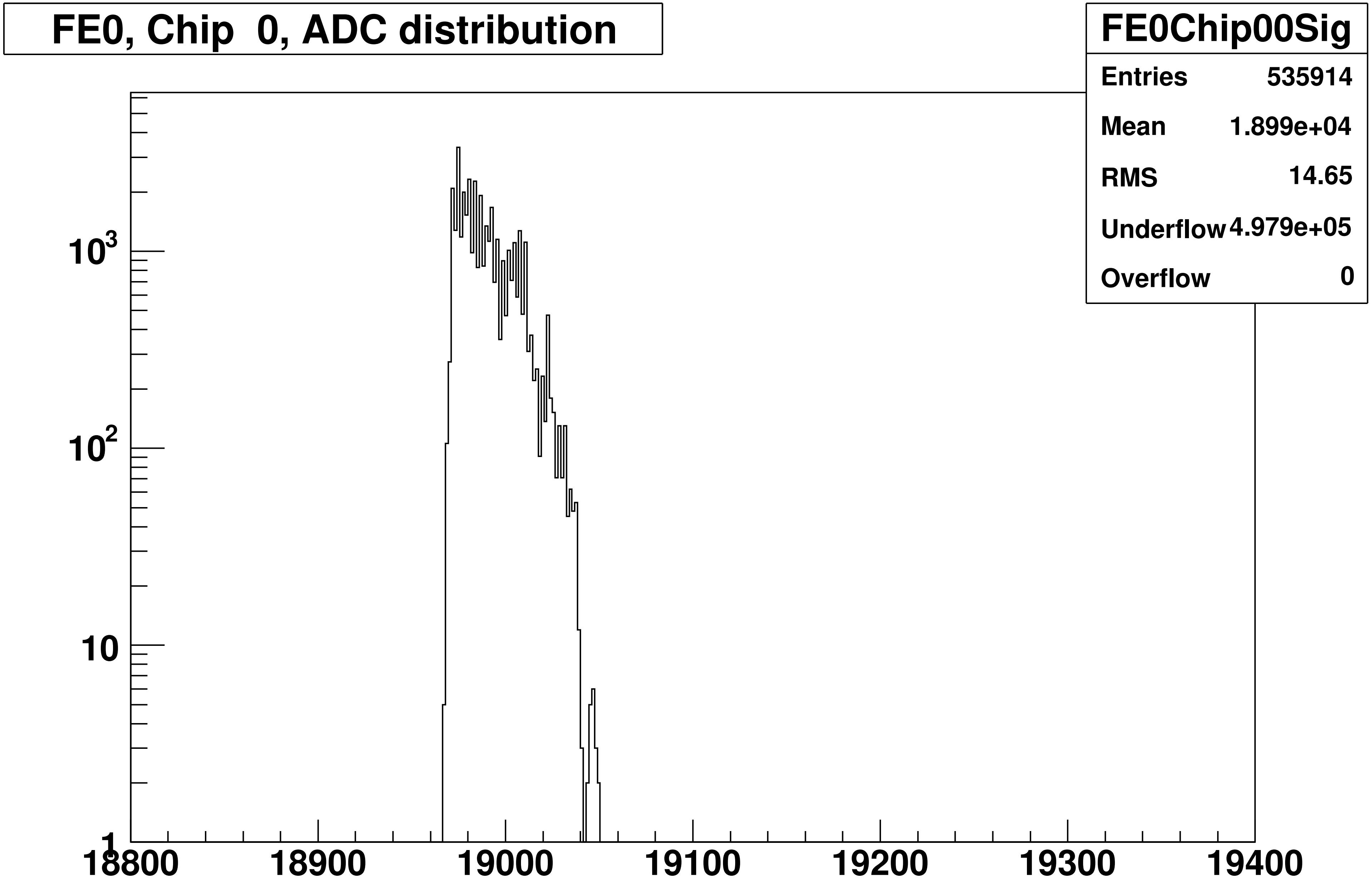
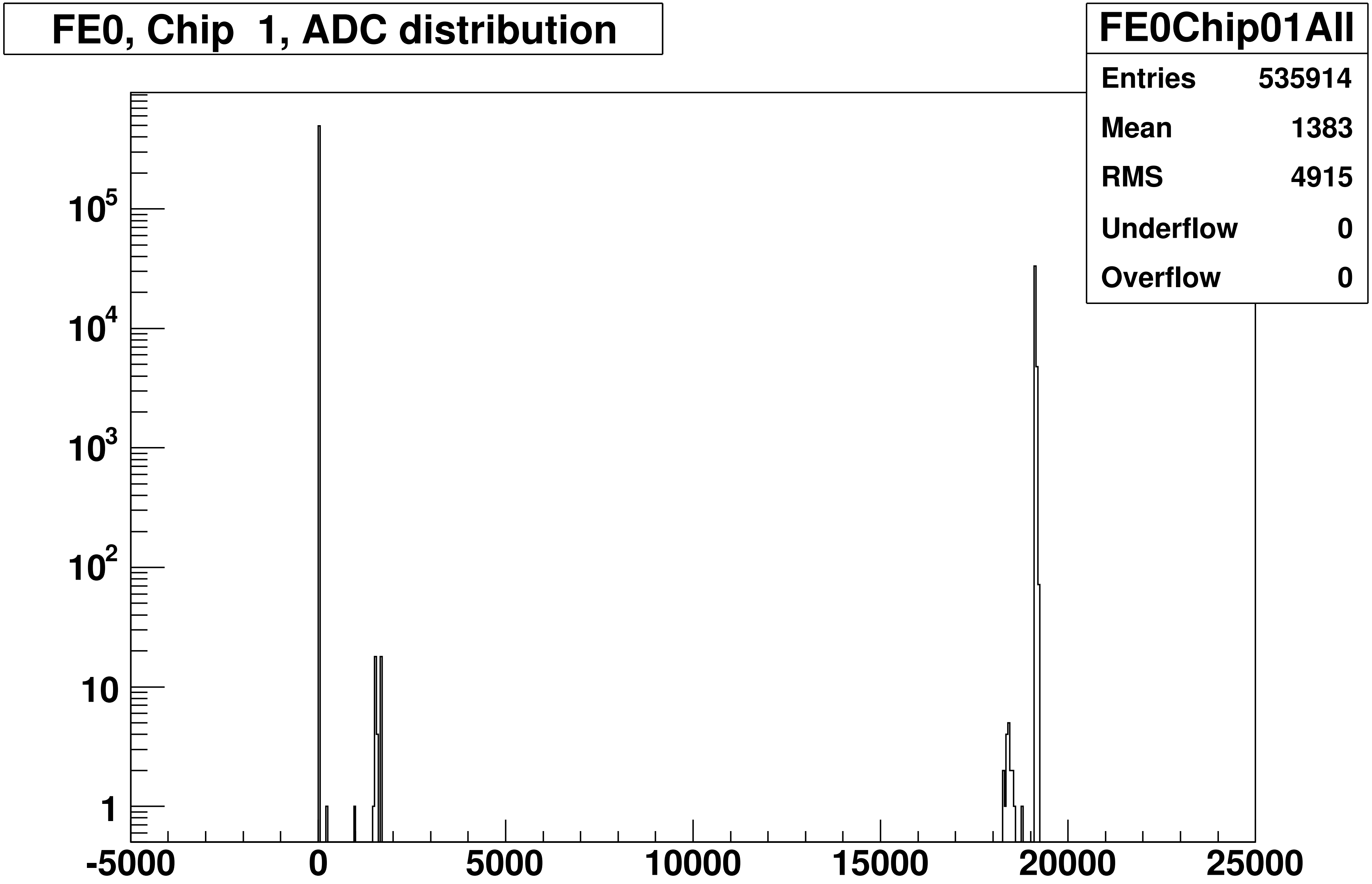

FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 535914
Mean 1340
RMS 4878
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



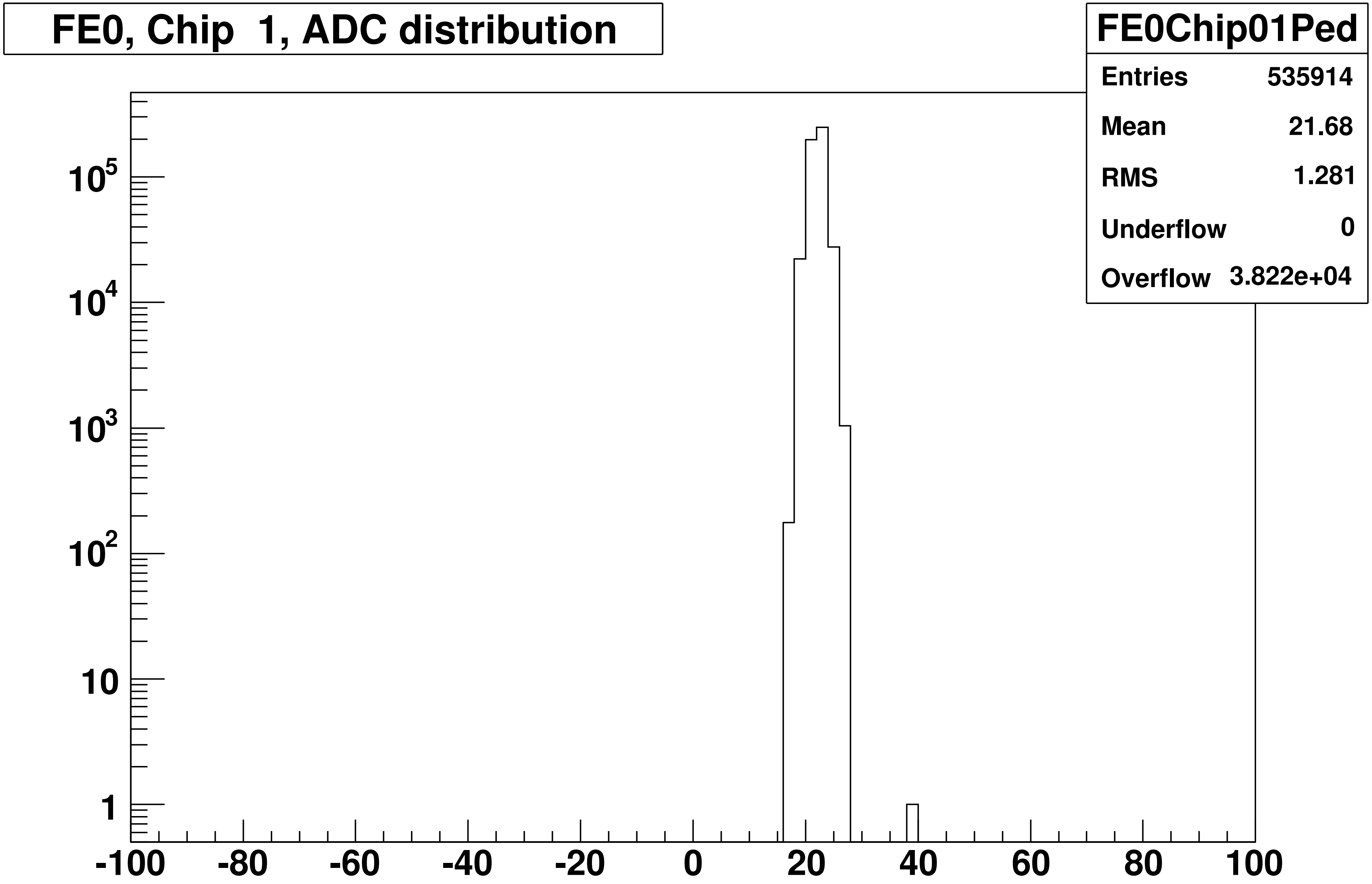
FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 535914
Mean -8.209
RMS 1.311
Underflow 0
Overflow 3.83e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

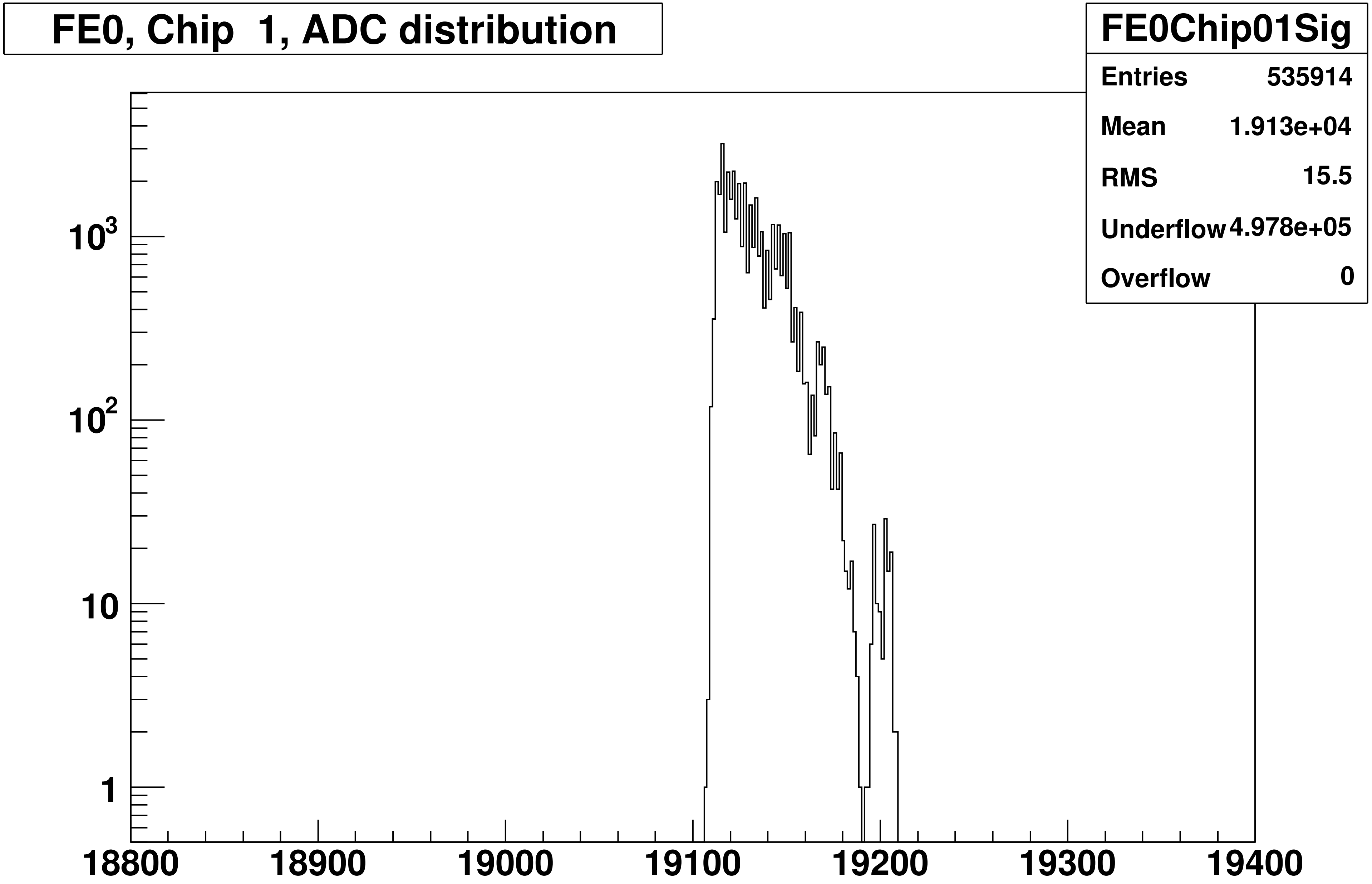


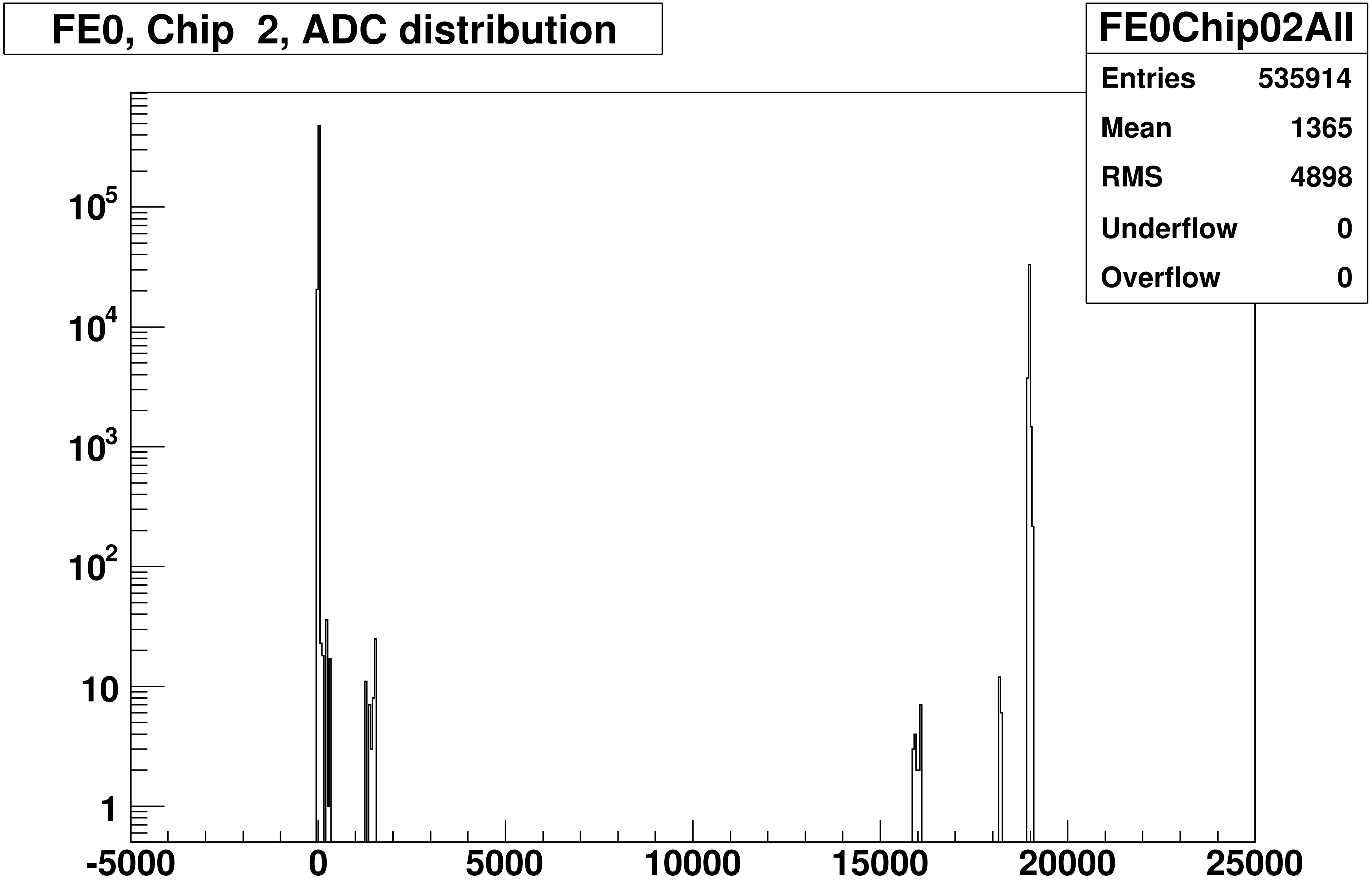
FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 535914
Mean 1.899e+04
RMS 14.65
Underflow 4.979e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



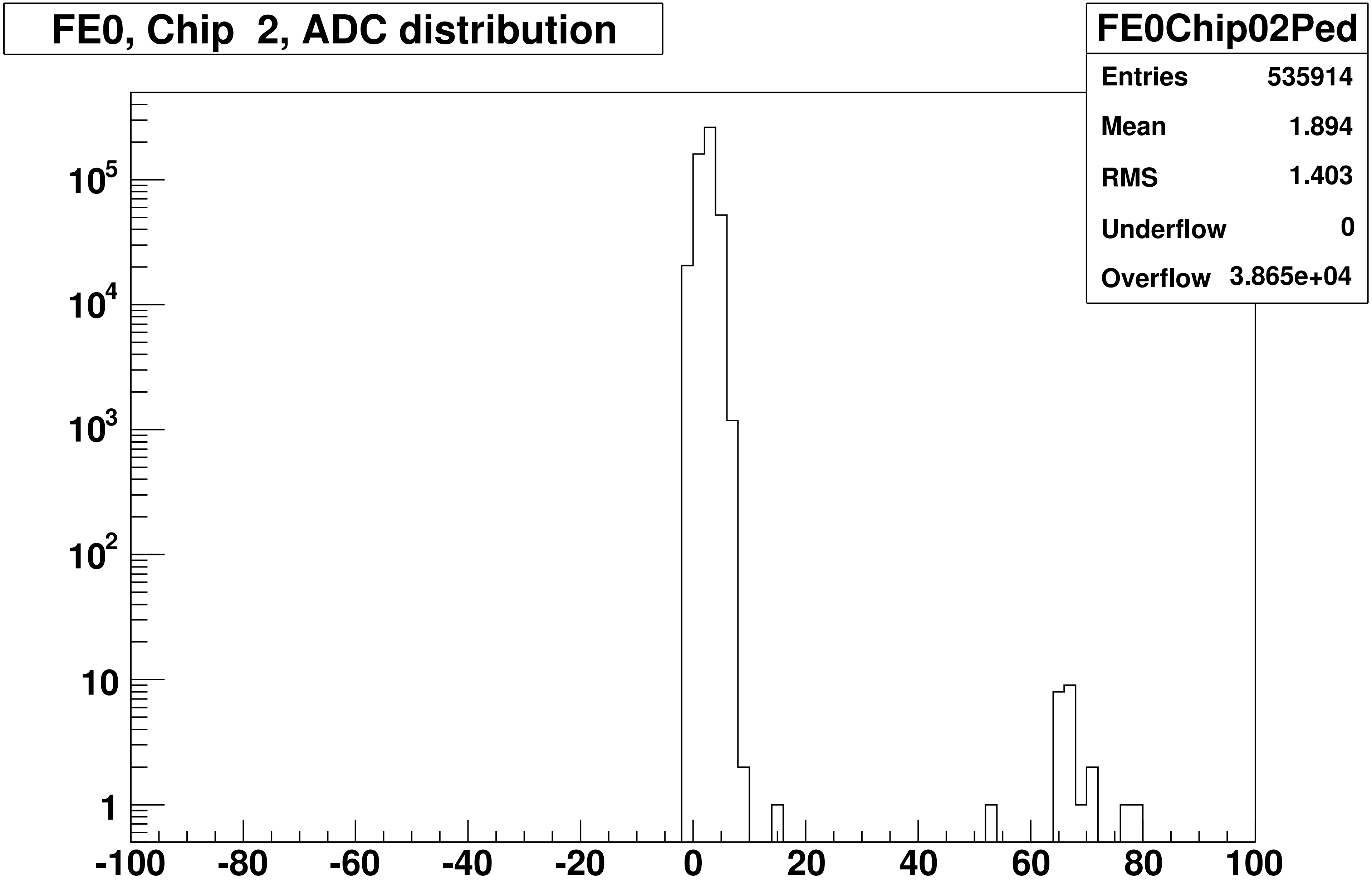
FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 535914
Mean 1383
RMS 4915
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

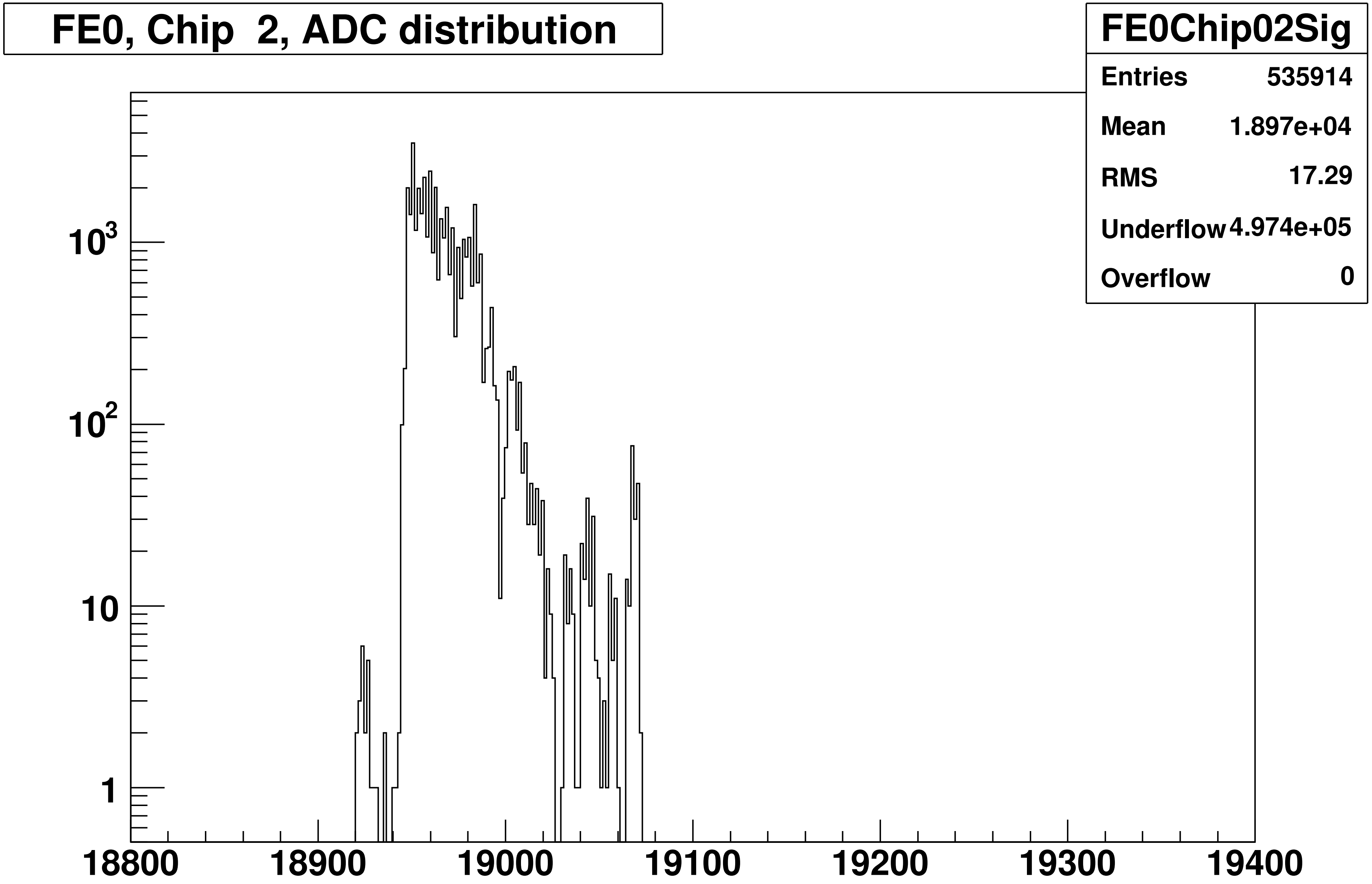

FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 535914
Mean 21.68
RMS 1.281
Underflow 0
Overflow 3.822e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

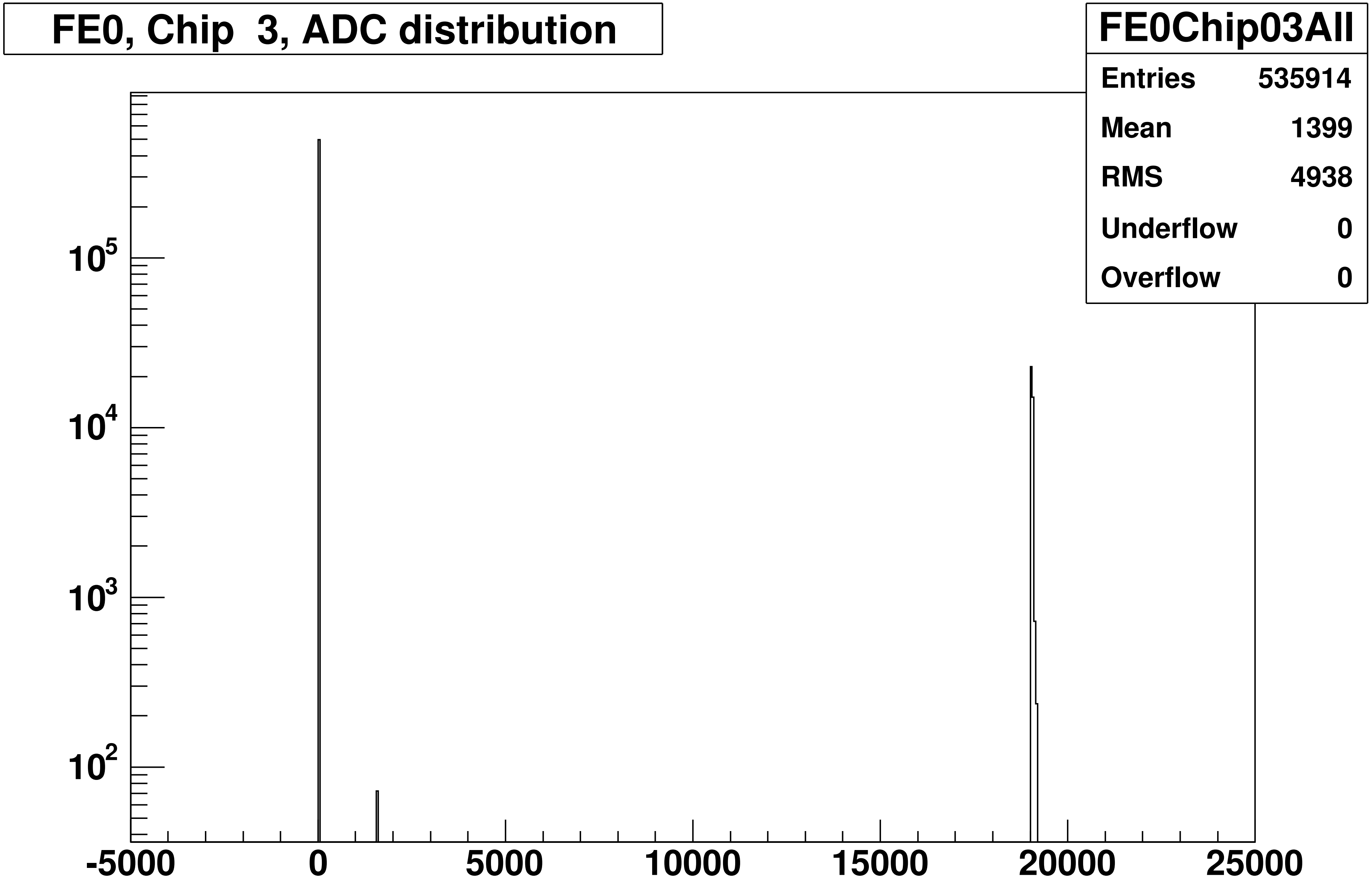

FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 535914
Mean 1.913e+04
RMS 15.5
Underflow 4.978e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

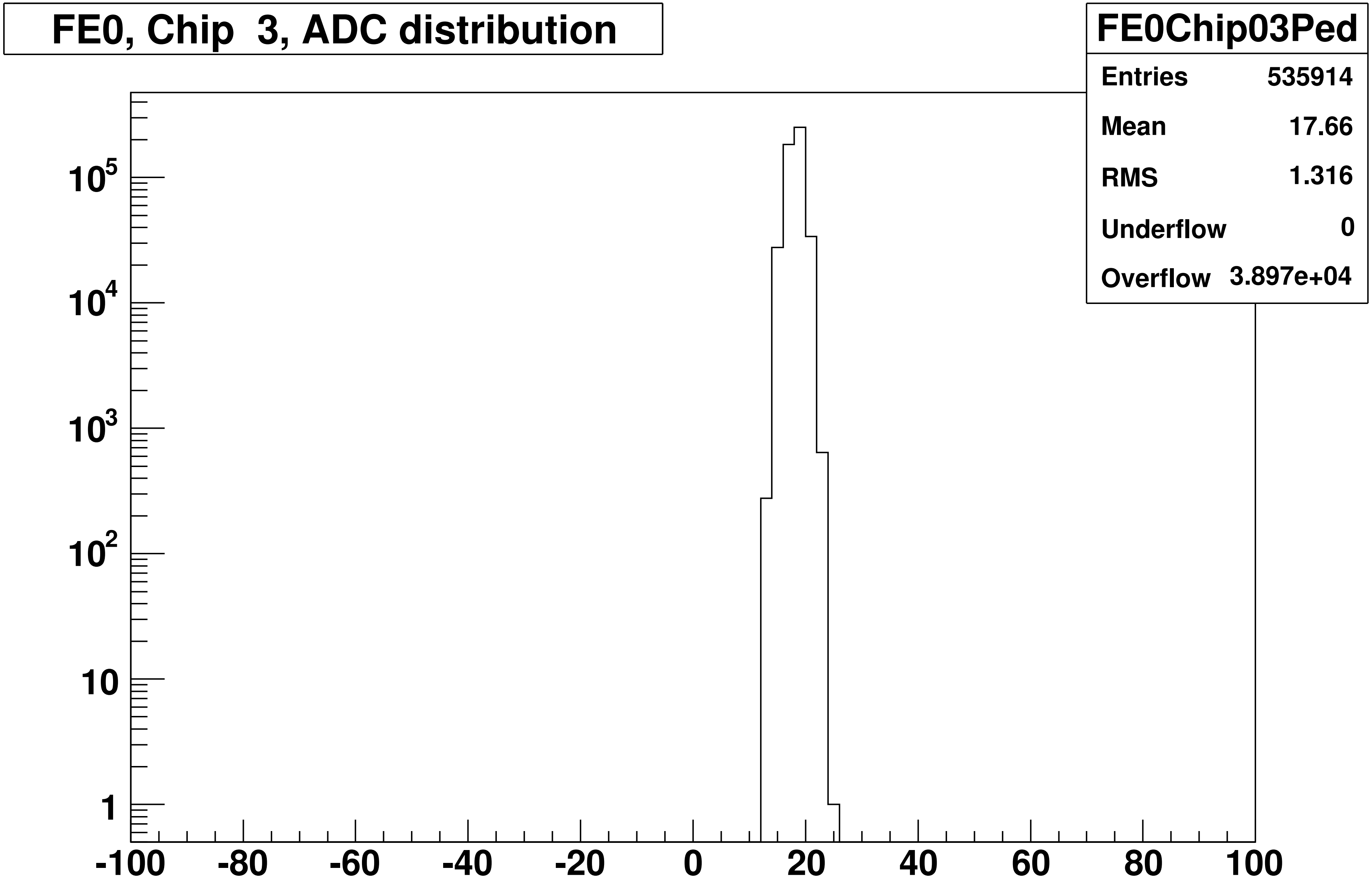


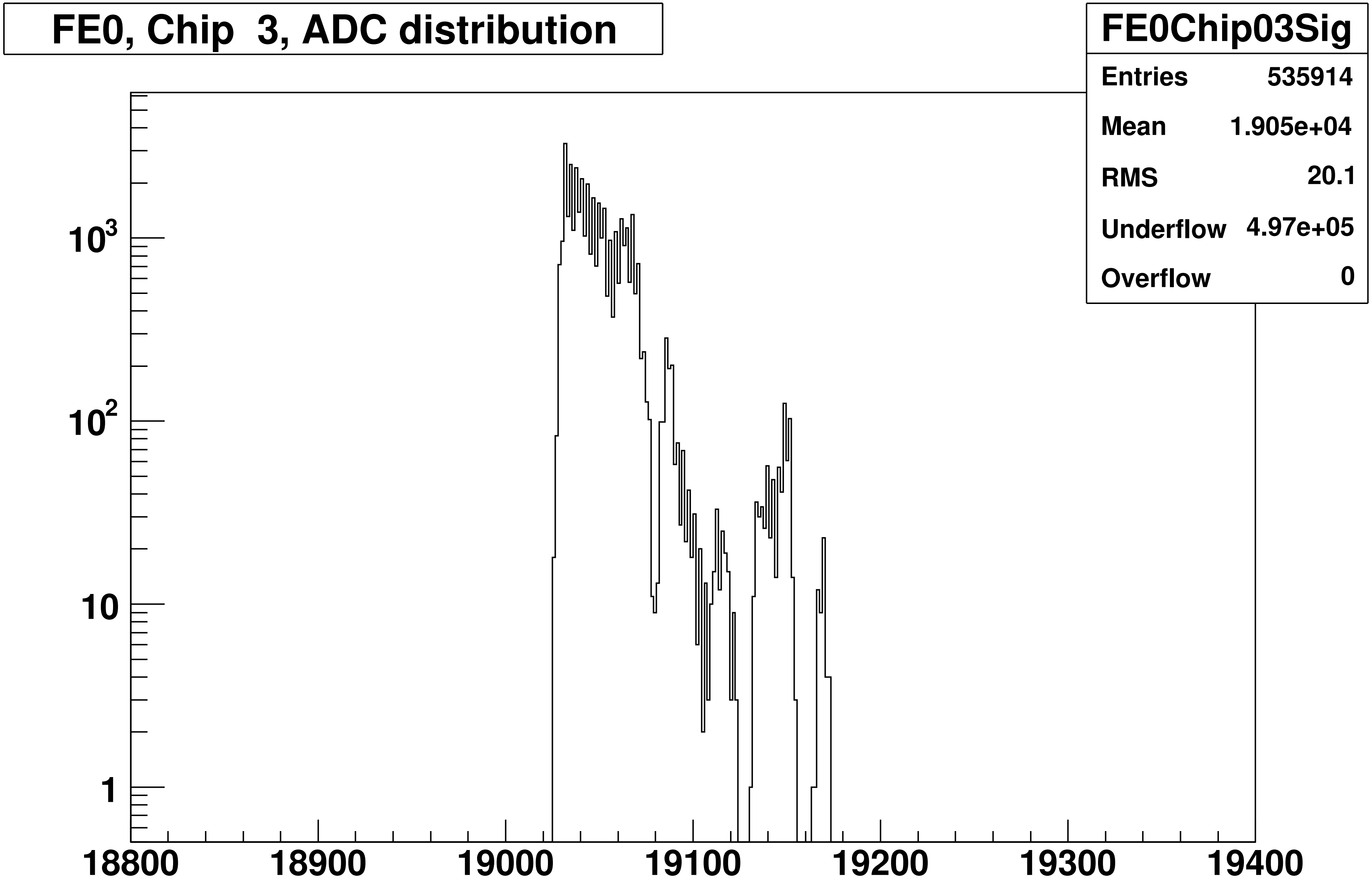
FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 535914
Mean 1365
RMS 4898
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

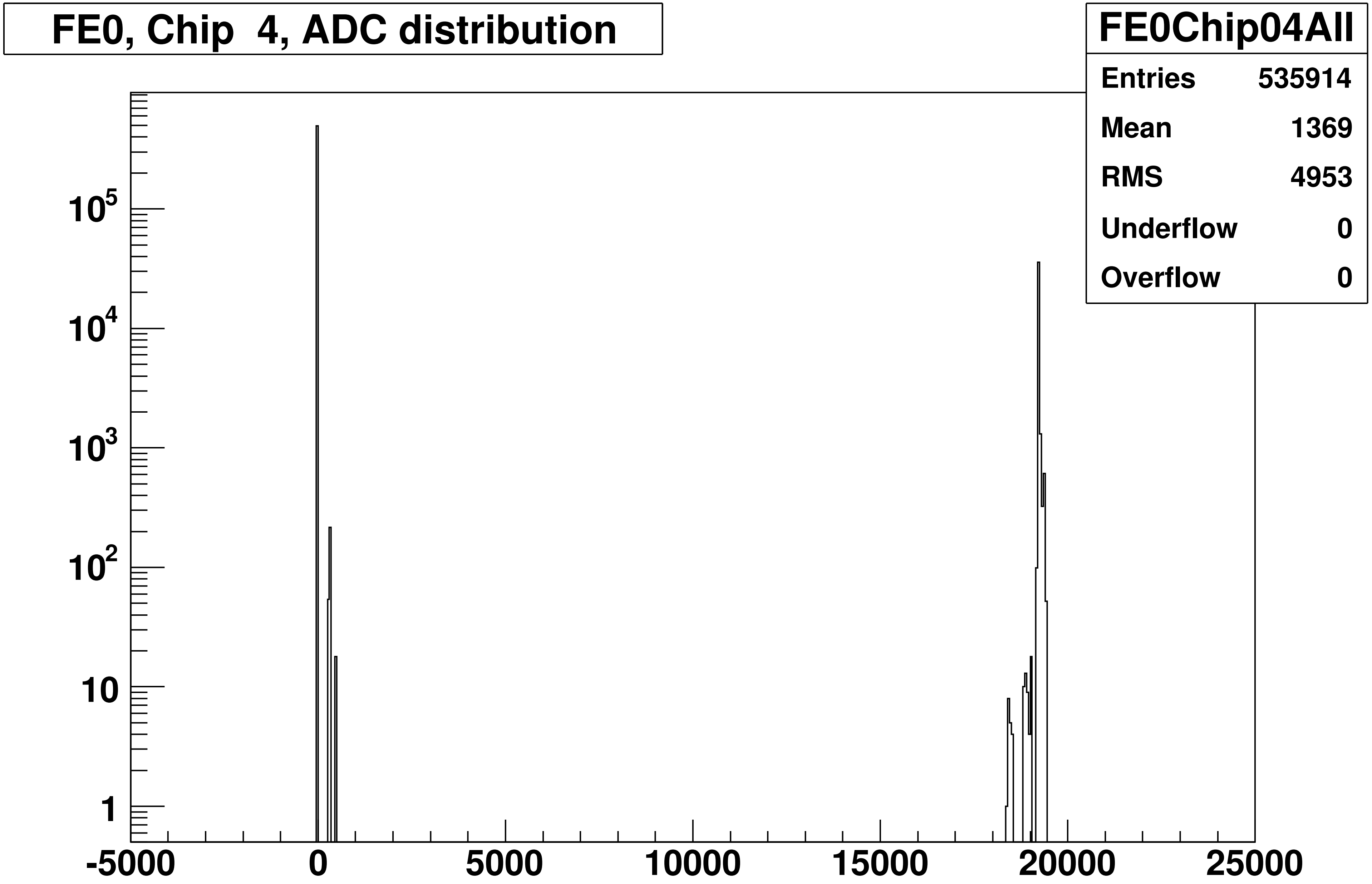

FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 535914
Mean 1.894
RMS 1.403
Underflow 0
Overflow 3.865e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

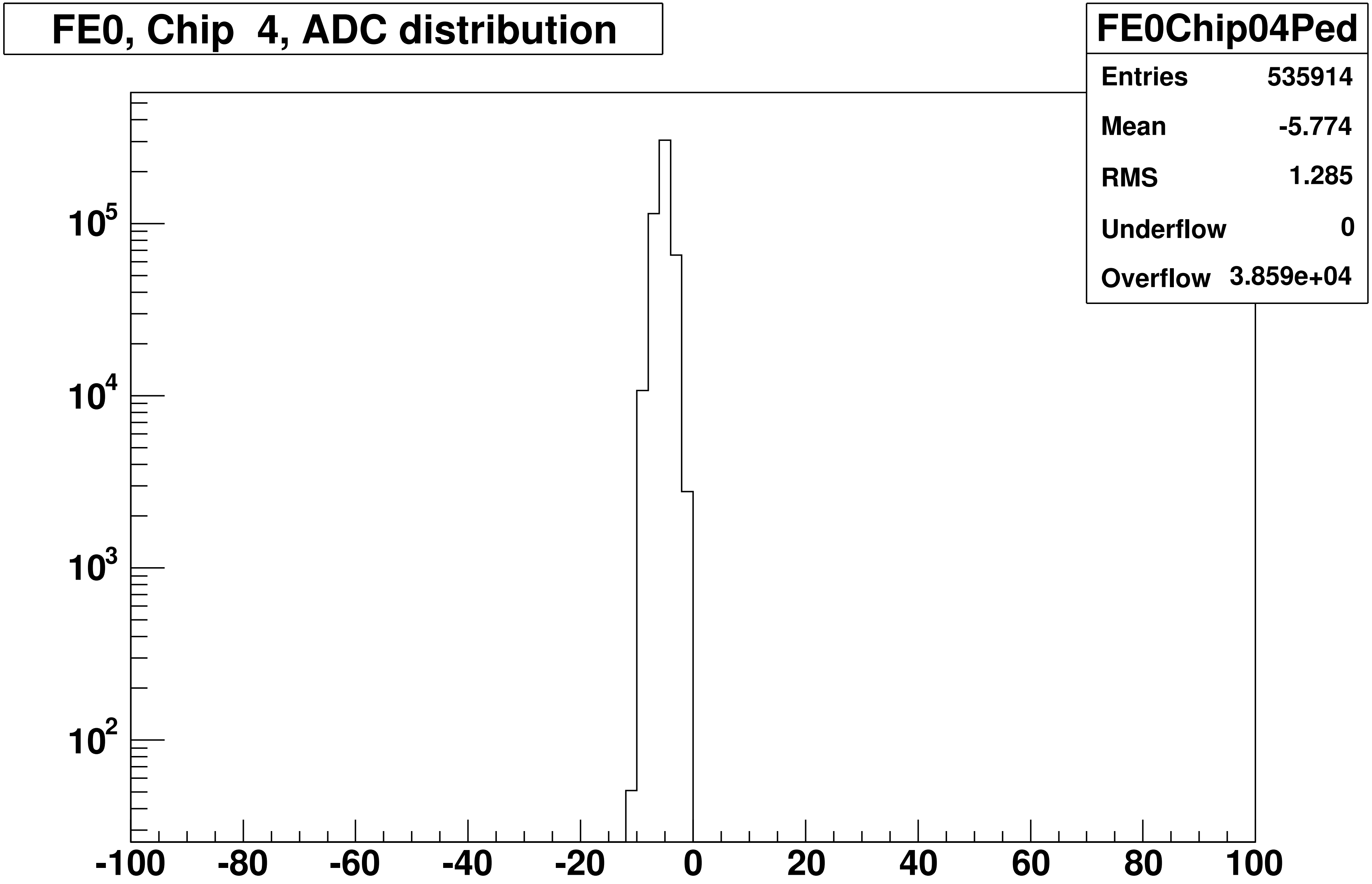

FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 535914
Mean 1.897e+04
RMS 17.29
Underflow 4.974e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 535914
Mean 1399
RMS 4938
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

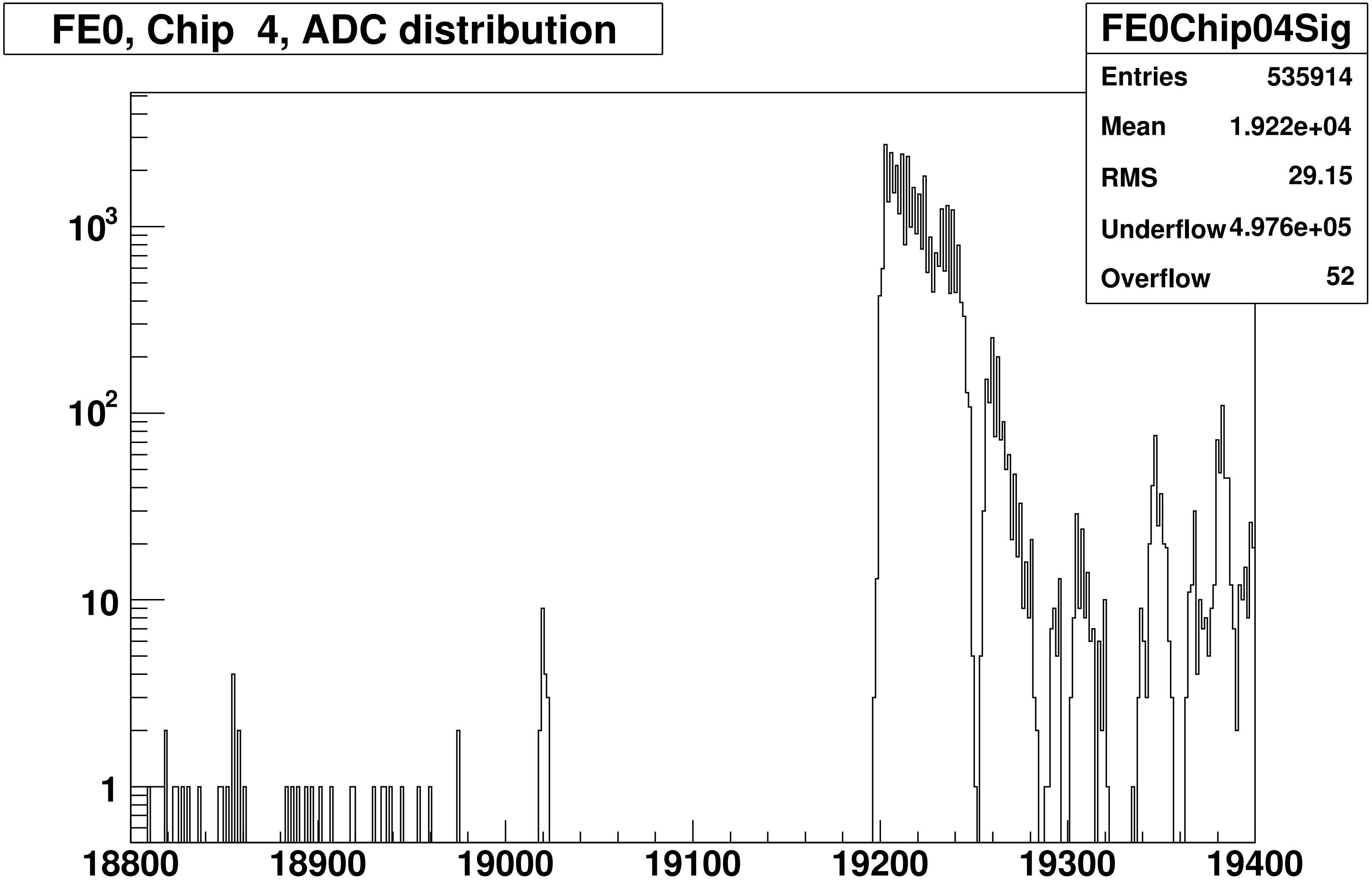

FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 535914
Mean 17.66
RMS 1.316
Underflow 0
Overflow 3.897e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

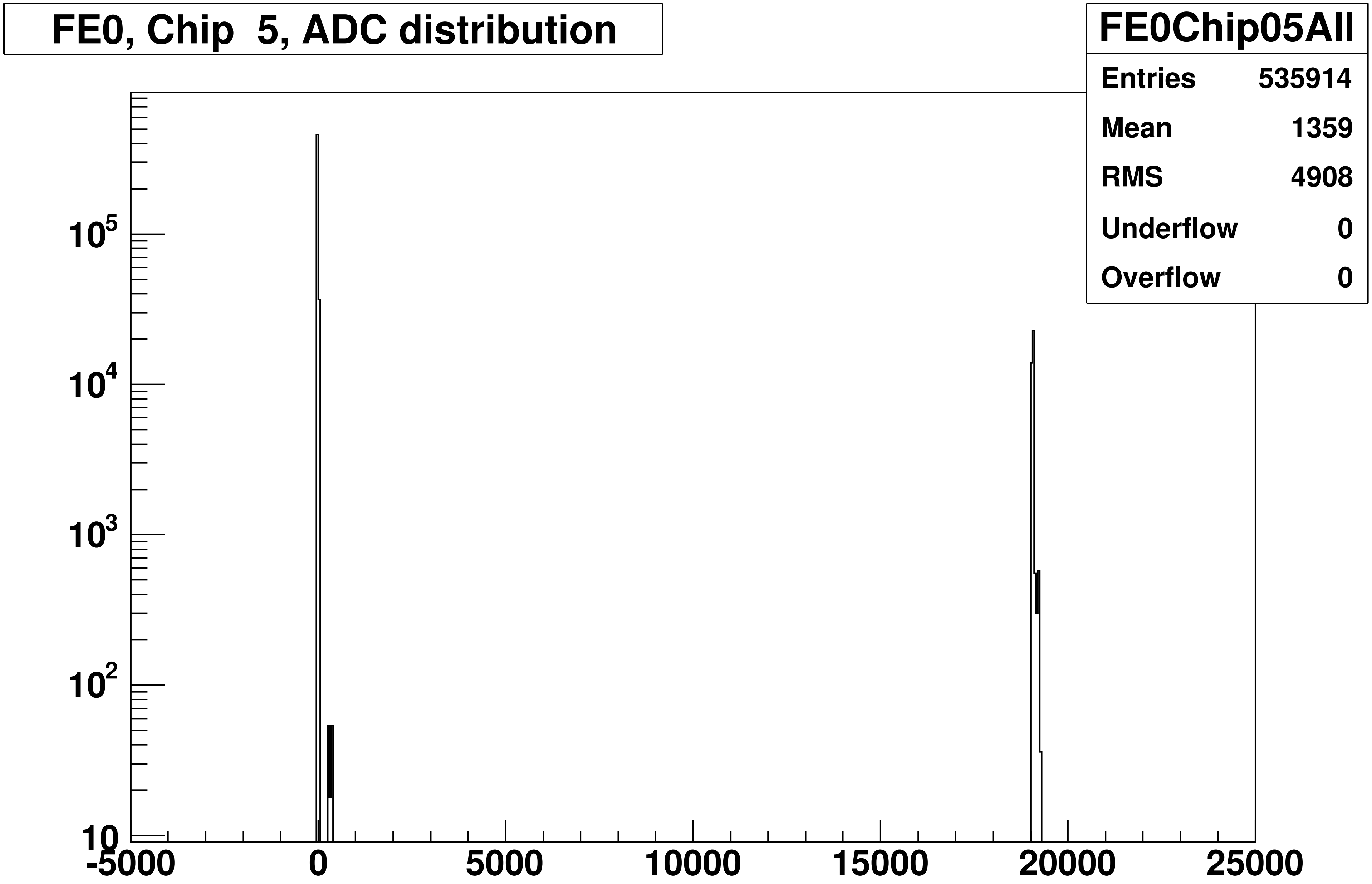

FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 535914
Mean 1.905e+04
RMS 20.1
Underflow 4.97e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

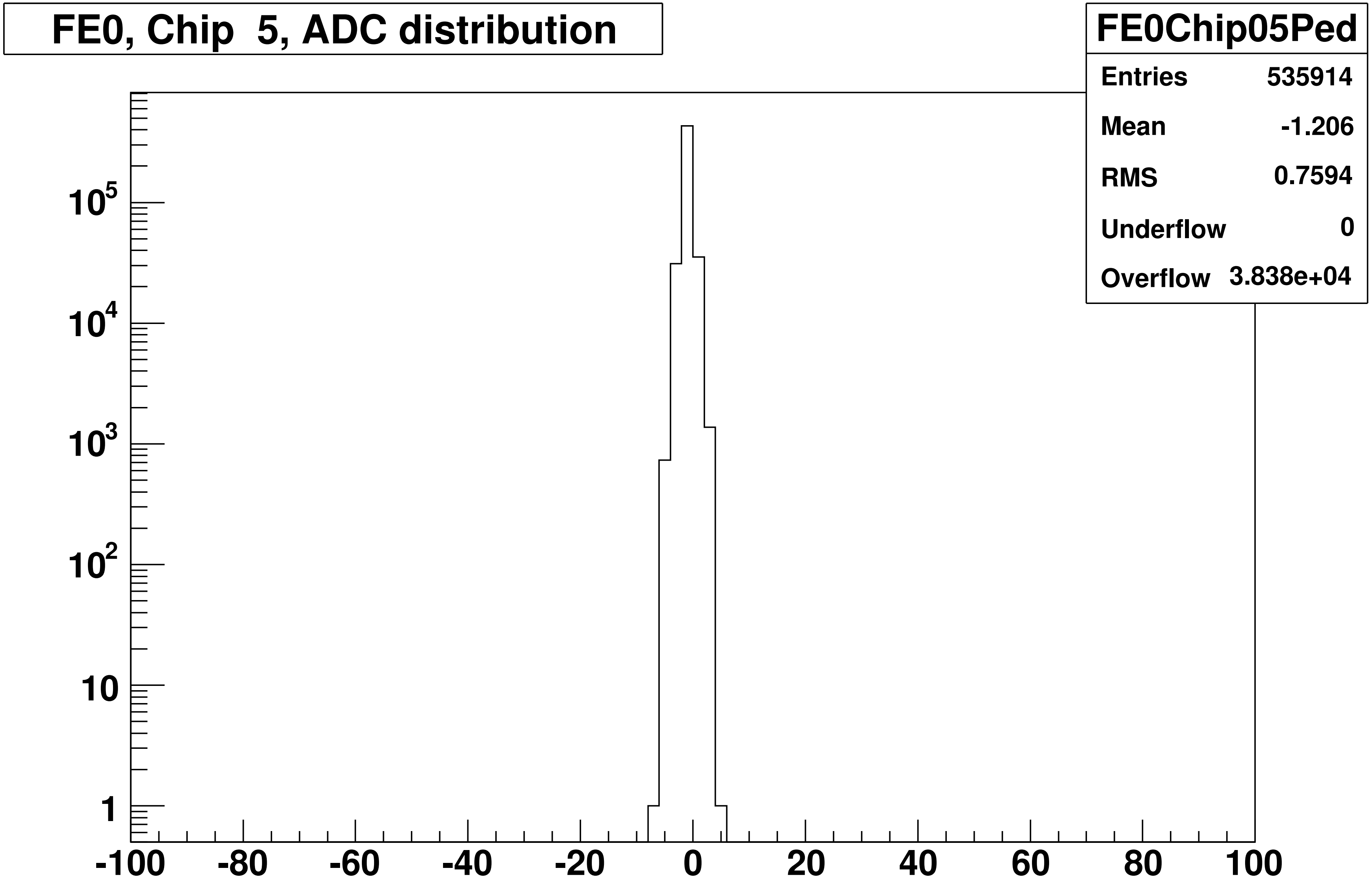

FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 535914
Mean 1369
RMS 4953
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

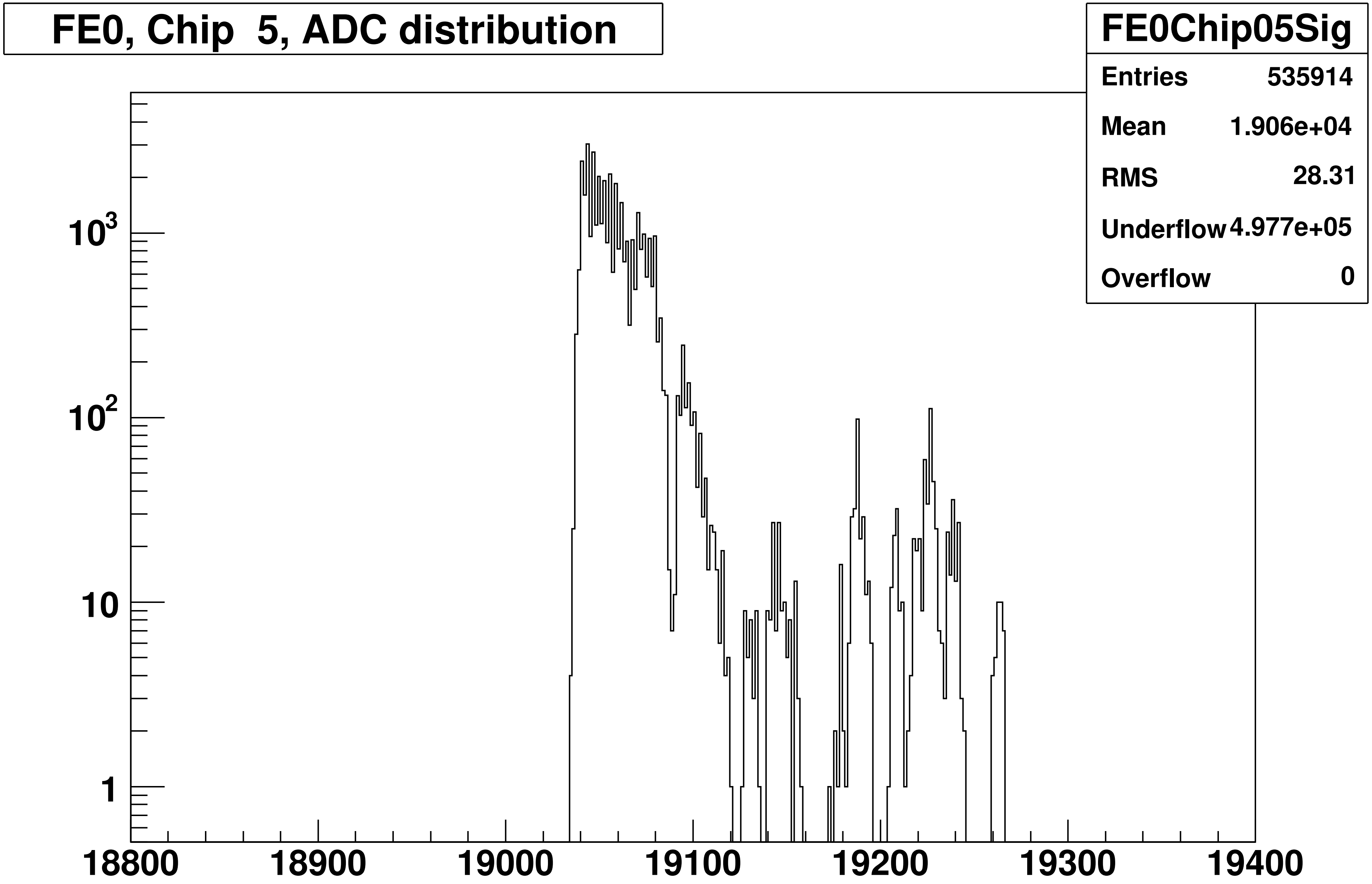


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 535914
Mean -5.774
RMS 1.285
Underflow 0
Overflow 3.859e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

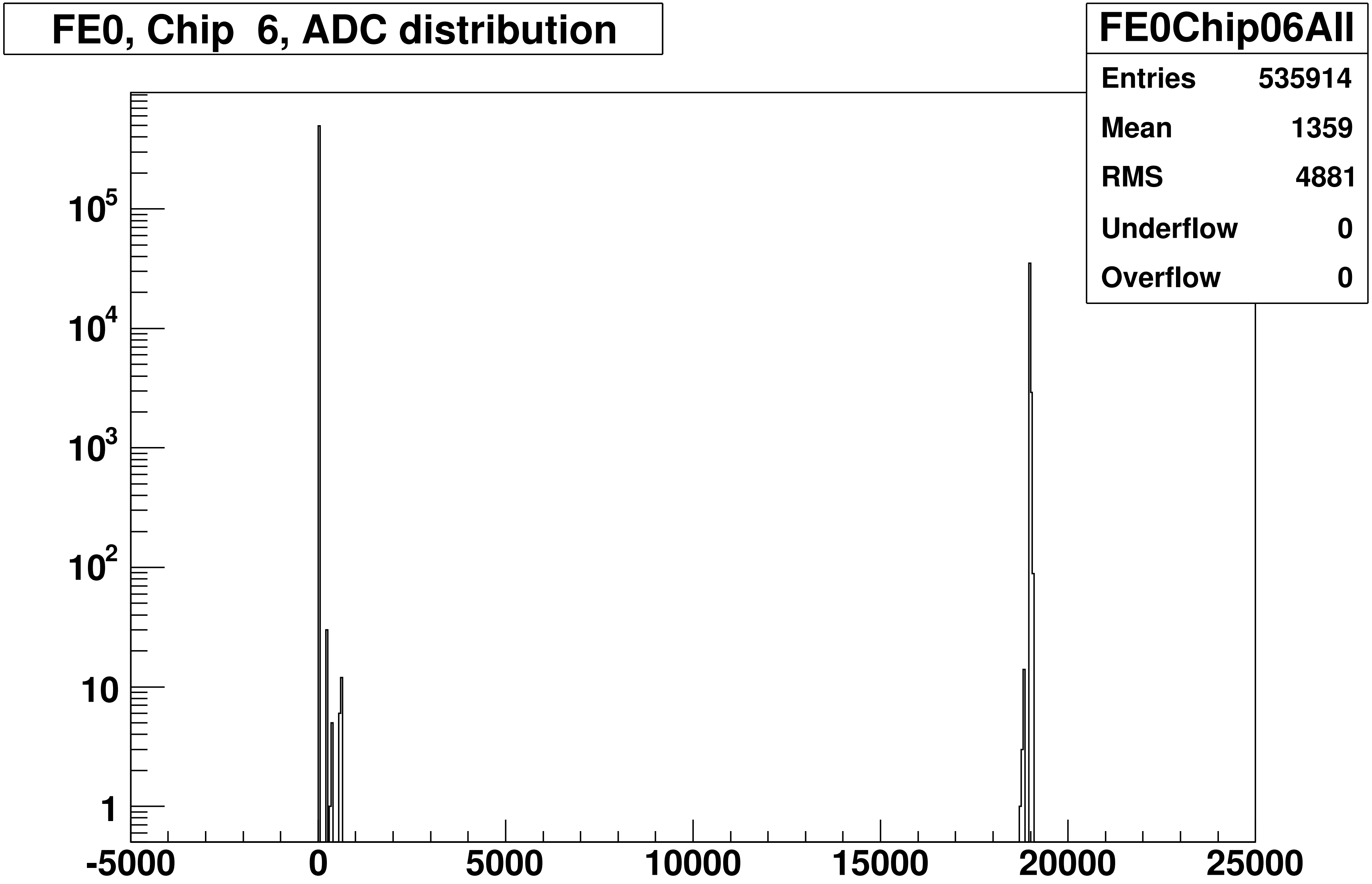

FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 535914
Mean 1.922e+04
RMS 29.15
Underflow 4.976e+05
Overflow 52
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400

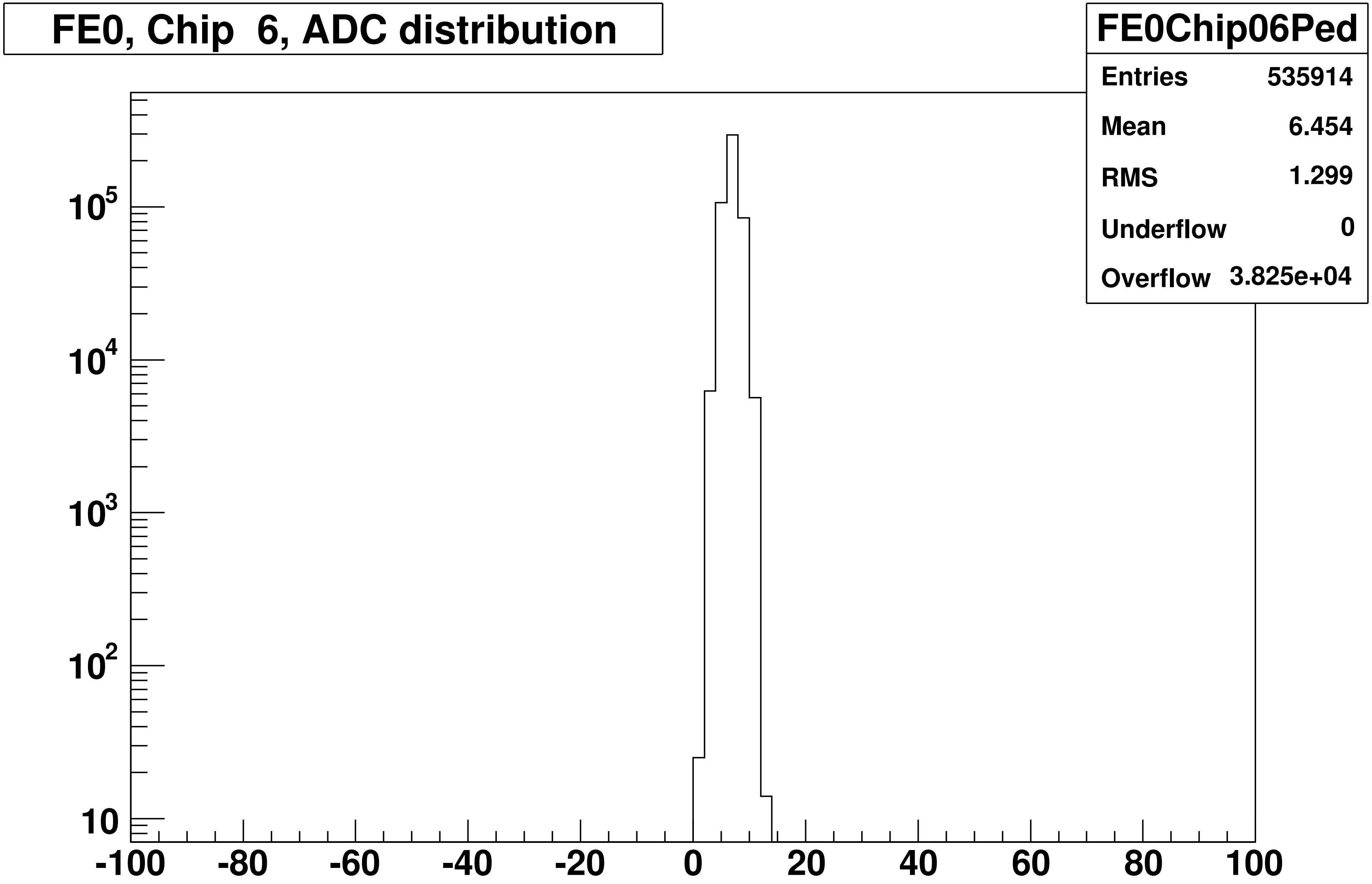

FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 535914
Mean 1359
RMS 4908
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 535914
Mean -1.206
RMS 0.7594
Underflow 0
Overflow 3.838e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

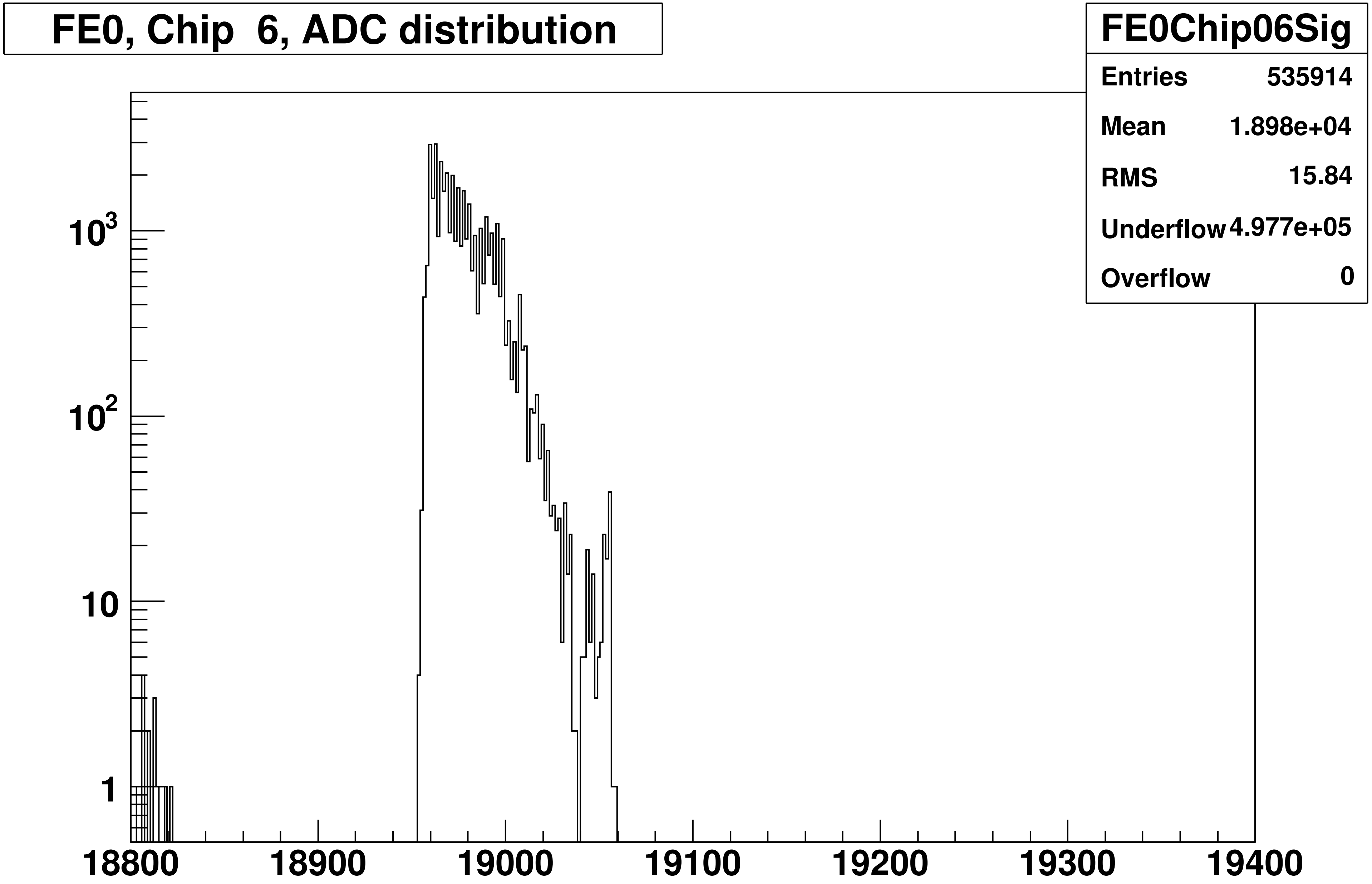


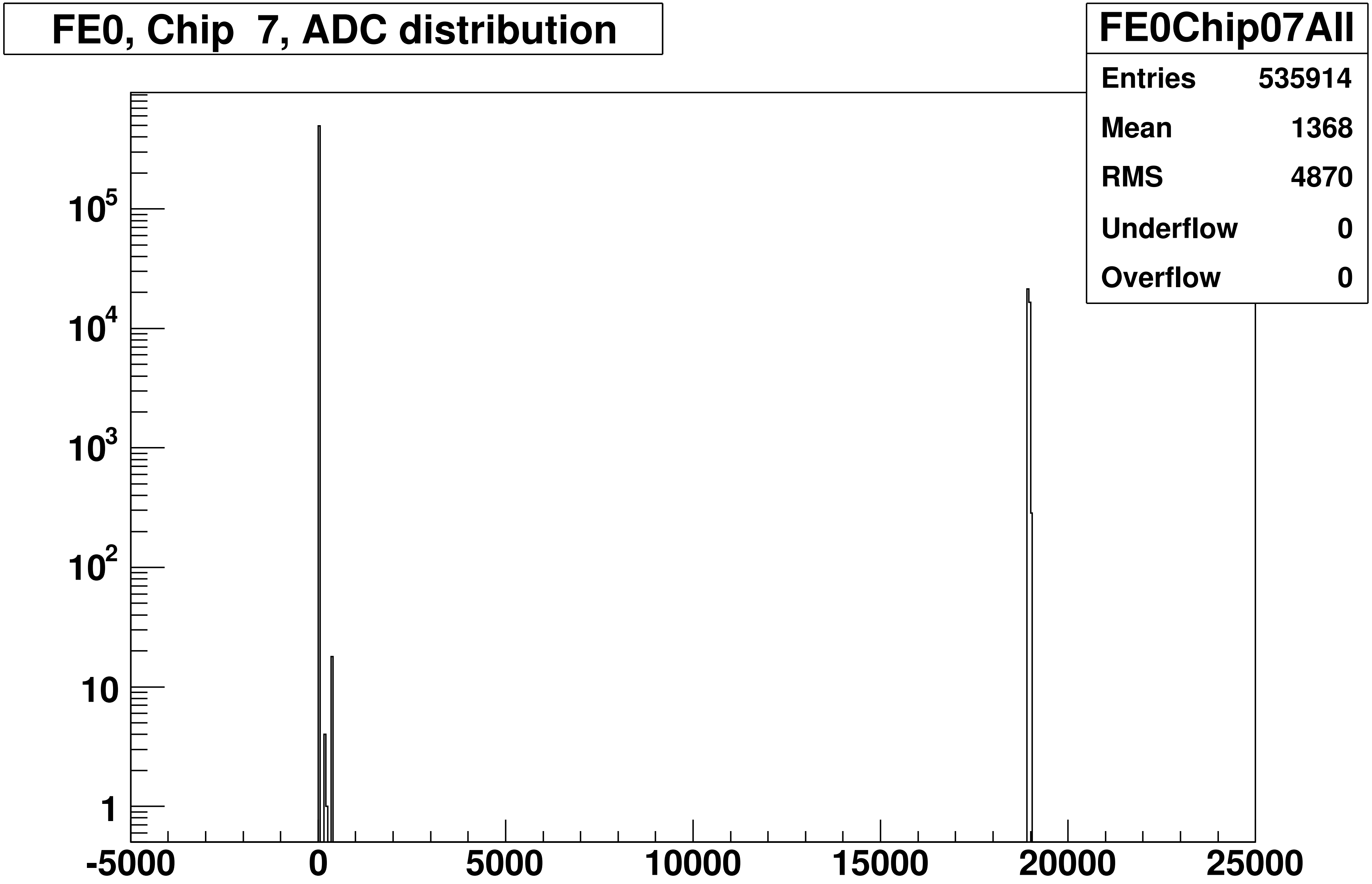
FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 535914
Mean 1.906e+04
RMS 28.31
Underflow 4.977e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 535914
Mean 1359
RMS 4881
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

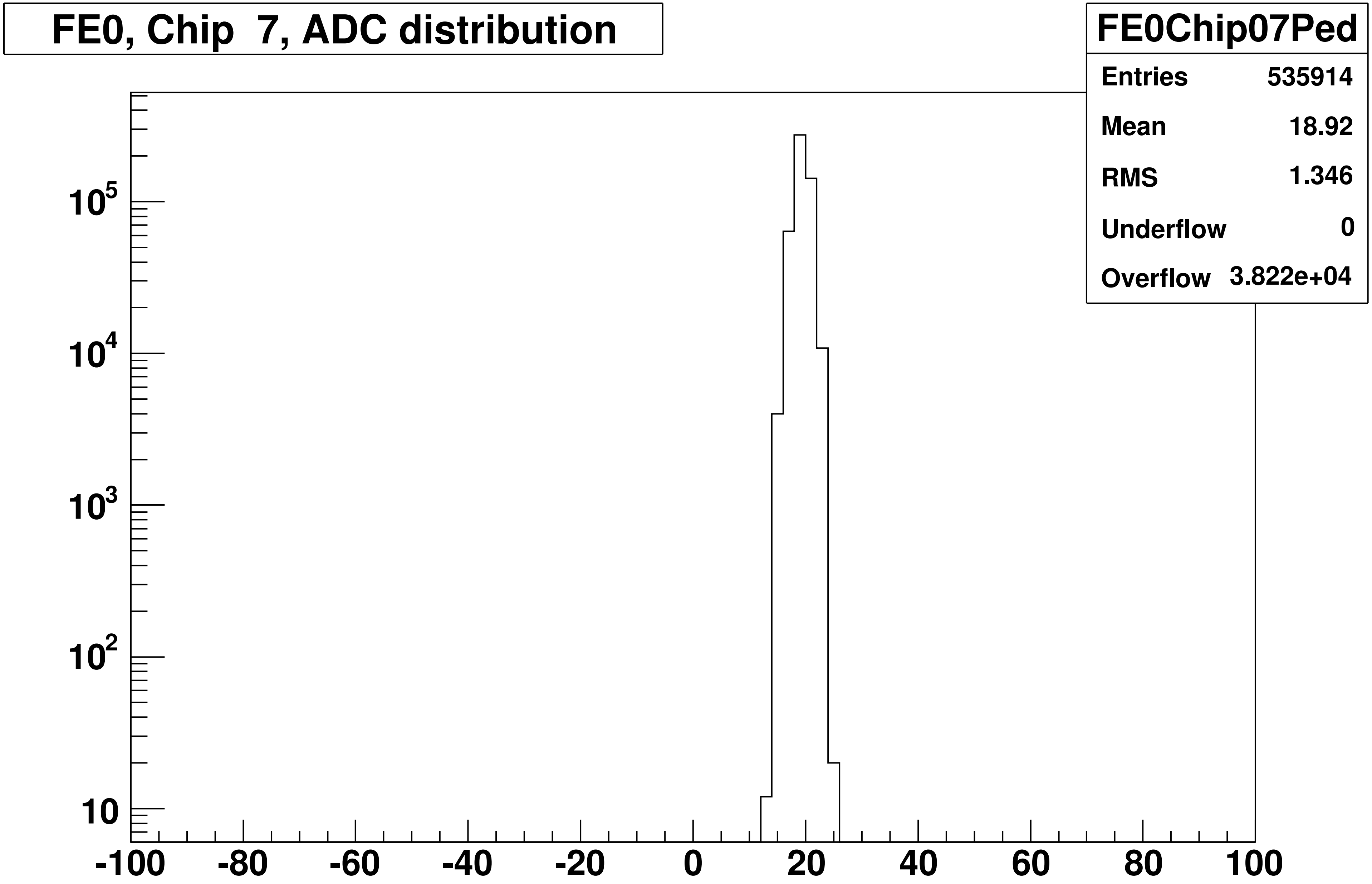


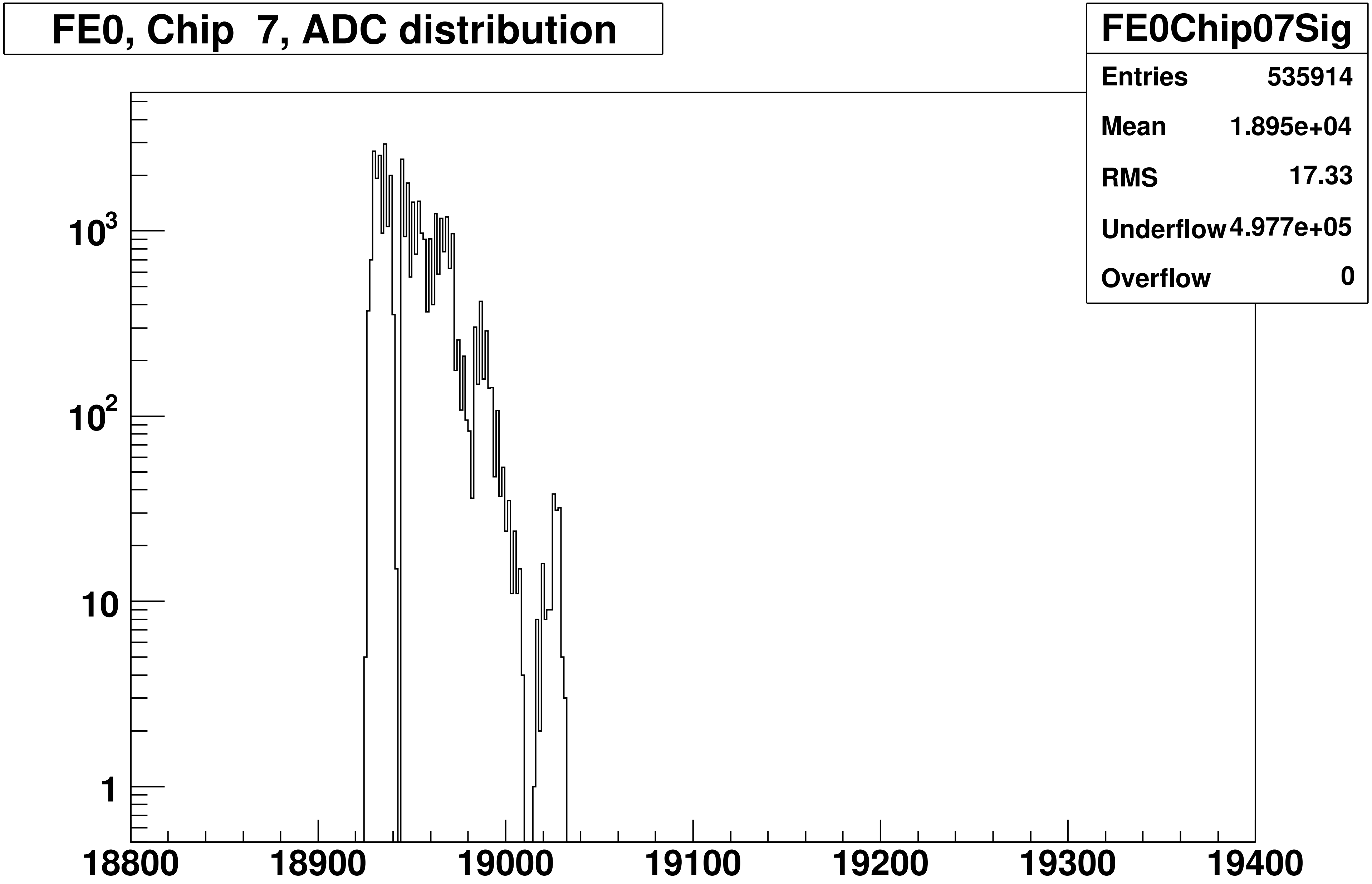
FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 535914
Mean 6.454
RMS 1.299
Underflow 0
Overflow 3.825e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

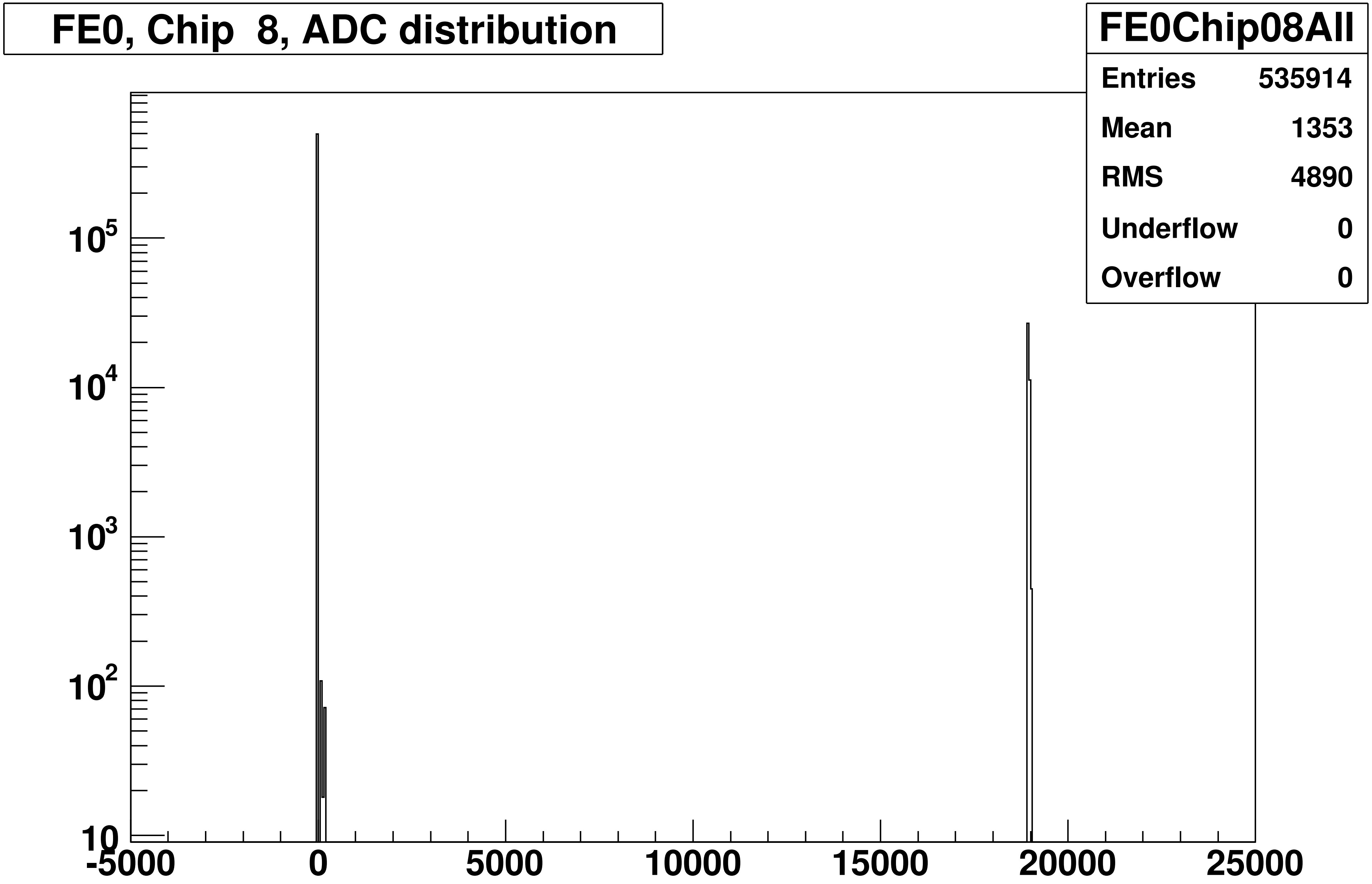

FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 535914
Mean 1.898e+04
RMS 15.84
Underflow 4.977e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



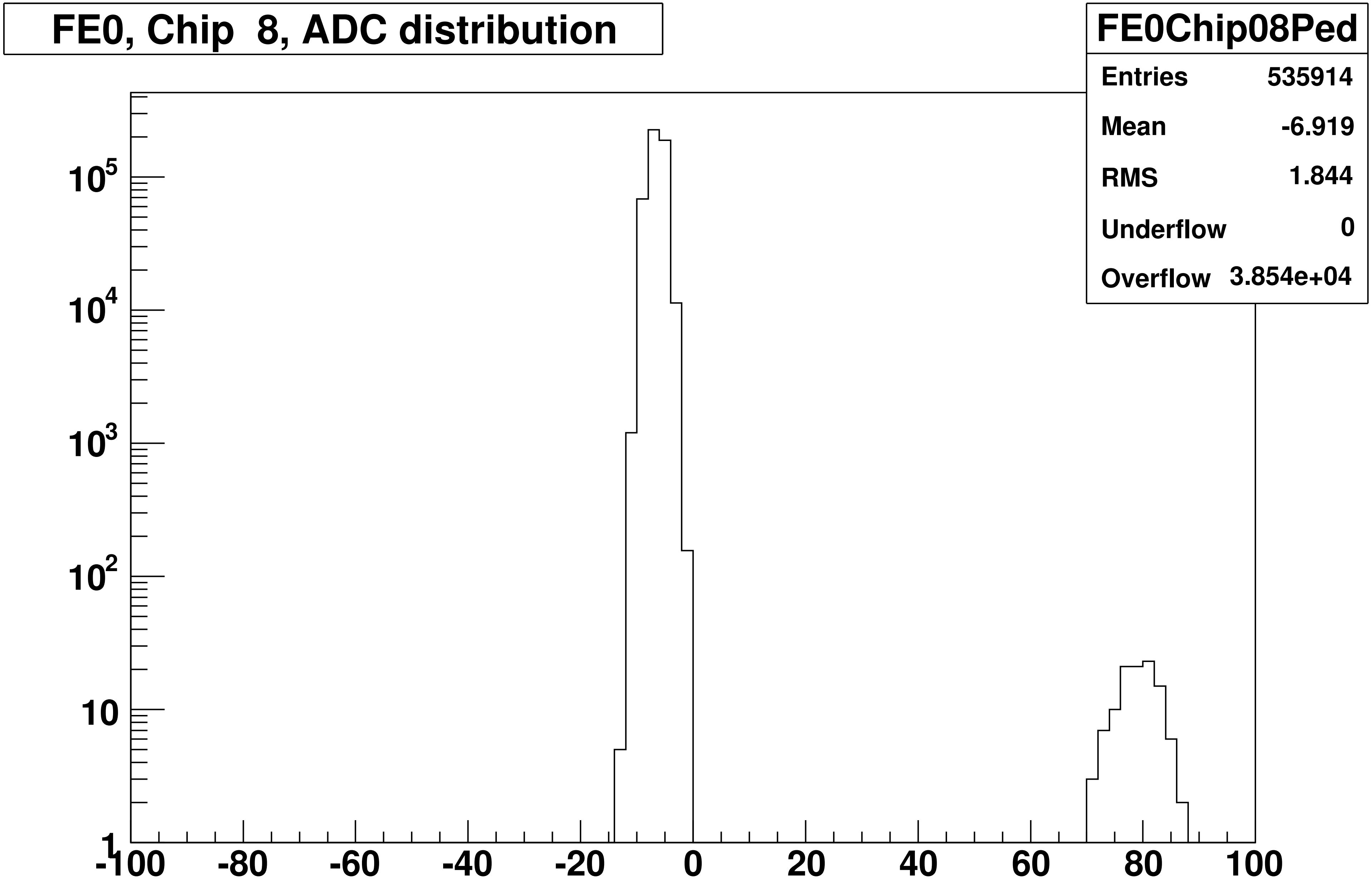
FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 535914
Mean 1368
RMS 4870
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

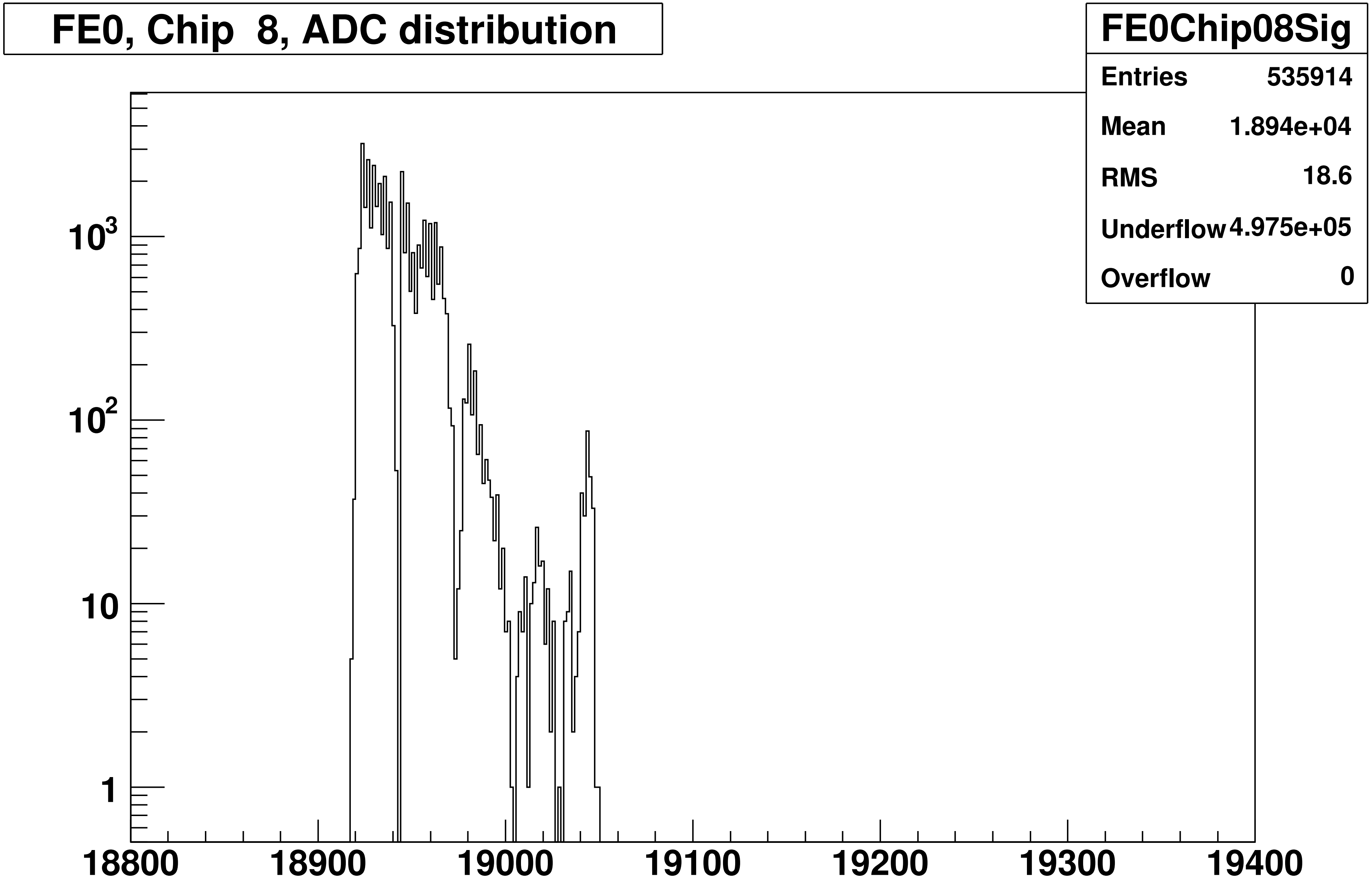

FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 535914
Mean 18.92
RMS 1.346
Underflow 0
Overflow 3.822e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

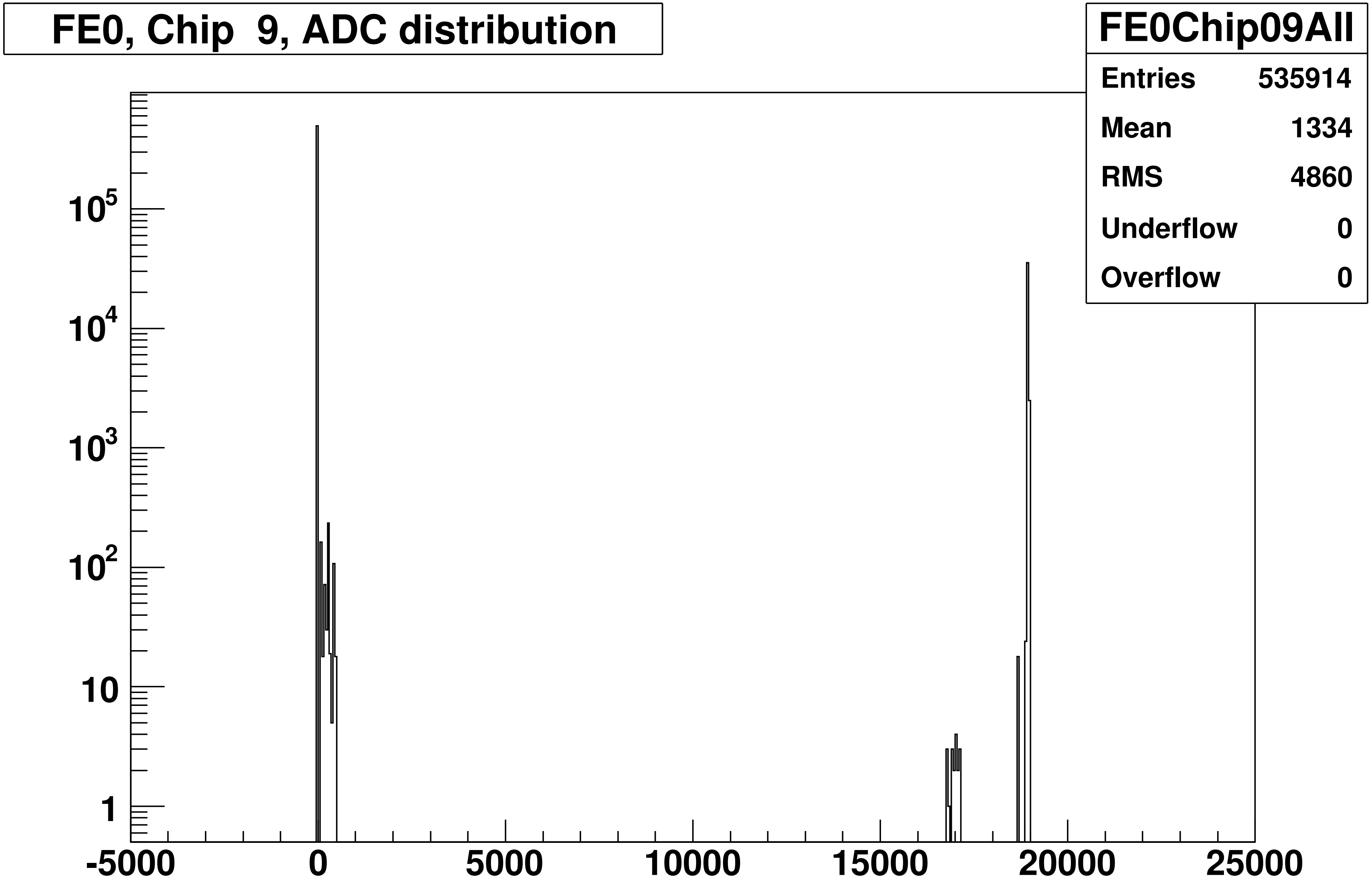

FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 535914
Mean 1.895e+04
RMS 17.33
Underflow 4.977e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

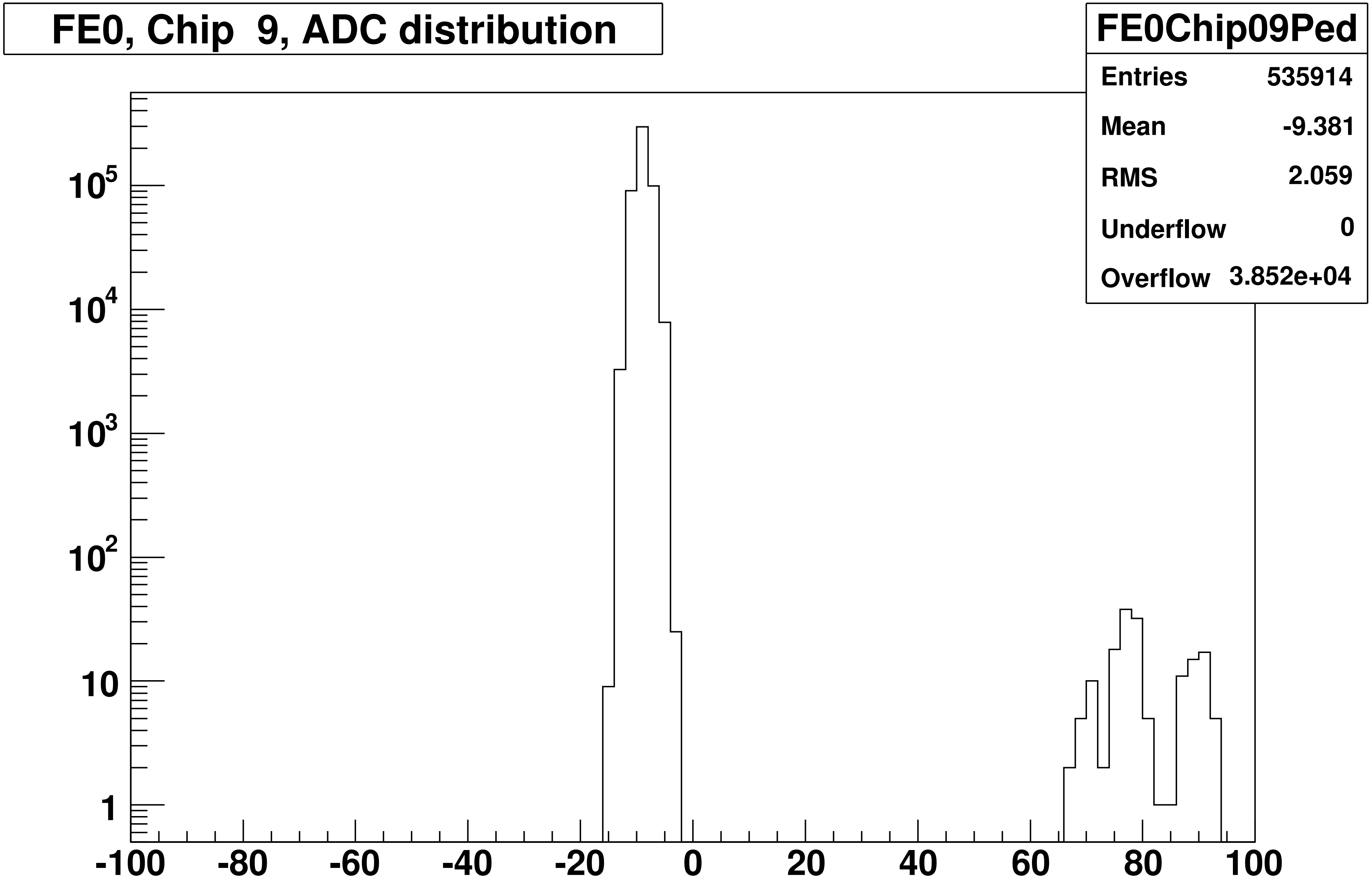


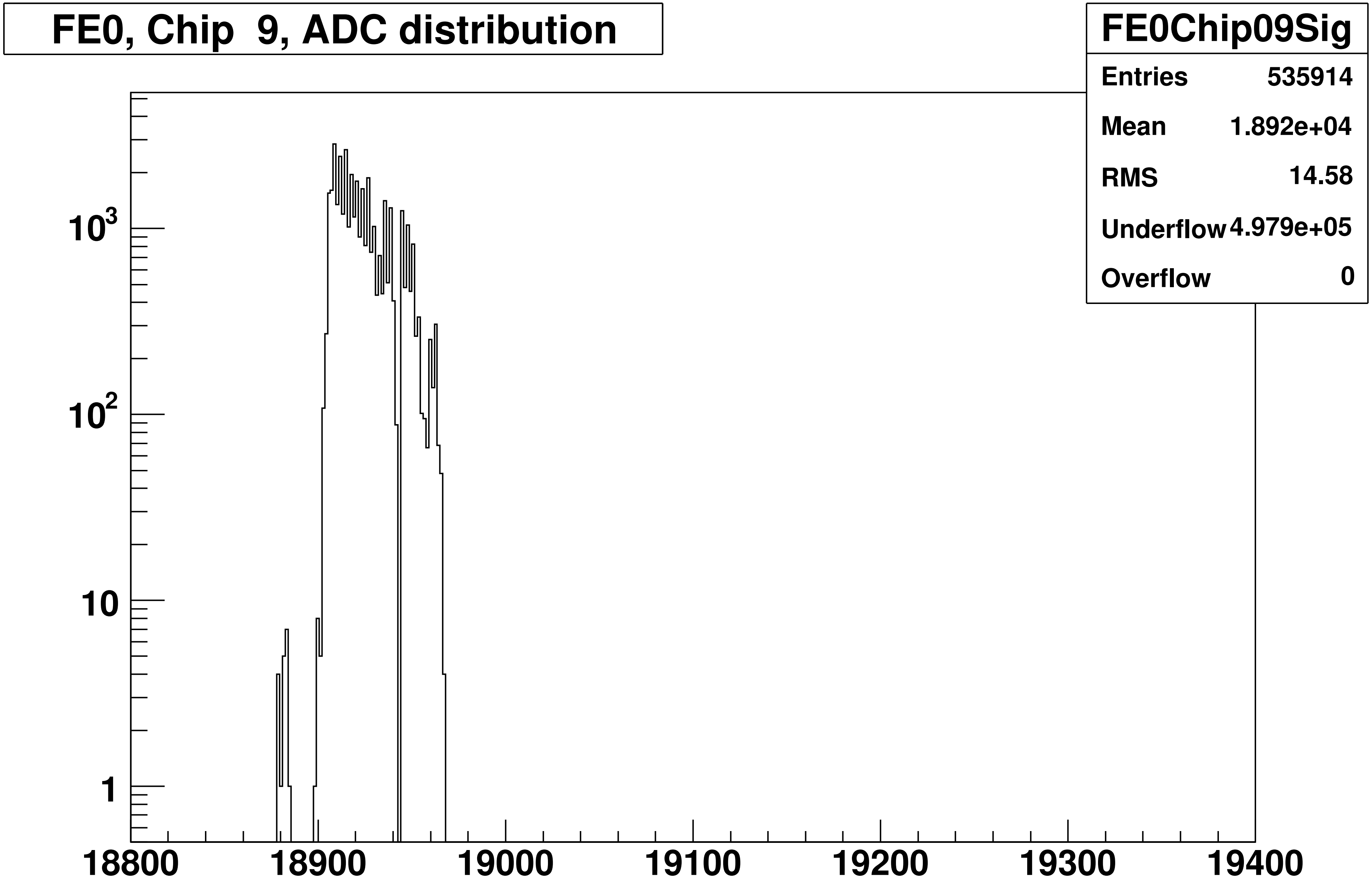
FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 535914
Mean 1353
RMS 4890
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

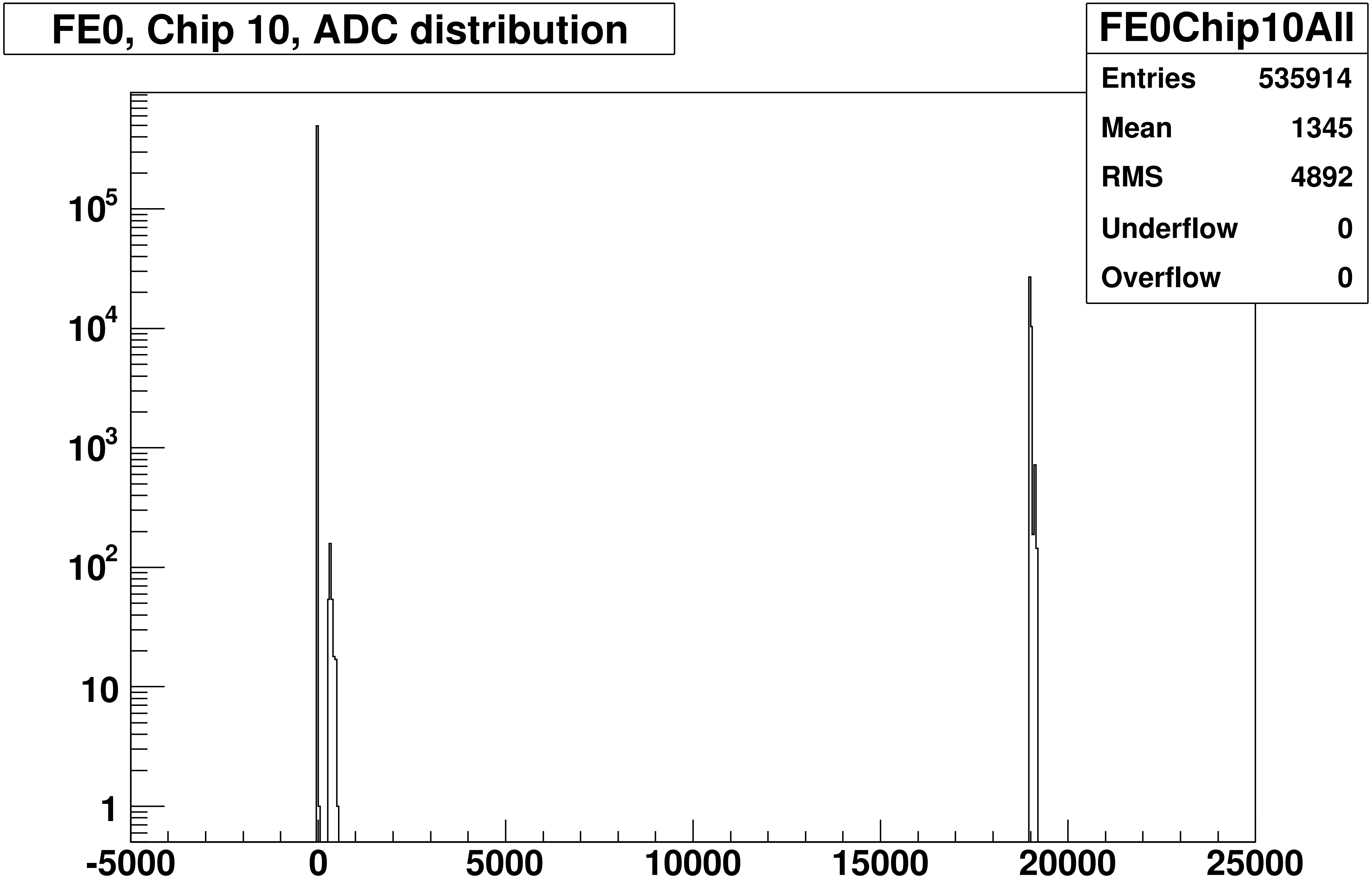

FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 535914
Mean -6.919
RMS 1.844
Underflow 0
Overflow 3.854e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 535914
Mean 1.894e+04
RMS 18.6
Underflow 4.975e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

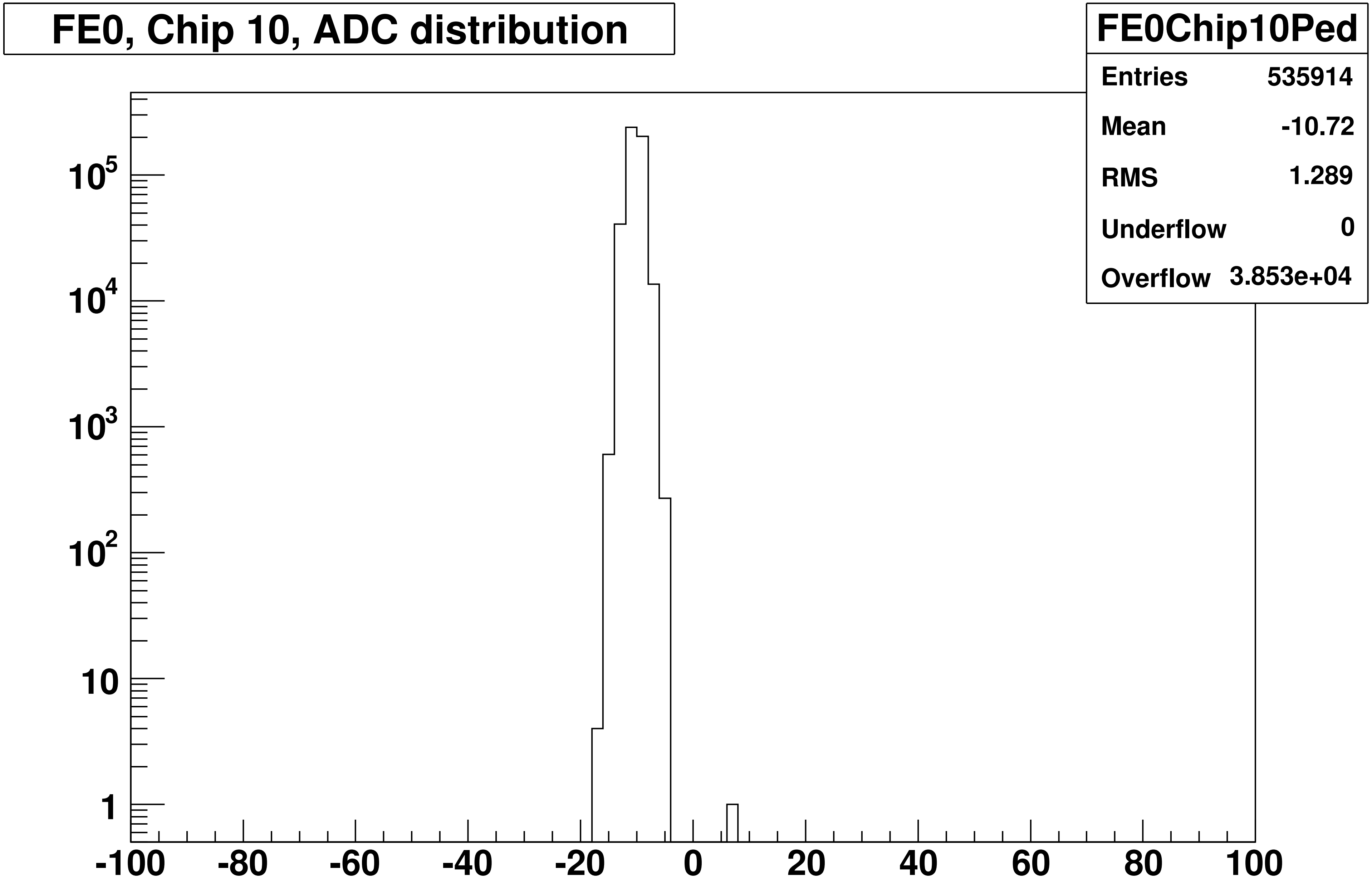

FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 535914
Mean 1334
RMS 4860
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

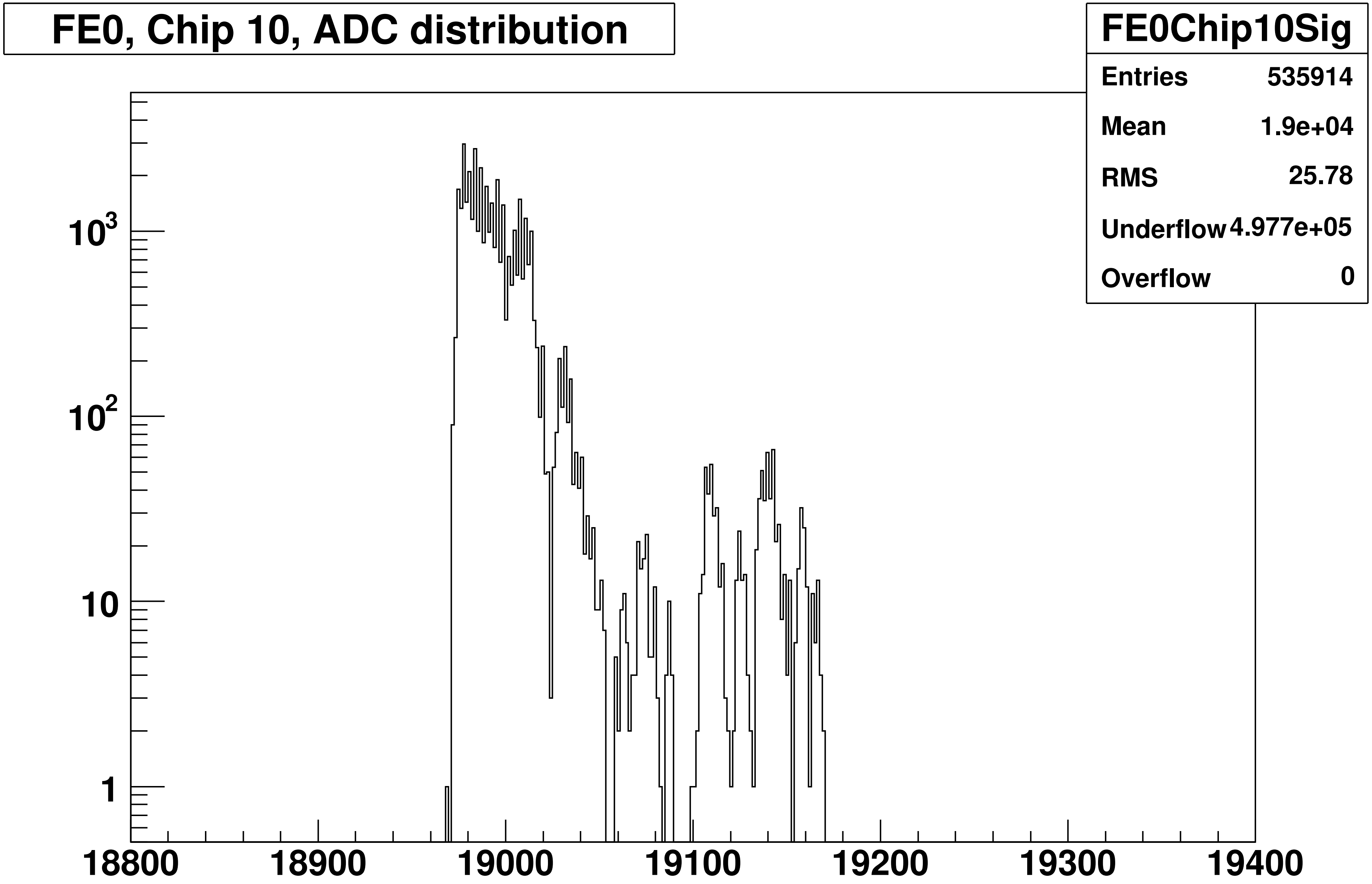

FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 535914
Mean -9.381
RMS 2.059
Underflow 0
Overflow 3.852e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

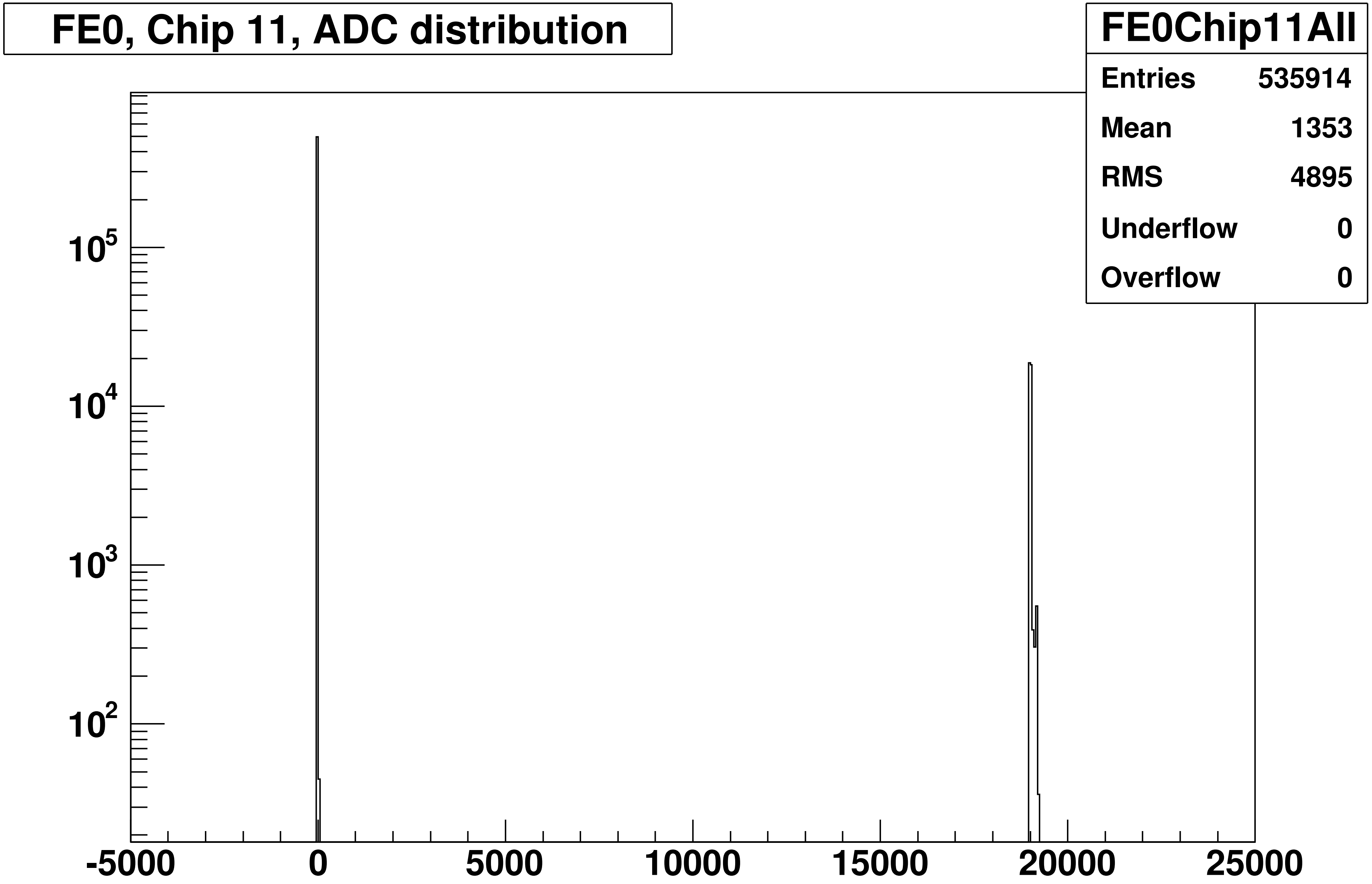

FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 535914
Mean 1.892e+04
RMS 14.58
Underflow 4.979e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

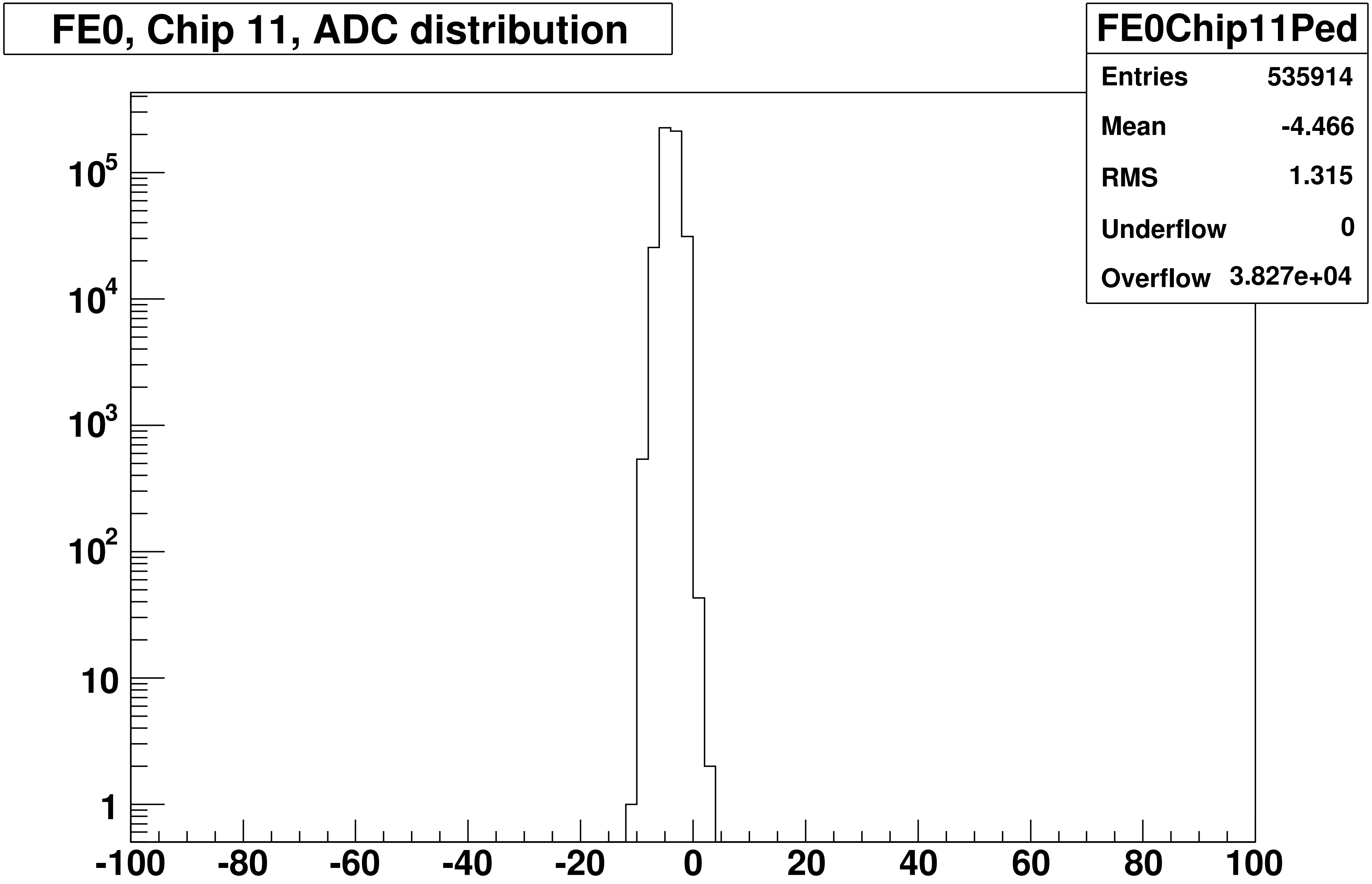


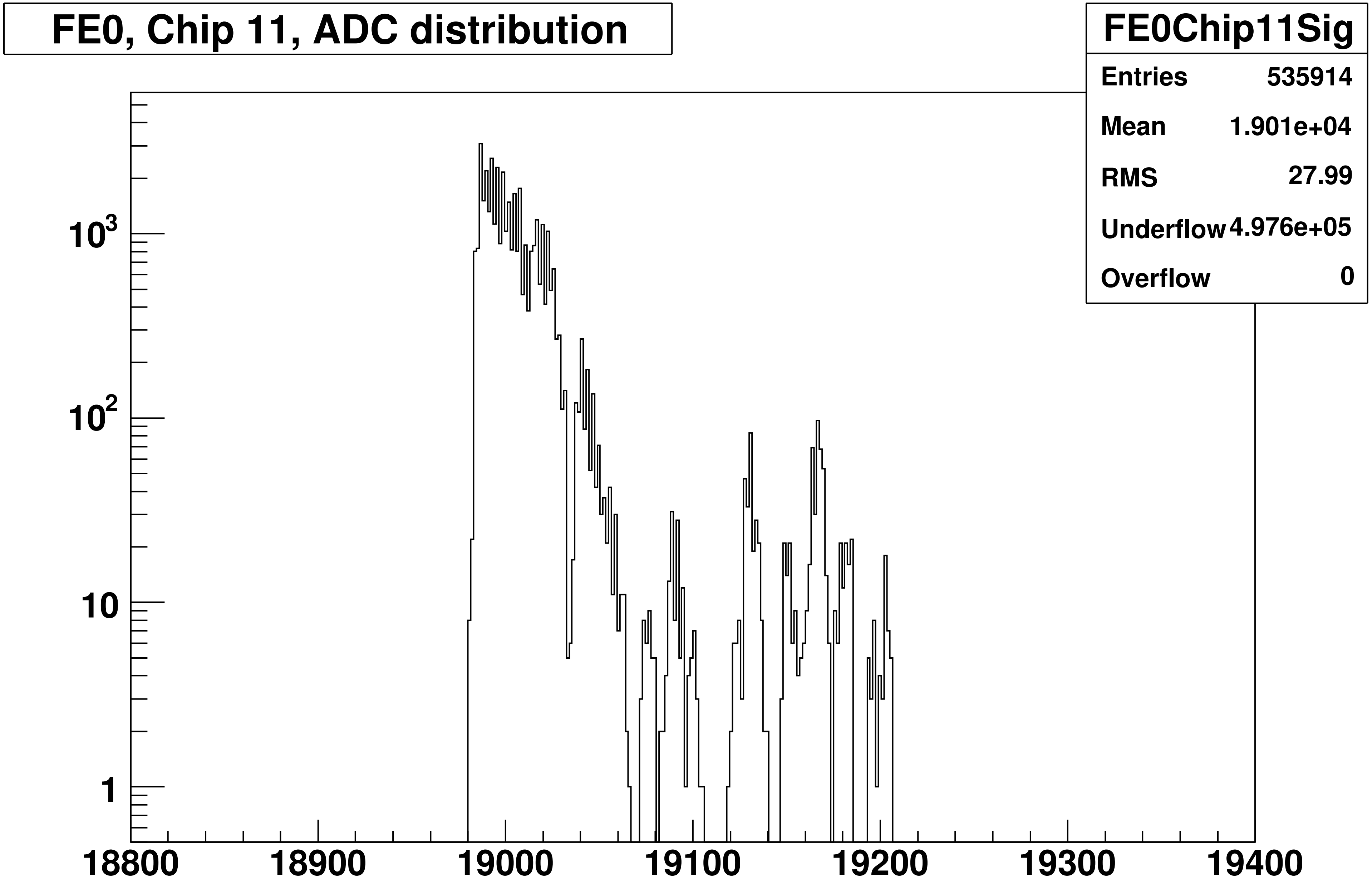
FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 535914
Mean 1345
RMS 4892
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 535914
Mean -10.72
RMS 1.289
Underflow 0
Overflow 3.853e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

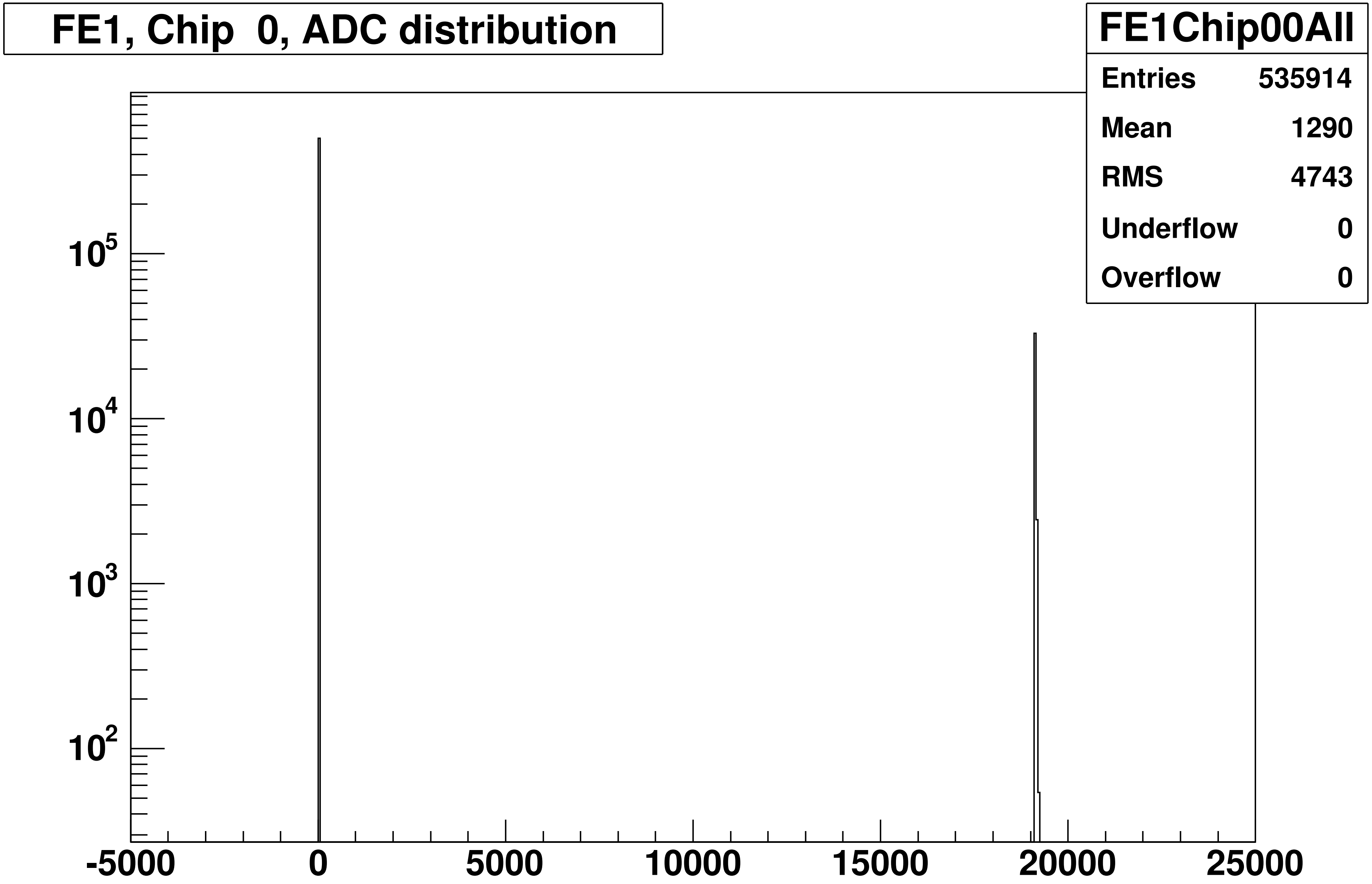

FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 535914
Mean 1.9e+04
RMS 25.78
Underflow 4.977e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

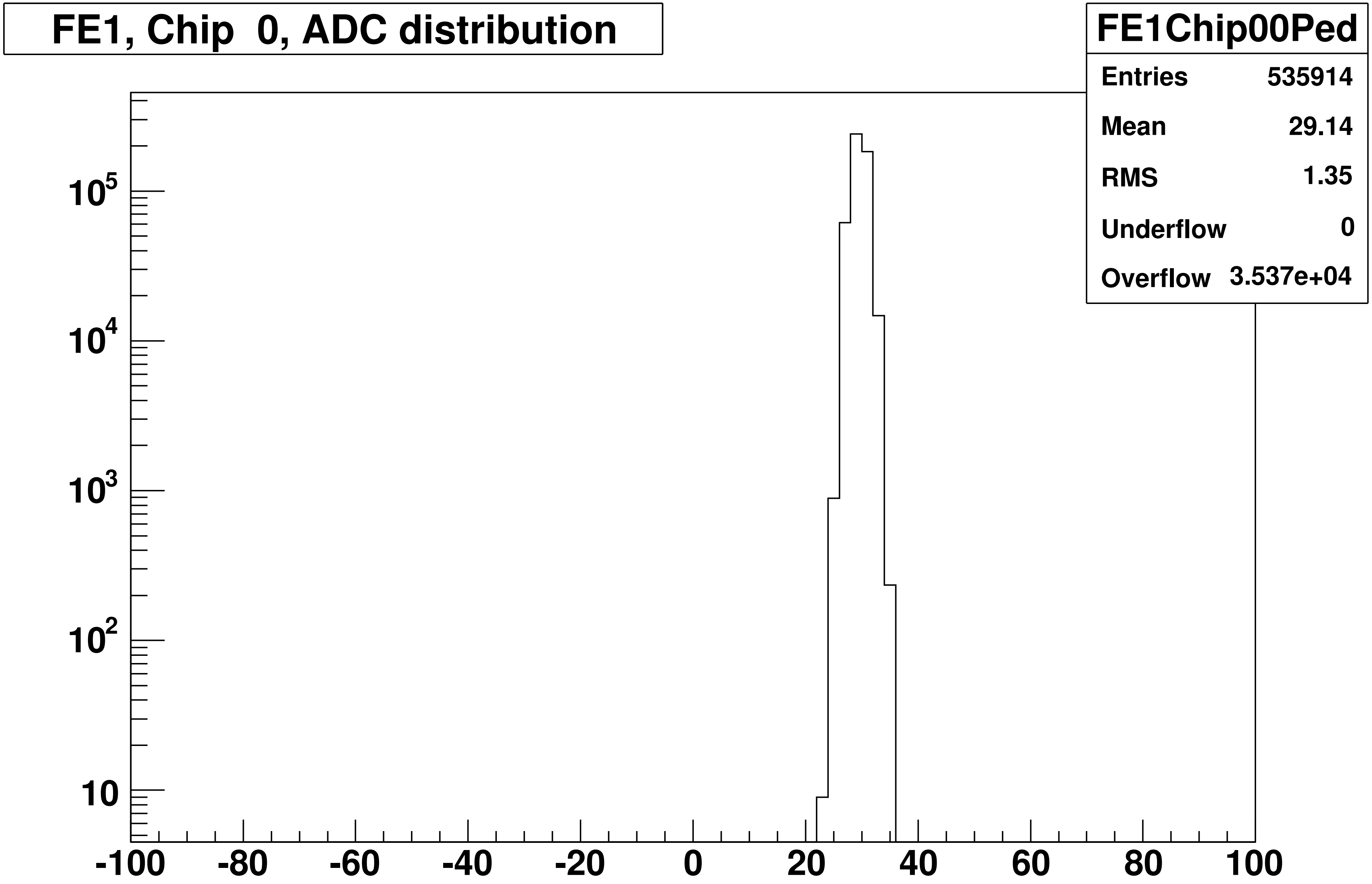

FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 535914
Mean 1353
RMS 4895
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 535914
Mean -4.466
RMS 1.315
Underflow 0
Overflow 3.827e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

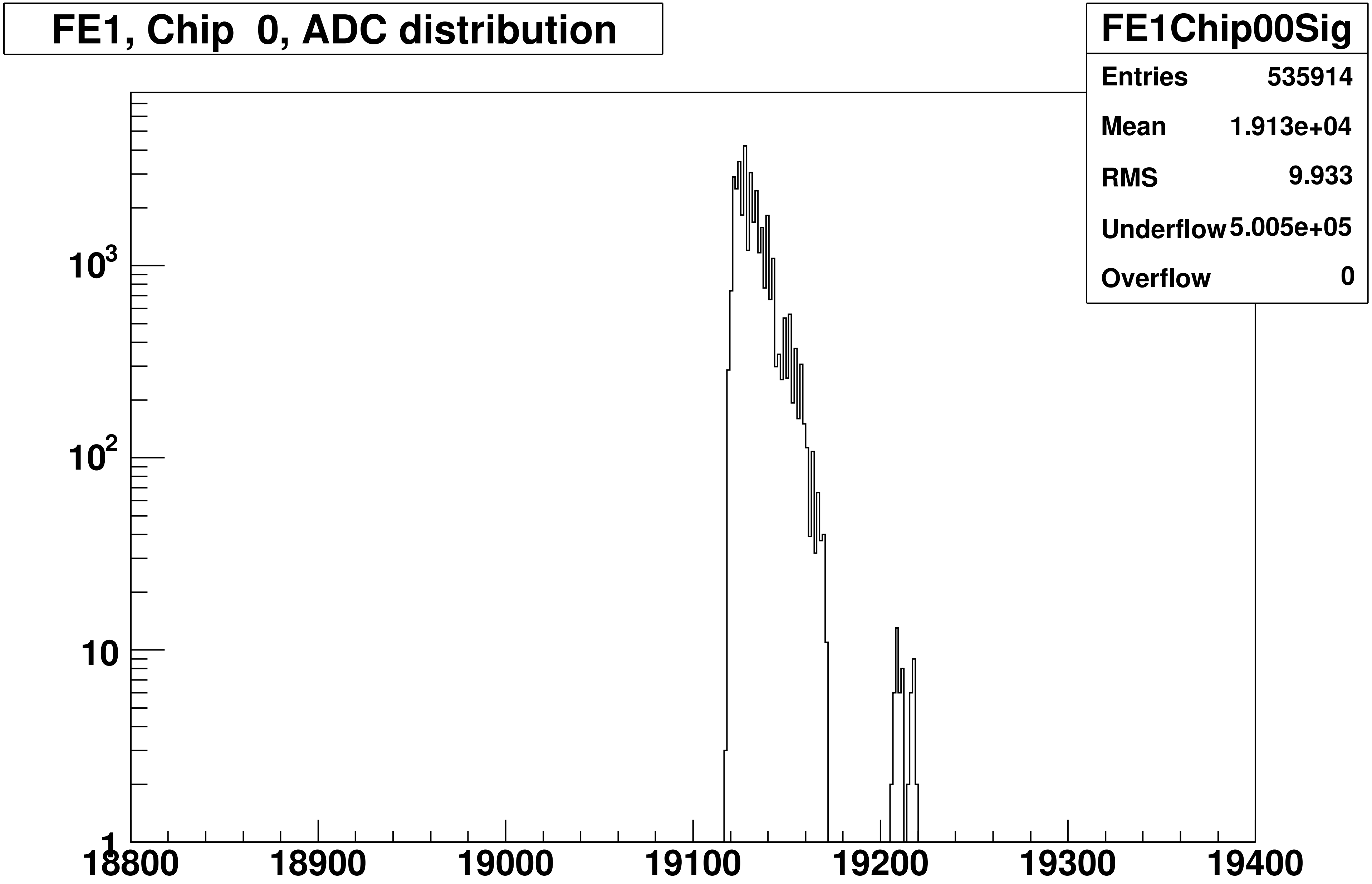


FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 535914
Mean 1.901e+04
RMS 27.99
Underflow 4.976e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

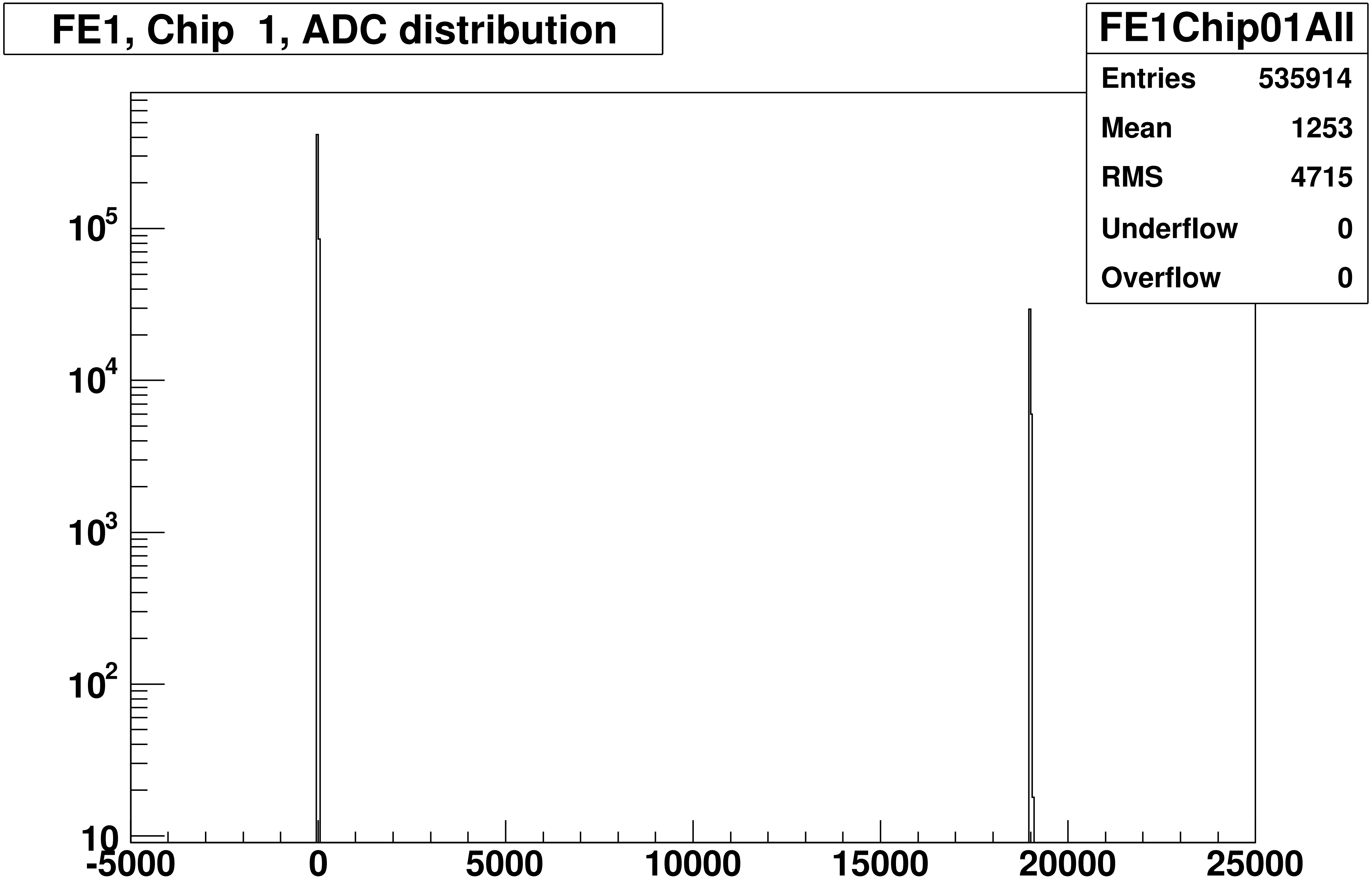

FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 535914
Mean 1290
RMS 4743
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



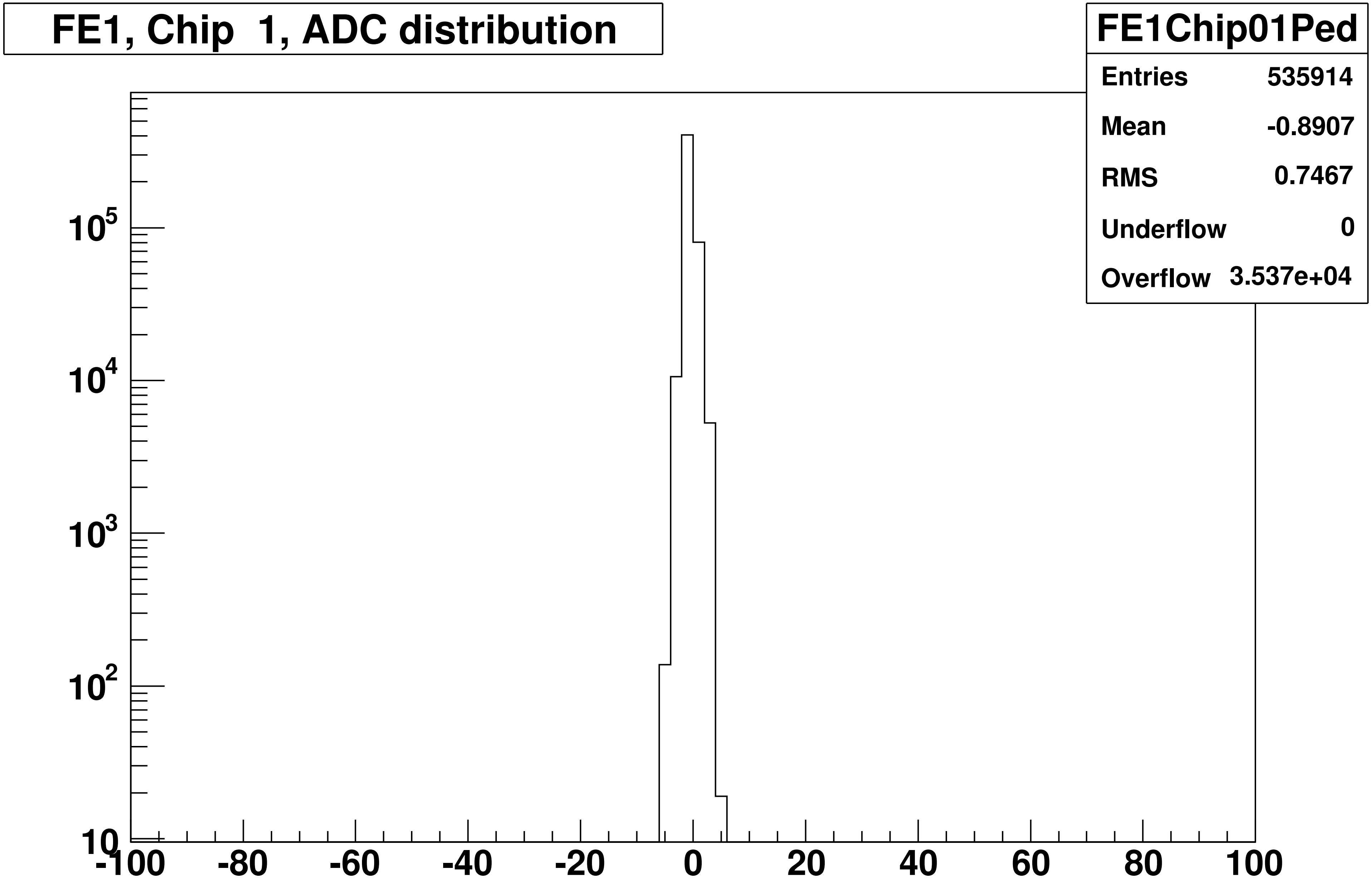
FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 535914
Mean 29.14
RMS 1.35
Underflow 0
Overflow 3.537e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

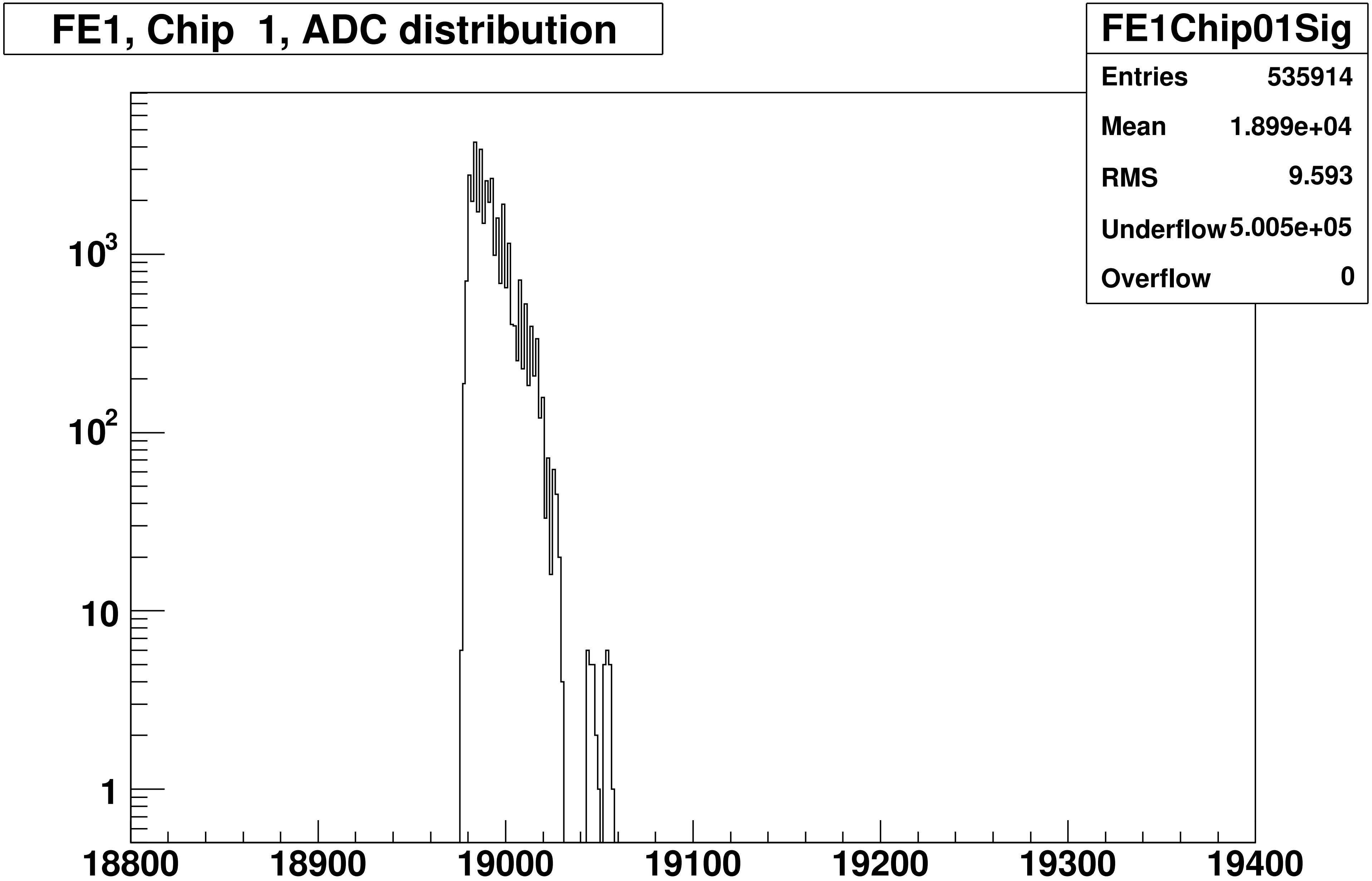


FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 535914
Mean 1.913e+04
RMS 9.933
Underflow 5.005e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

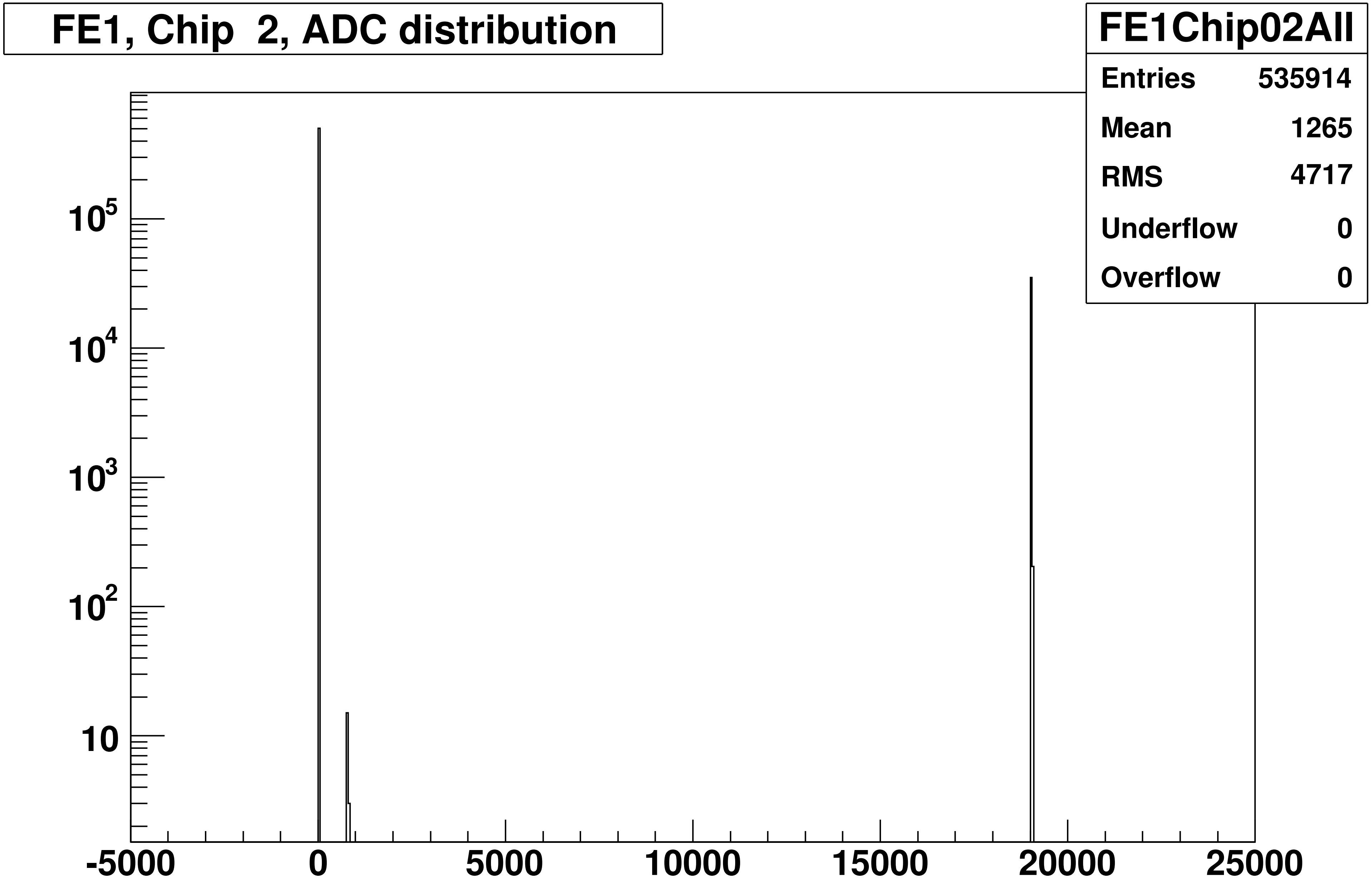


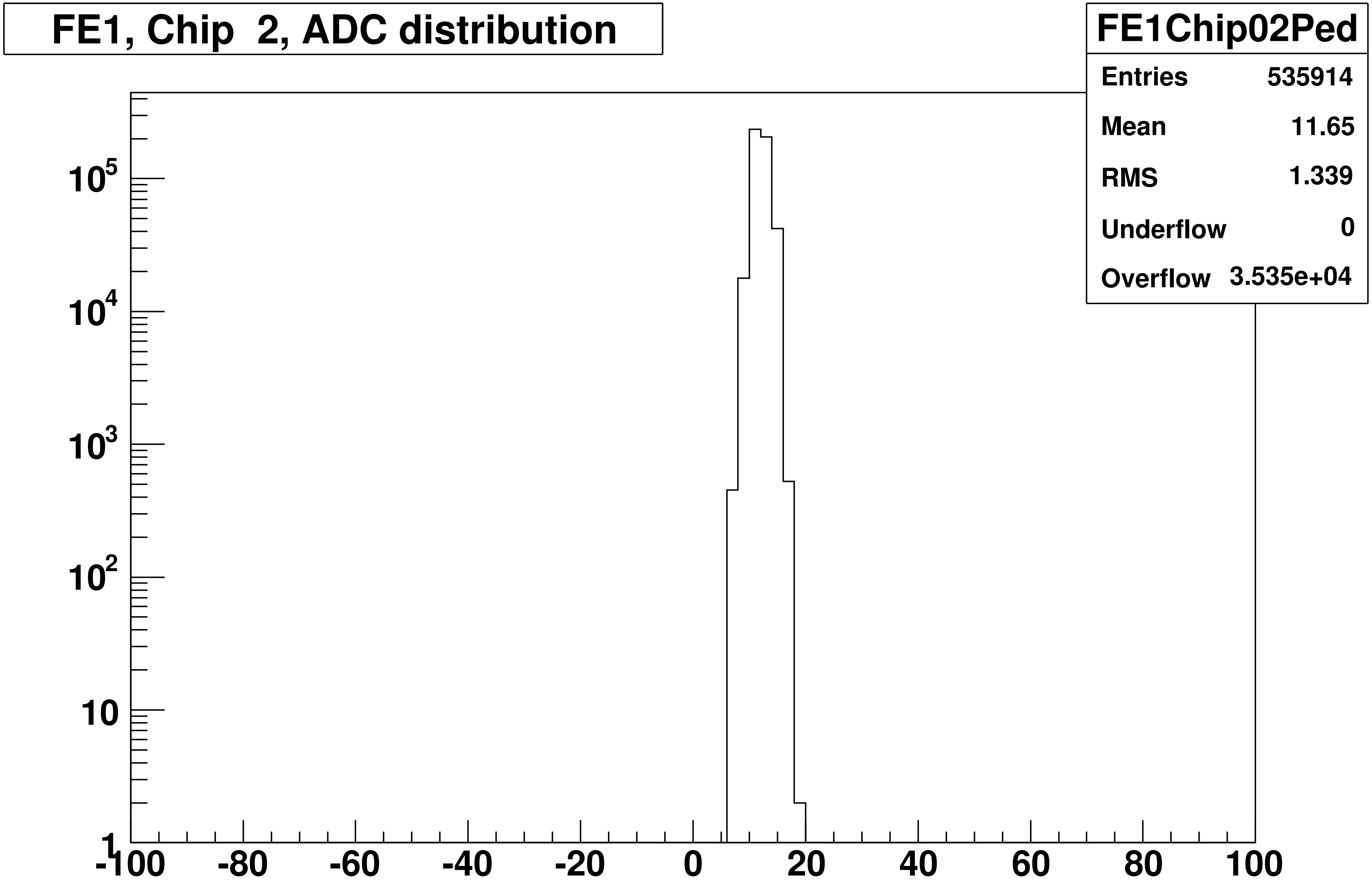
FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 535914
Mean 1253
RMS 4715
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

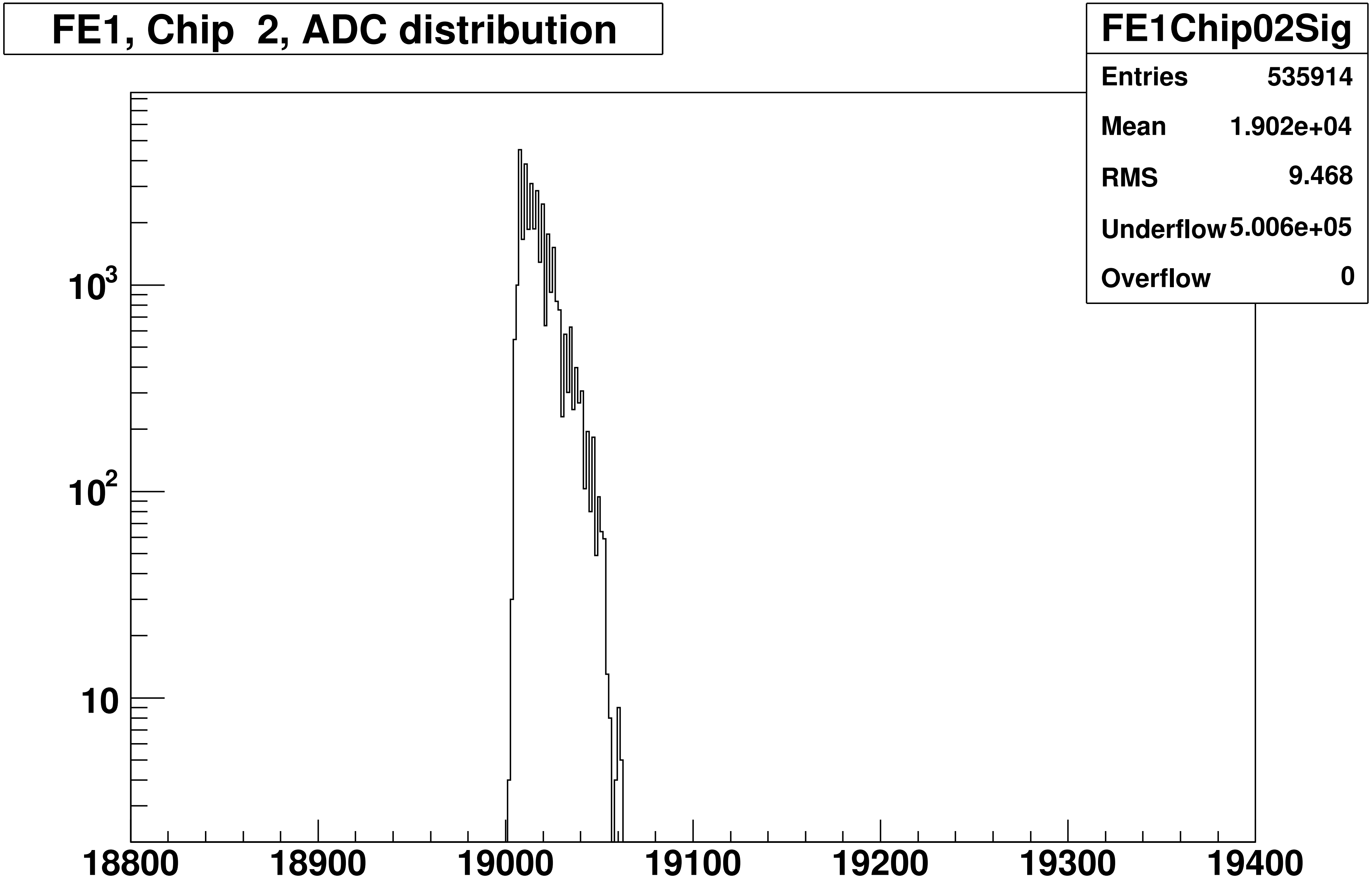

FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 535914
Mean -0.8907
RMS 0.7467
Underflow 0
Overflow 3.537e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



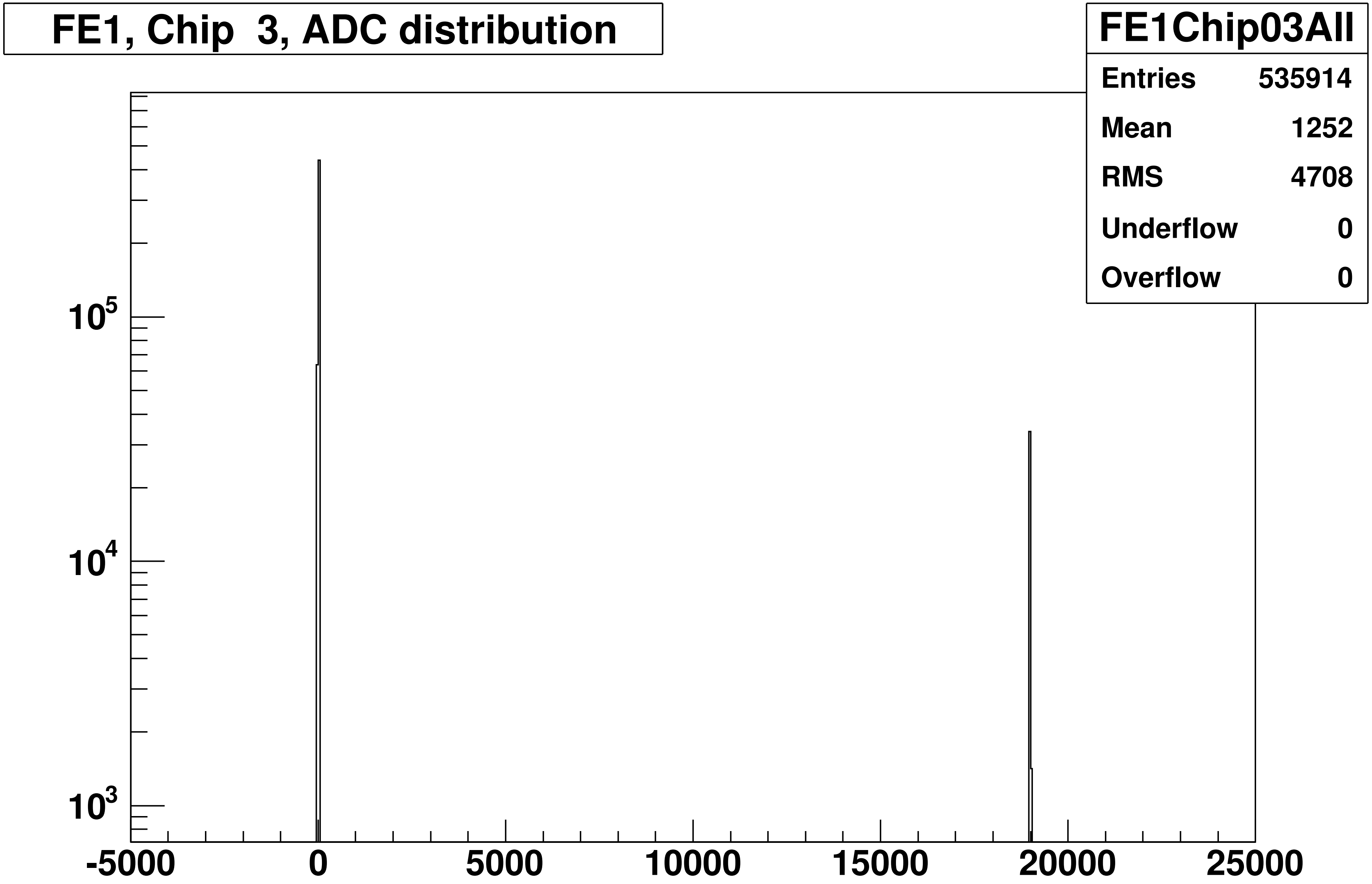
FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 535914
Mean 1.899e+04
RMS 9.593
Underflow 5.005e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 535914
Mean 1265
RMS 4717
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

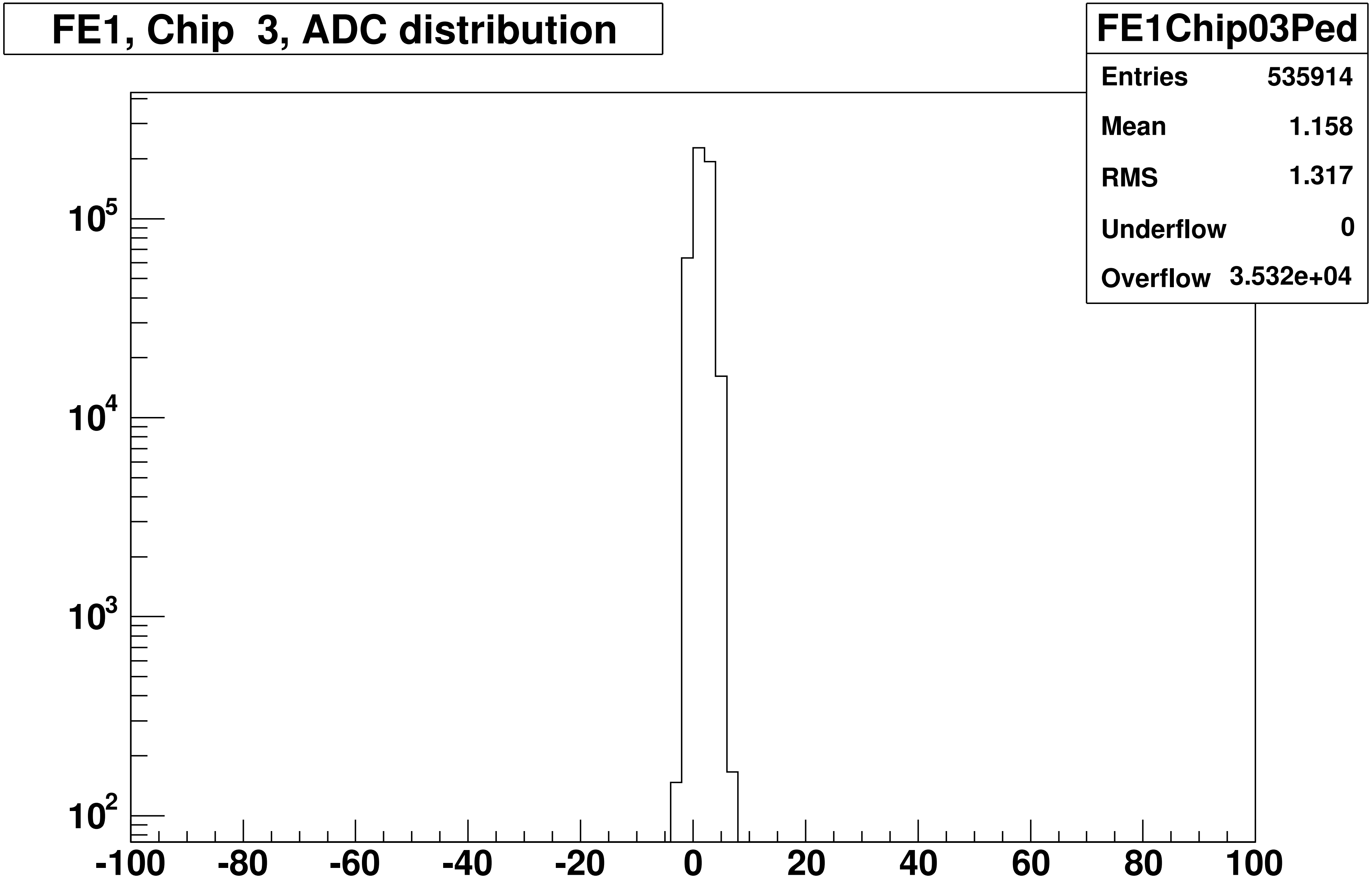

FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 535914
Mean 11.65
RMS 1.339
Underflow 0
Overflow 3.535e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

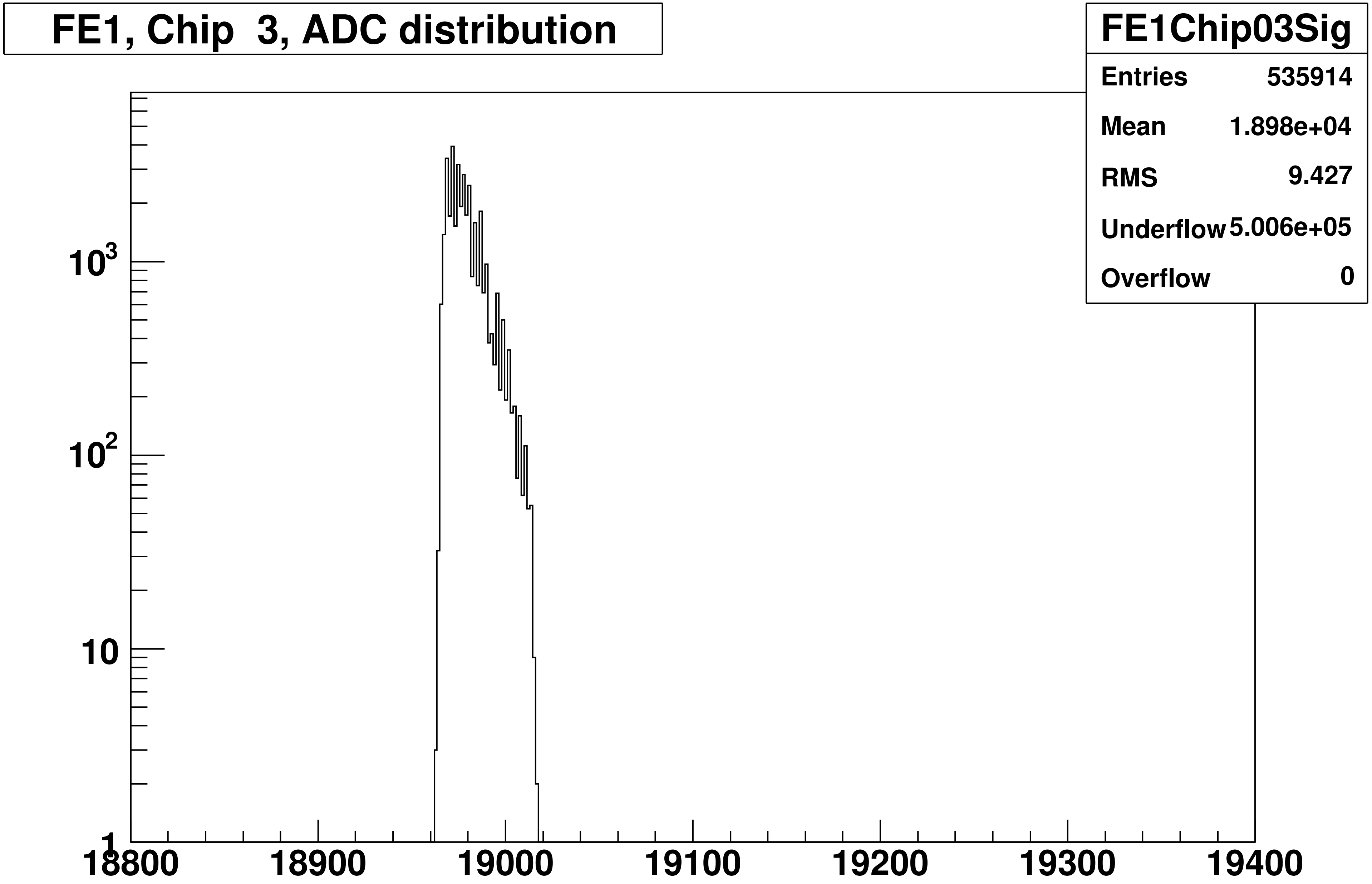


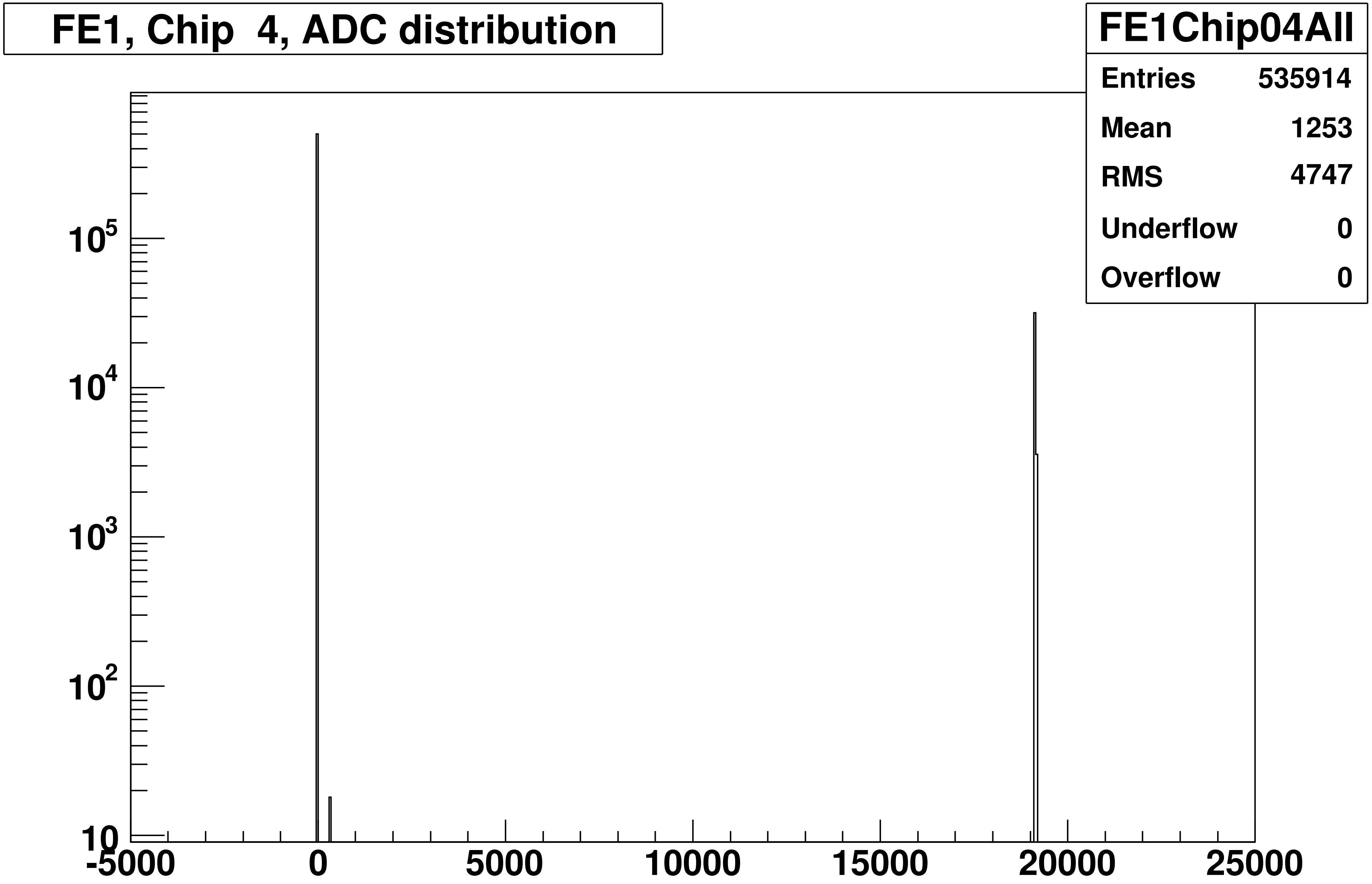
FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 535914
Mean 1.902e+04
RMS 9.468
Underflow 5.006e+05
Overflow 0
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400

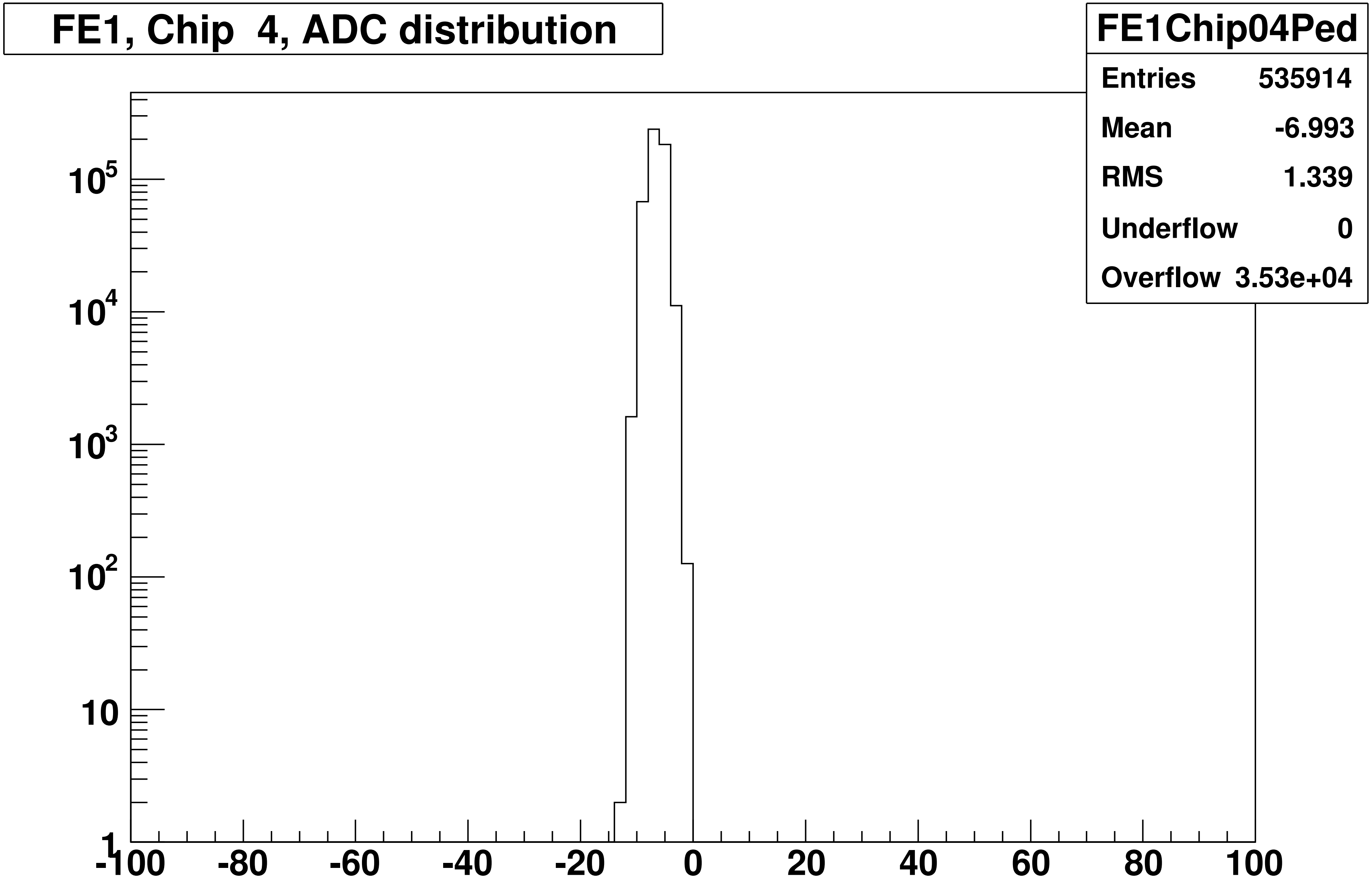


FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 535914
Mean 1252
RMS 4708
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

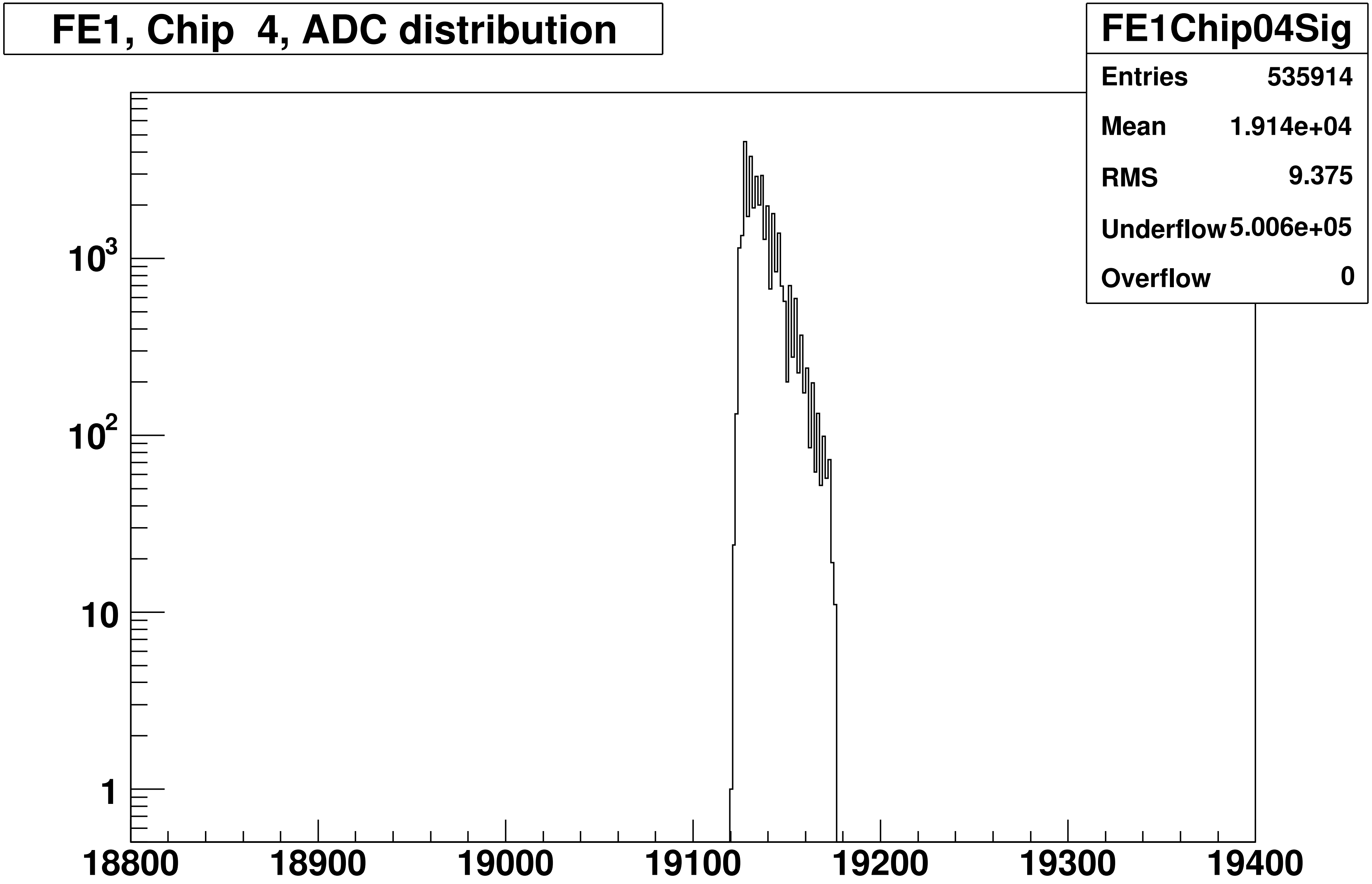

FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 535914
Mean 1.158
RMS 1.317
Underflow 0
Overflow 3.532e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

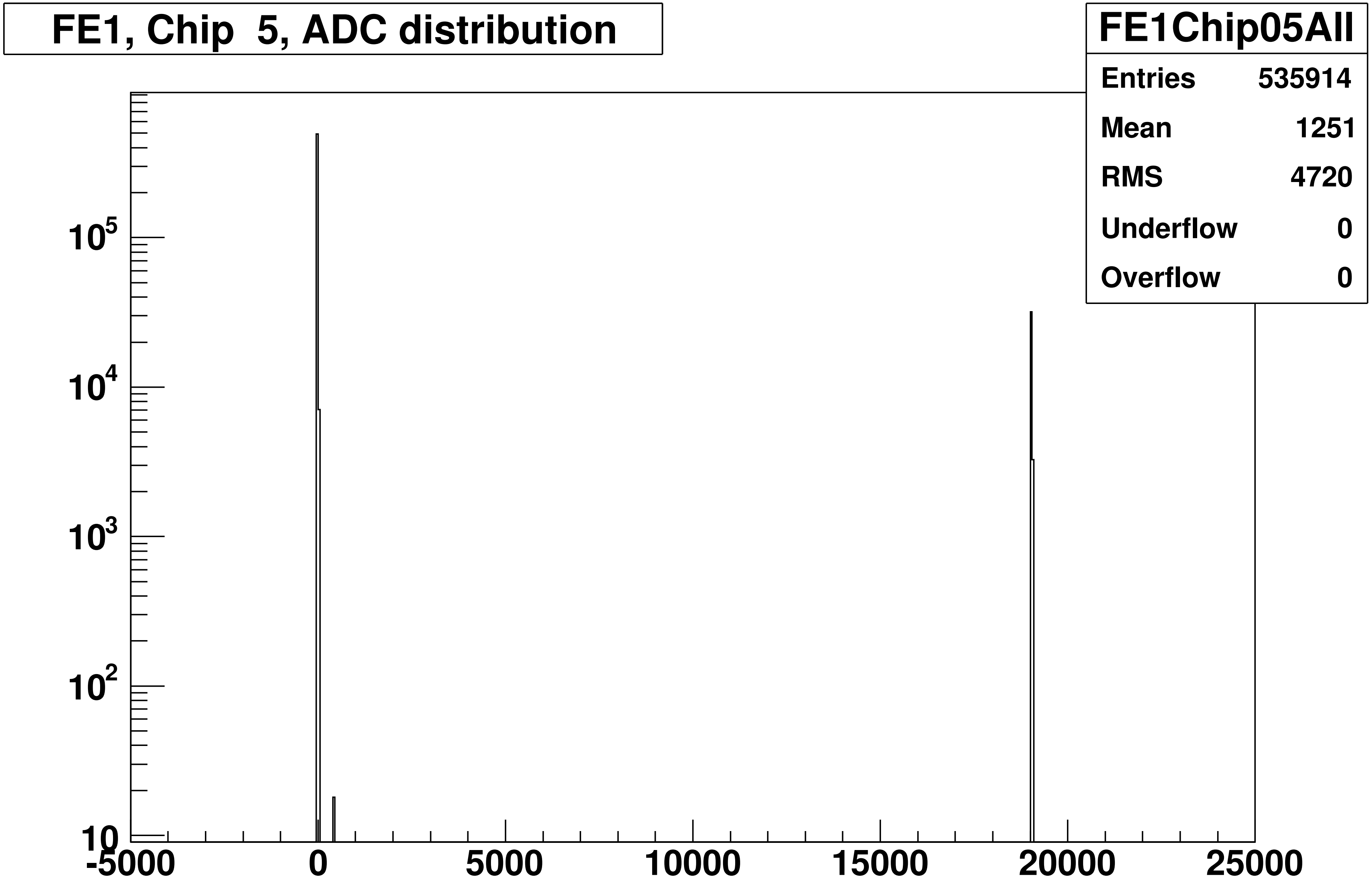

FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 535914
Mean 1.898e+04
RMS 9.427
Underflow 5.006e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 535914
Mean 1253
RMS 4747
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

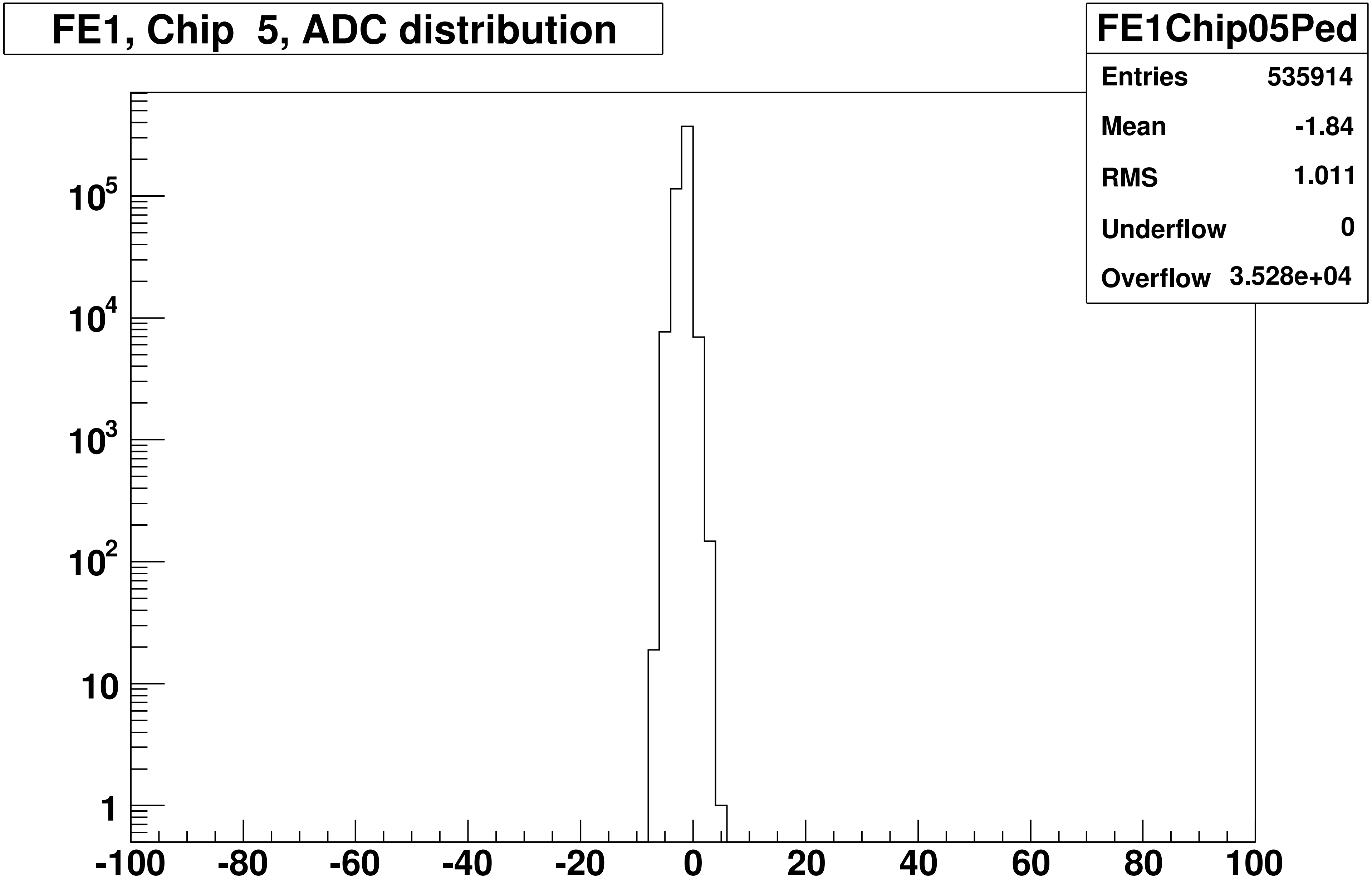


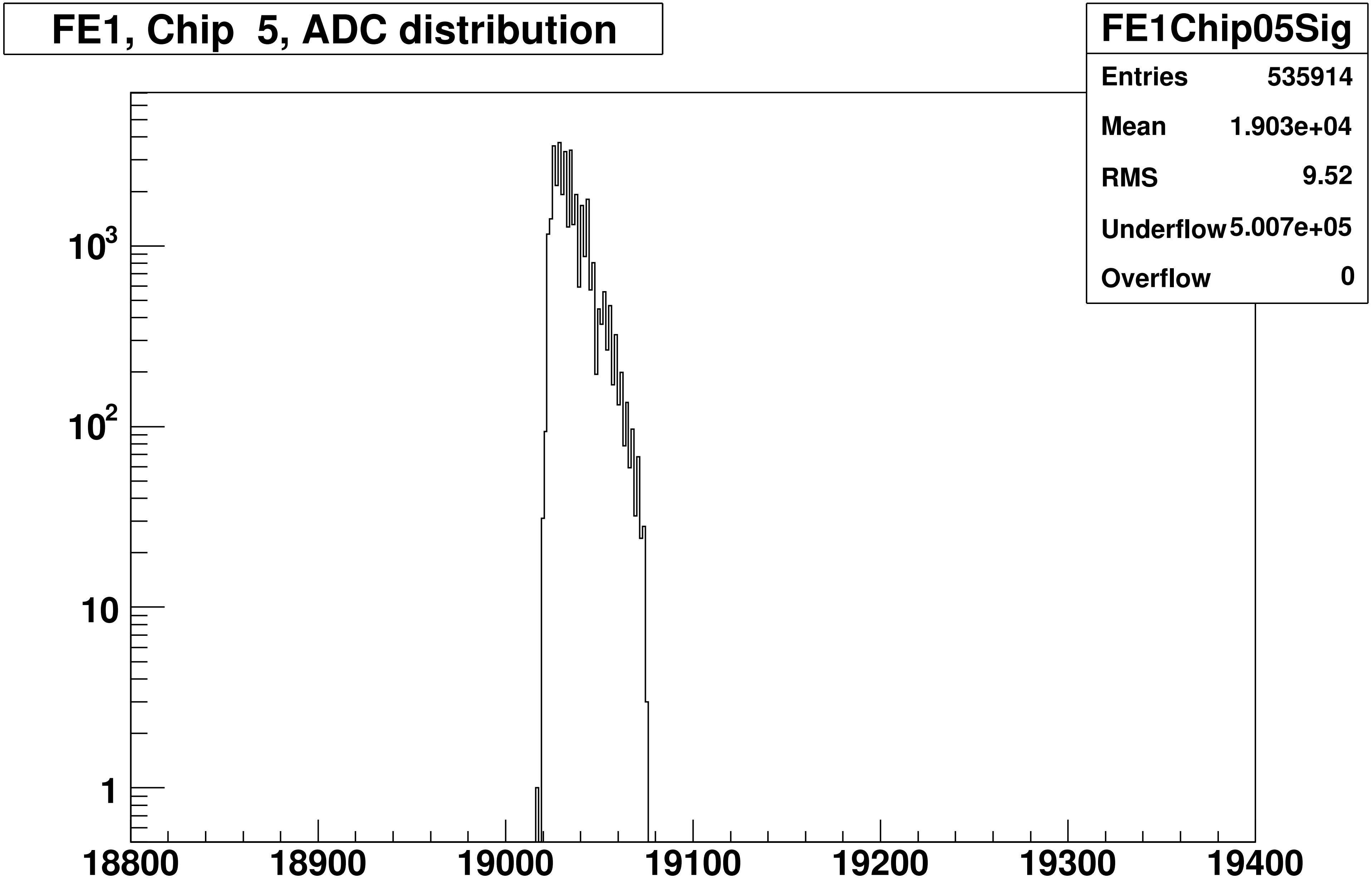
FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 535914
Mean -6.993
RMS 1.339
Underflow 0
Overflow 3.53e+04
1
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

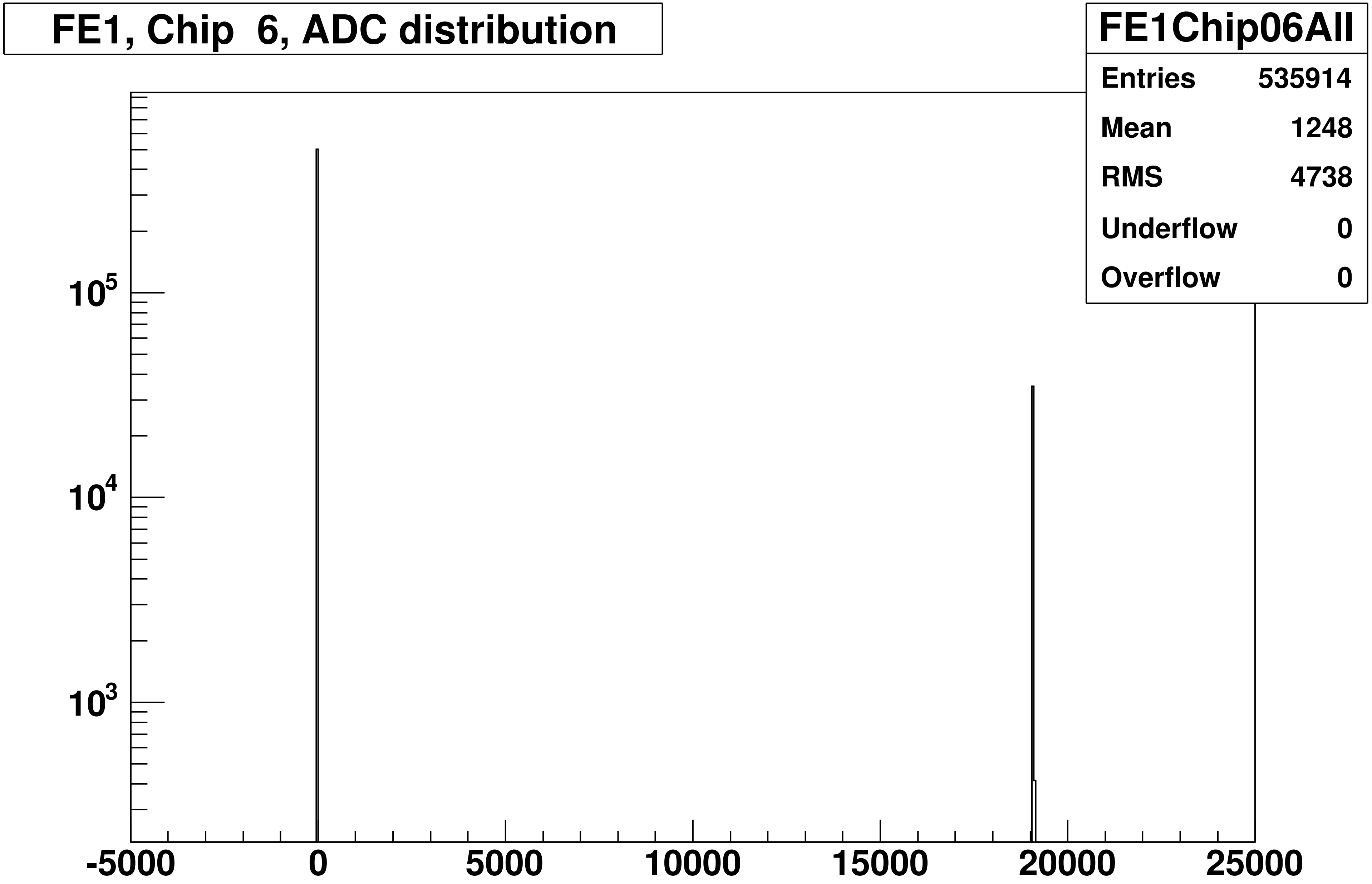

FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 535914
Mean 1.914e+04
RMS 9.375
Underflow 5.006e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

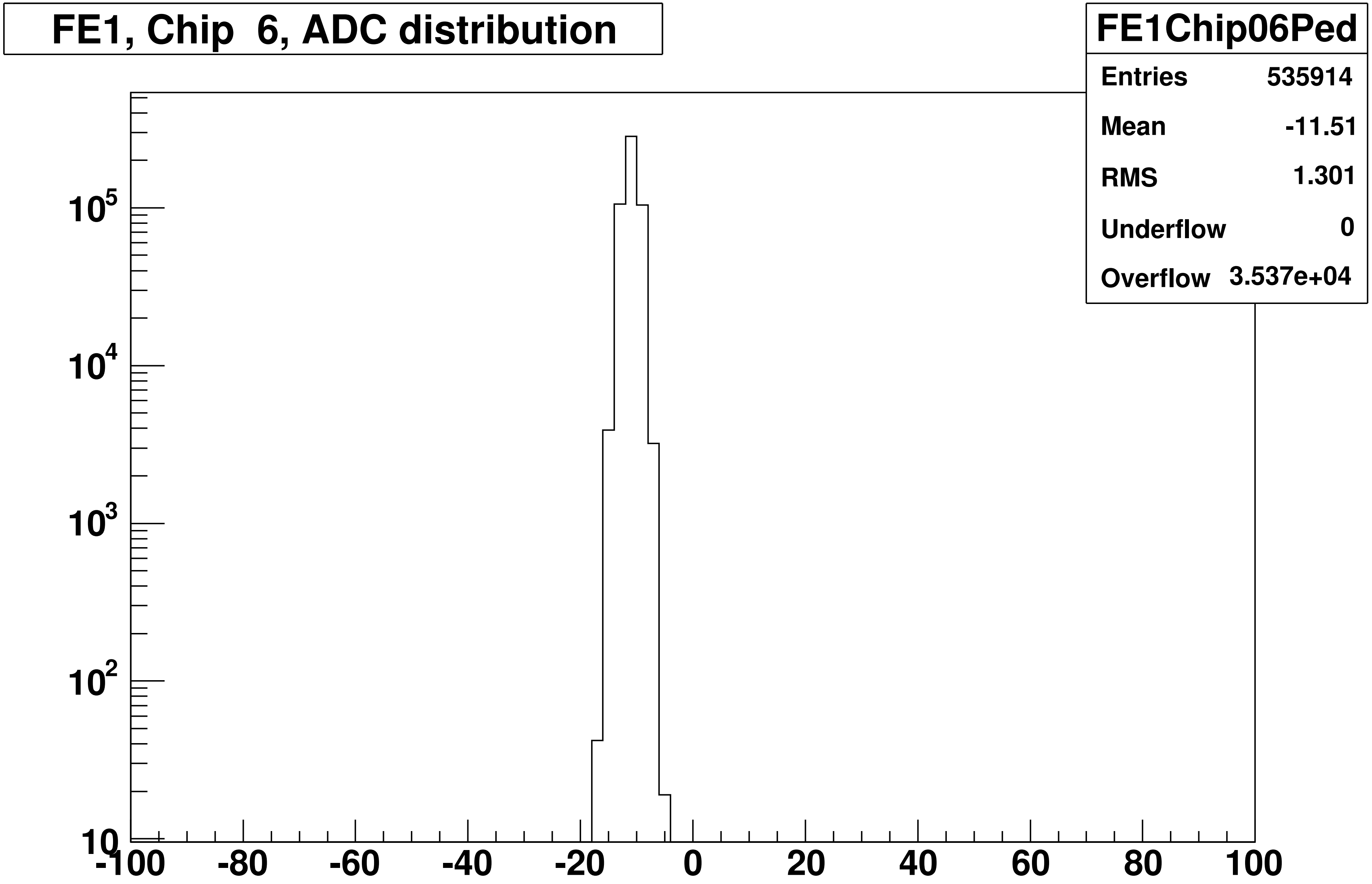


FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 535914
Mean 1251
RMS 4720
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

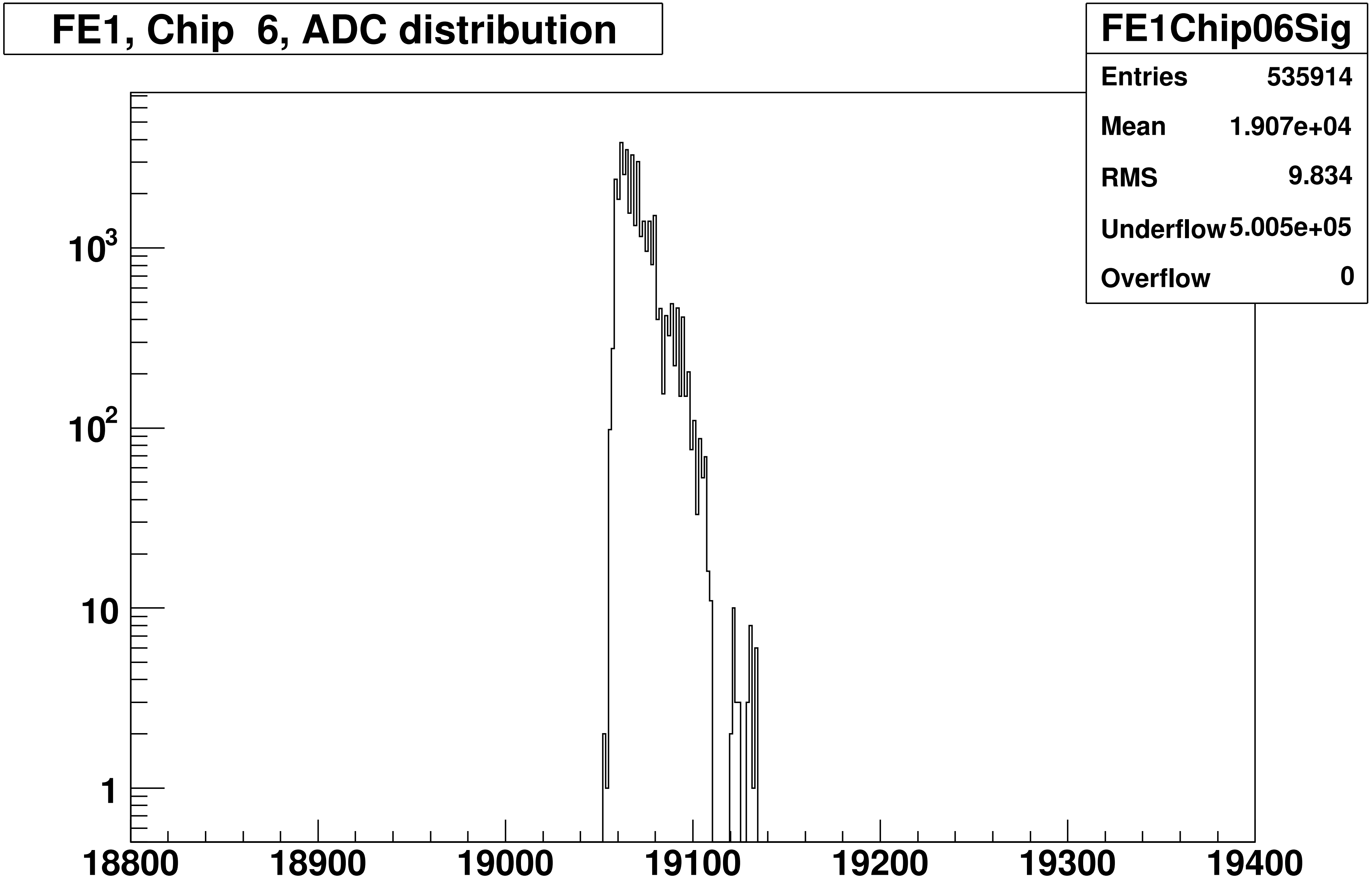

FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 535914
Mean -1.84
RMS 1.011
Underflow 0
Overflow 3.528e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 535914
Mean 1.903e+04
RMS 9.52
Underflow 5.007e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400

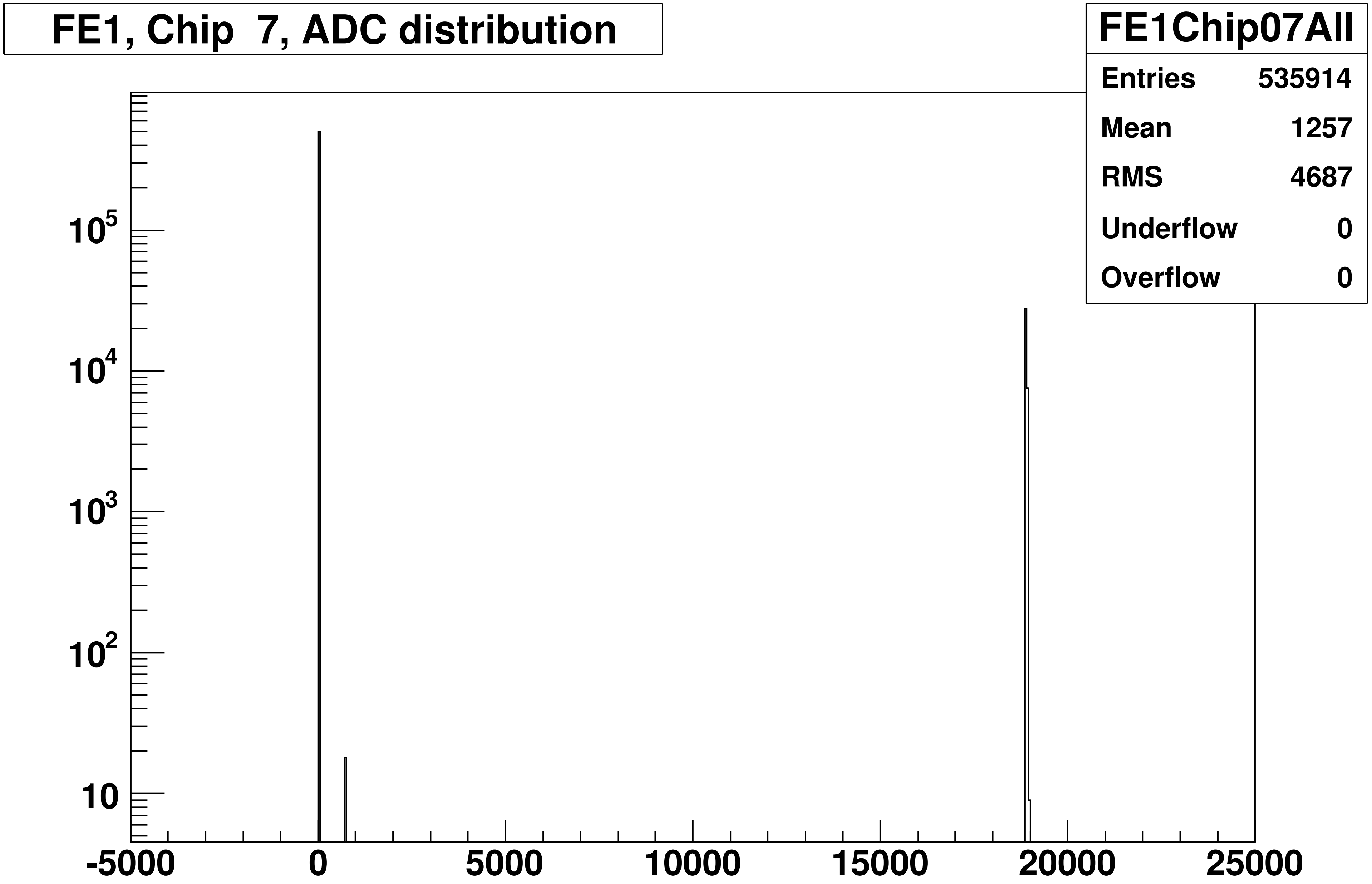

FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 535914
Mean 1248
RMS 4738
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



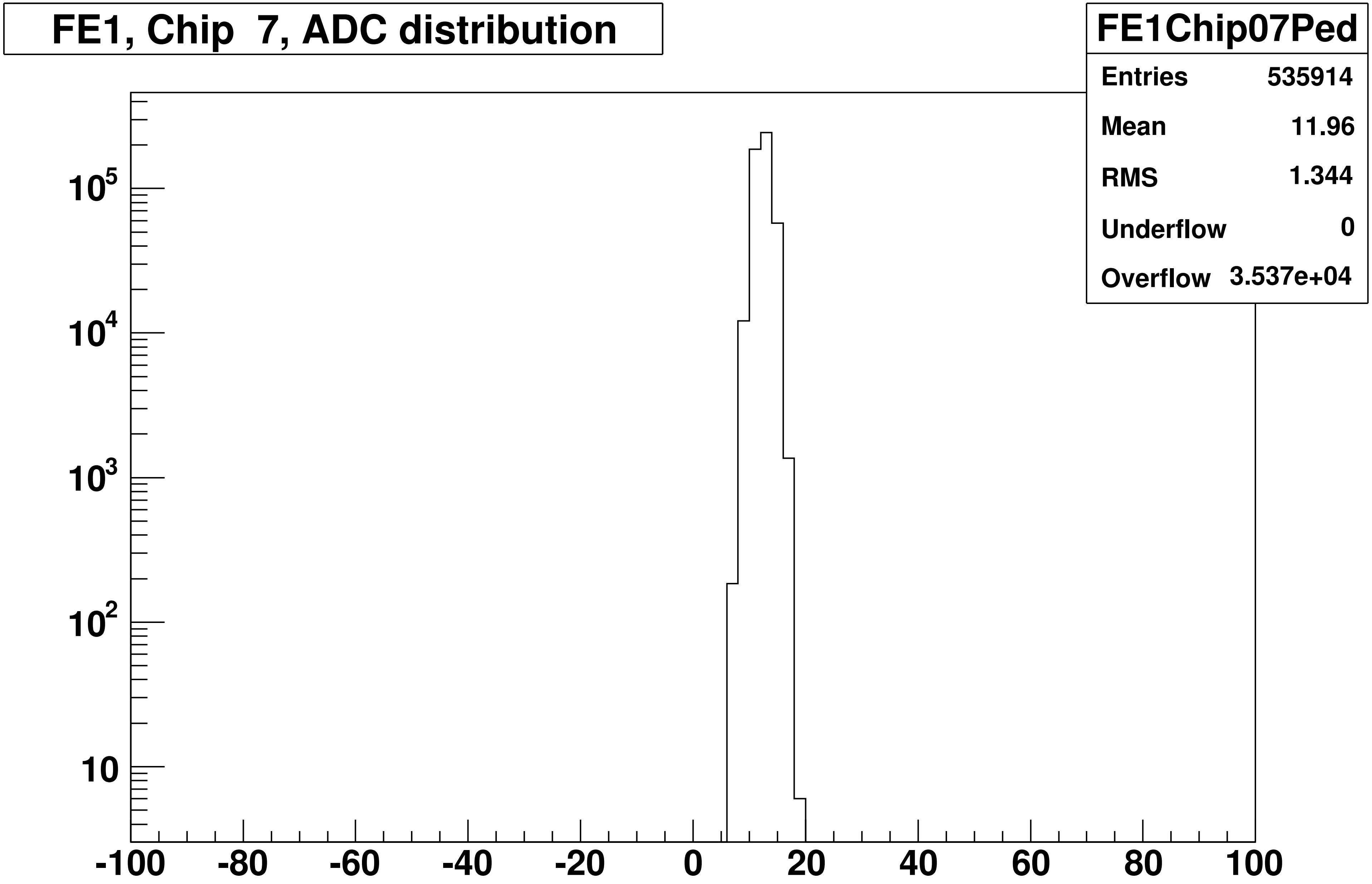
FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 535914
Mean -11.51
RMS 1.301
Underflow 0
Overflow 3.537e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100



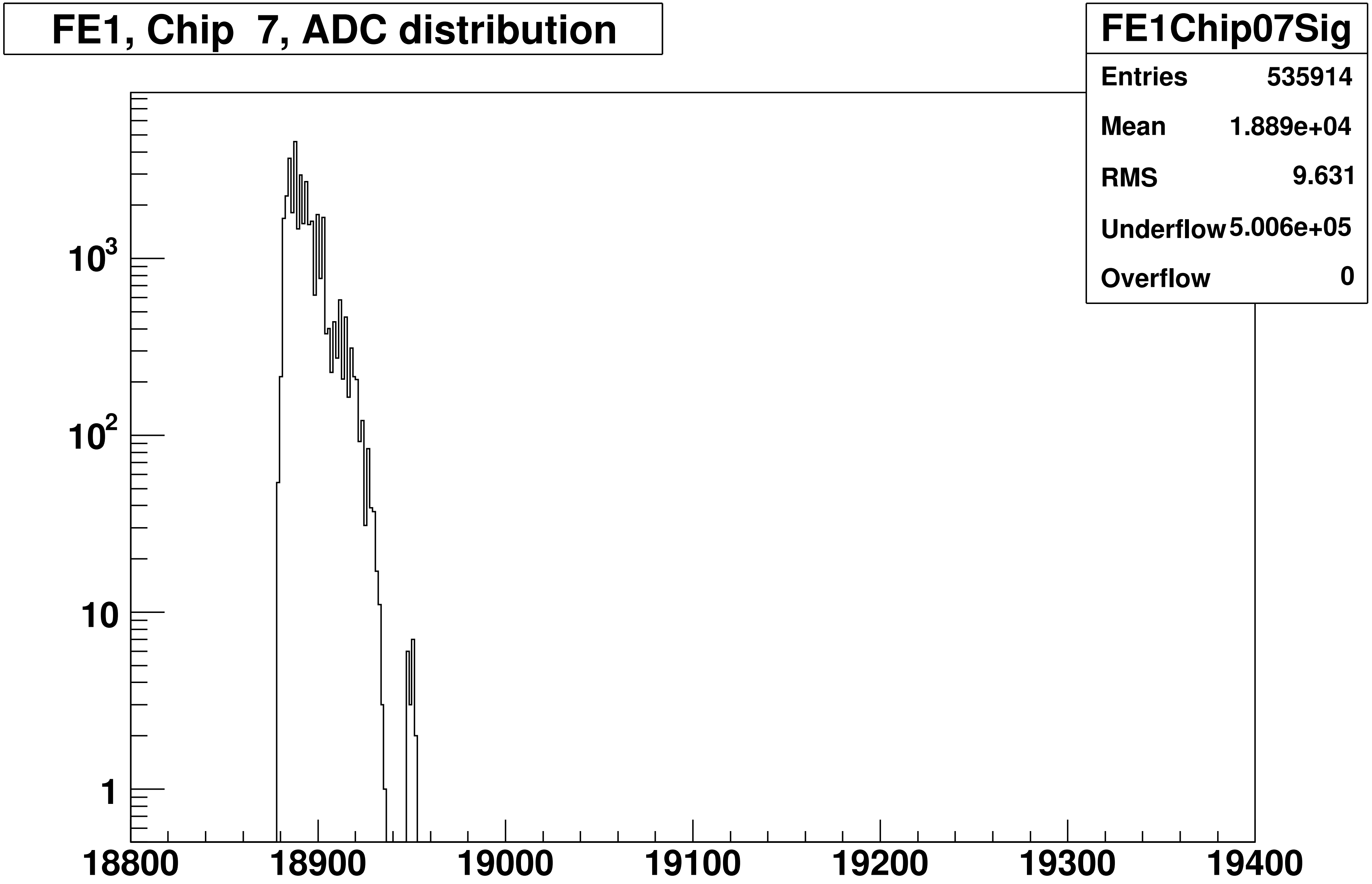
FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 535914
Mean 1.907e+04
RMS 9.834
Underflow 5.005e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

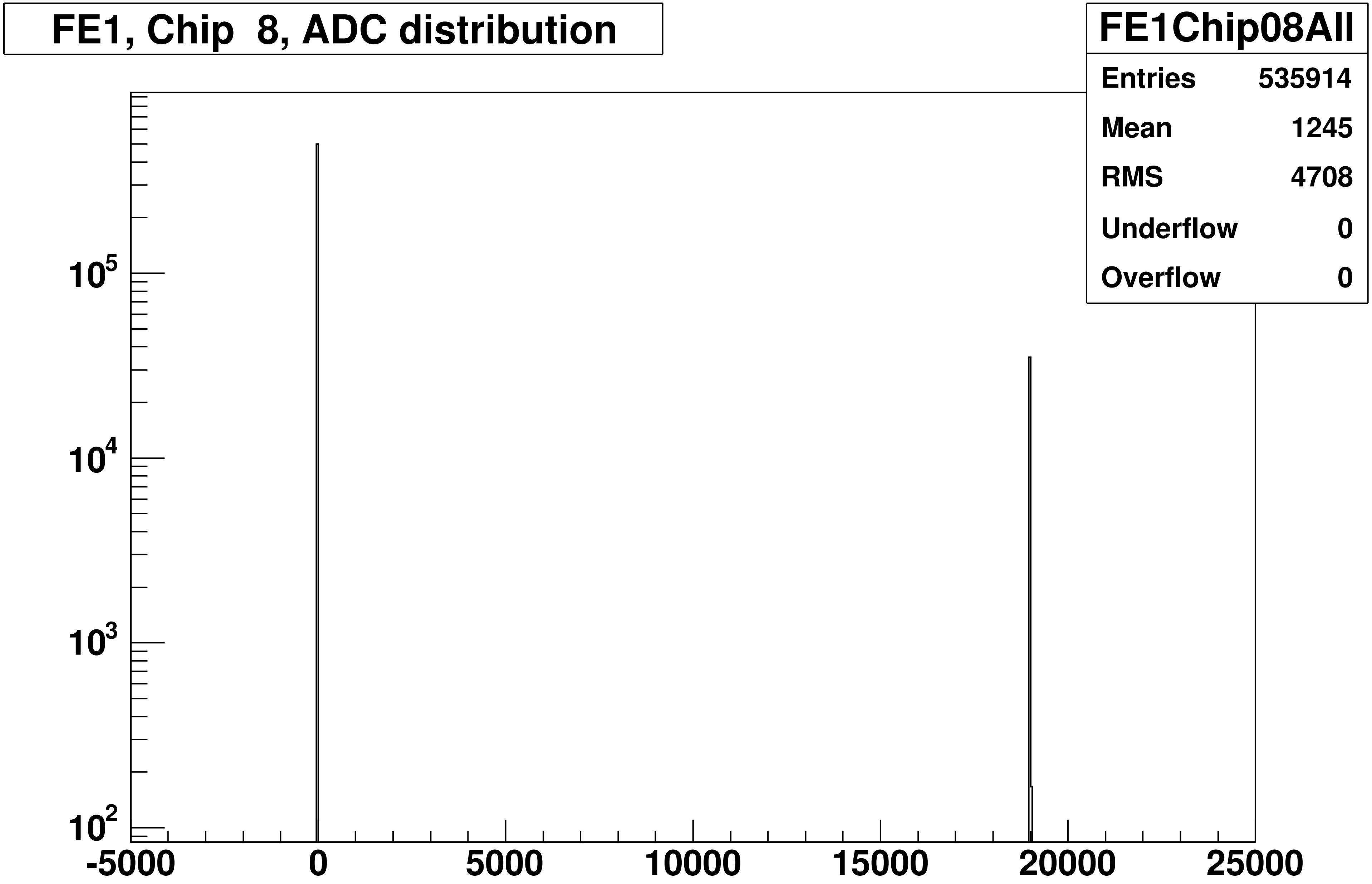


FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 535914
Mean 1257
RMS 4687
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

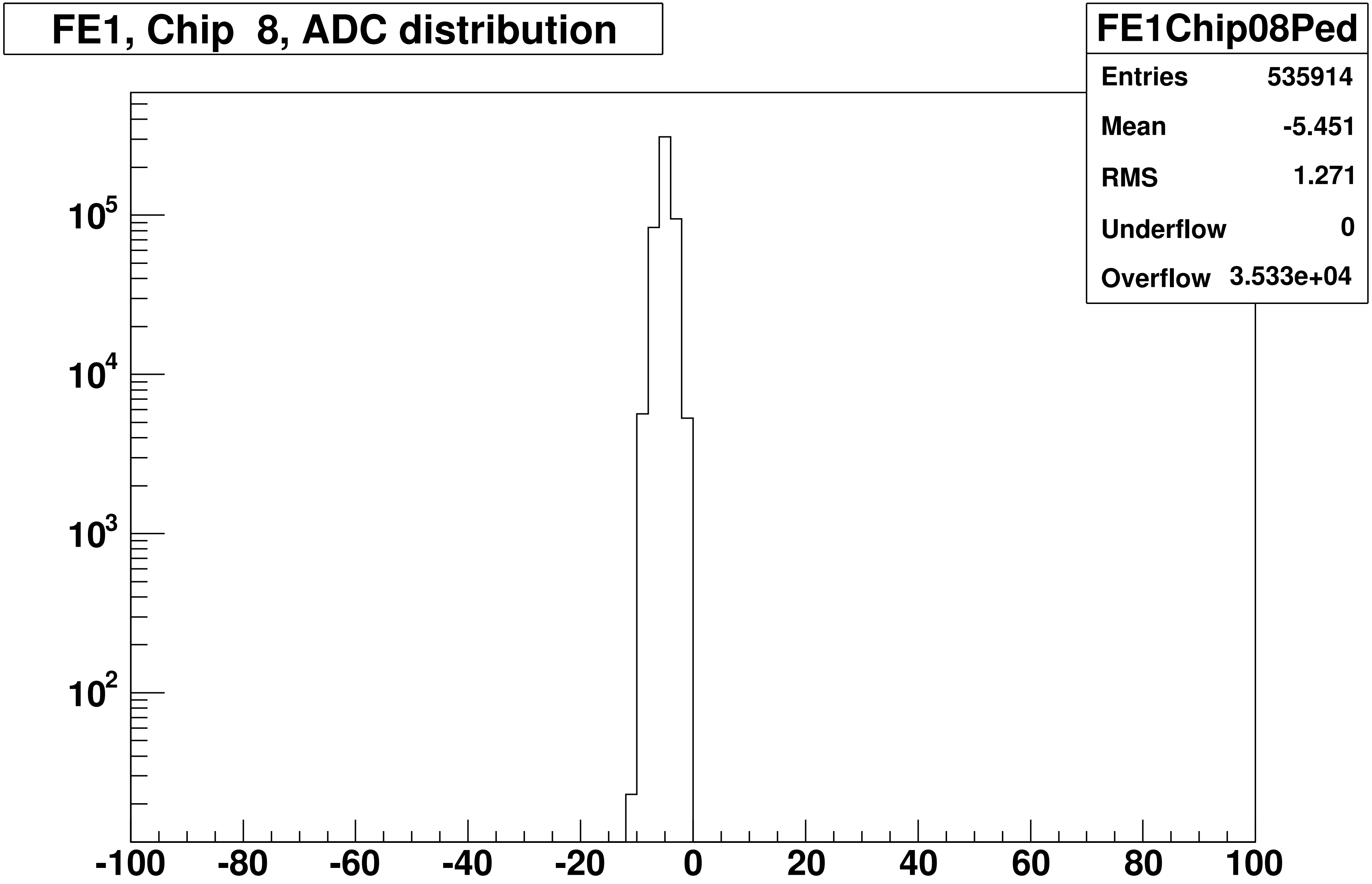


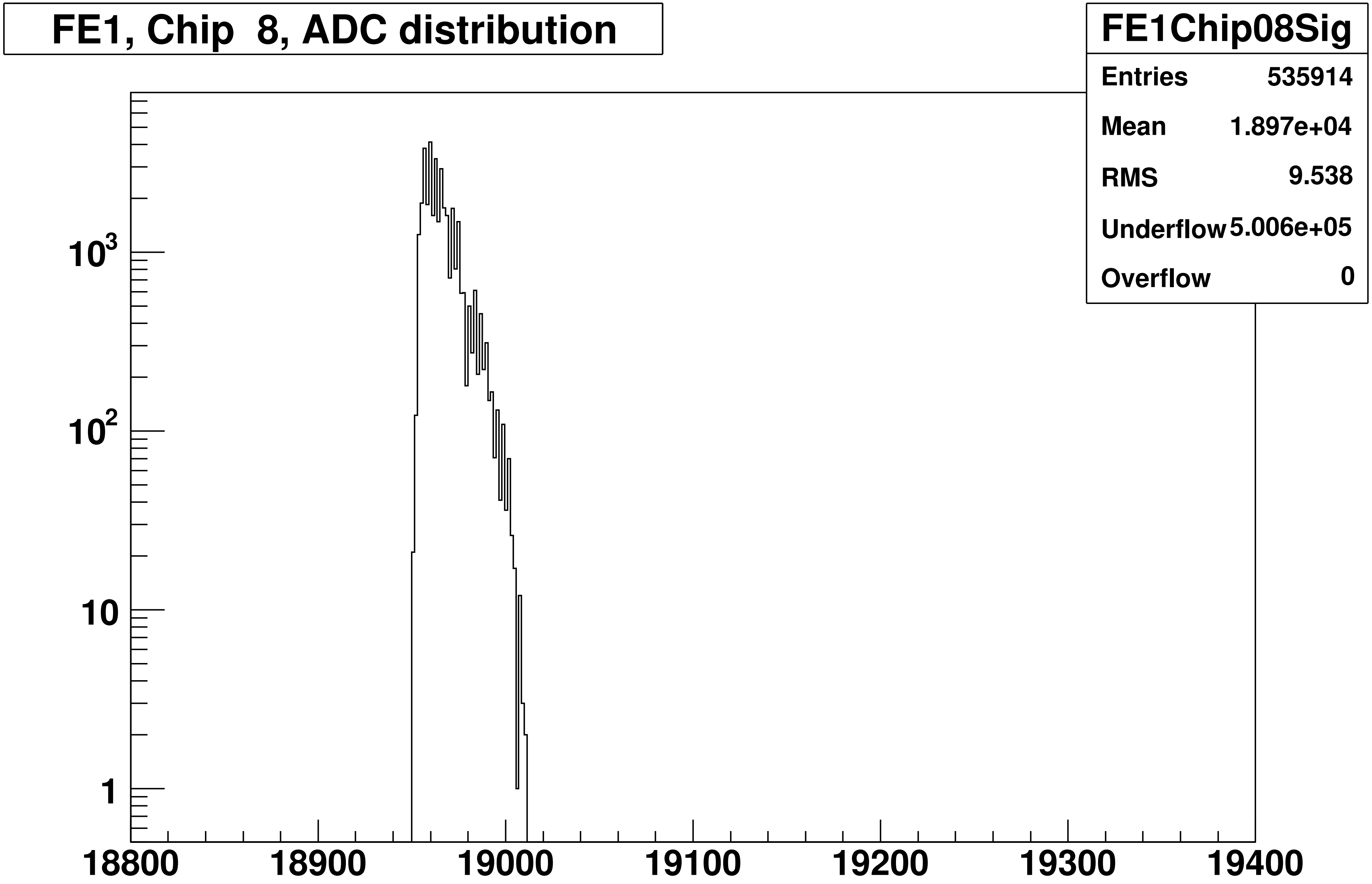
FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 535914
Mean 11.96
RMS 1.344
Underflow 0
Overflow 3.537e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 535914
Mean 1.889e+04
RMS 9.631
Underflow 5.006e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

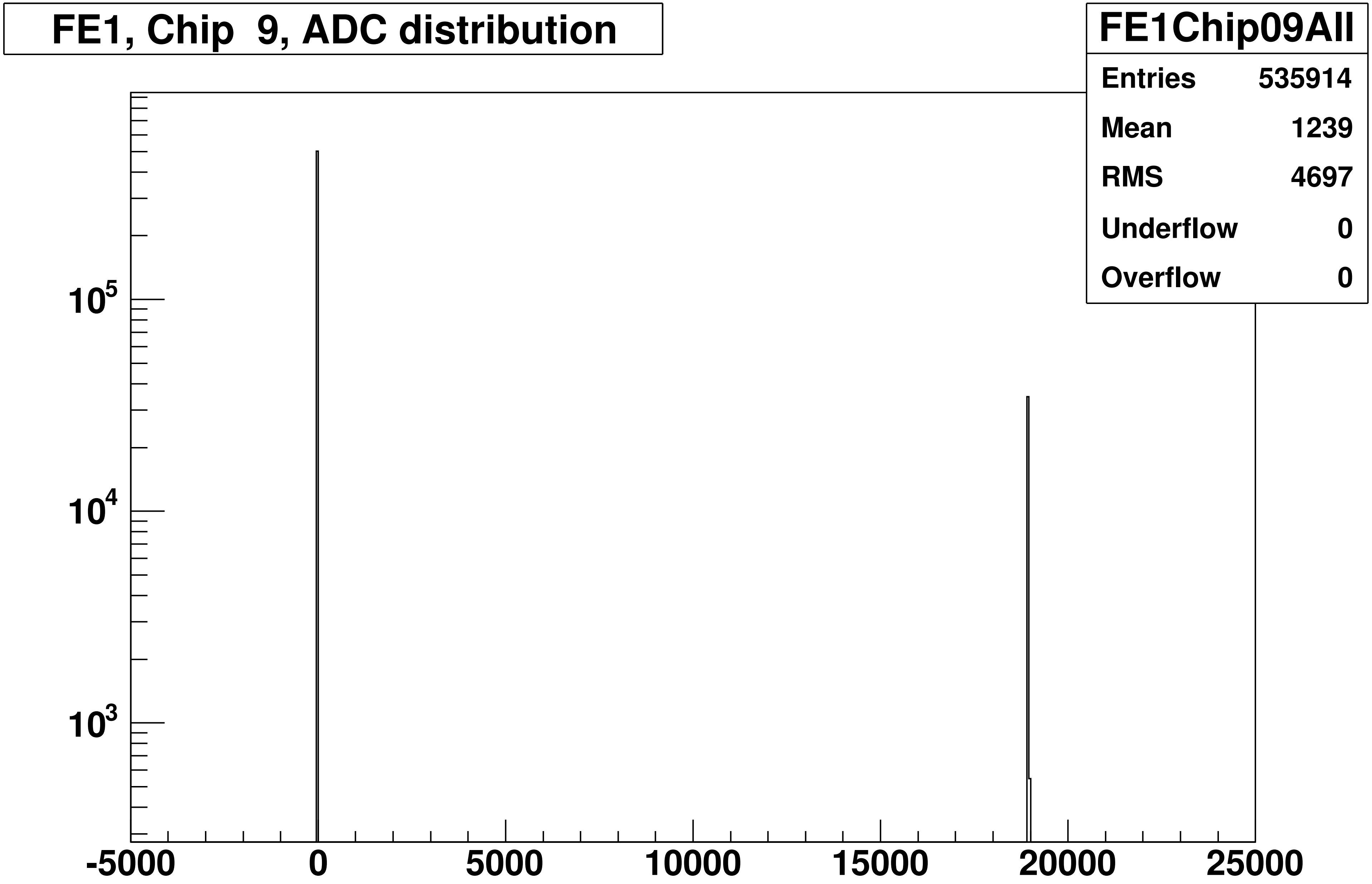


FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 535914
Mean 1245
RMS 4708
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

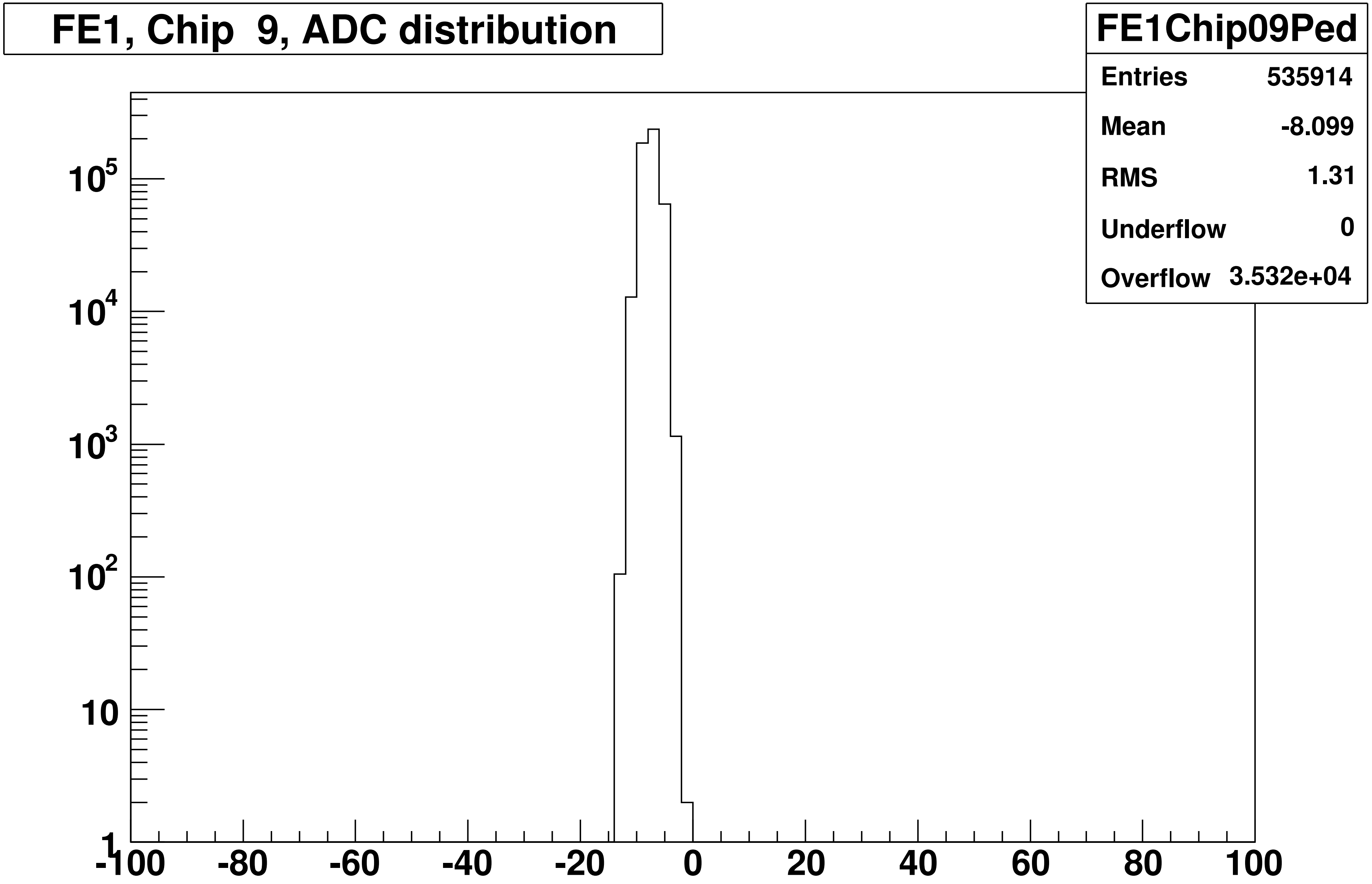

FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 535914
Mean -5.451
RMS 1.271
Underflow 0
Overflow 3.533e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

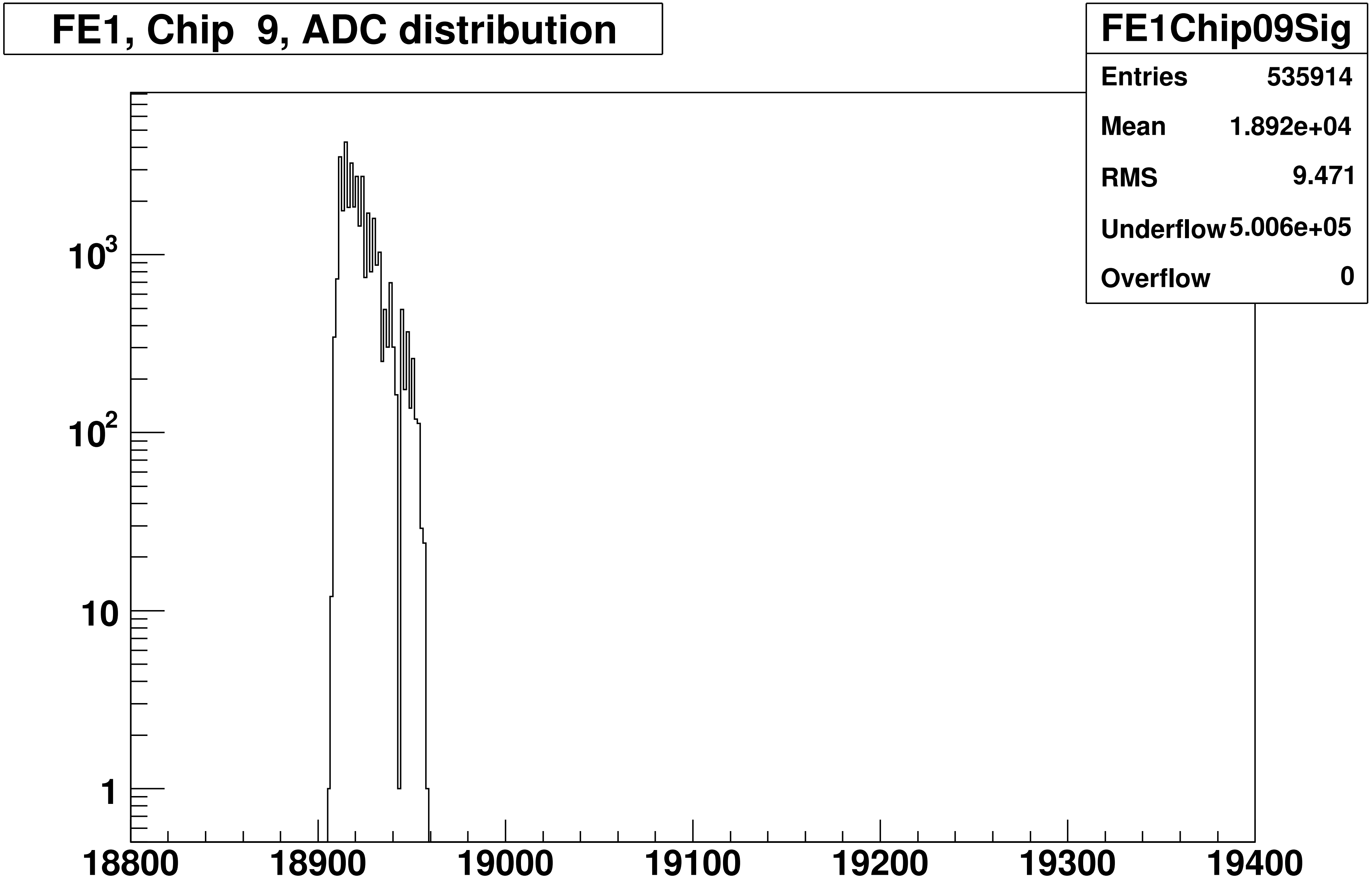


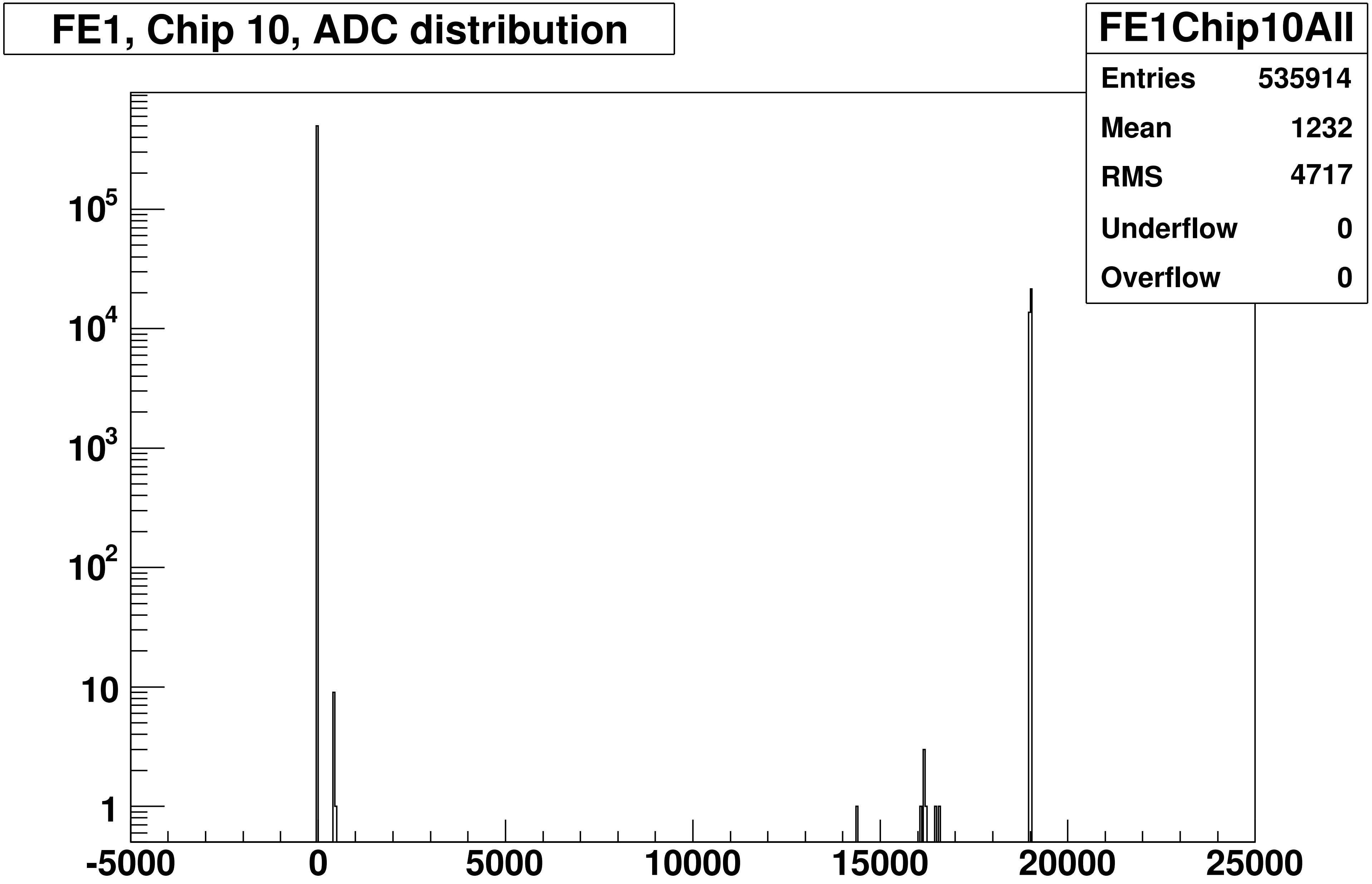
FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 535914
Mean 1.897e+04
RMS 9.538
Underflow 5.006e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

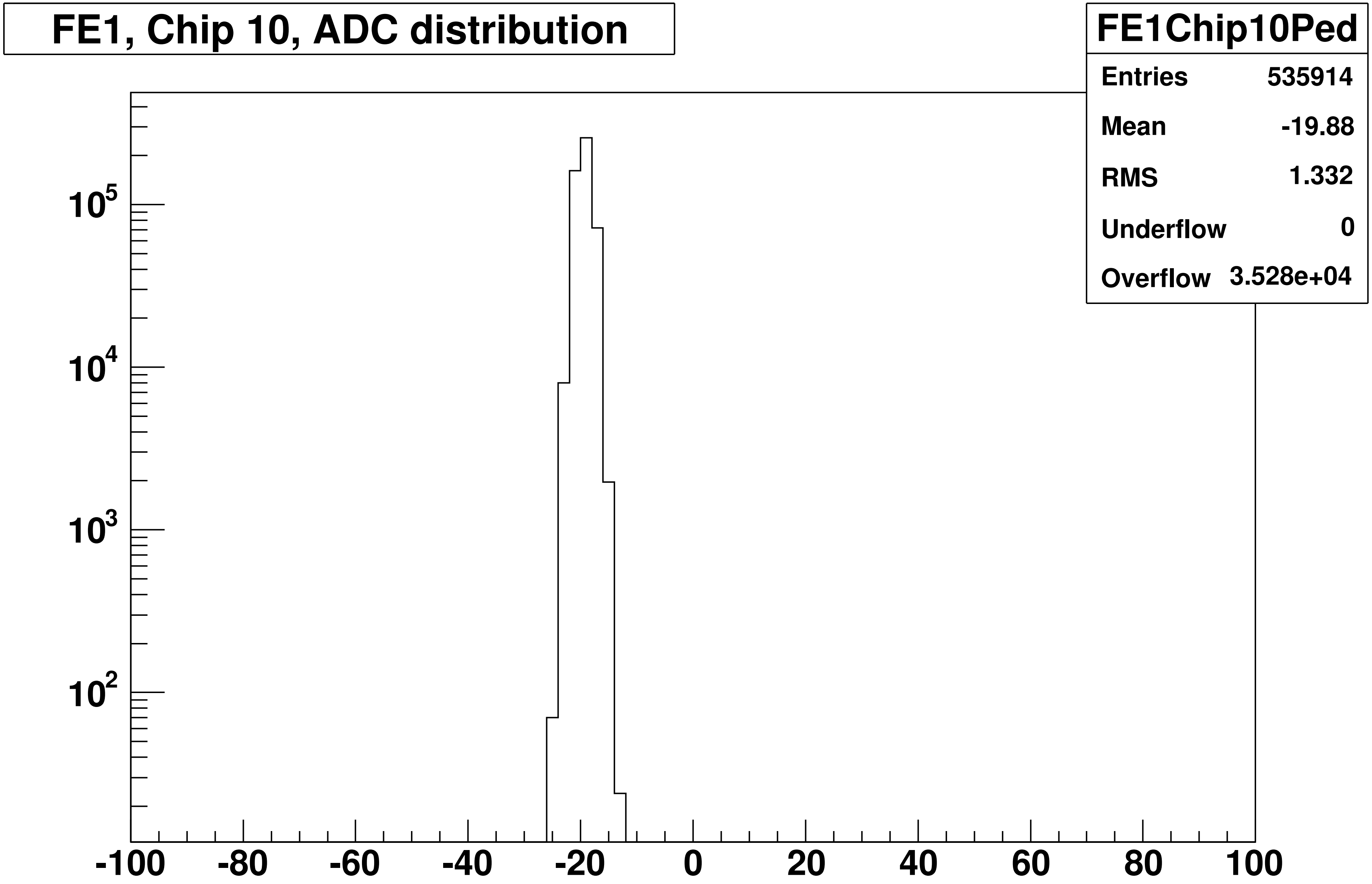


FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 535914
Mean 1239
RMS 4697
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

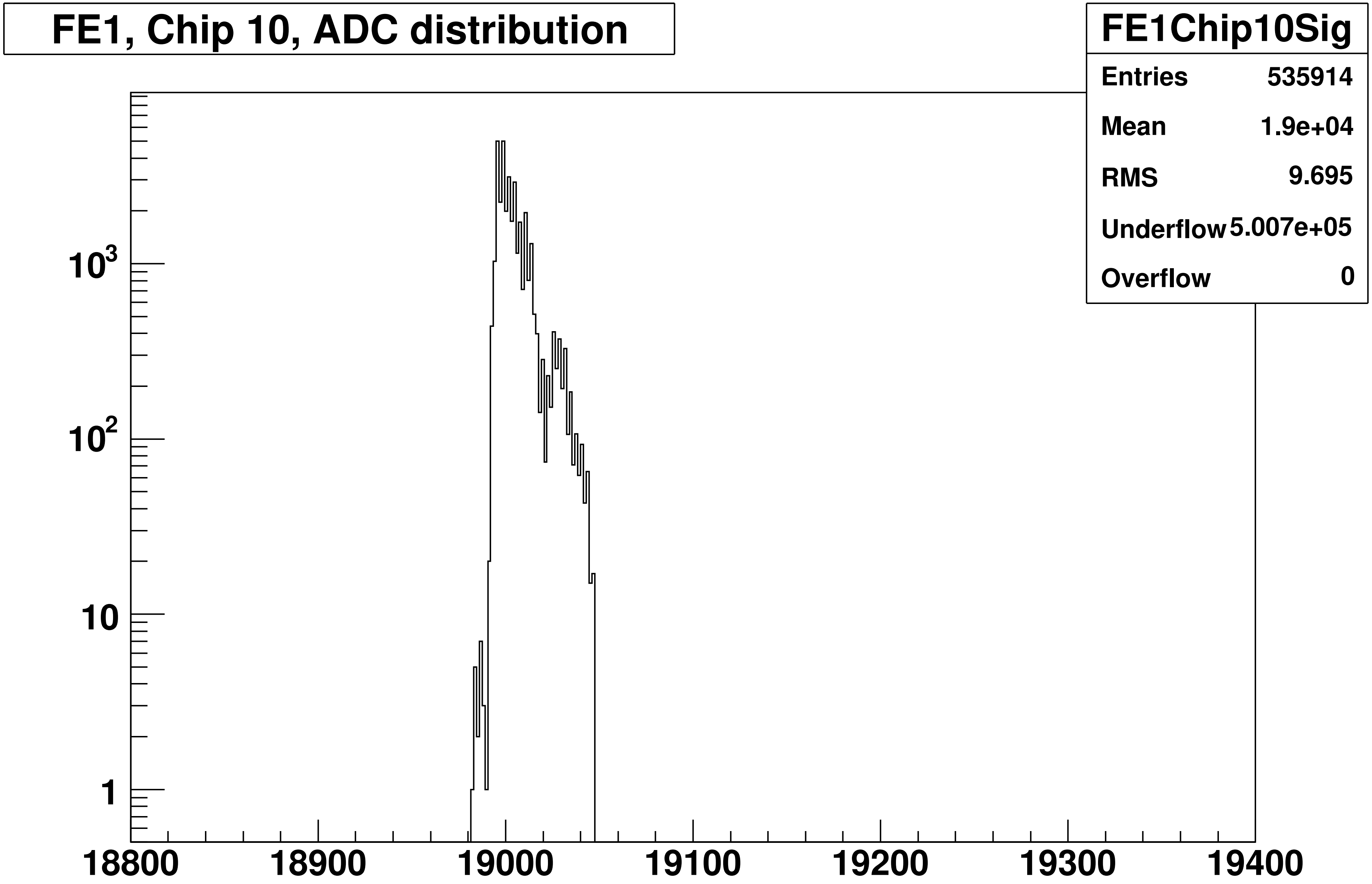

FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 535914
Mean -8.099
RMS 1.31
Underflow 0
Overflow 3.532e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

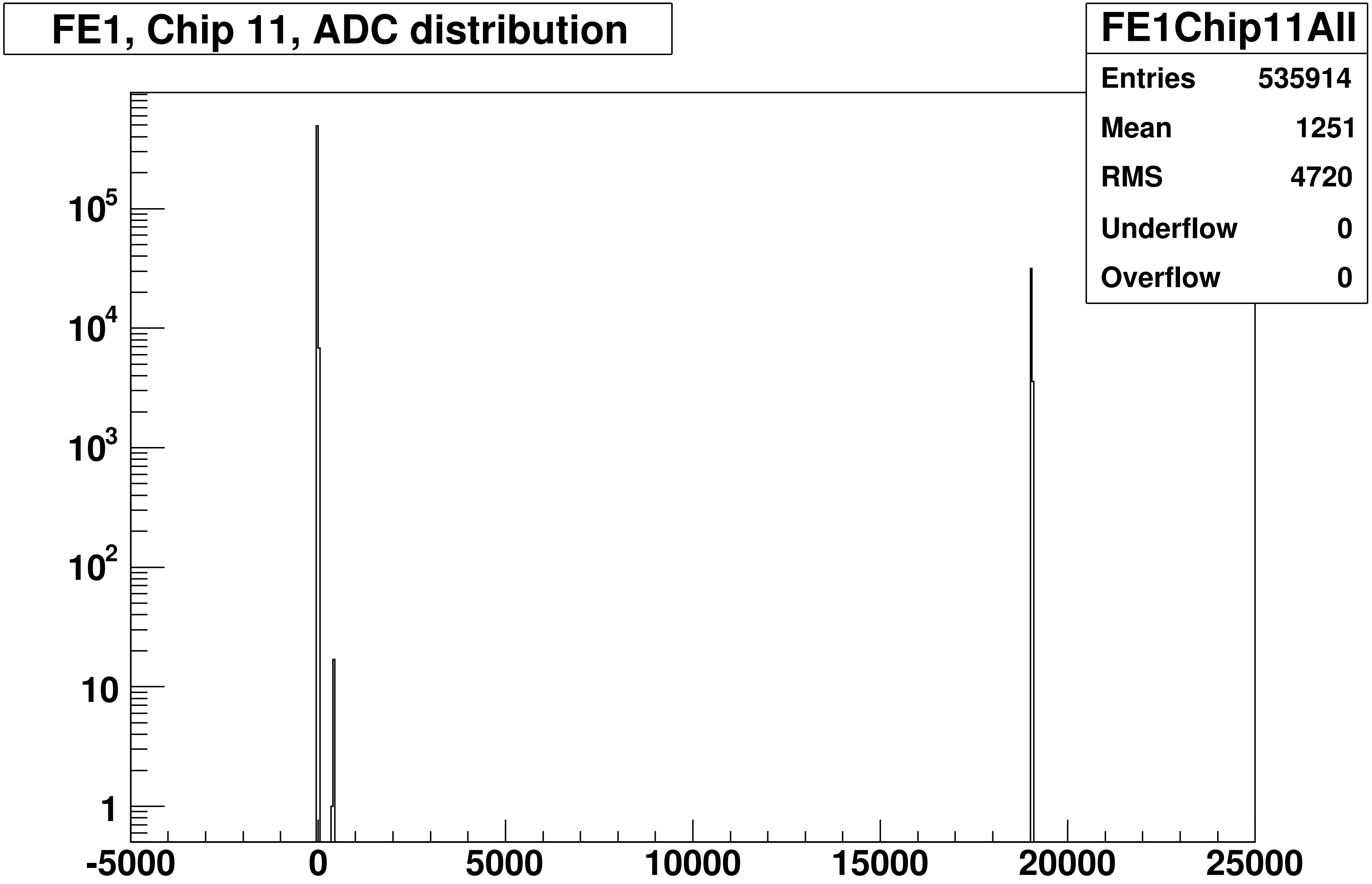

FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 535914
Mean 1.892e+04
RMS 9.471
Underflow 5.006e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

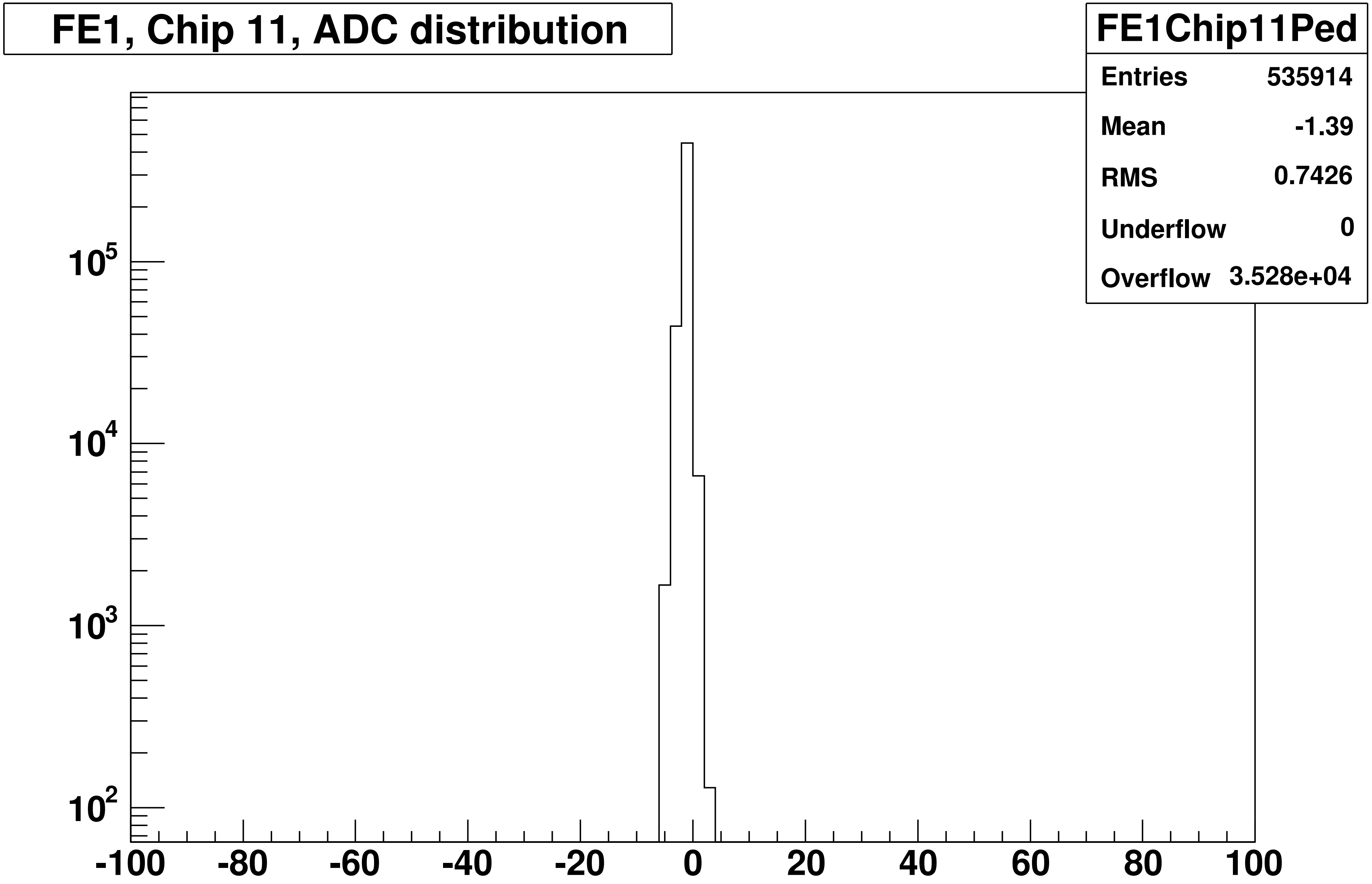

FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 535914
Mean 1232
RMS 4717
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

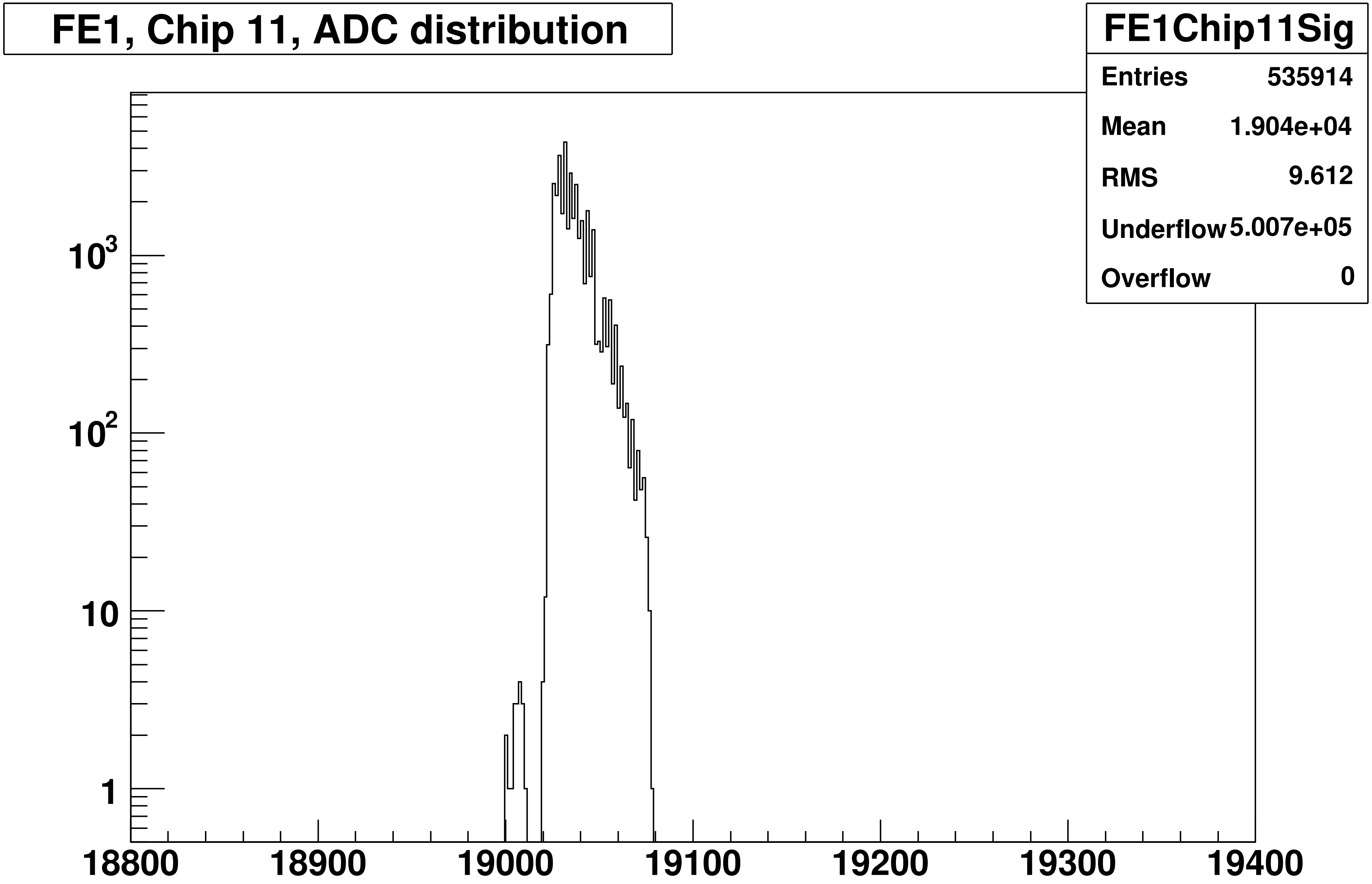


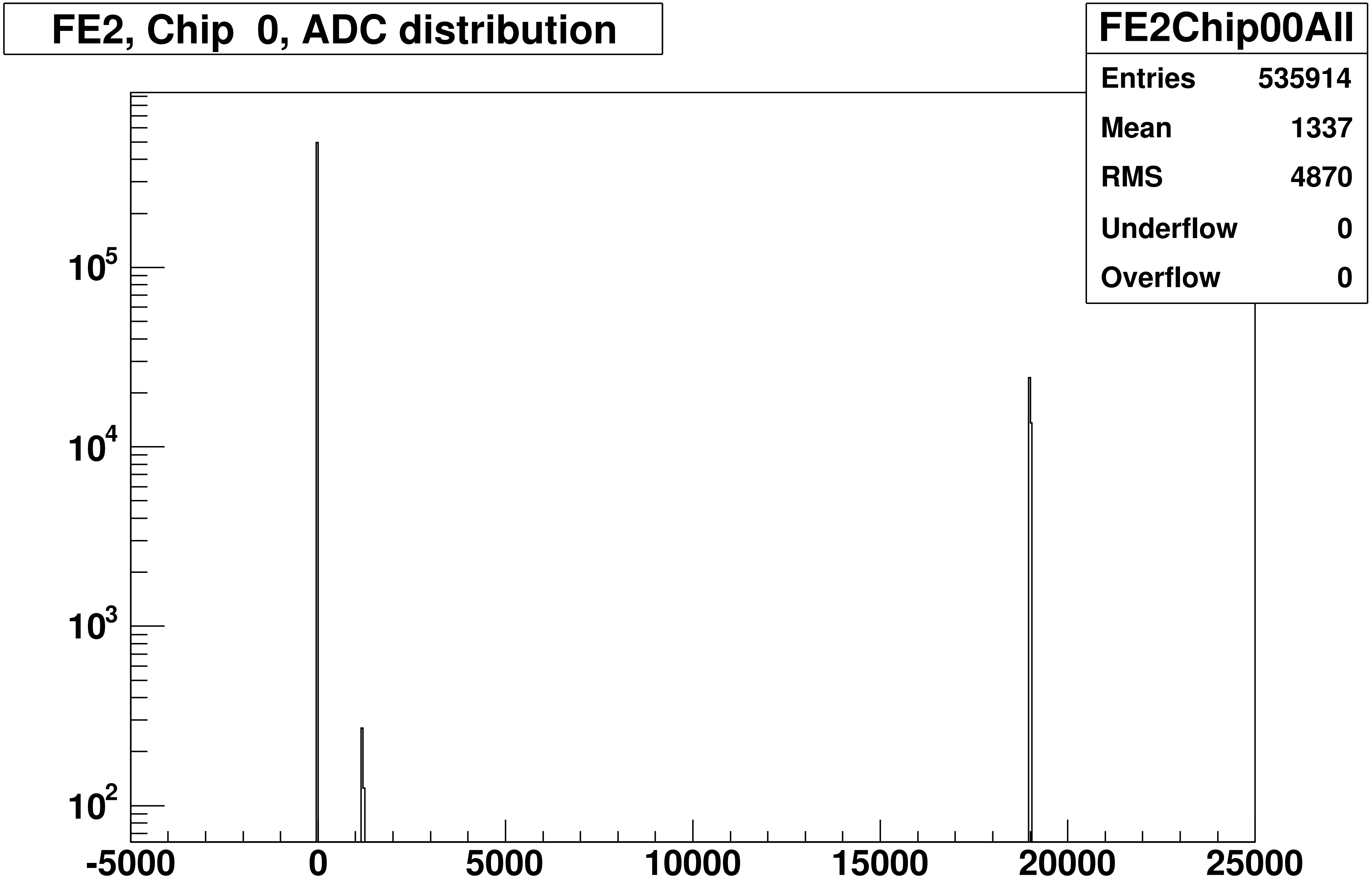
FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 535914
Mean -19.88
RMS 1.332
Underflow 0
Overflow 3.528e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 535914
Mean 1.9e+04
RMS 9.695
Underflow 5.007e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

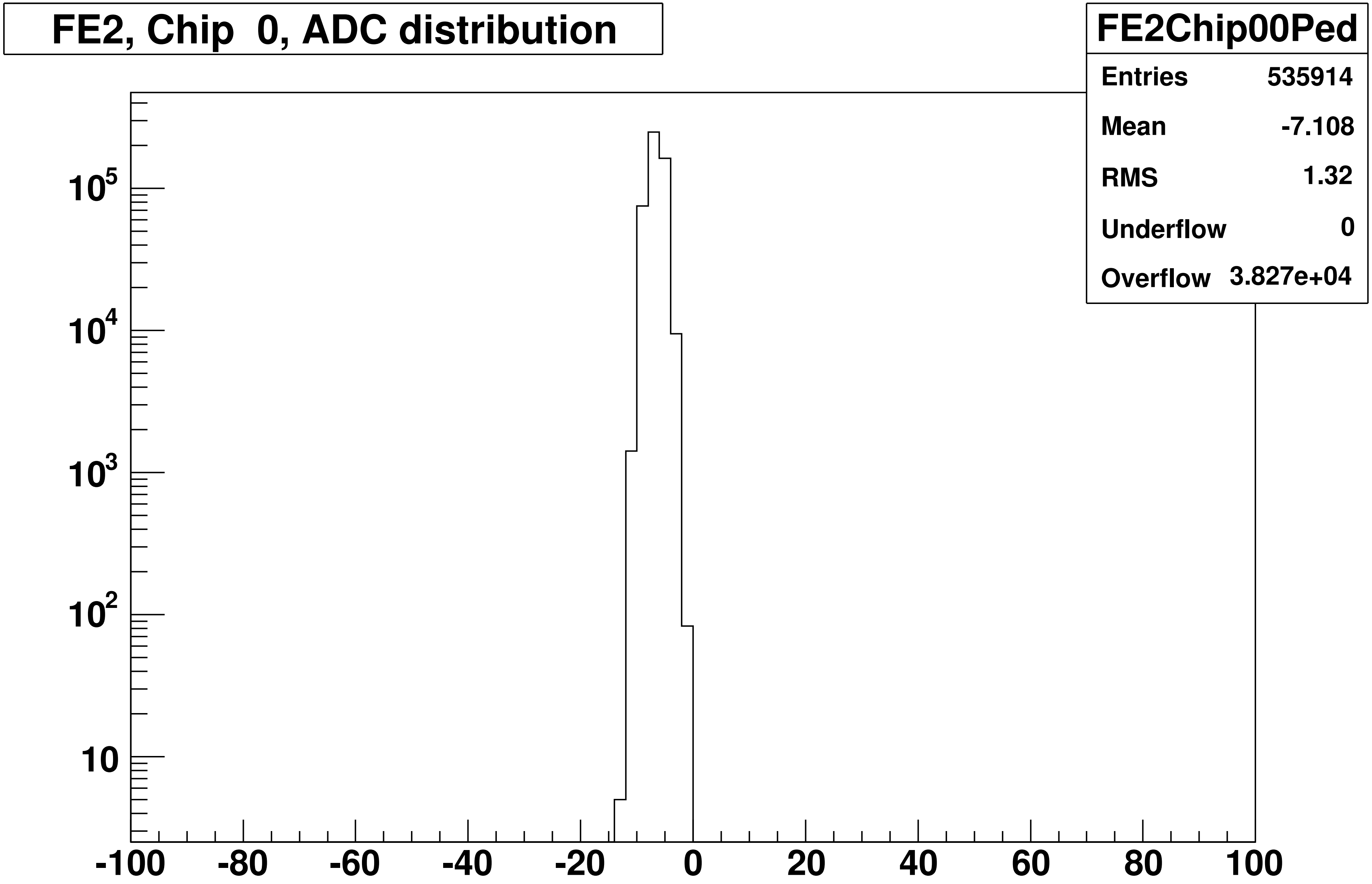

FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 535914
Mean 1251
RMS 4720
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

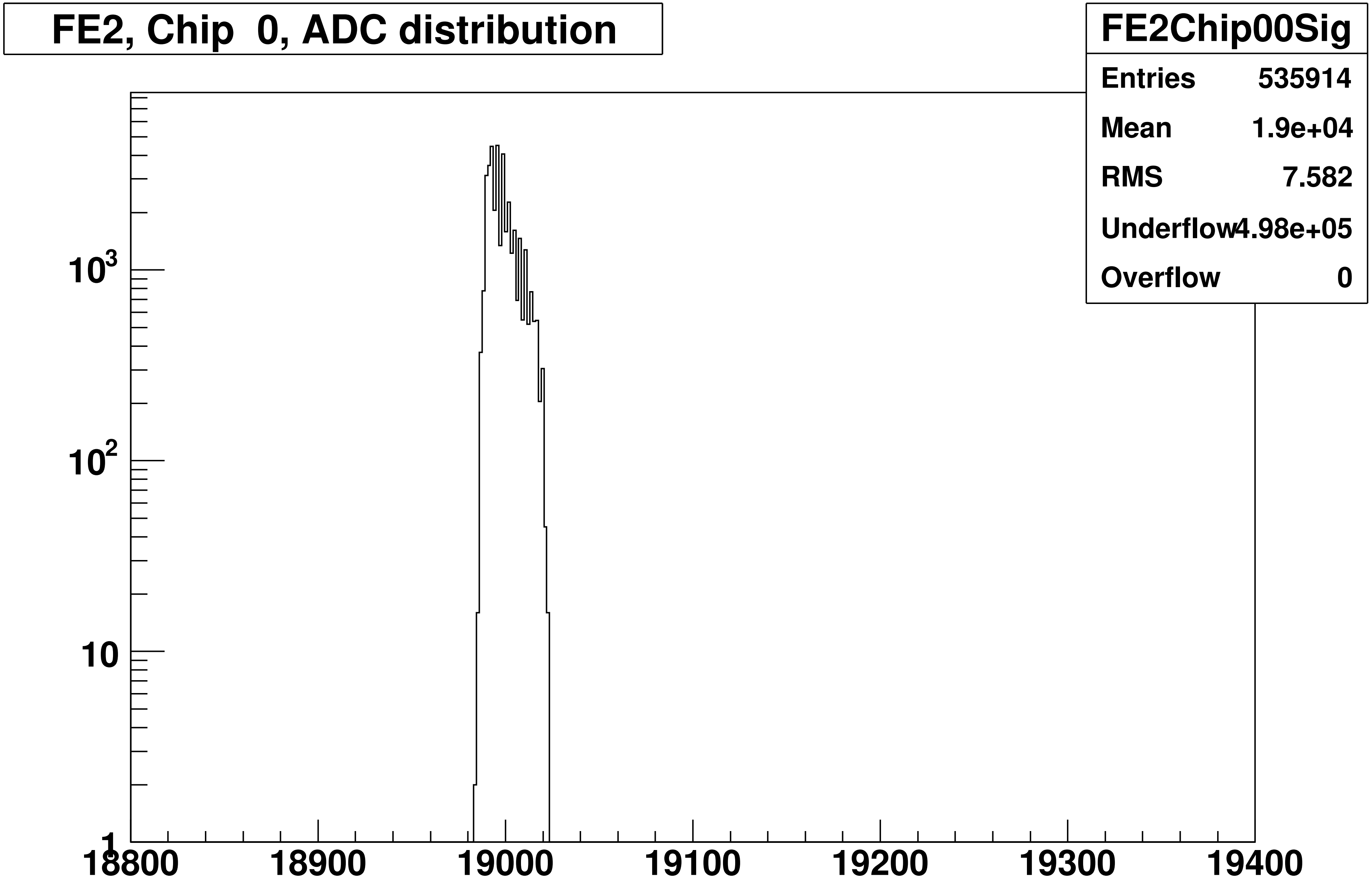

FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 535914
Mean -1.39
RMS 0.7426
Underflow 0
Overflow 3.528e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 535914
Mean 1.904e+04
RMS 9.612
Underflow 5.007e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

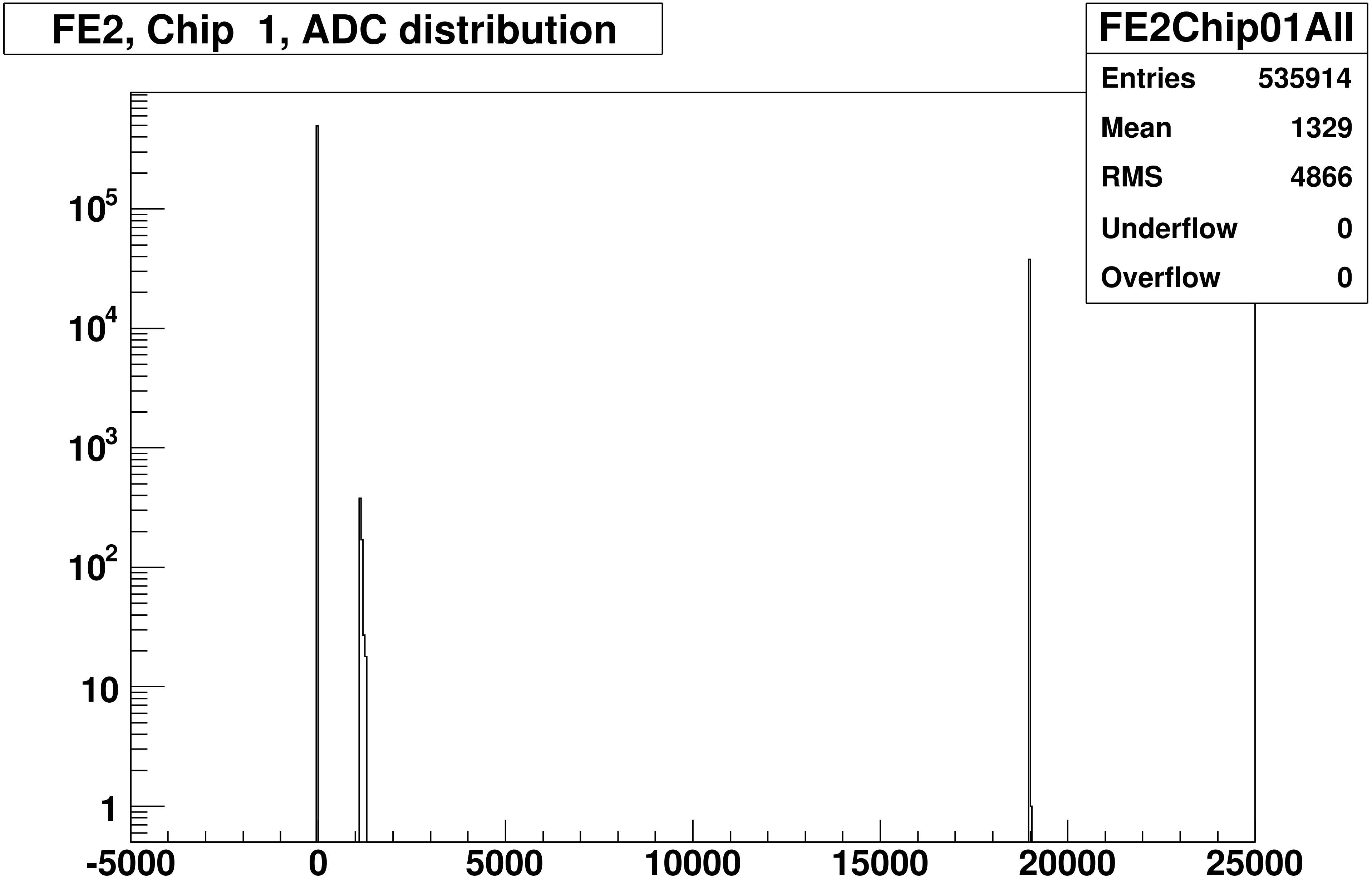


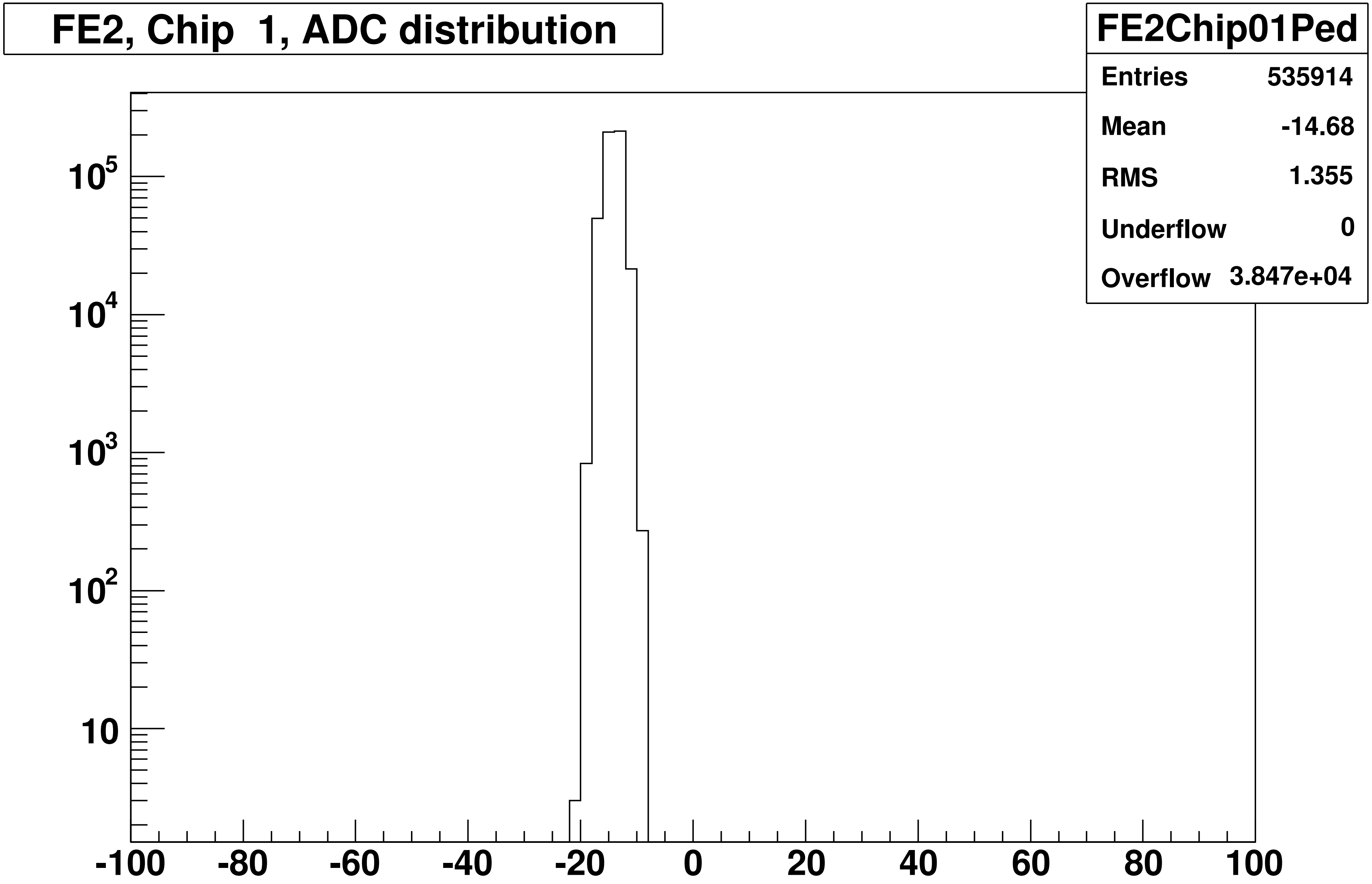
FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 535914
Mean 1337
RMS 4870
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

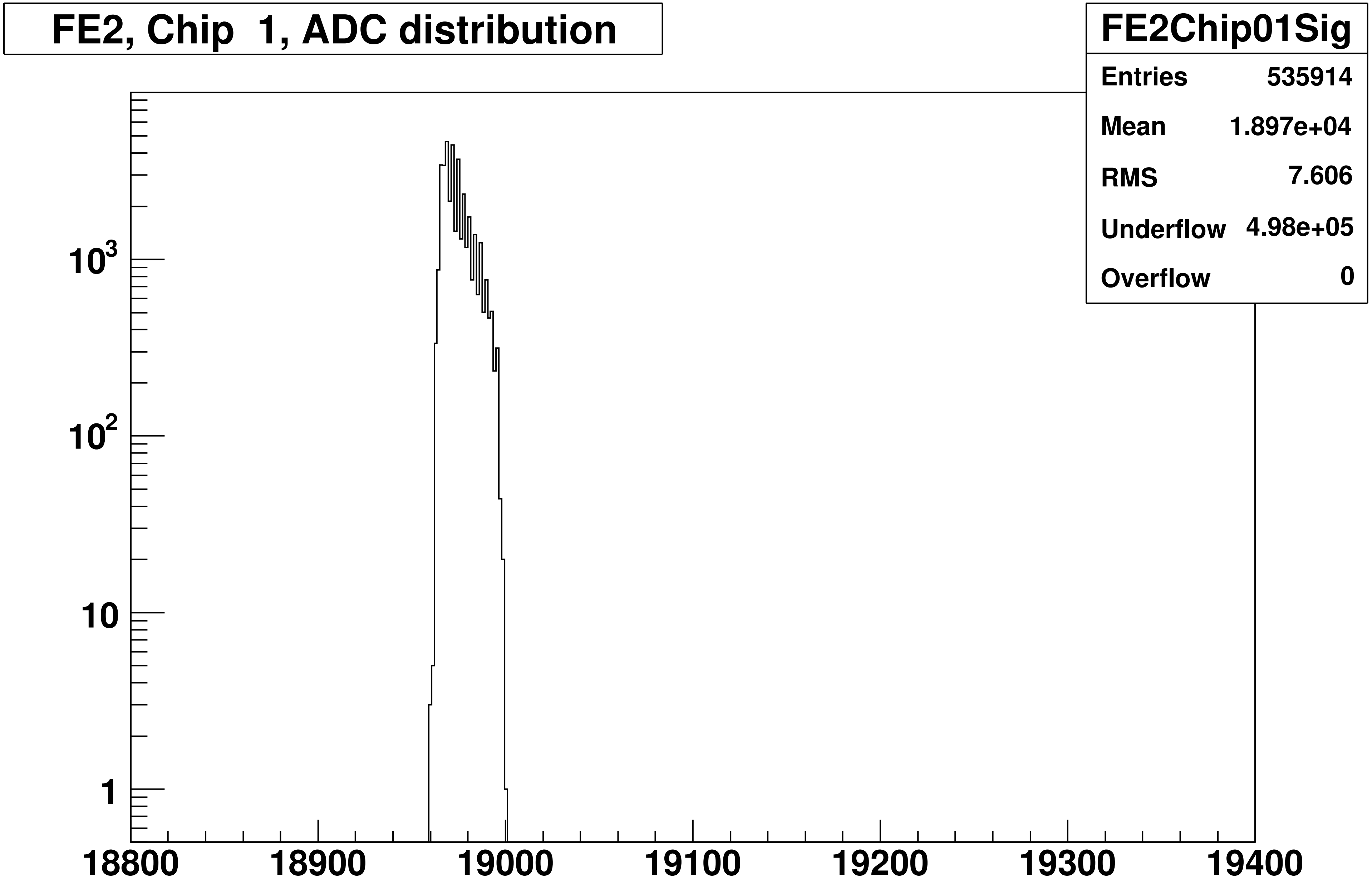

FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 535914
Mean -7.108
RMS 1.32
Underflow 0
Overflow 3.827e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



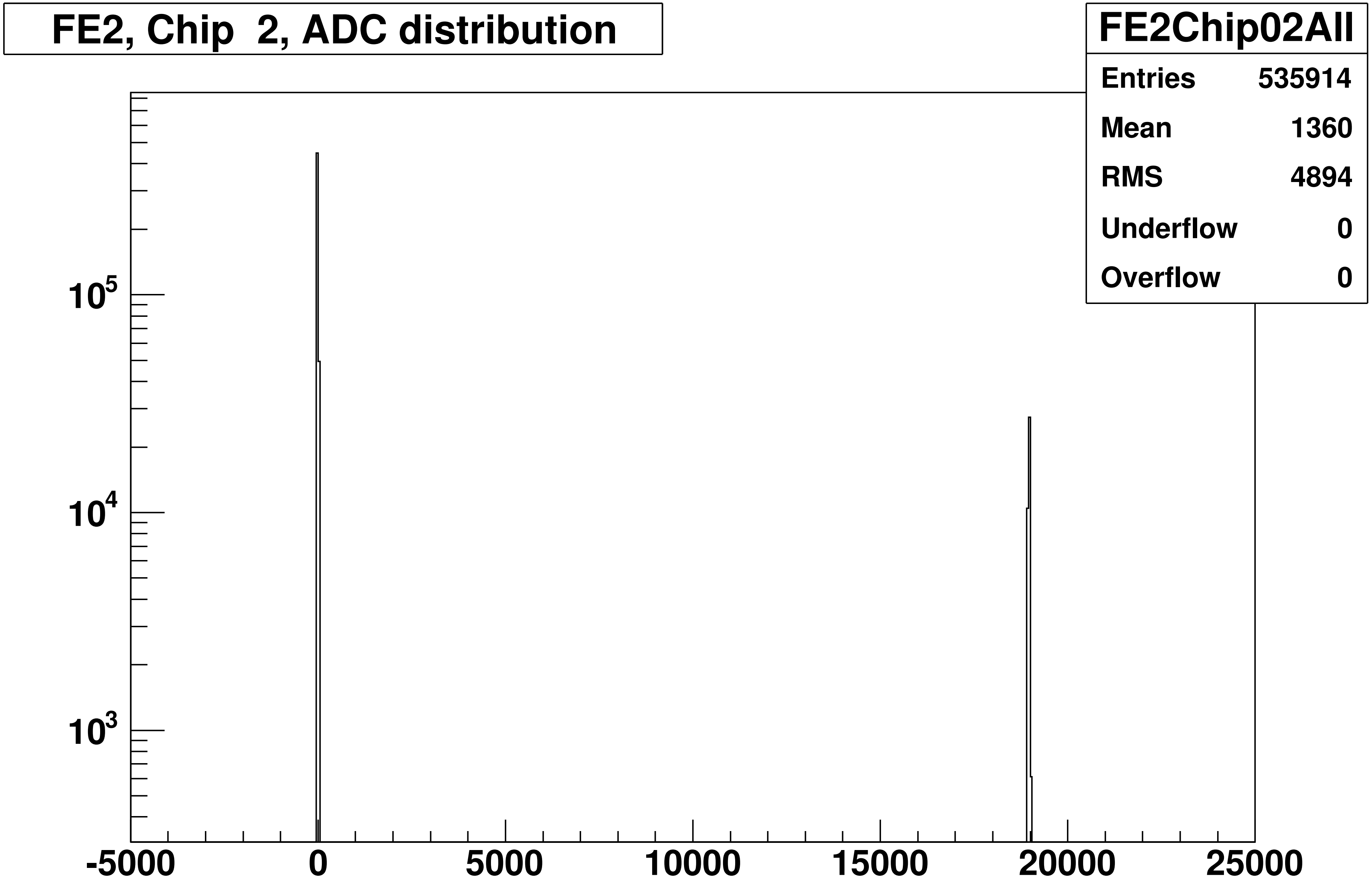
FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 535914
Mean 1.9e+04
RMS 7.582
Underflow 4.98e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 535914
Mean 1329
RMS 4866
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

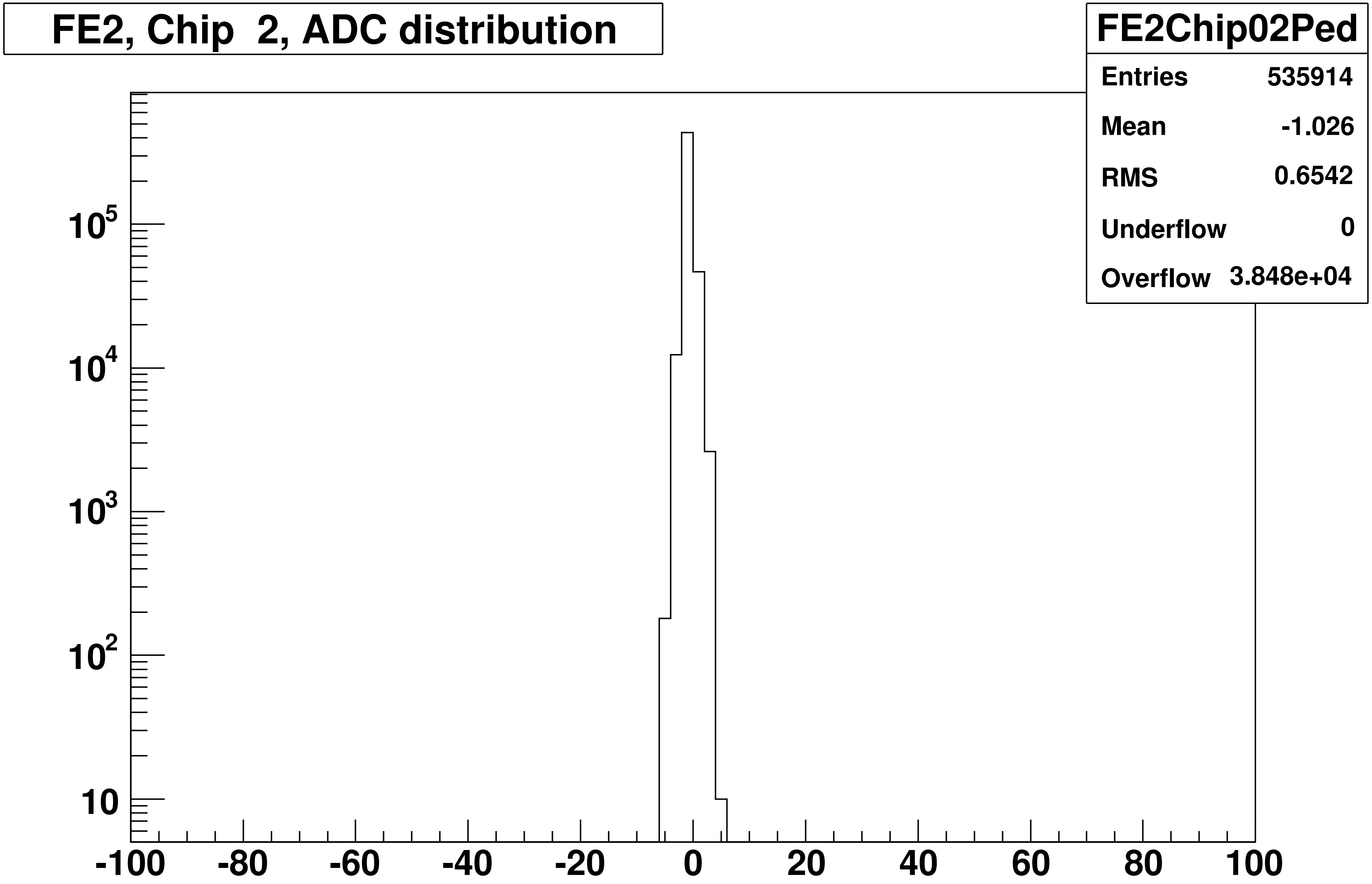

FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 535914
Mean -14.68
RMS 1.355
Underflow 0
Overflow 3.847e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

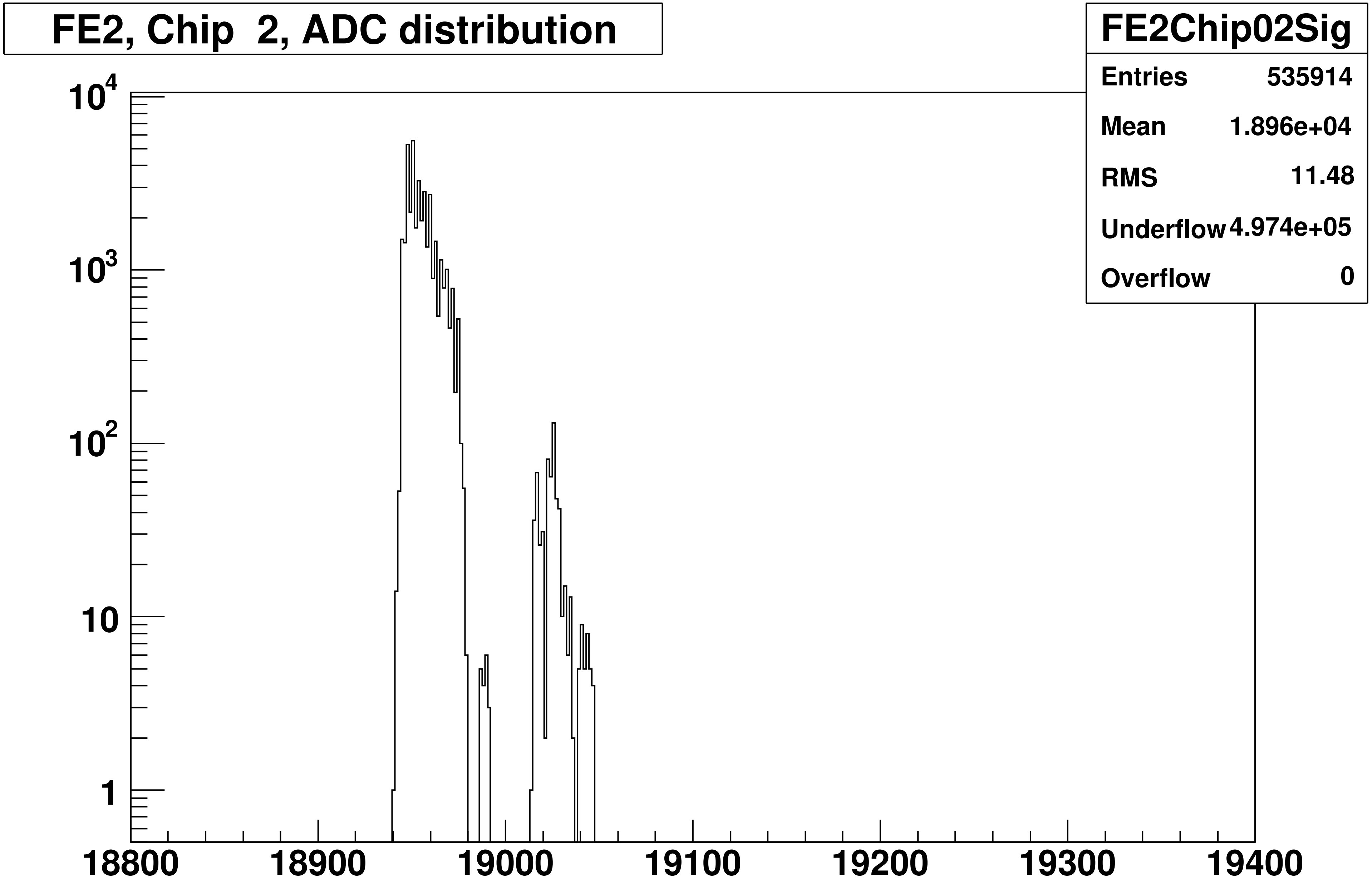


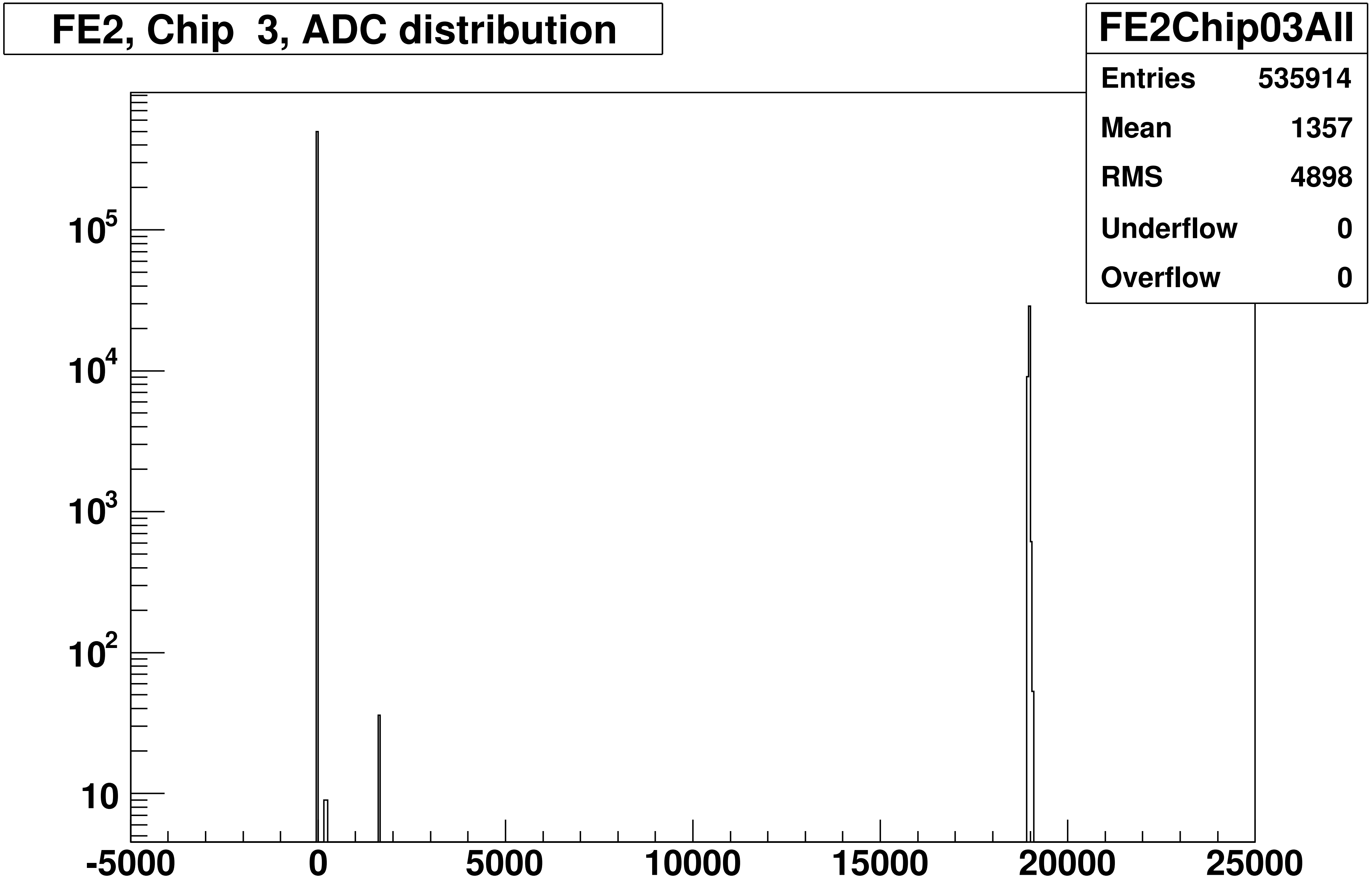
FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 535914
Mean 1.897e+04
RMS 7.606
Underflow 4.98e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

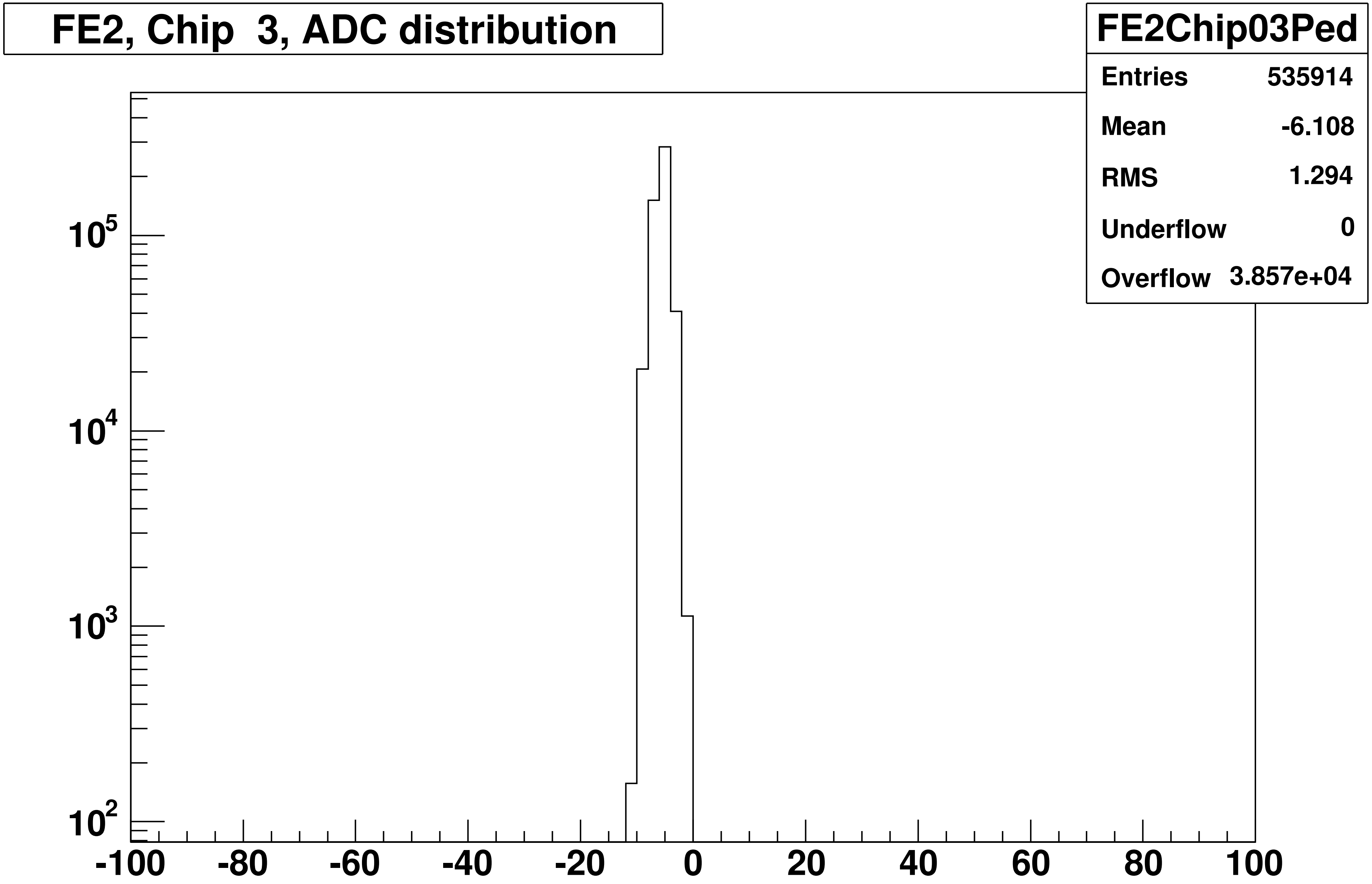


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 535914
Mean 1360
RMS 4894
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

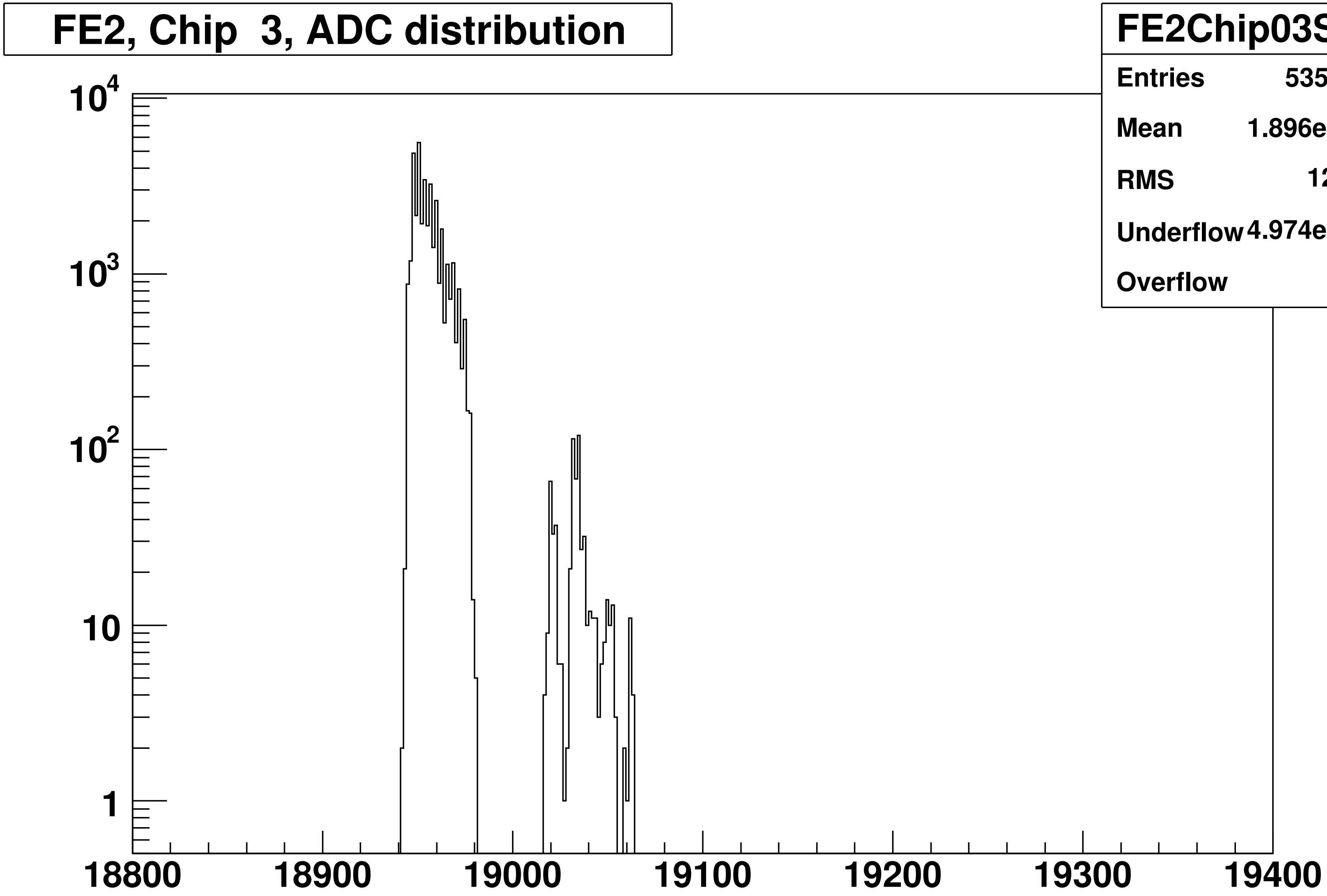

FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 535914
Mean -1.026
RMS 0.6542
Underflow 0
Overflow 3.848e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 535914
Mean 1.896e+04
RMS 11.48
Underflow 4.974e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

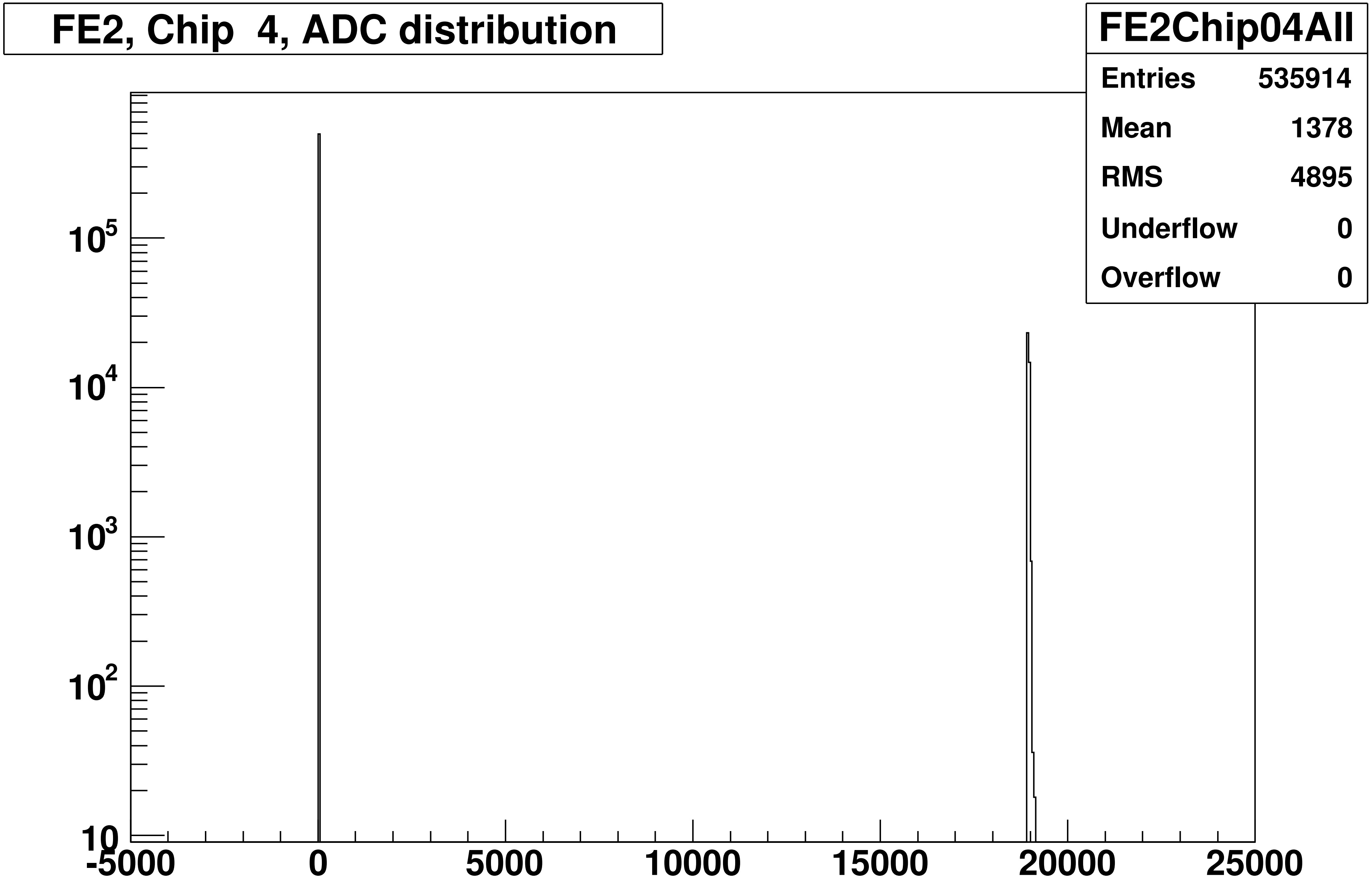

FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 535914
Mean 1357
RMS 4898
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



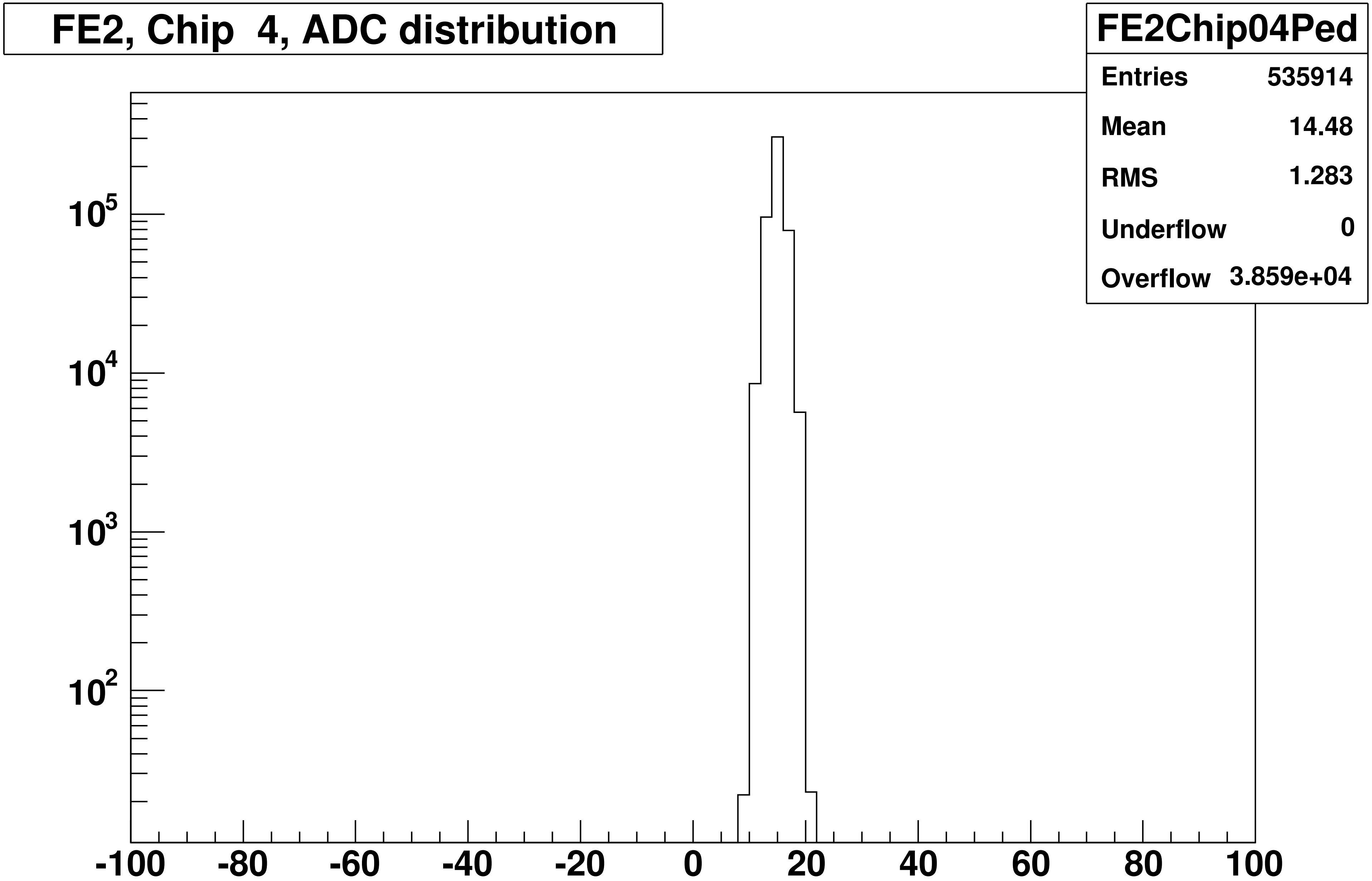
FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 535914
Mean -6.108
RMS 1.294
Underflow 0
Overflow 3.857e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

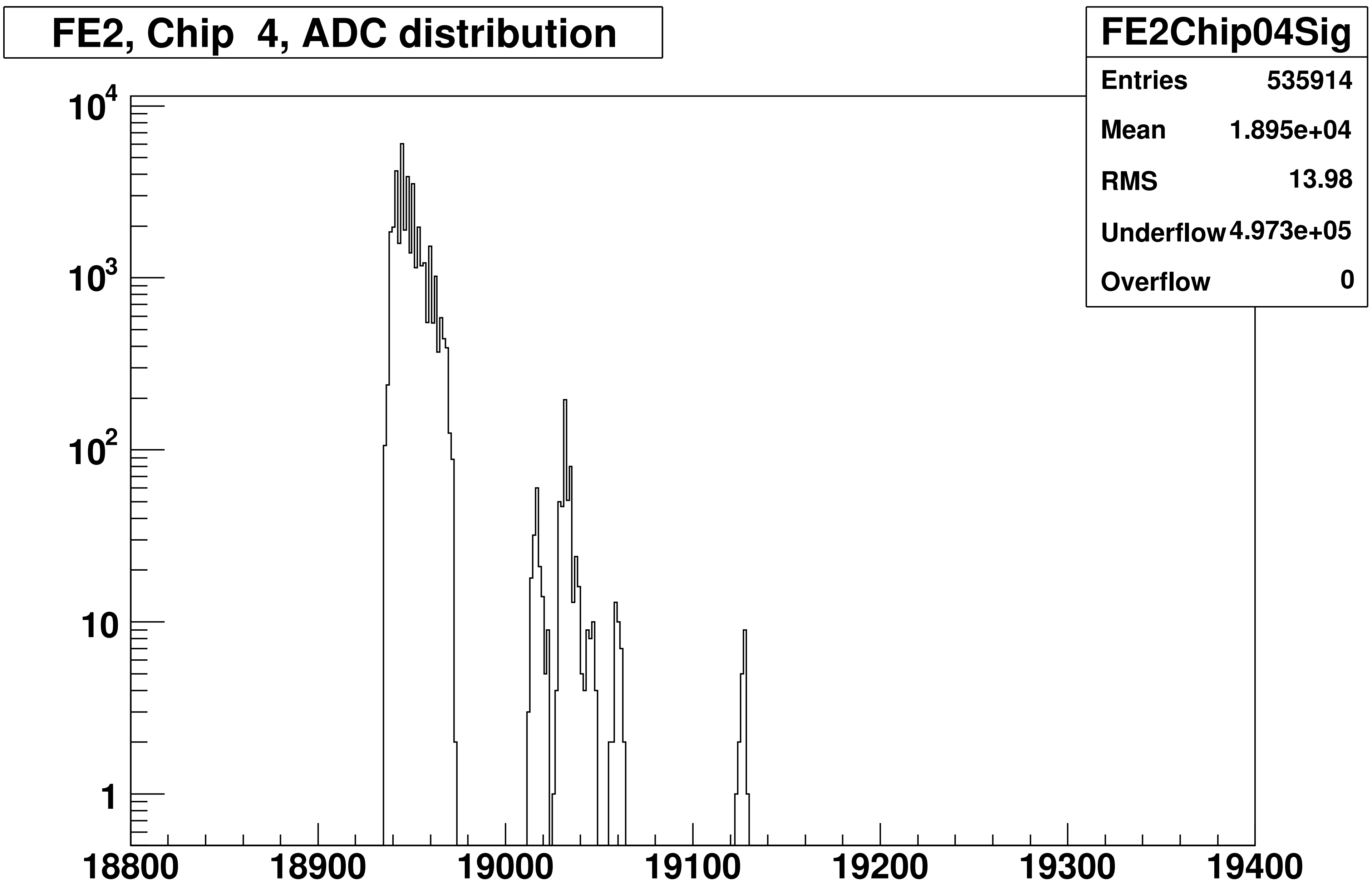


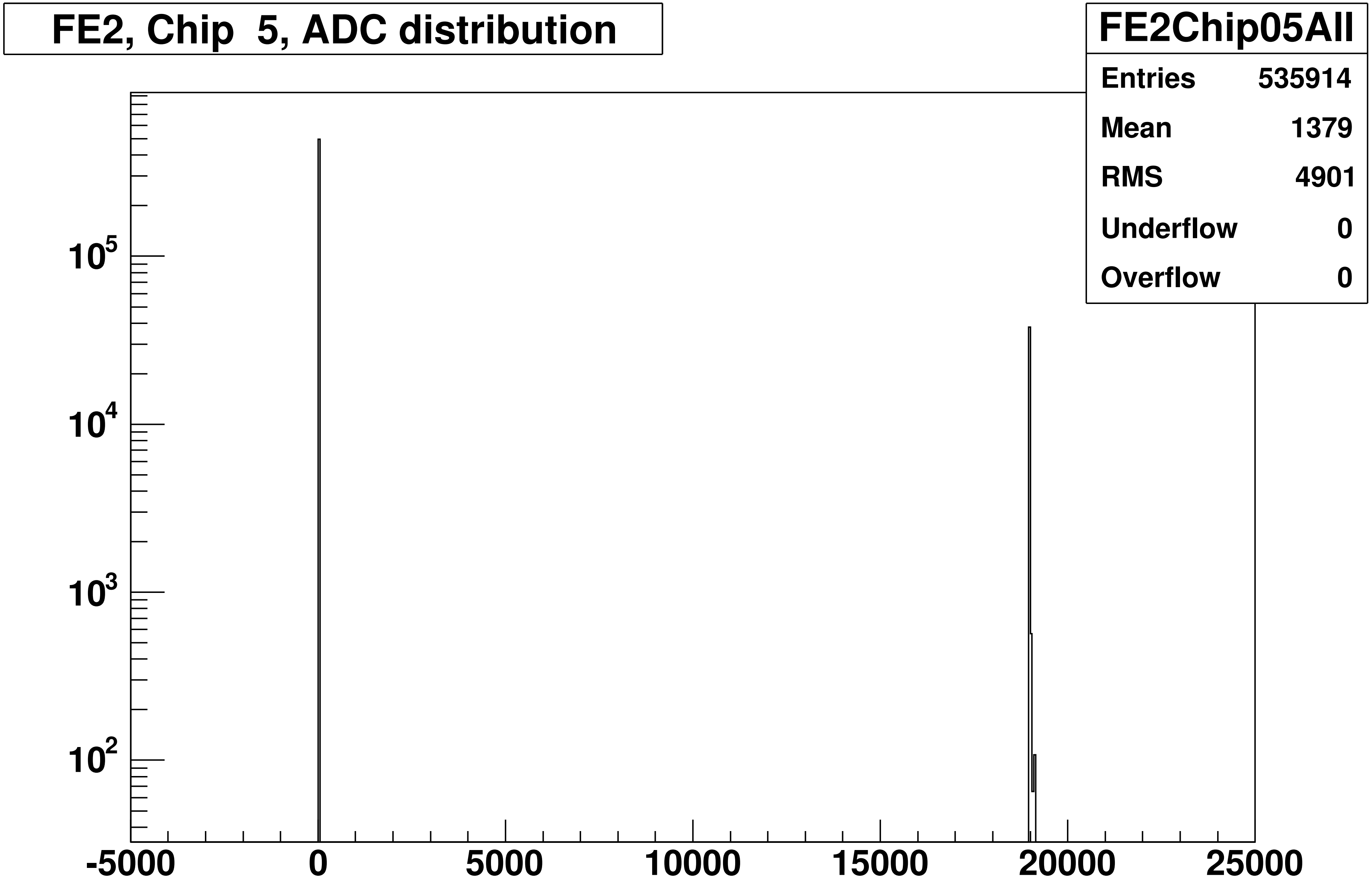
FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 535914
Mean 1.896e+04
RMS 12.64
Underflow 4.974e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



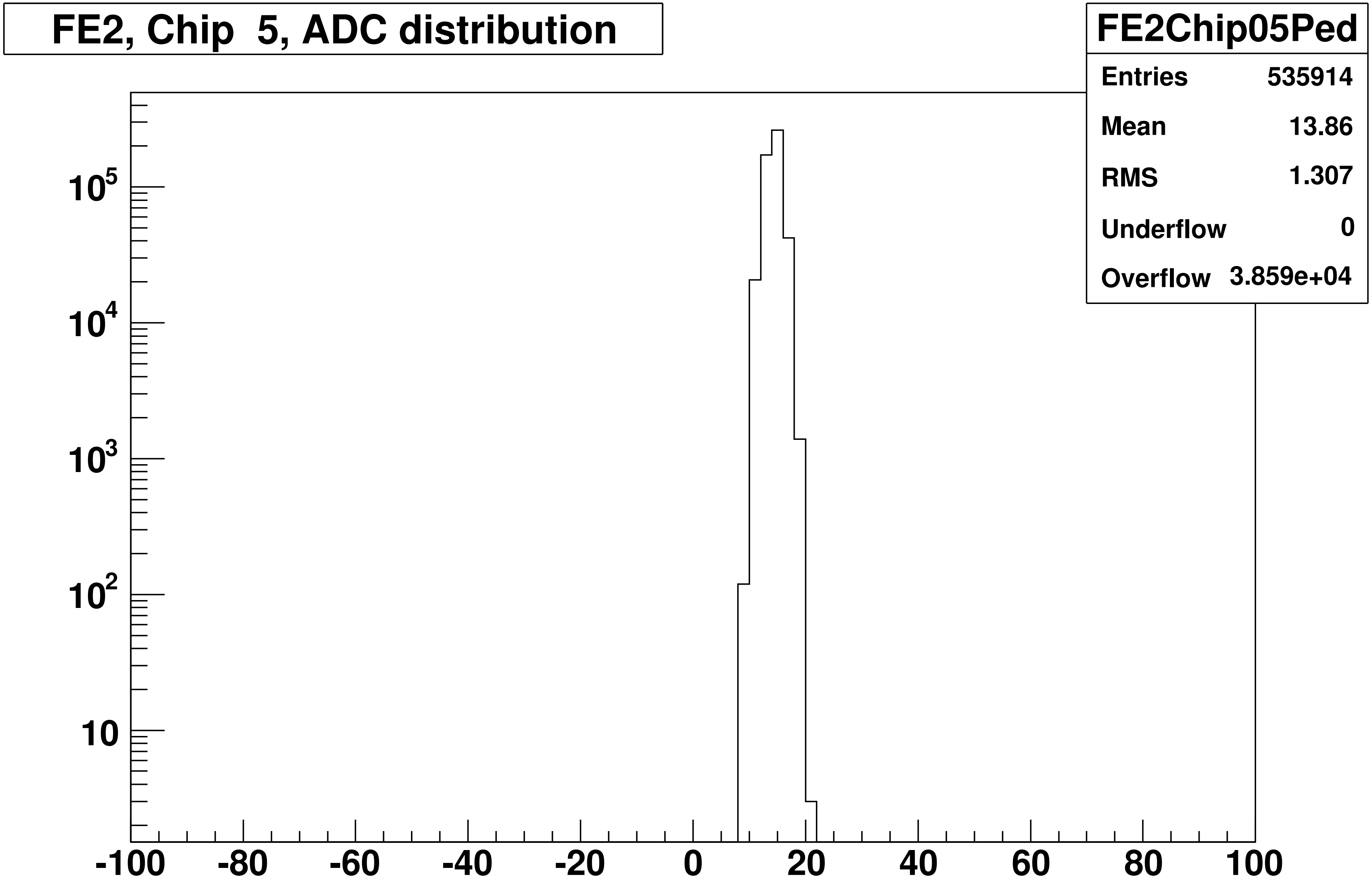
FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 535914
Mean 1378
RMS 4895
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 535914
Mean 14.48
RMS 1.283
Underflow 0
Overflow 3.859e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

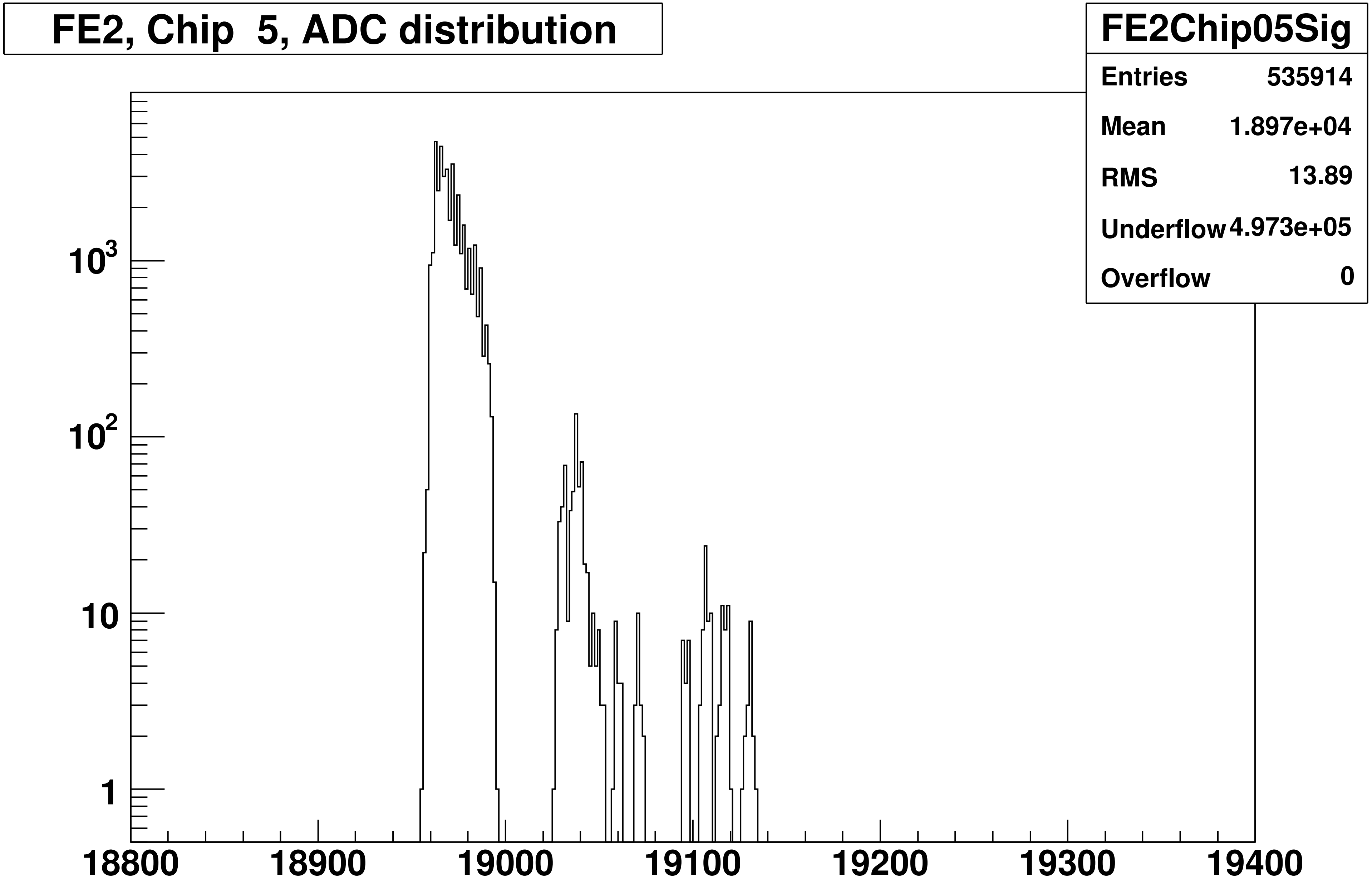

FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 535914
Mean 1.895e+04
RMS 13.98
Underflow 4.973e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



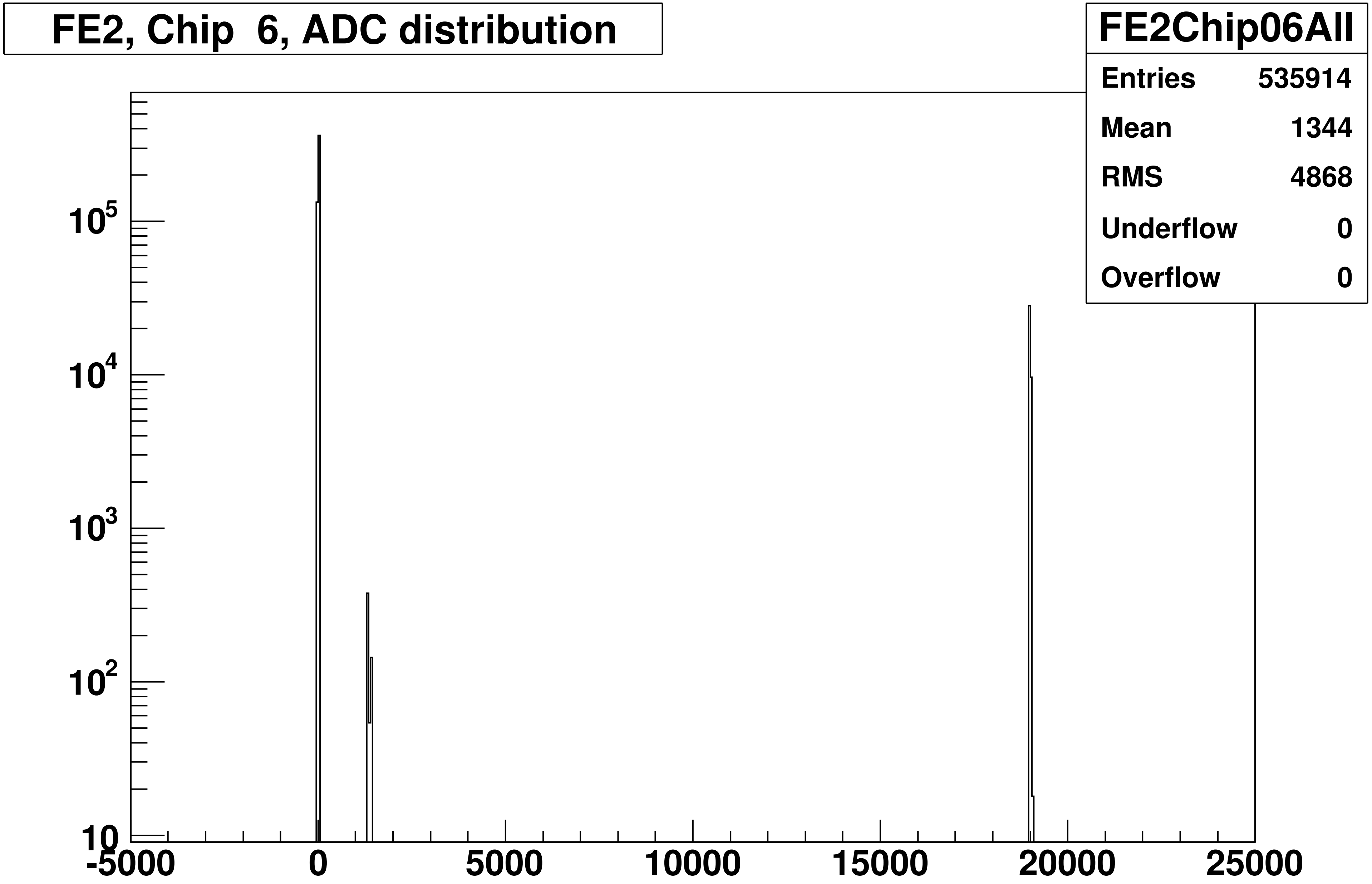
FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 535914
Mean 1379
RMS 4901
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

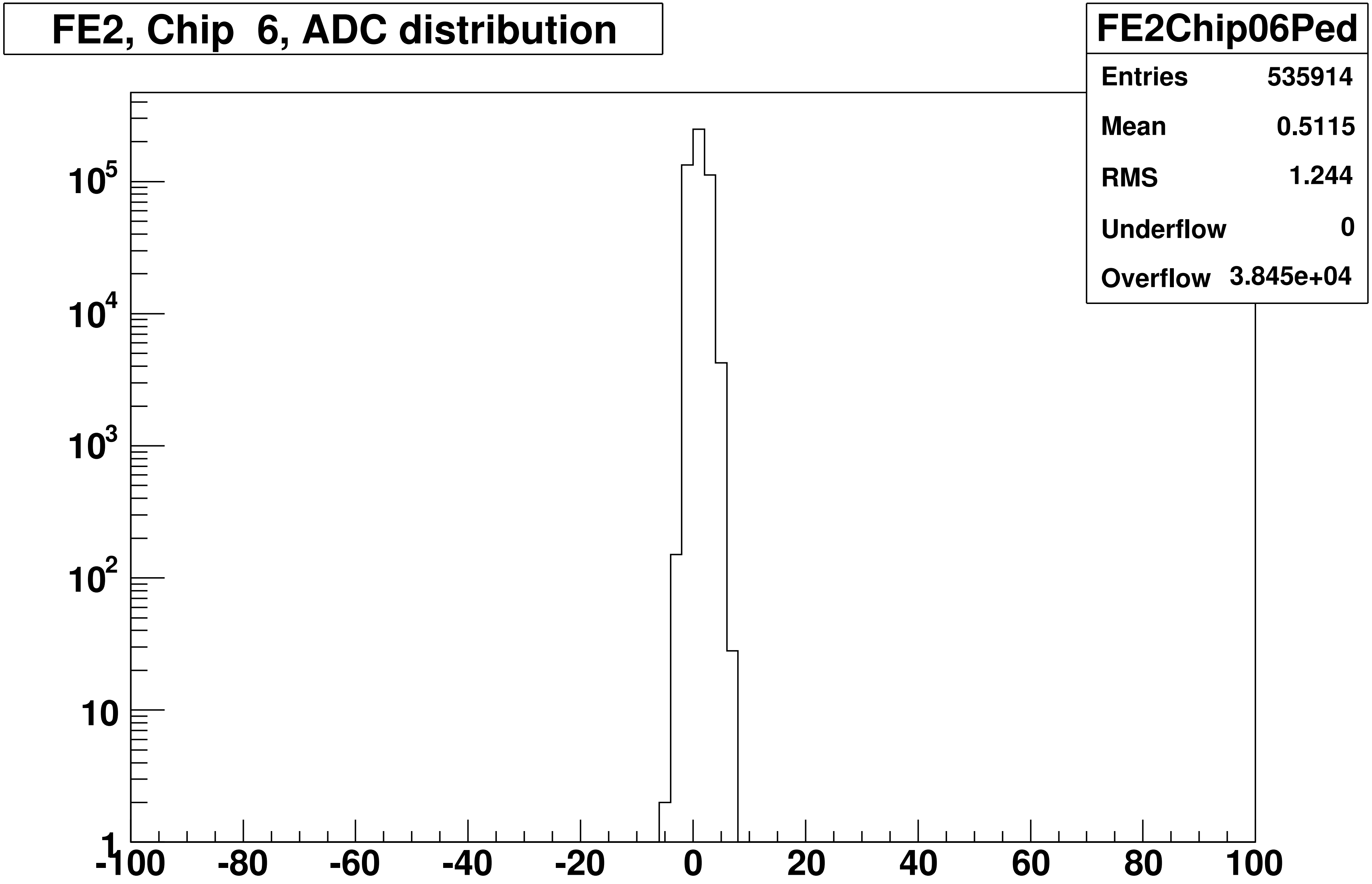


FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 535914
Mean 13.86
RMS 1.307
Underflow 0
Overflow 3.859e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

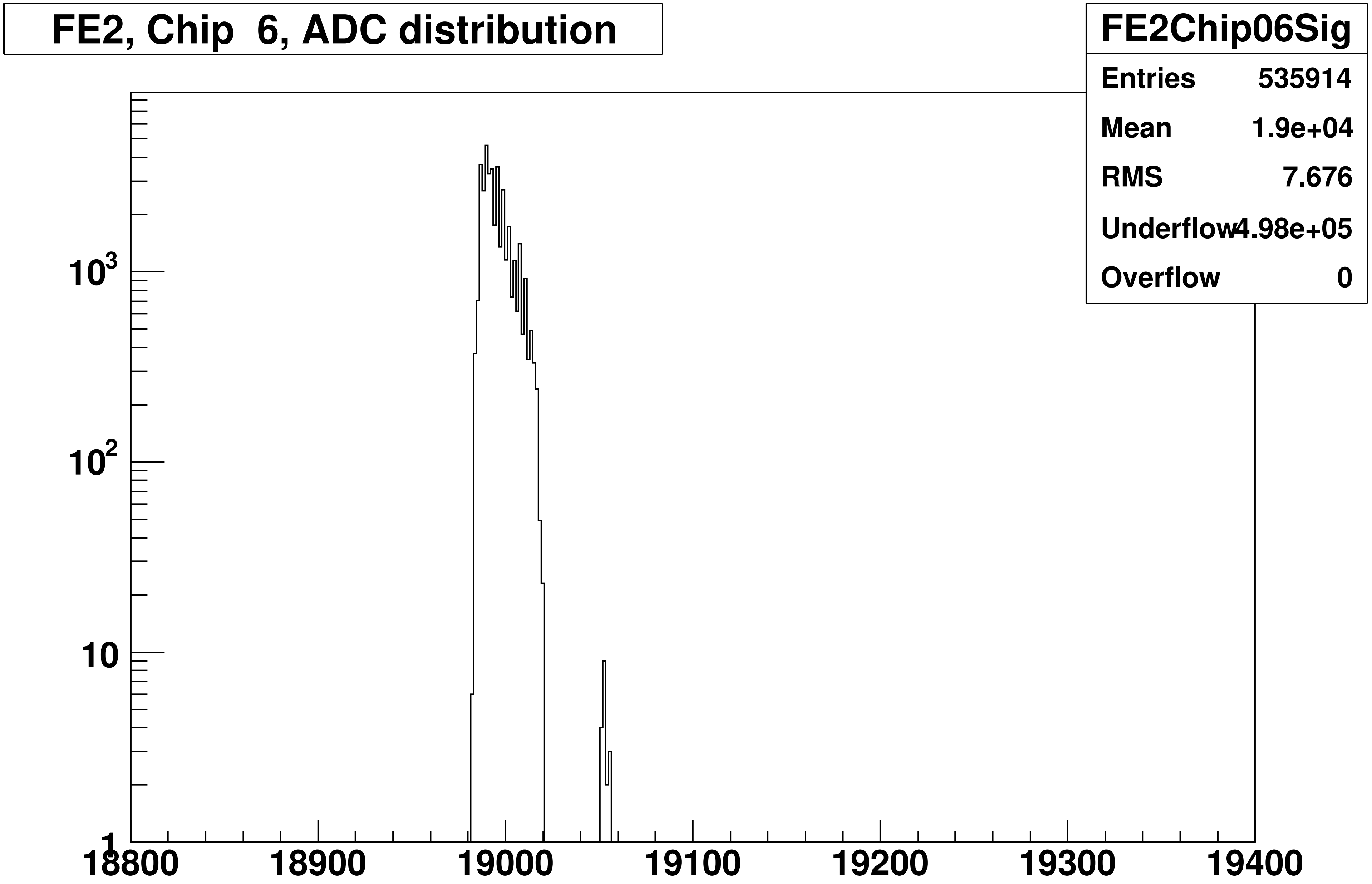


FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 535914
Mean 1.897e+04
RMS 13.89
Underflow 4.973e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400

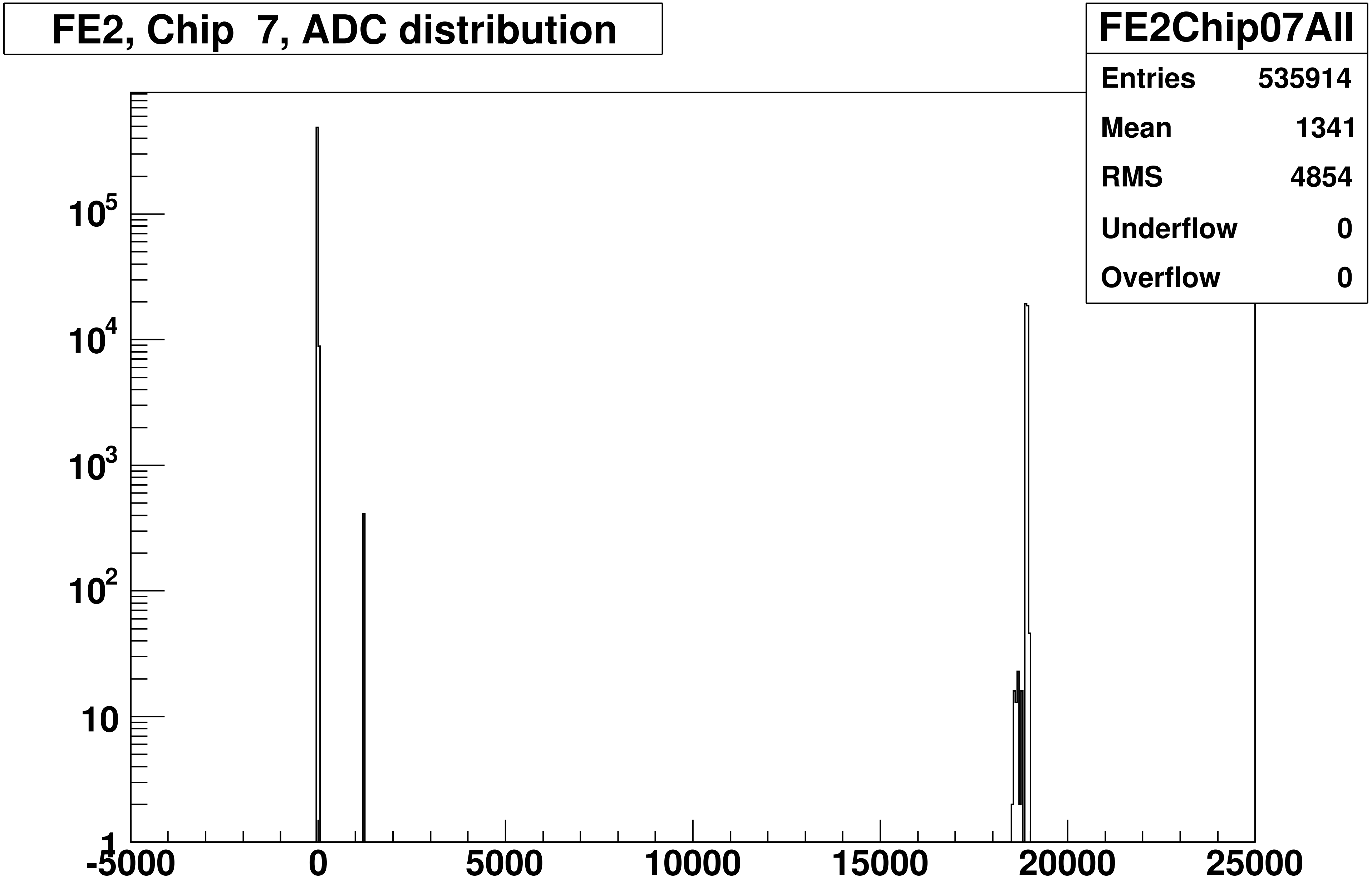

FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 535914
Mean 1344
RMS 4868
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

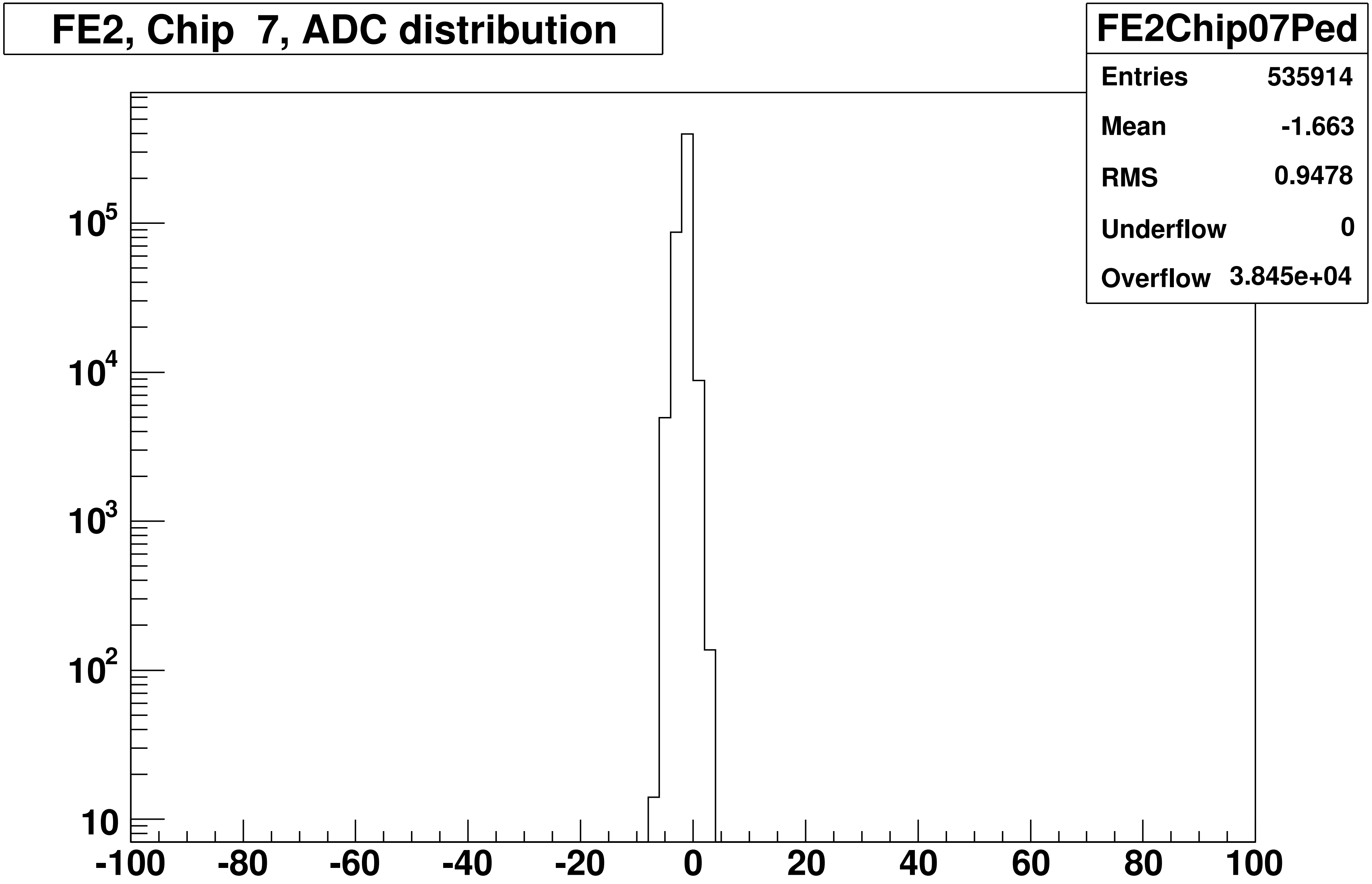


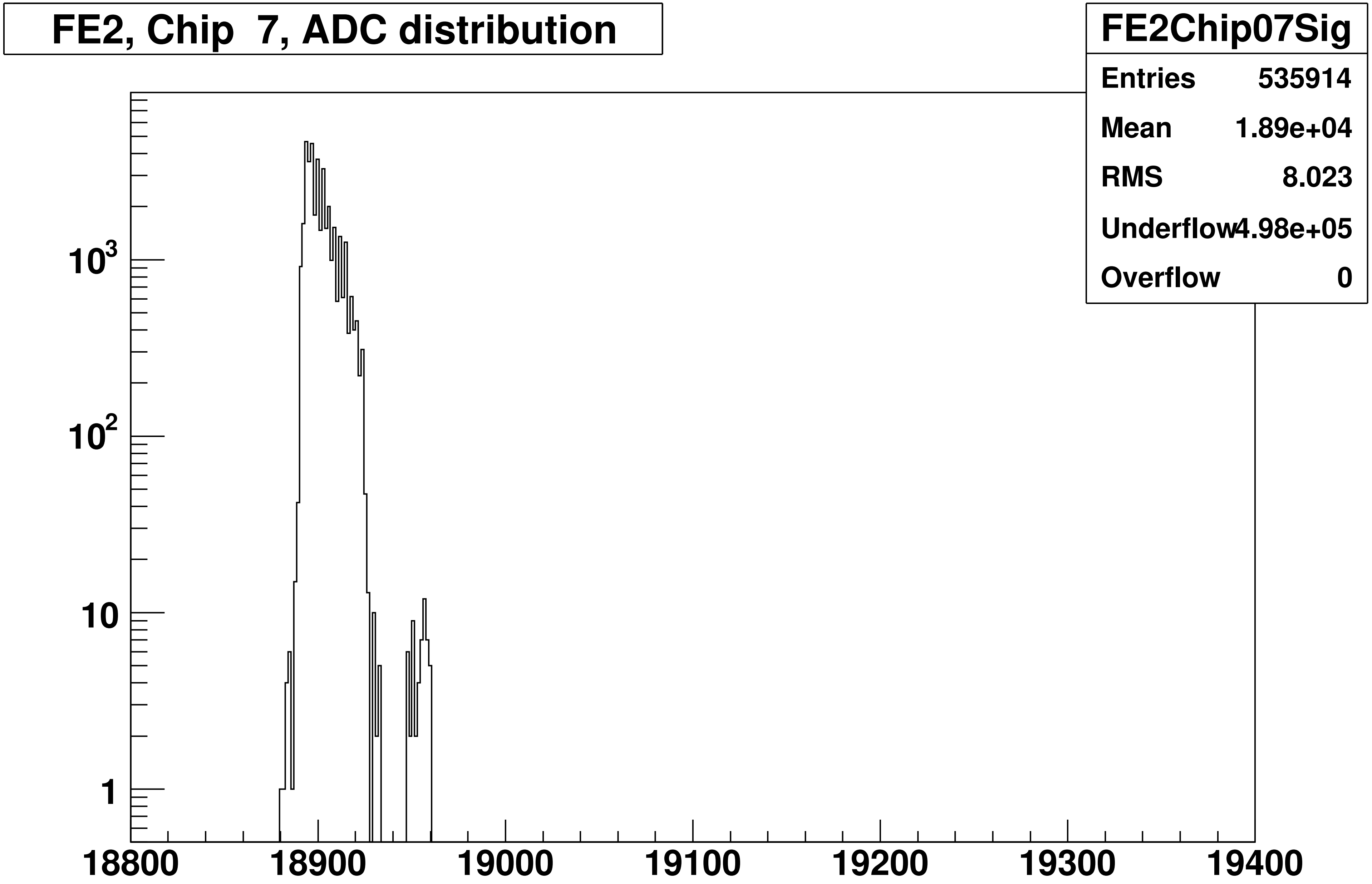
FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 535914
Mean 0.5115
RMS 1.244
Underflow 0
Overflow 3.845e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

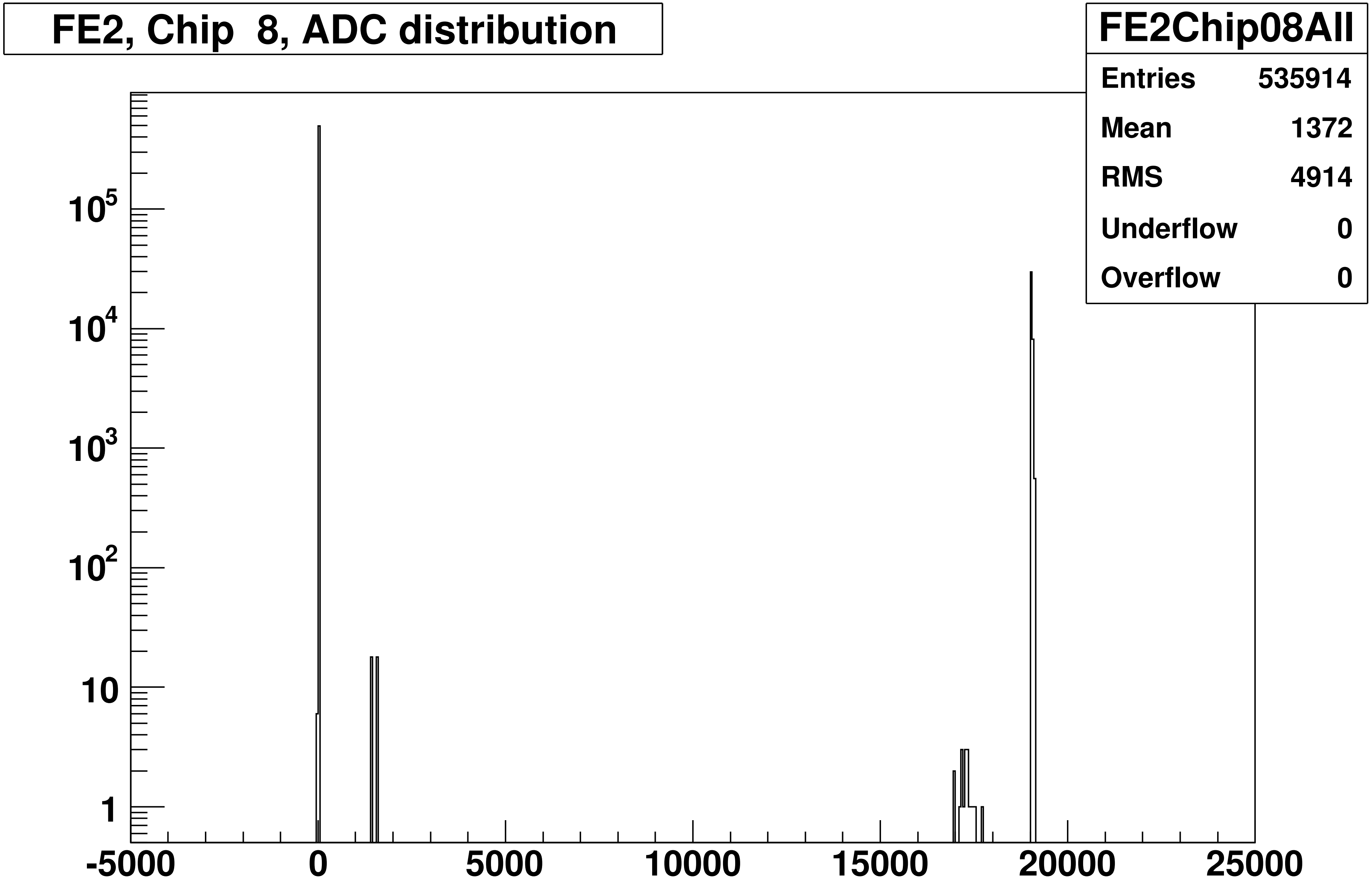


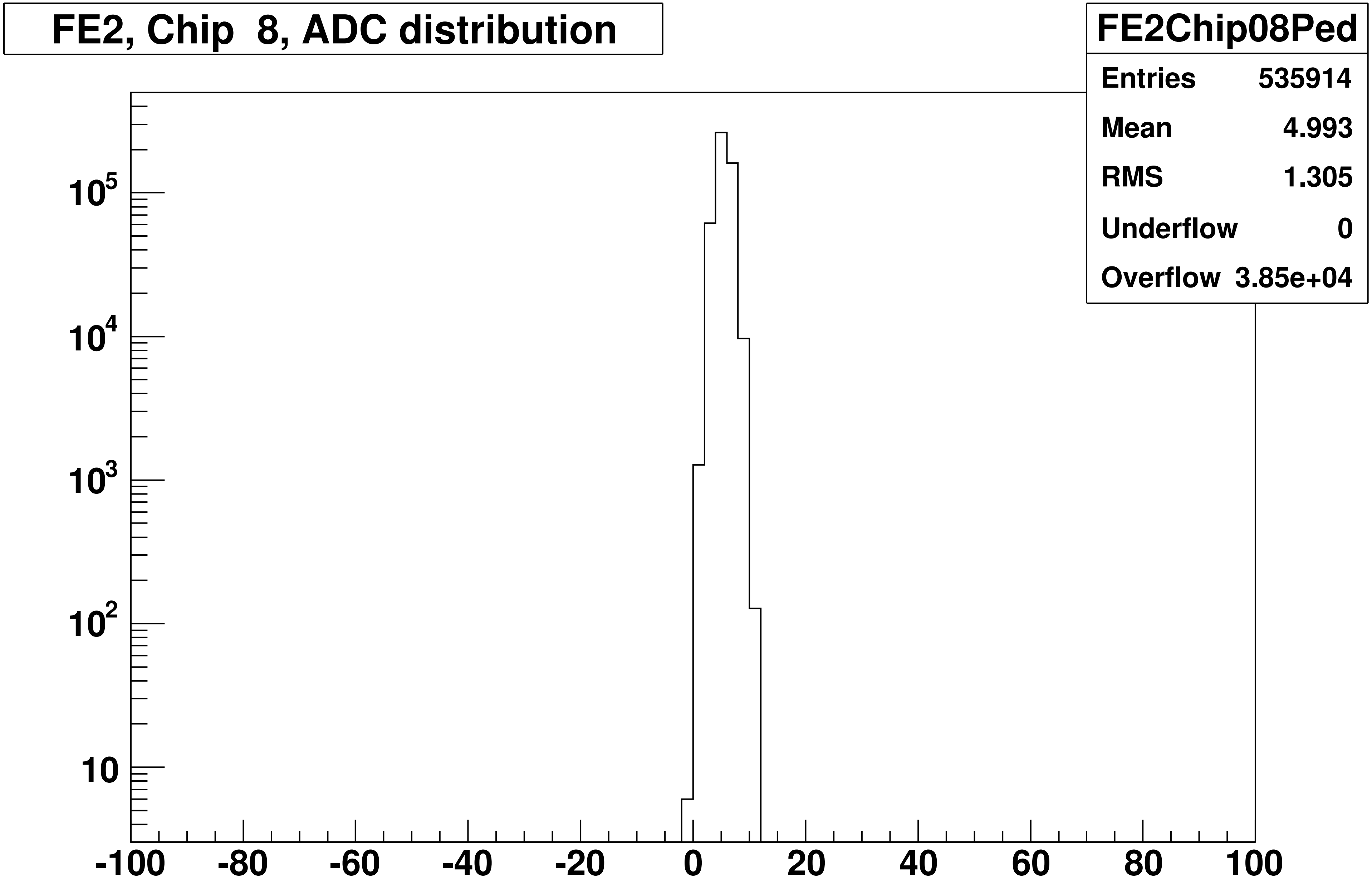
FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 535914
Mean 1.9e+04
RMS 7.676
Underflow 4.98e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

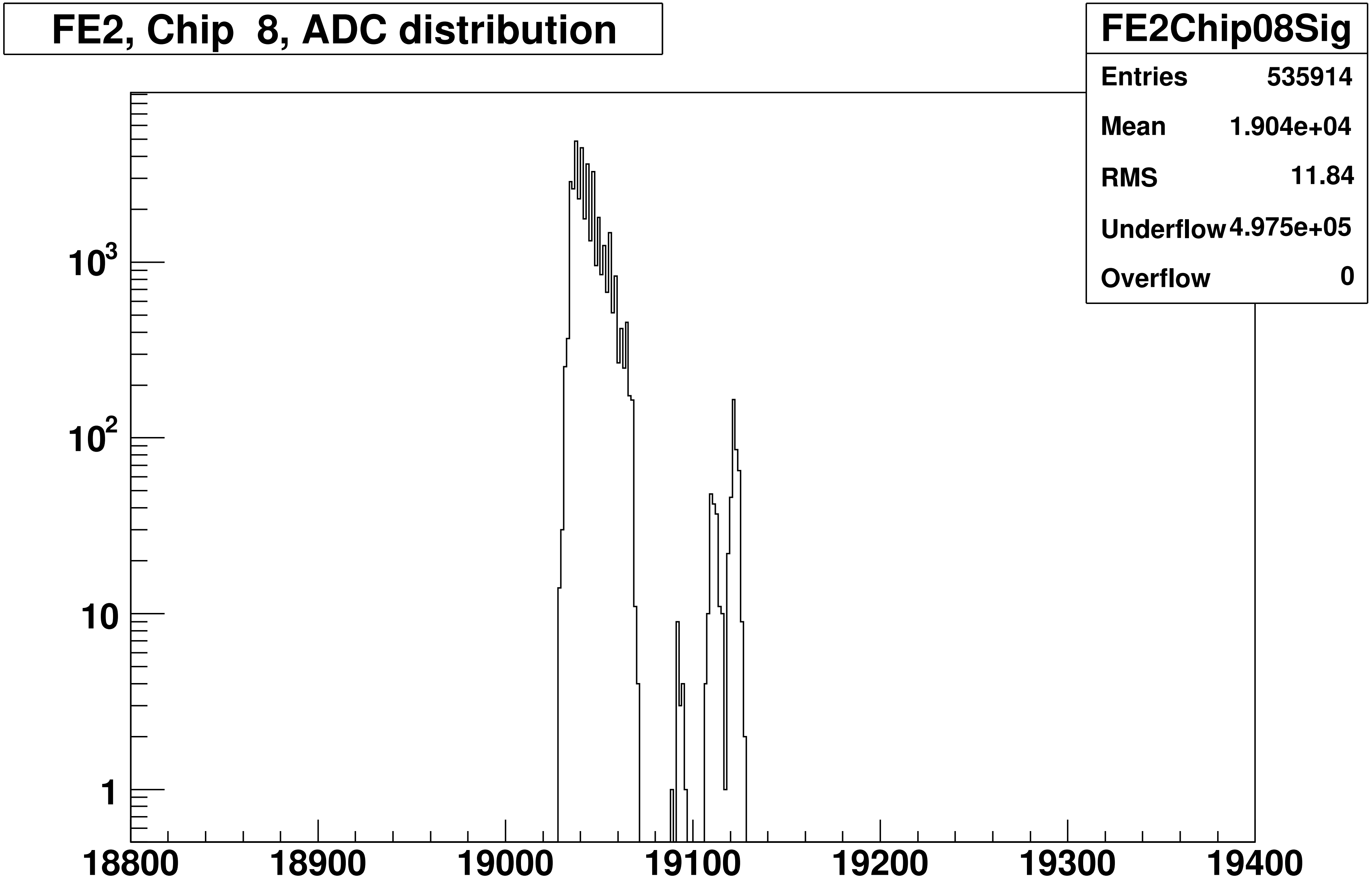

FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 535914
Mean 1341
RMS 4854
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

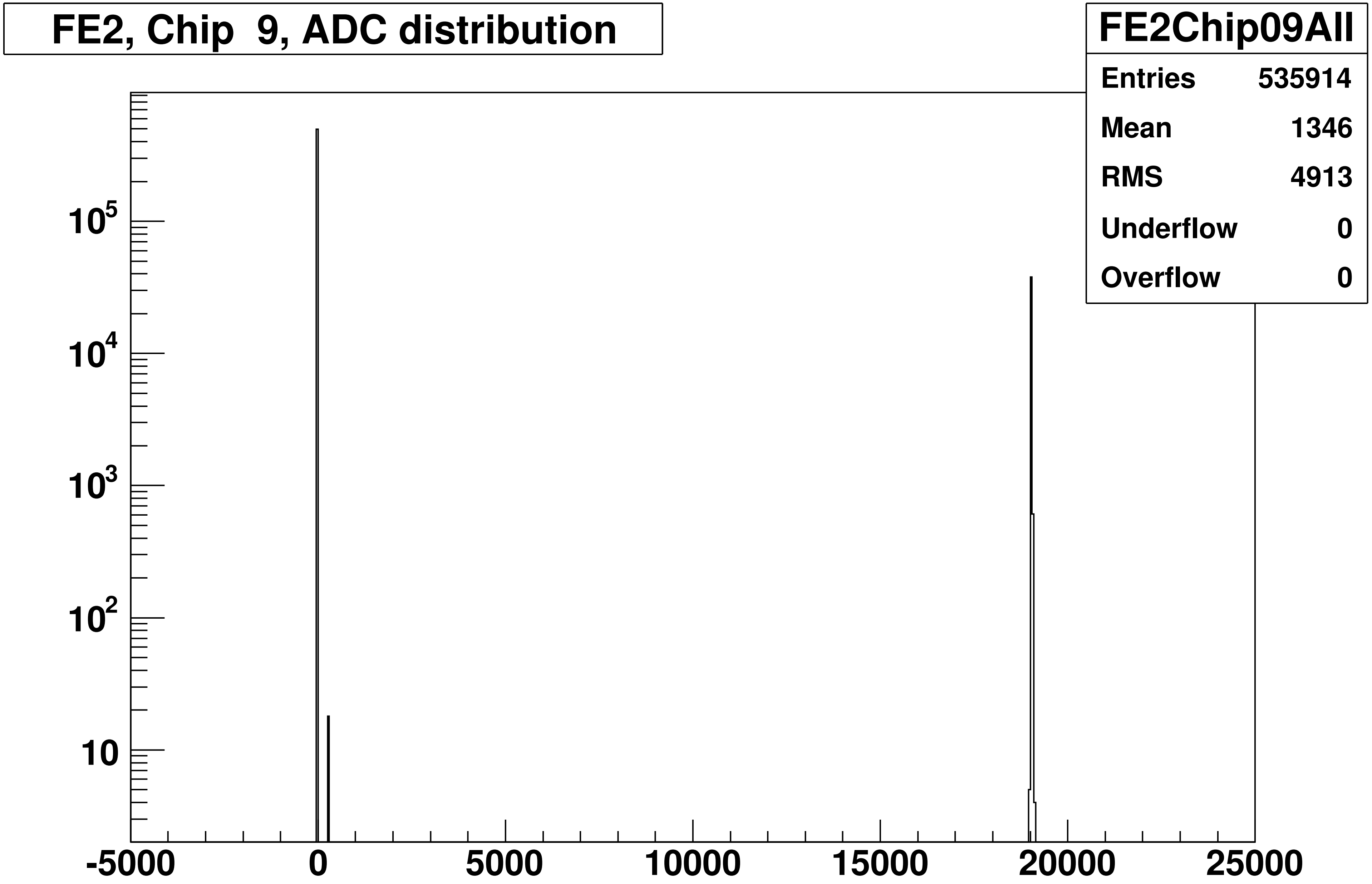

FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 535914
Mean -1.663
RMS 0.9478
Underflow 0
Overflow 3.845e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

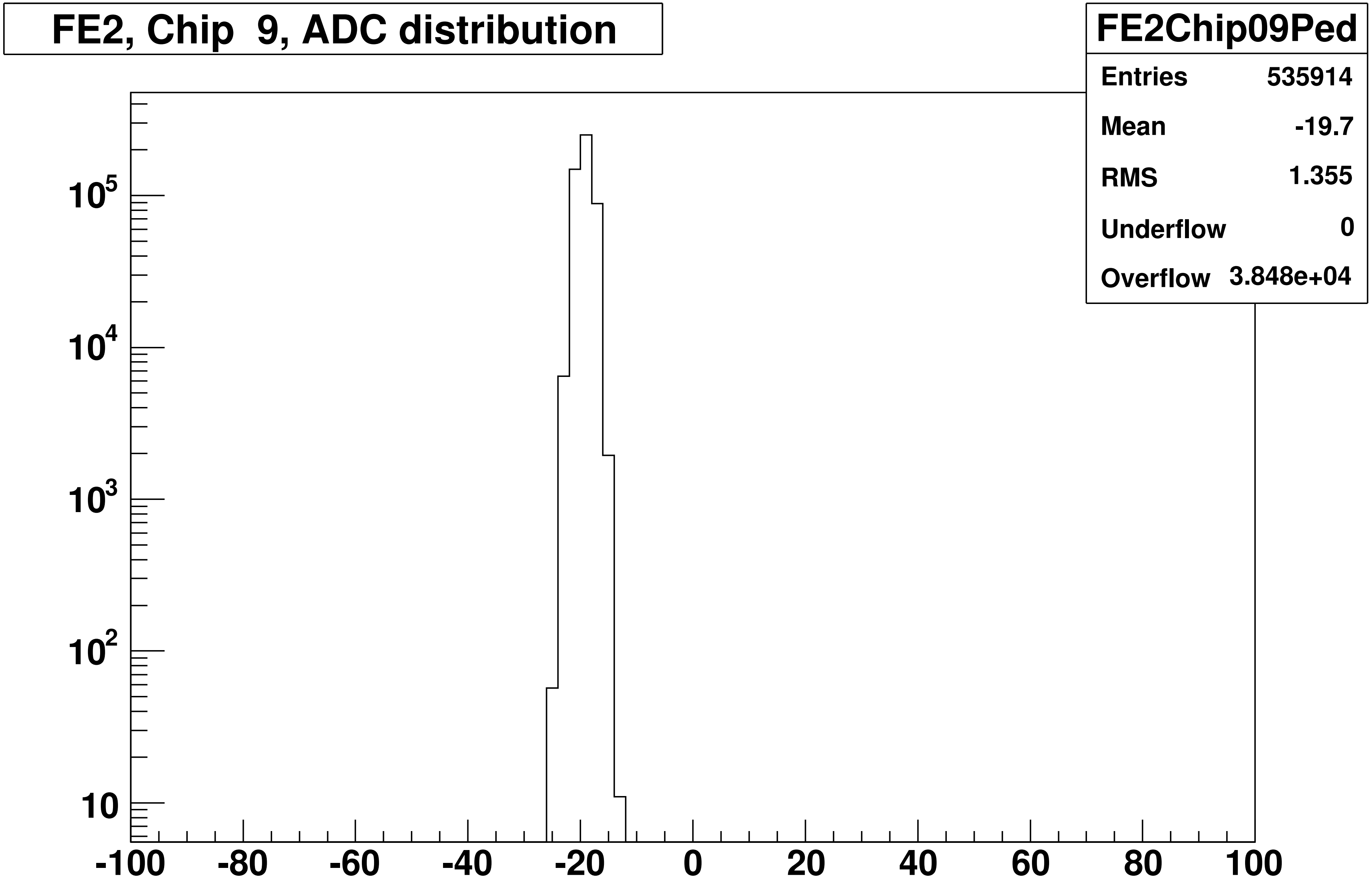

FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 535914
Mean 1.89e+04
RMS 8.023
Underflow 4.98e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

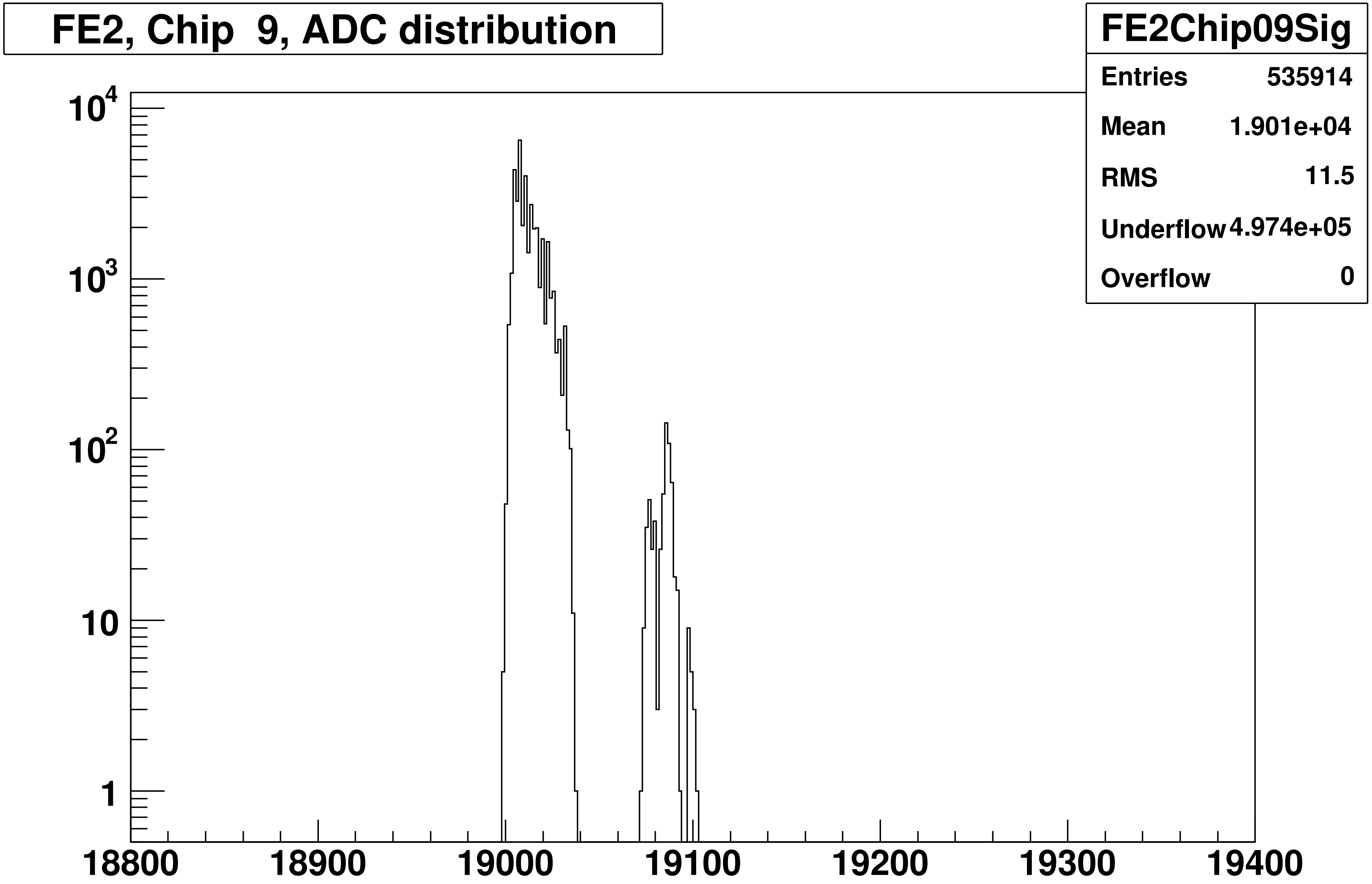

FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 535914
Mean 1372
RMS 4914
Underflow 0
Overflow 0
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 535914
Mean 4.993
RMS 1.305
Underflow 0
Overflow 3.85e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

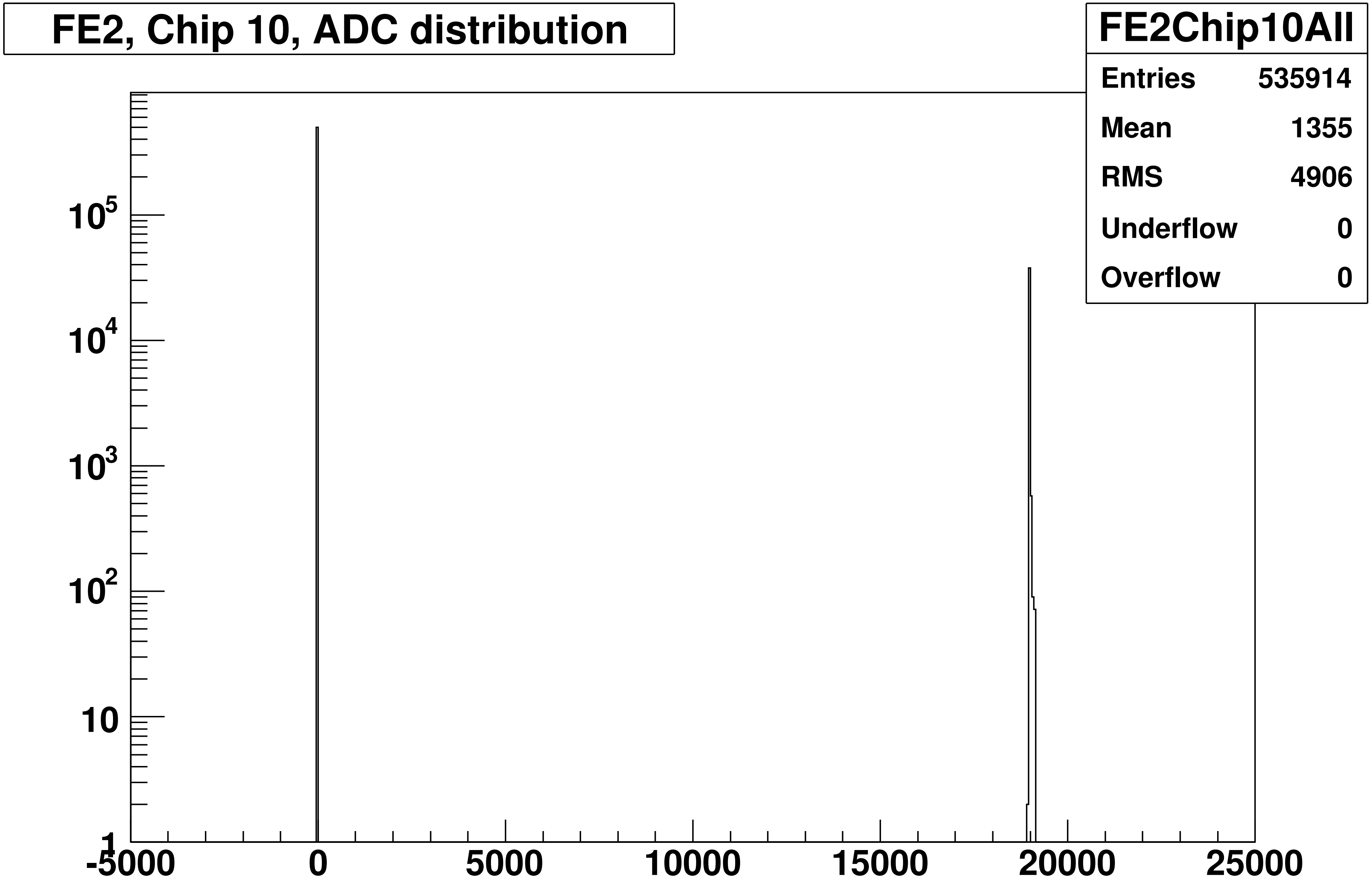

FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 535914
Mean 1.904e+04
RMS 11.84
Underflow 4.975e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

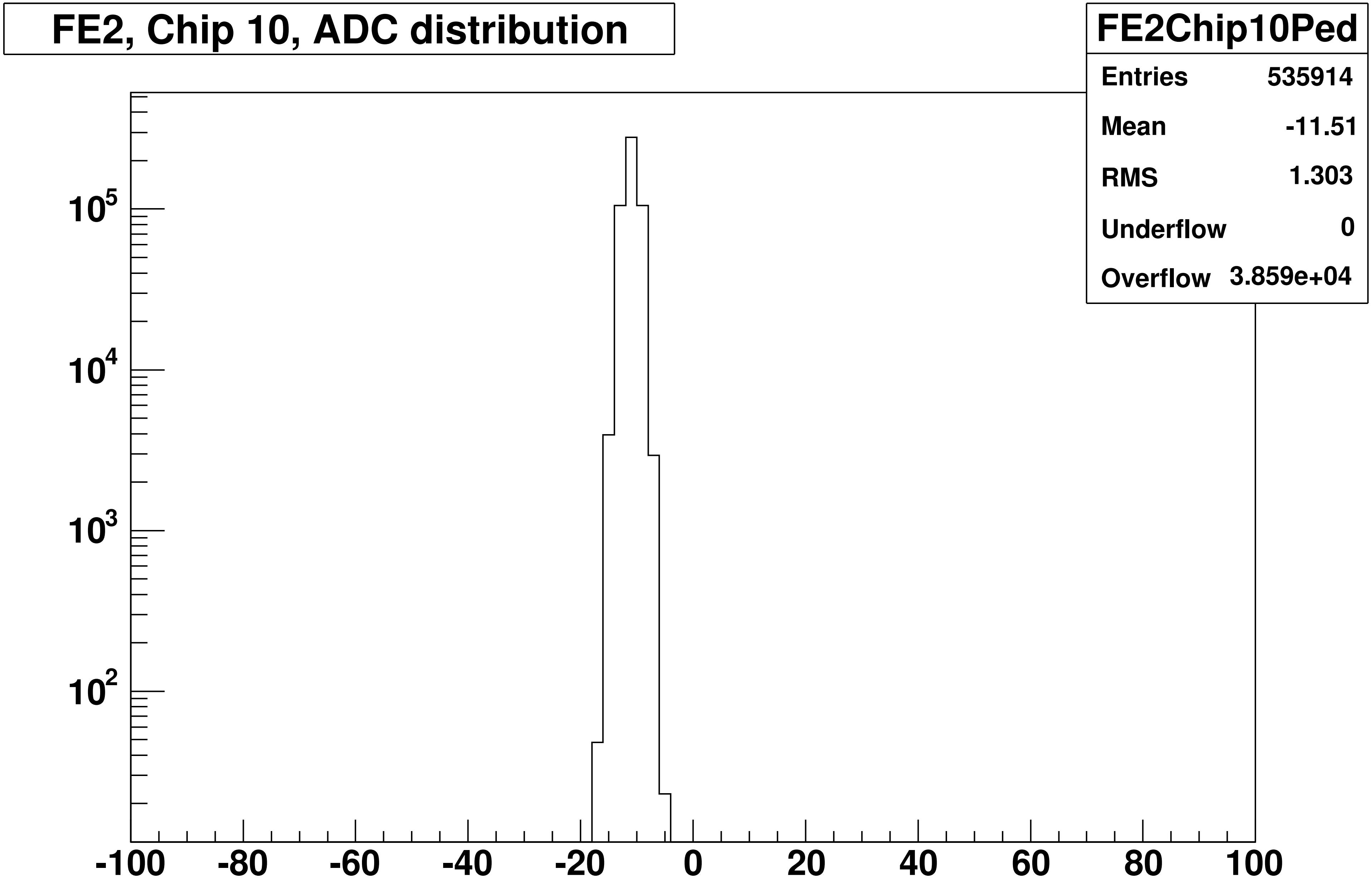

FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 535914
Mean 1346
RMS 4913
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

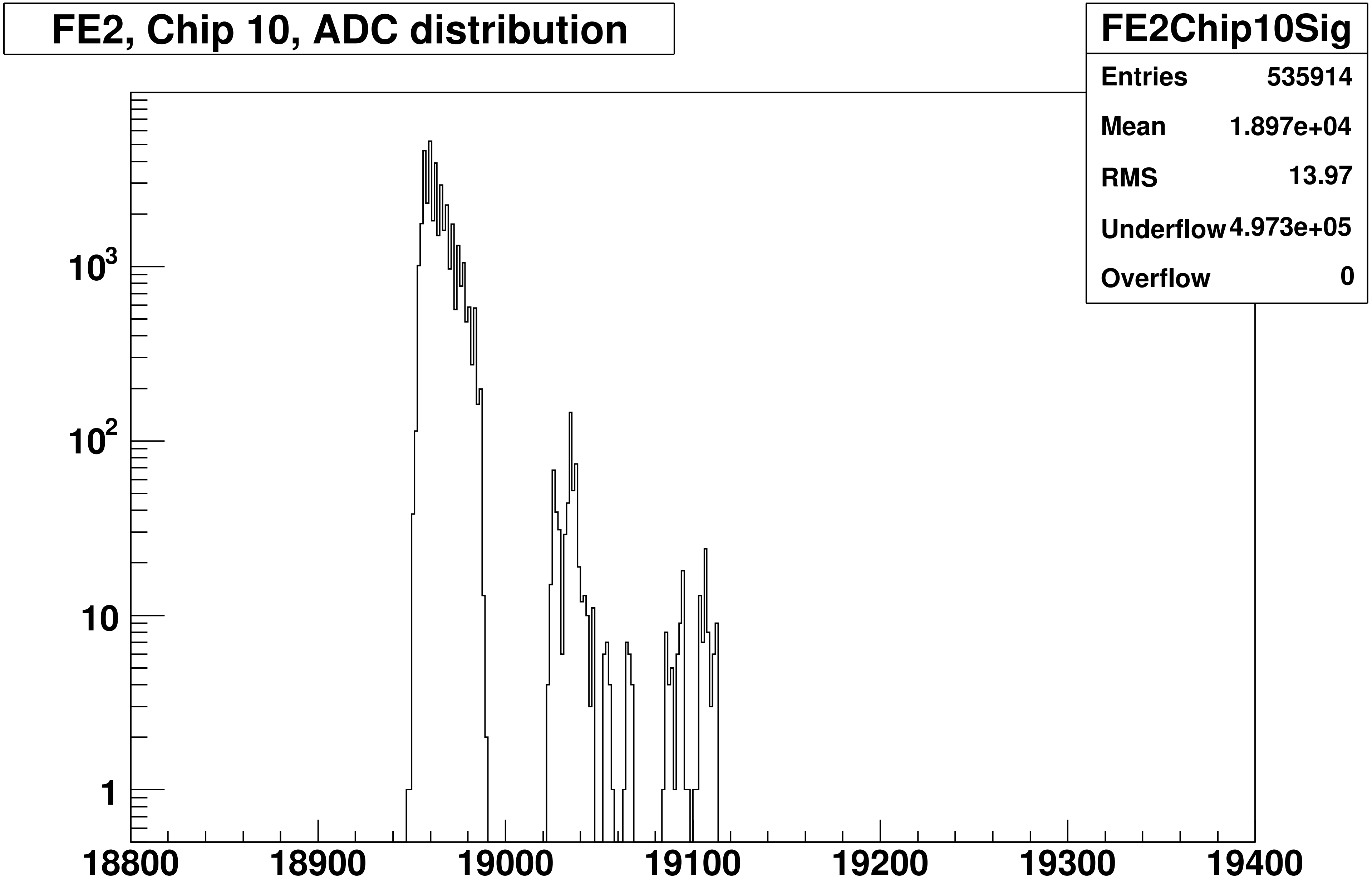

FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 535914
Mean -19.7
RMS 1.355
Underflow 0
Overflow 3.848e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

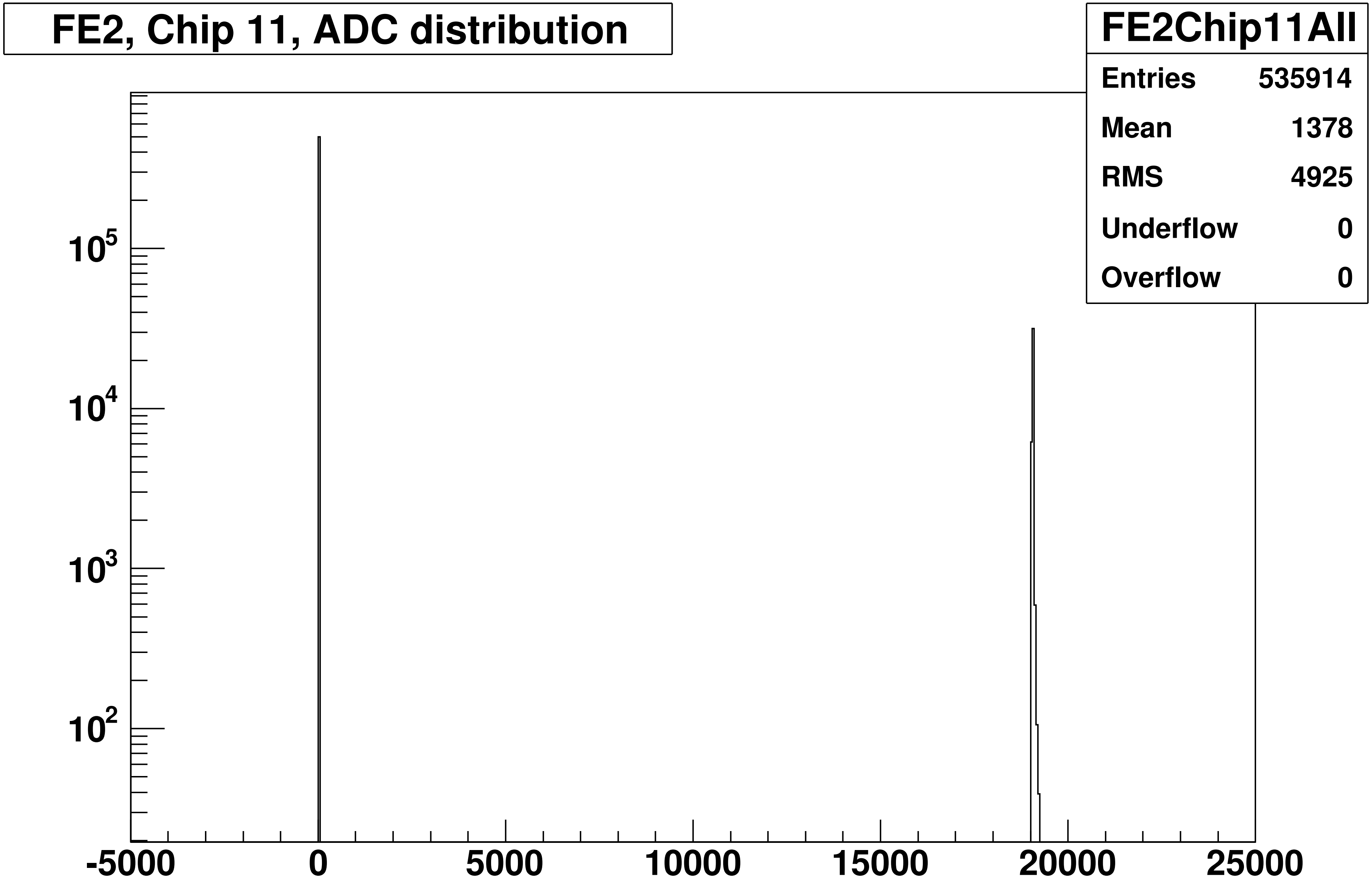


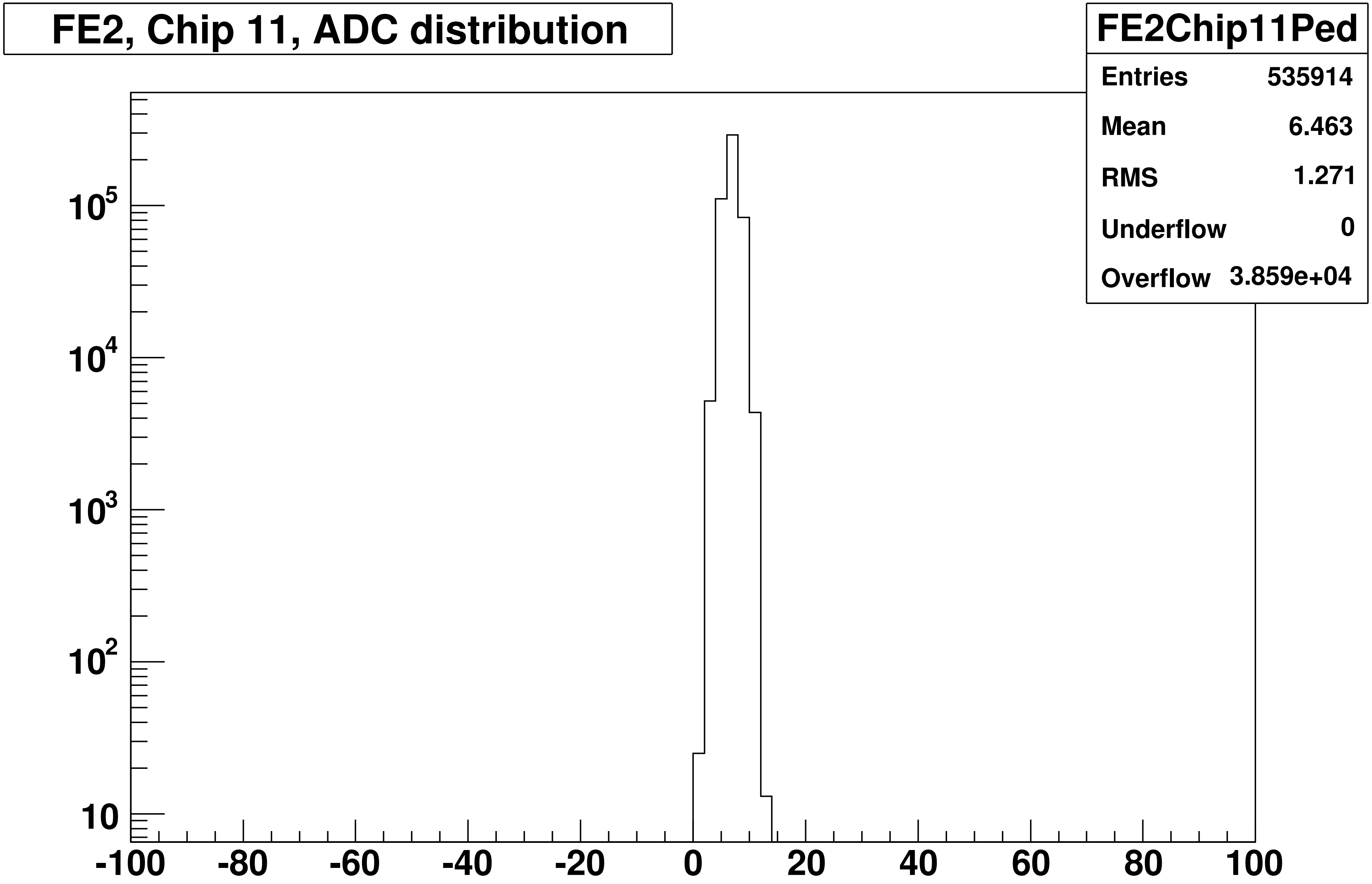
FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 535914
Mean 1.901e+04
RMS 11.5
Underflow 4.974e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

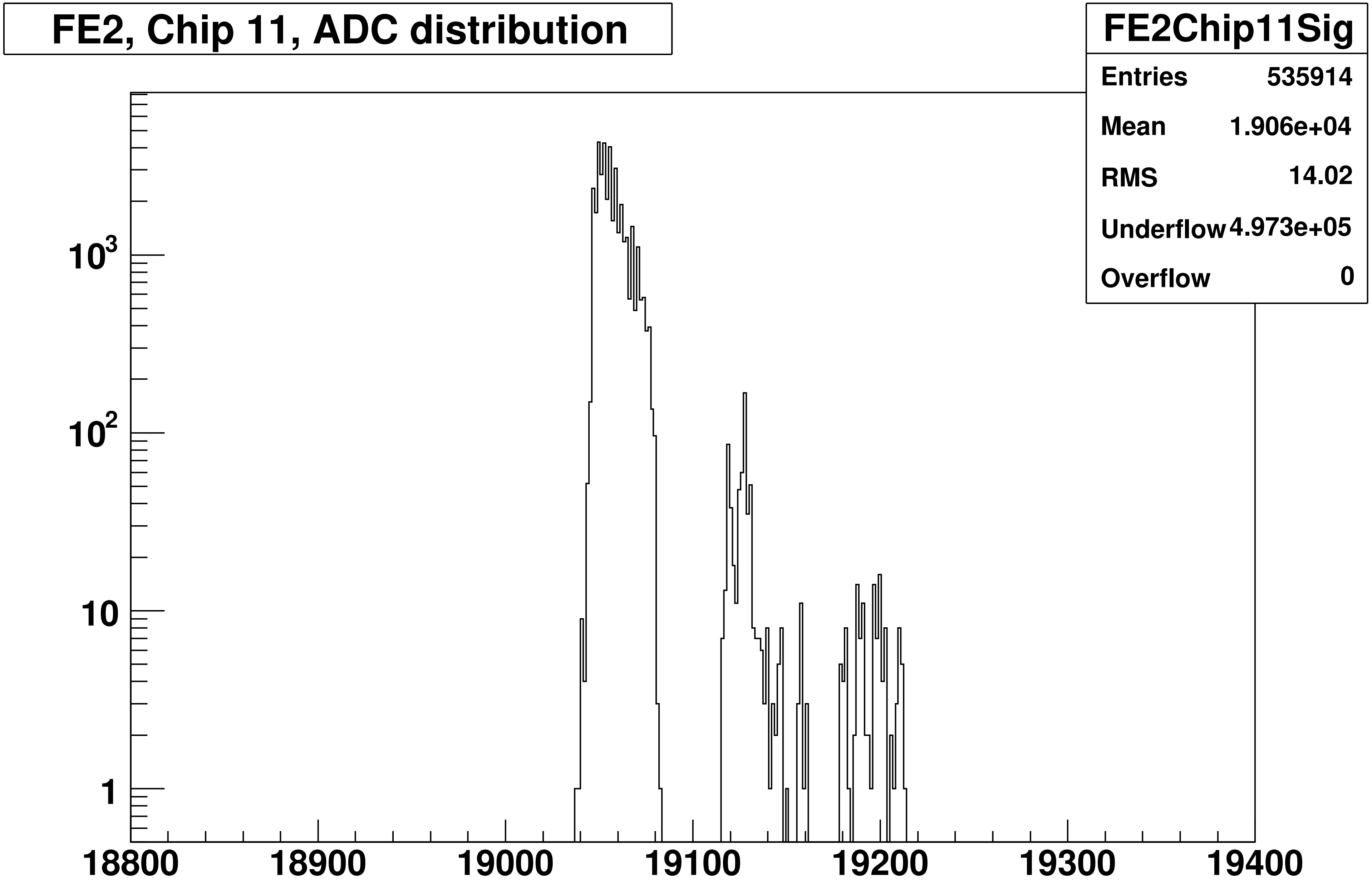

FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 535914
Mean 1355
RMS 4906
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 535914
Mean -11.51
RMS 1.303
Underflow 0
Overflow 3.859e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

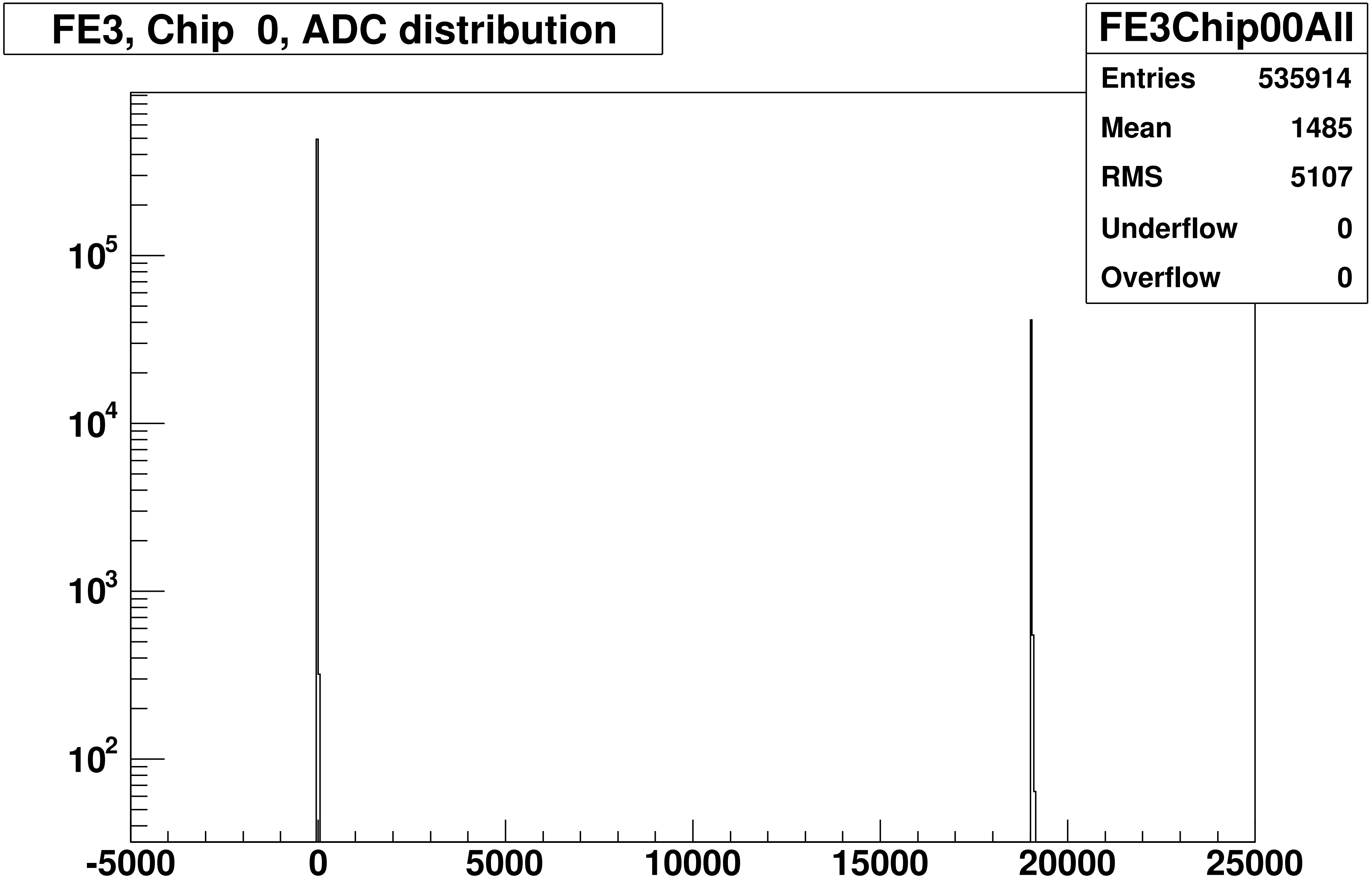

FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 535914
Mean 1.897e+04
RMS 13.97
Underflow 4.973e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 535914
Mean 1378
RMS 4925
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

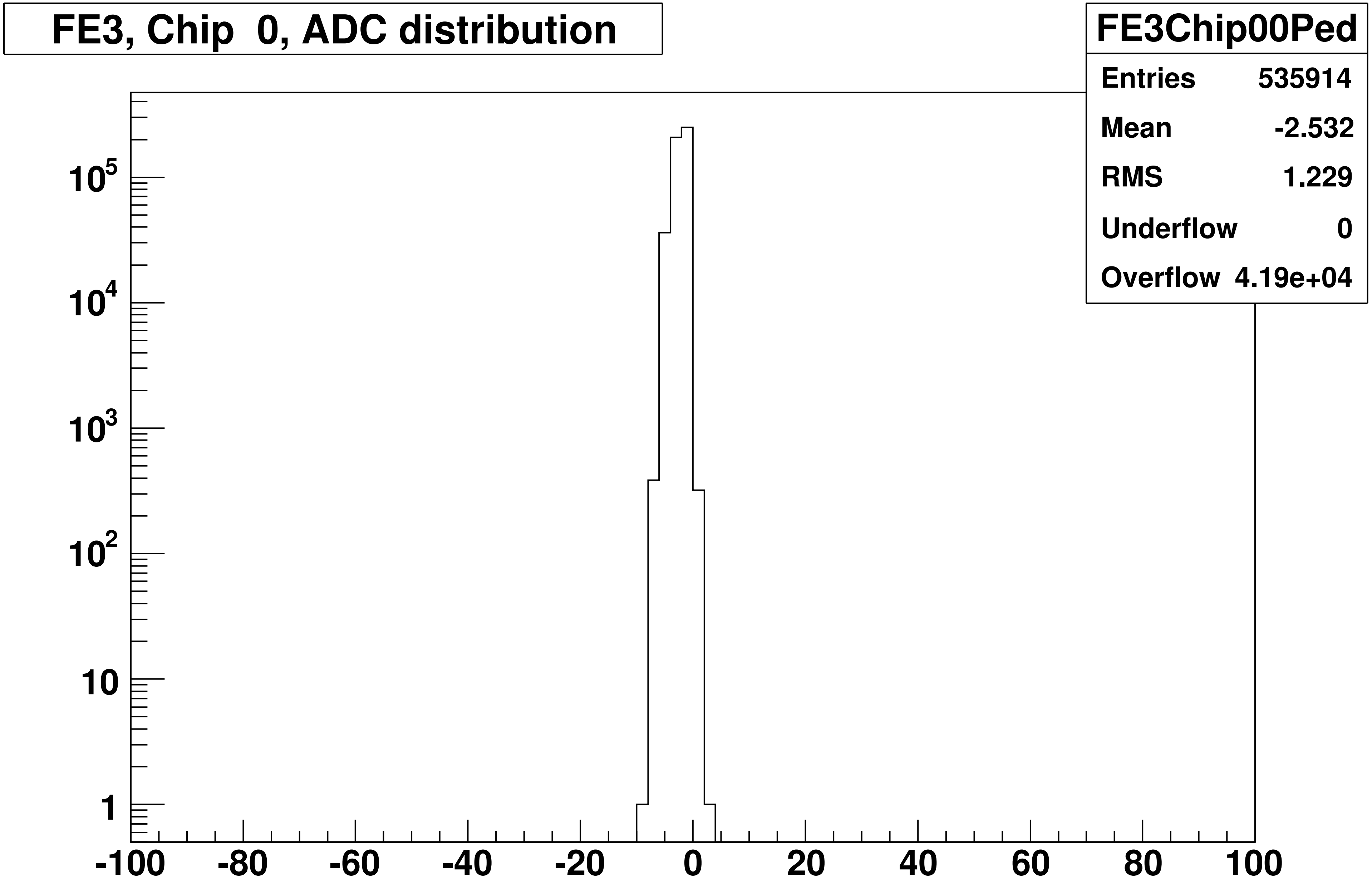

FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 535914
Mean 6.463
RMS 1.271
Underflow 0
Overflow 3.859e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

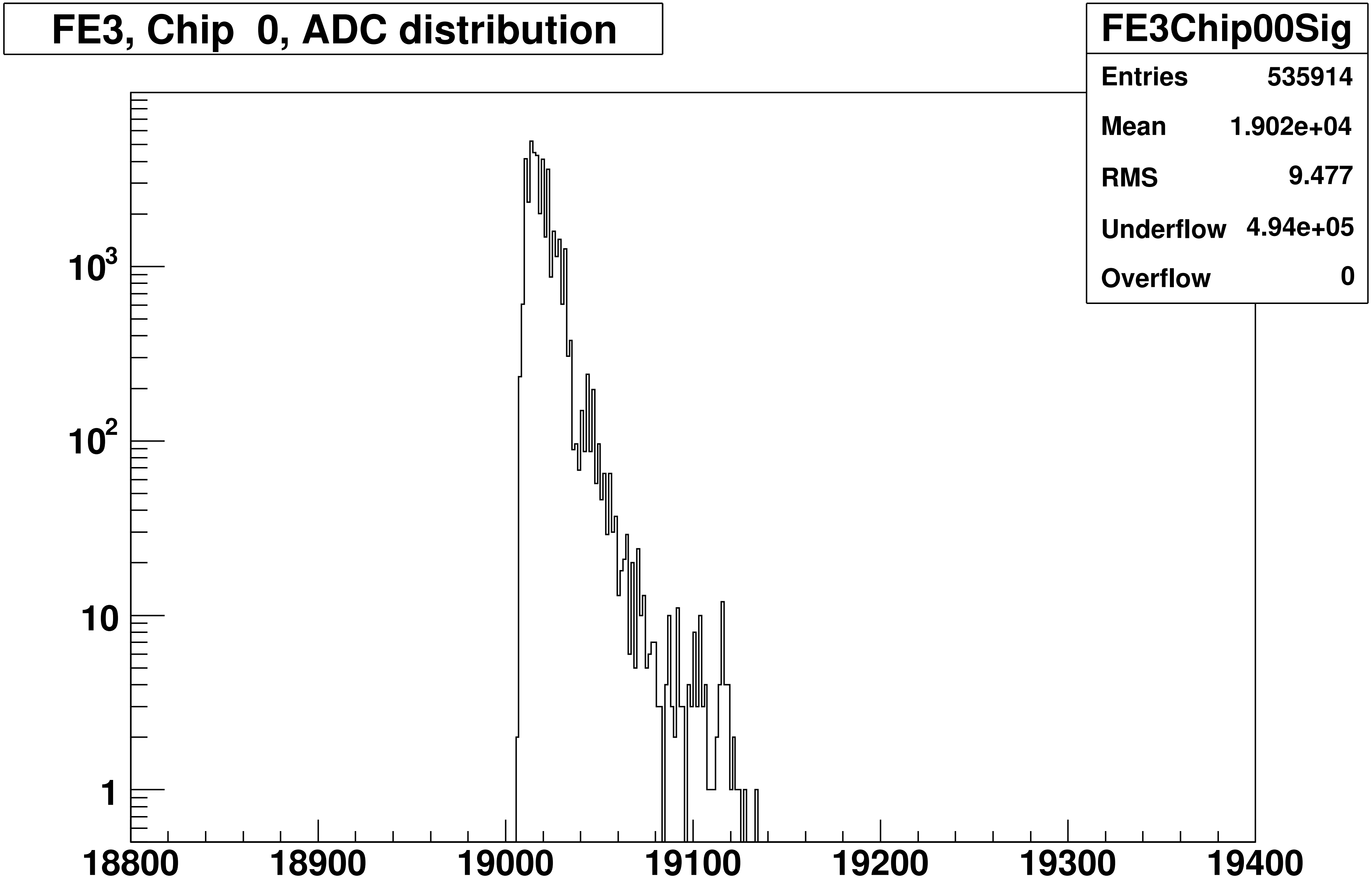


FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 535914
Mean 1.906e+04
RMS 14.02
Underflow 4.973e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

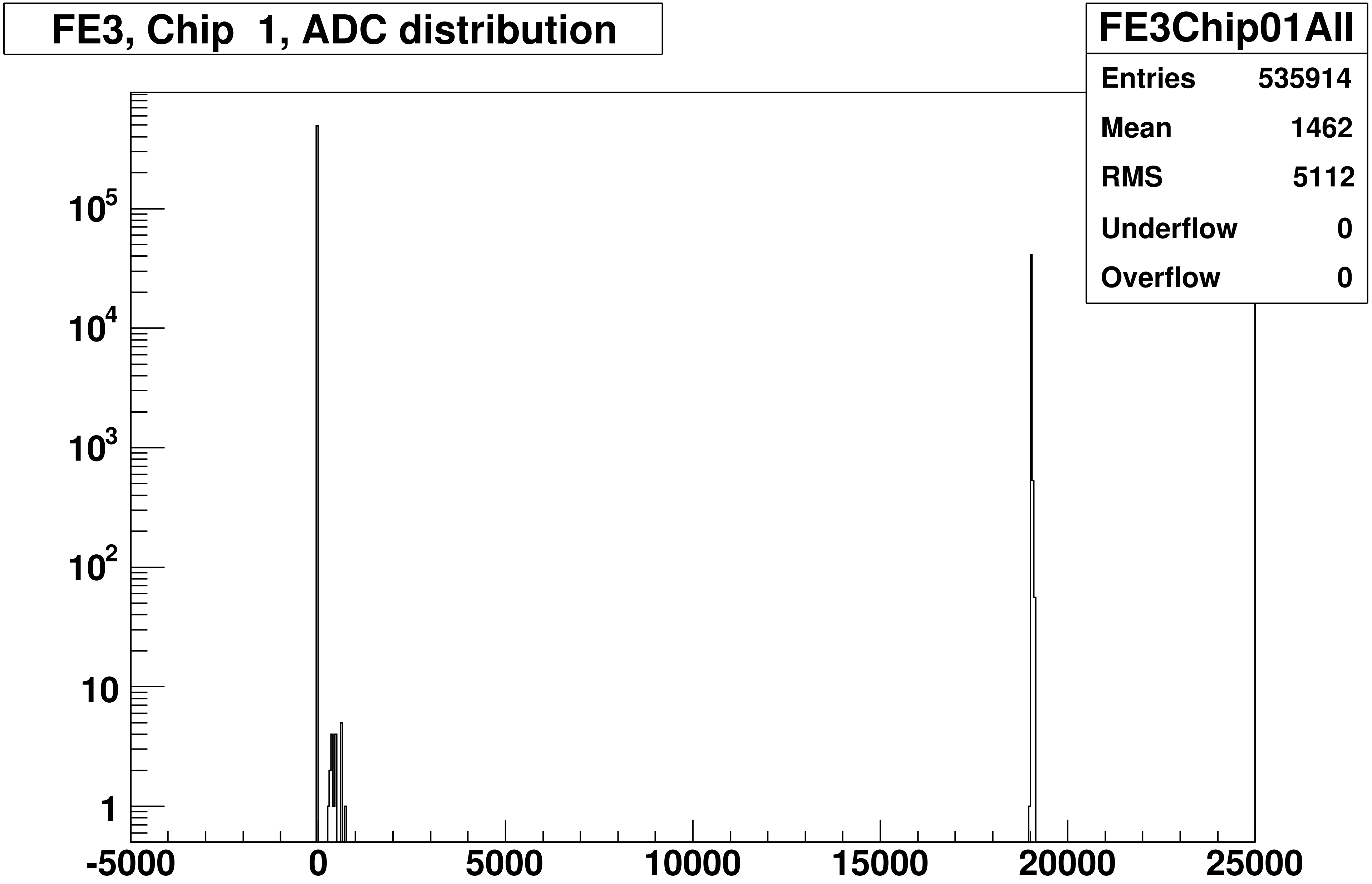


FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 535914
Mean 1485
RMS 5107
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

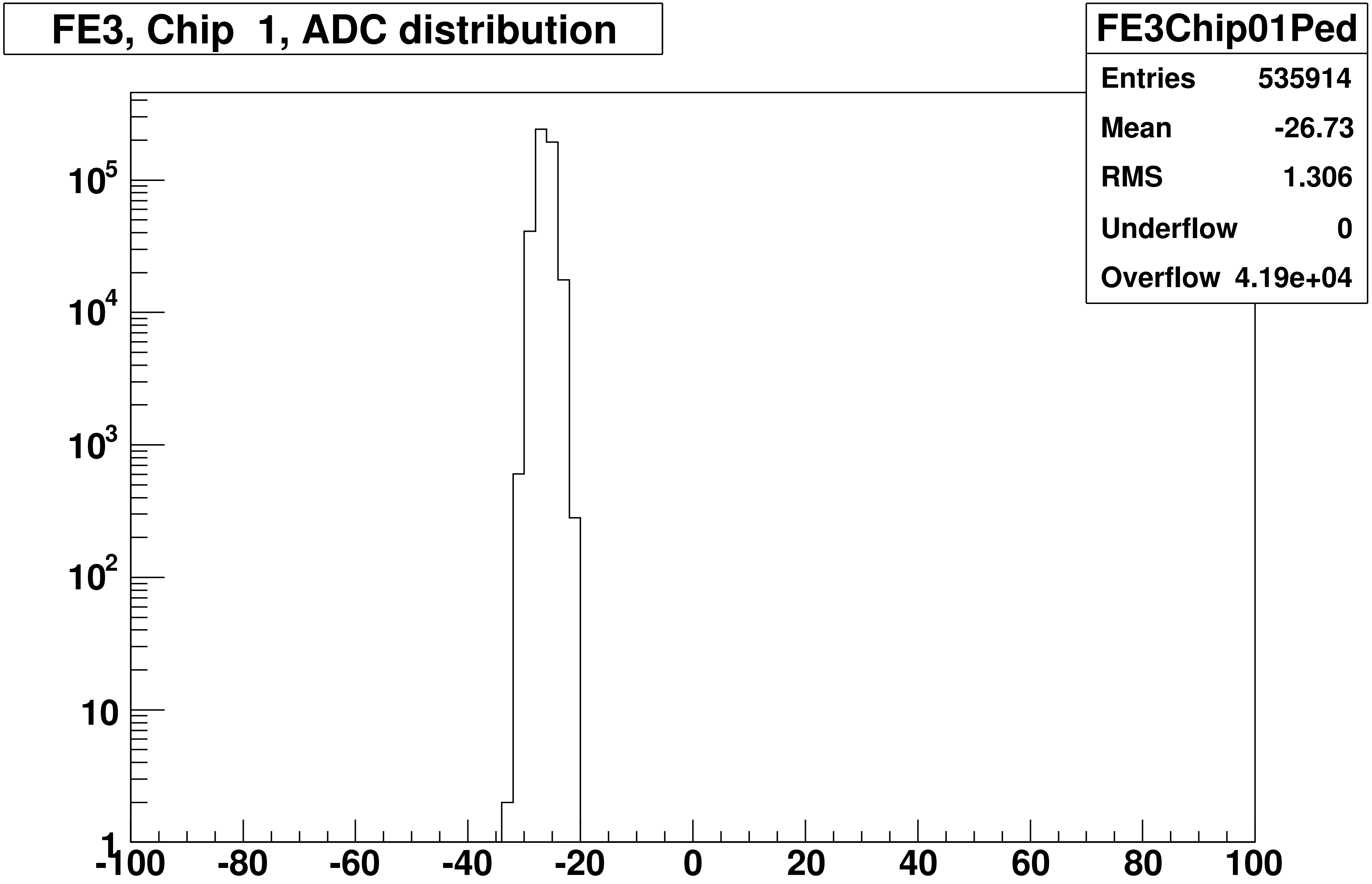

FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 535914
Mean -2.532
RMS 1.229
Underflow 0
Overflow 4.19e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



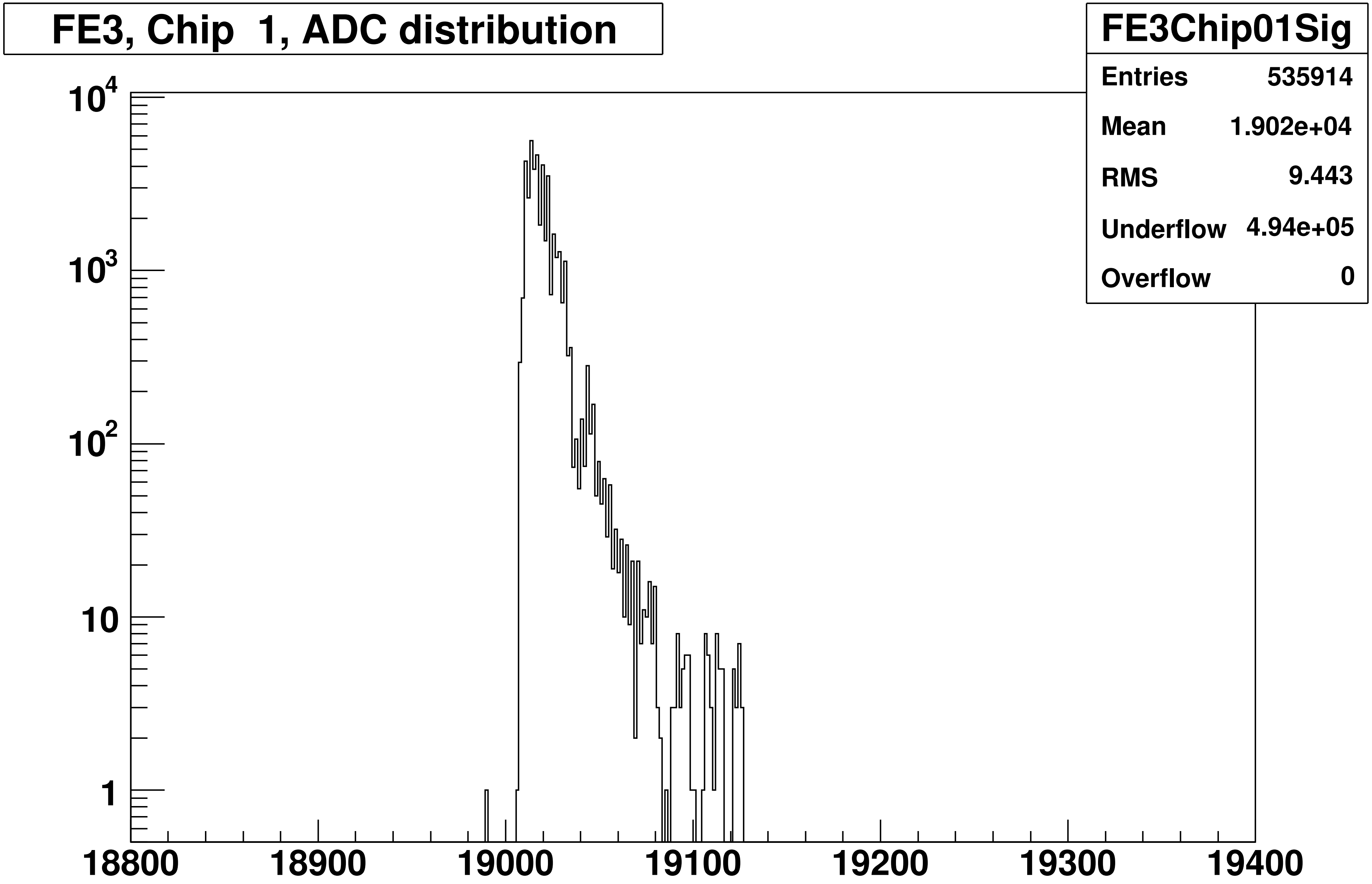
FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 535914
Mean 1.902e+04
RMS 9.477
Underflow 4.94e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



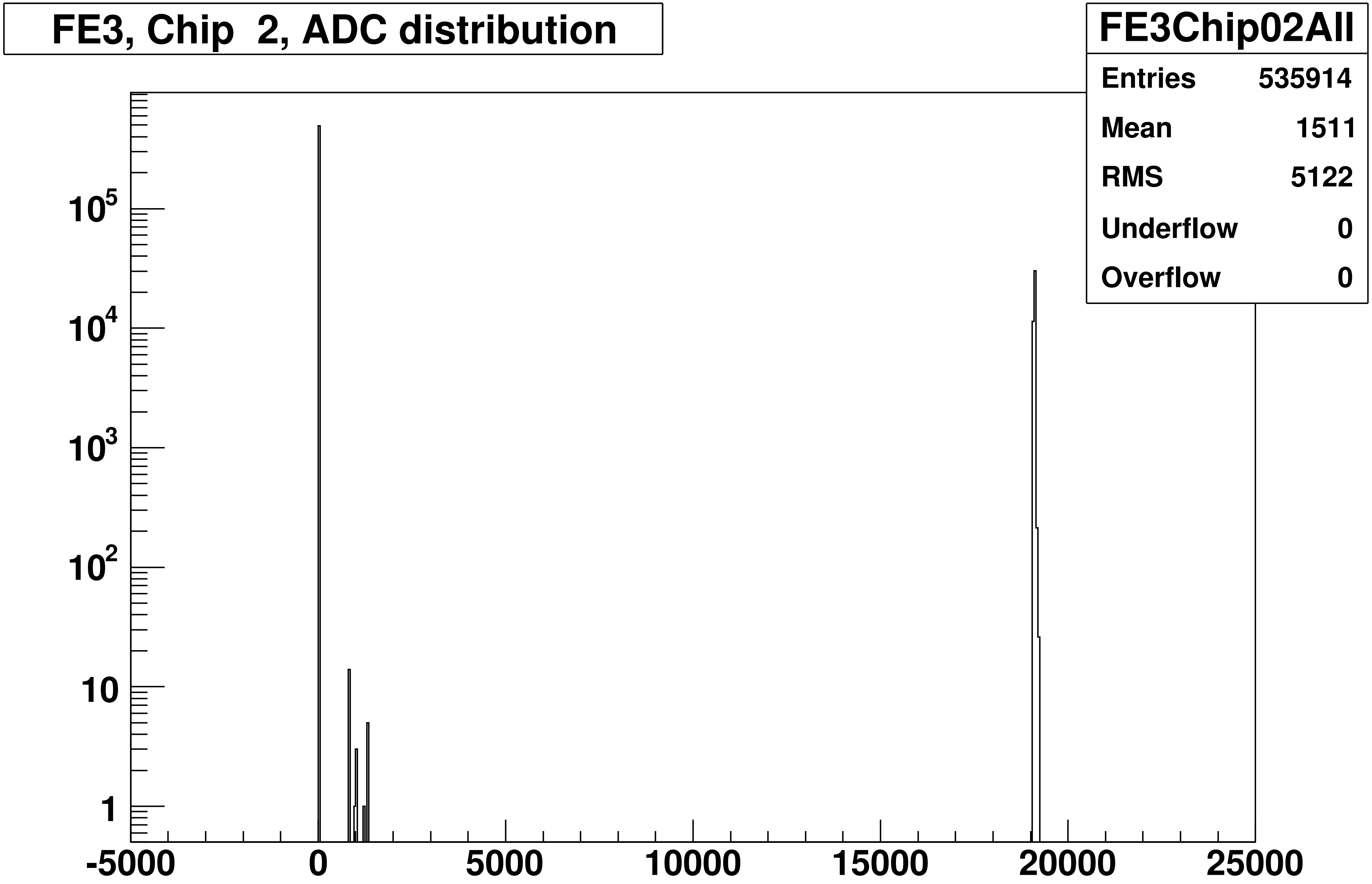
FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 535914
Mean 1462
RMS 5112
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

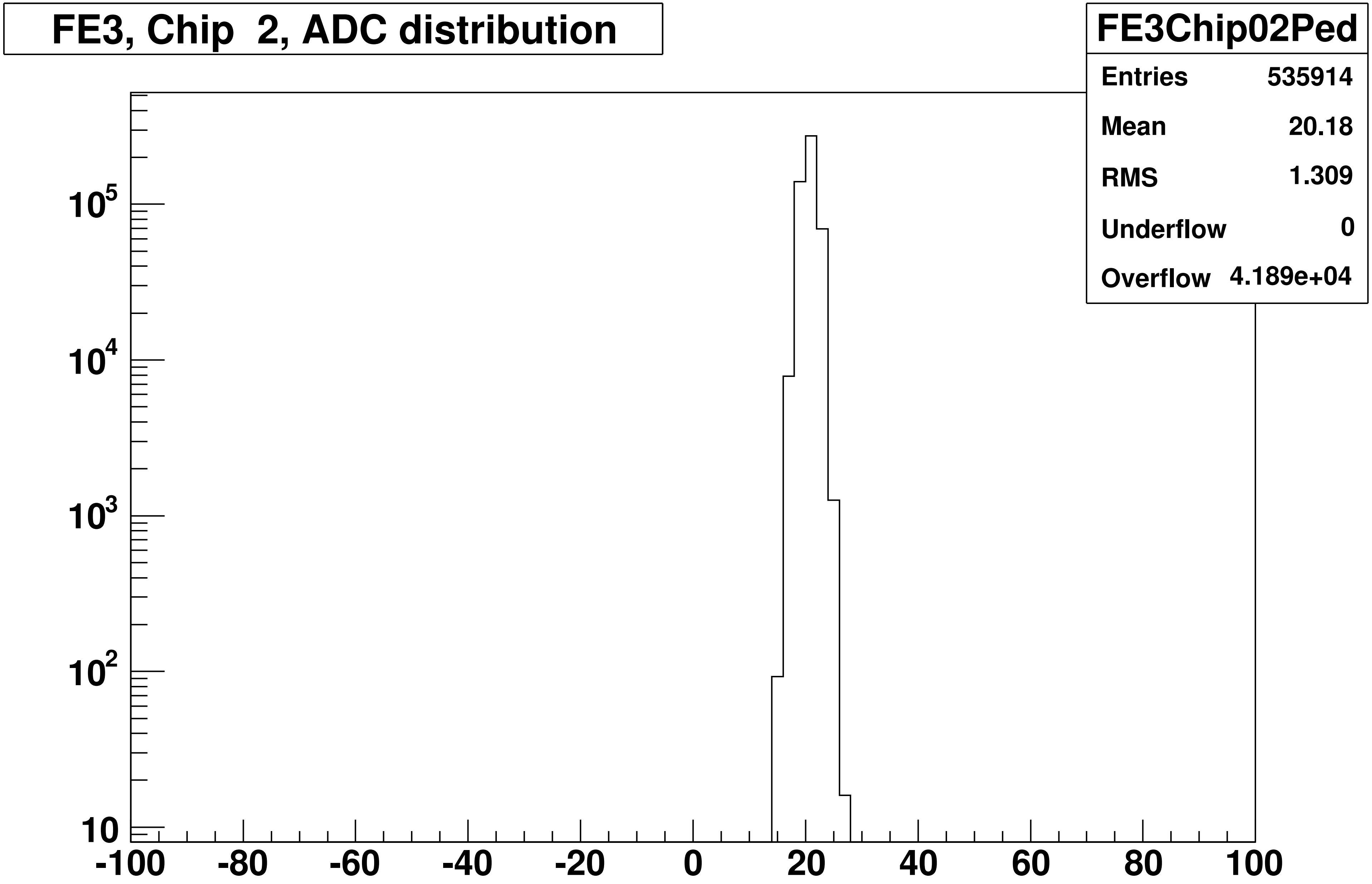


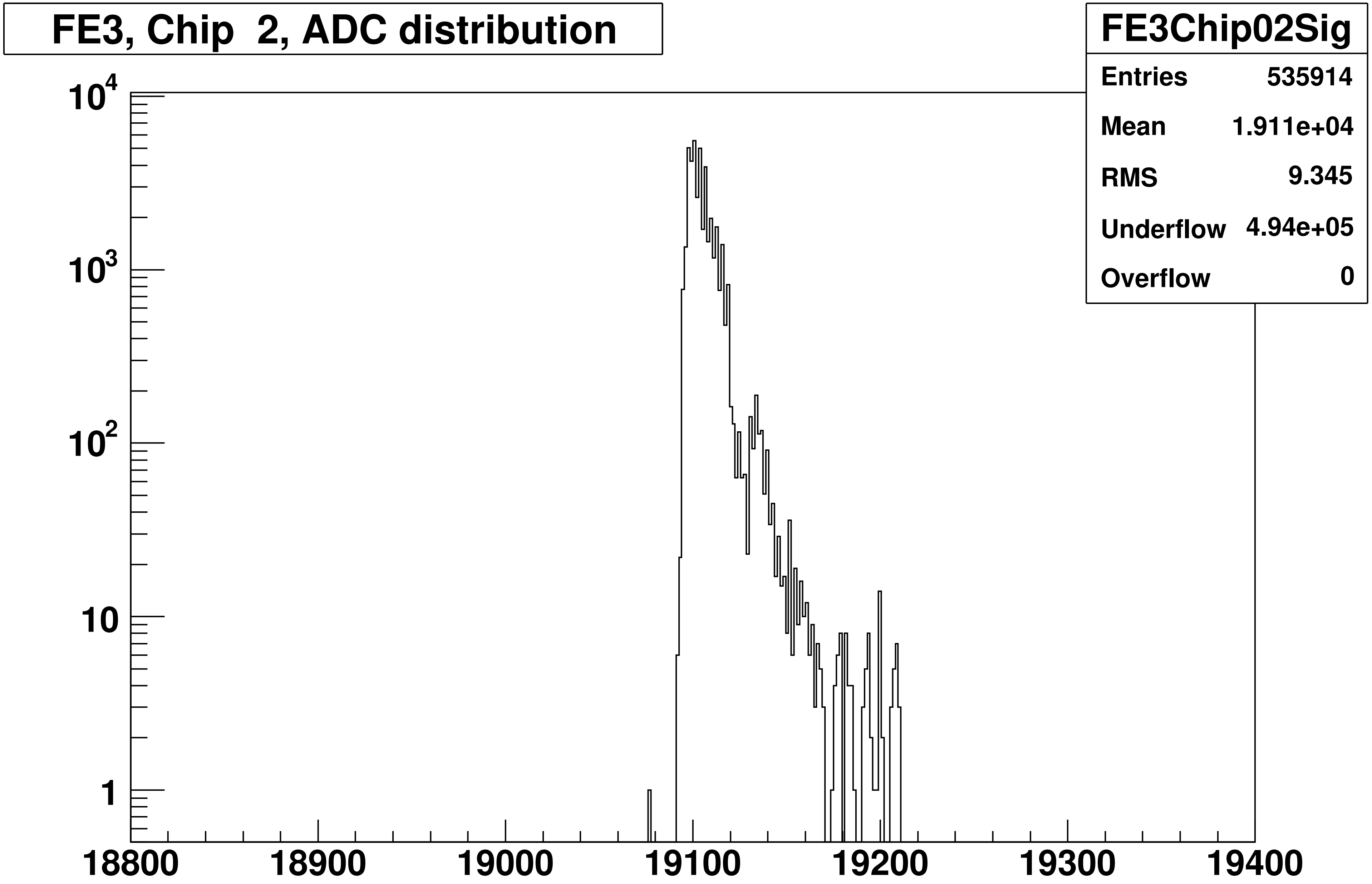
FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 535914
Mean -26.73
RMS 1.306
Underflow 0
Overflow 4.19e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

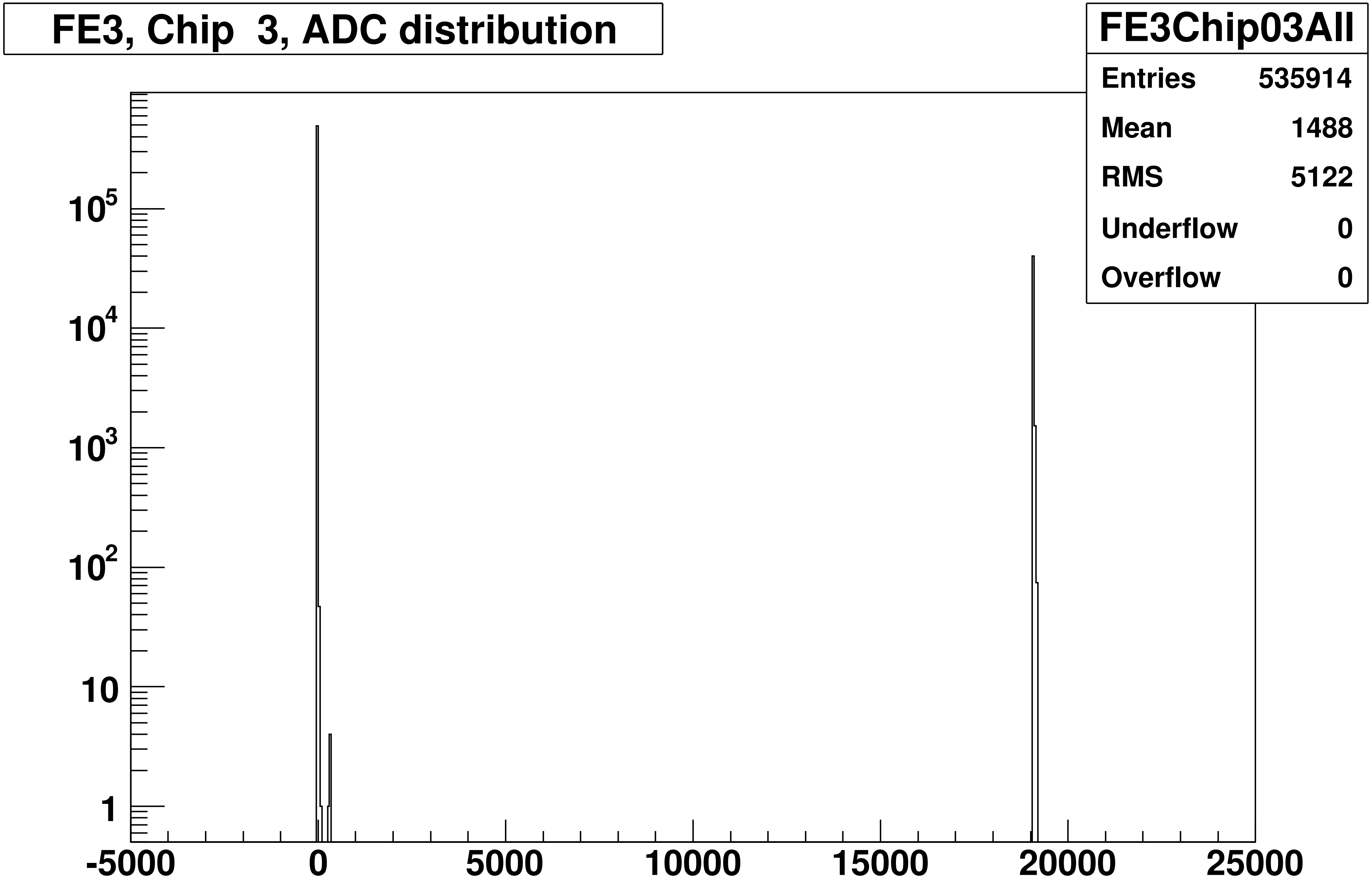


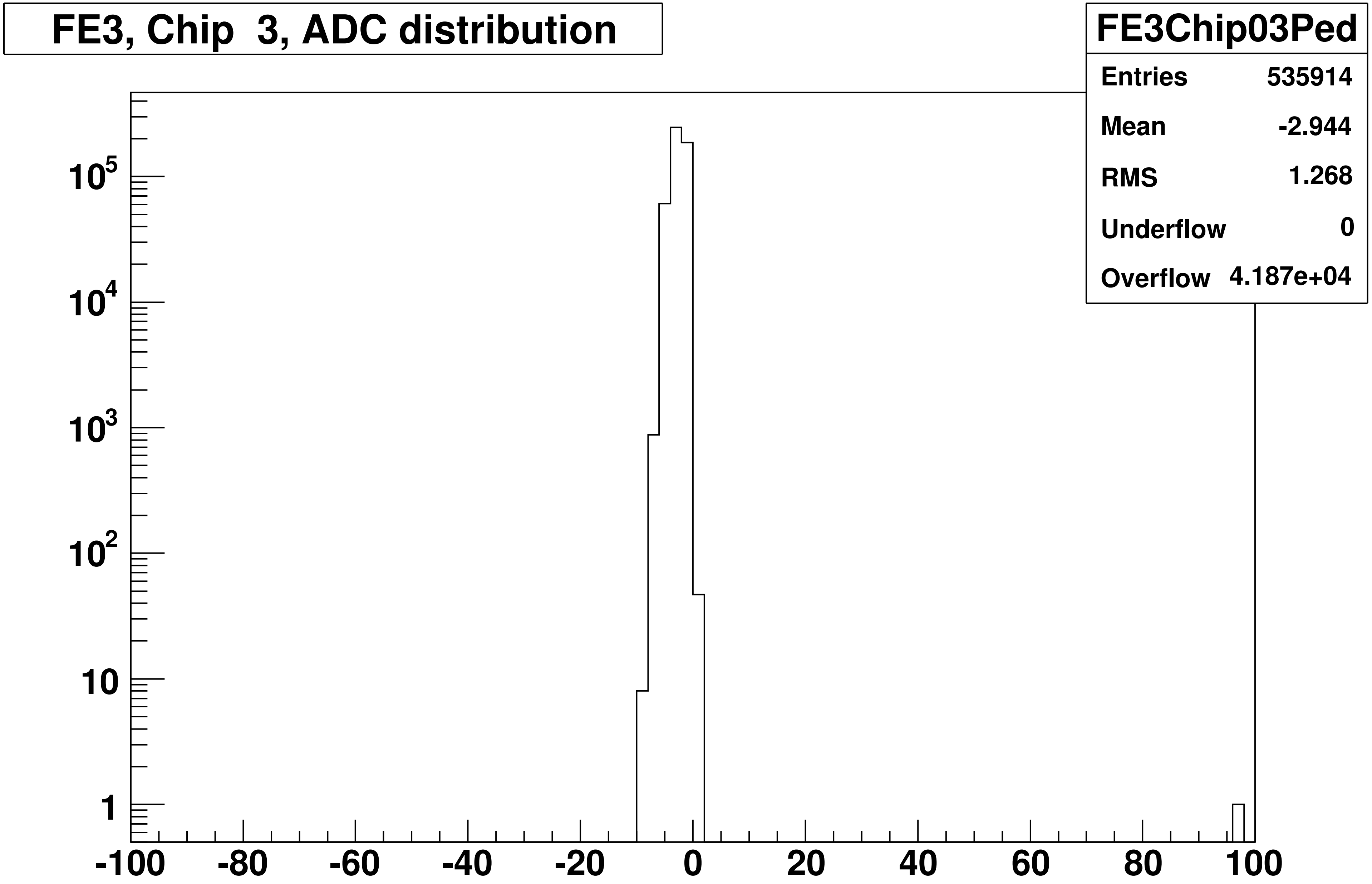
FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 535914
Mean 1.902e+04
RMS 9.443
Underflow 4.94e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

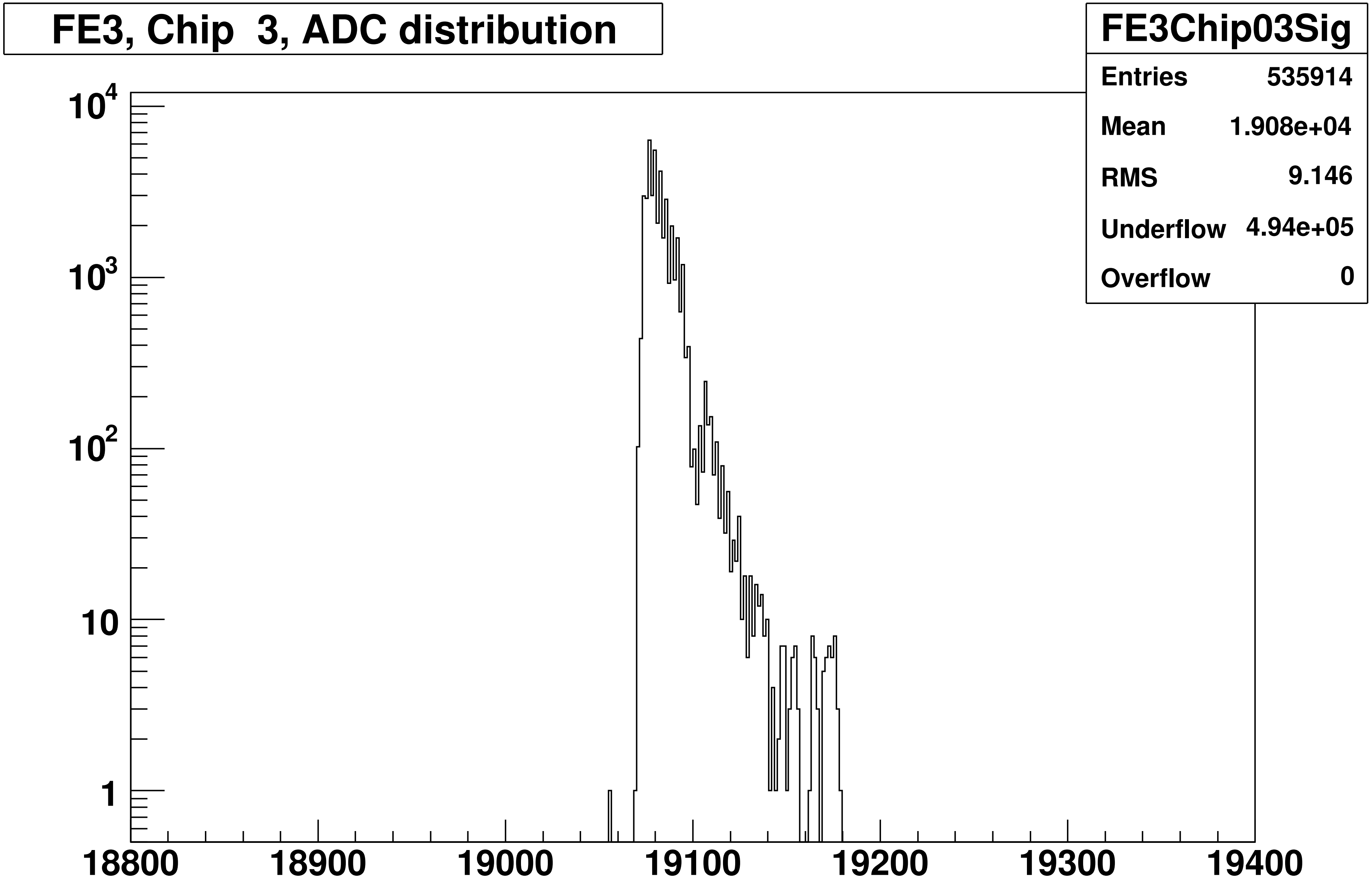

FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 535914
Mean 1511
RMS 5122
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 535914
Mean 20.18
RMS 1.309
Underflow 0
Overflow 4.189e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

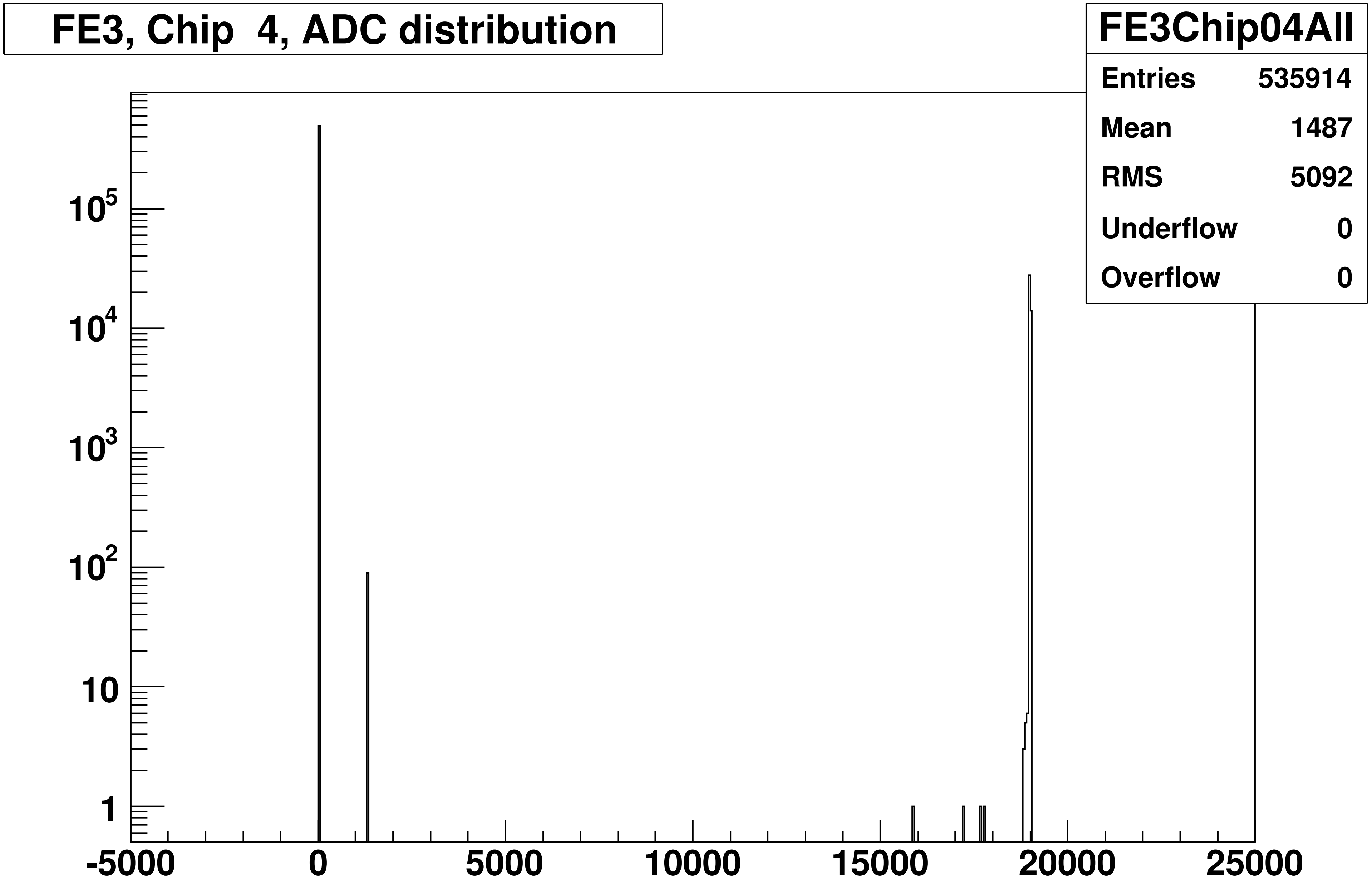

FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 535914
Mean 1.911e+04
RMS 9.345
Underflow 4.94e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 535914
Mean 1488
RMS 5122
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

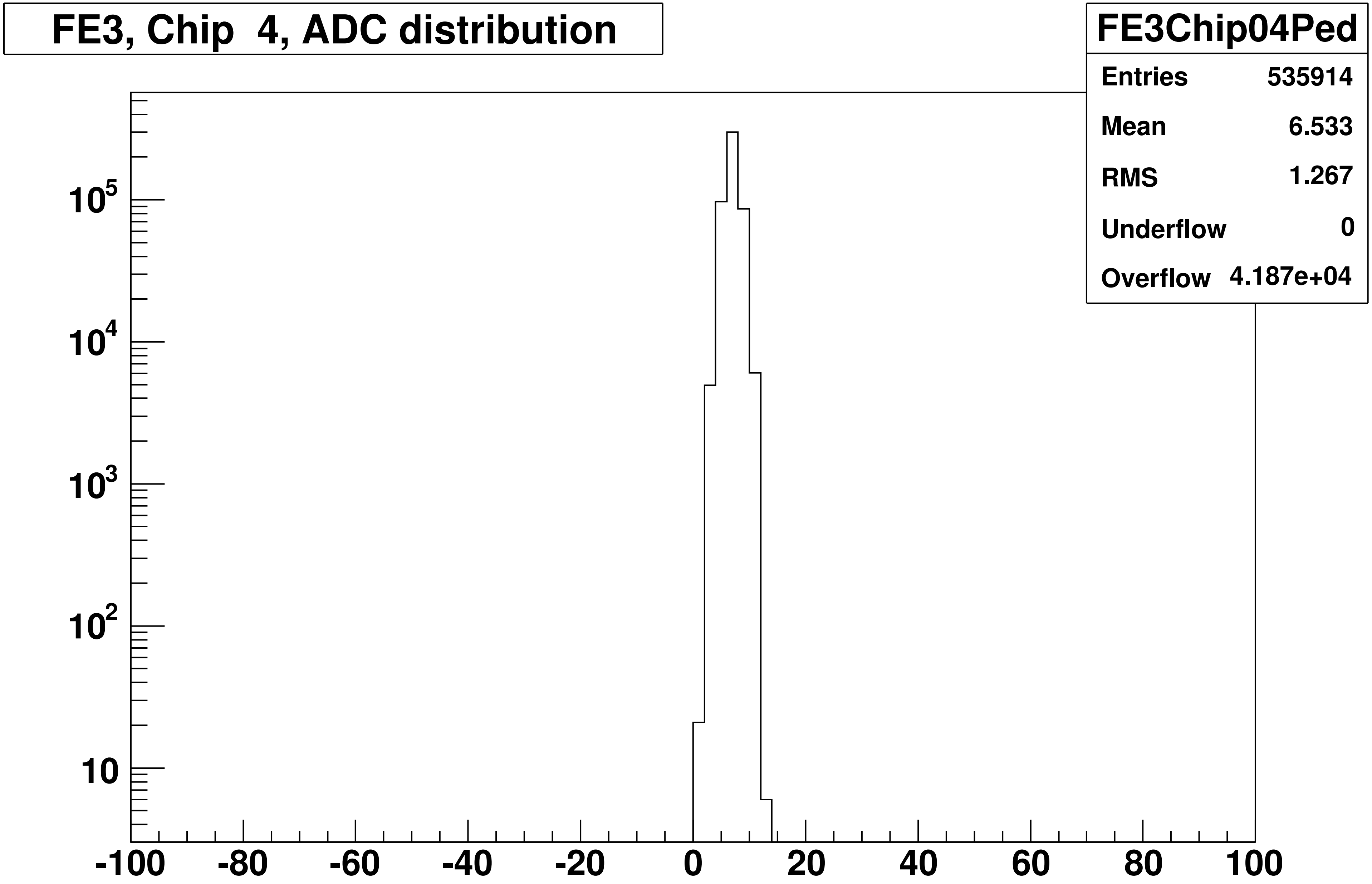

FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 535914
Mean -2.944
RMS 1.268
Underflow 0
Overflow 4.187e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



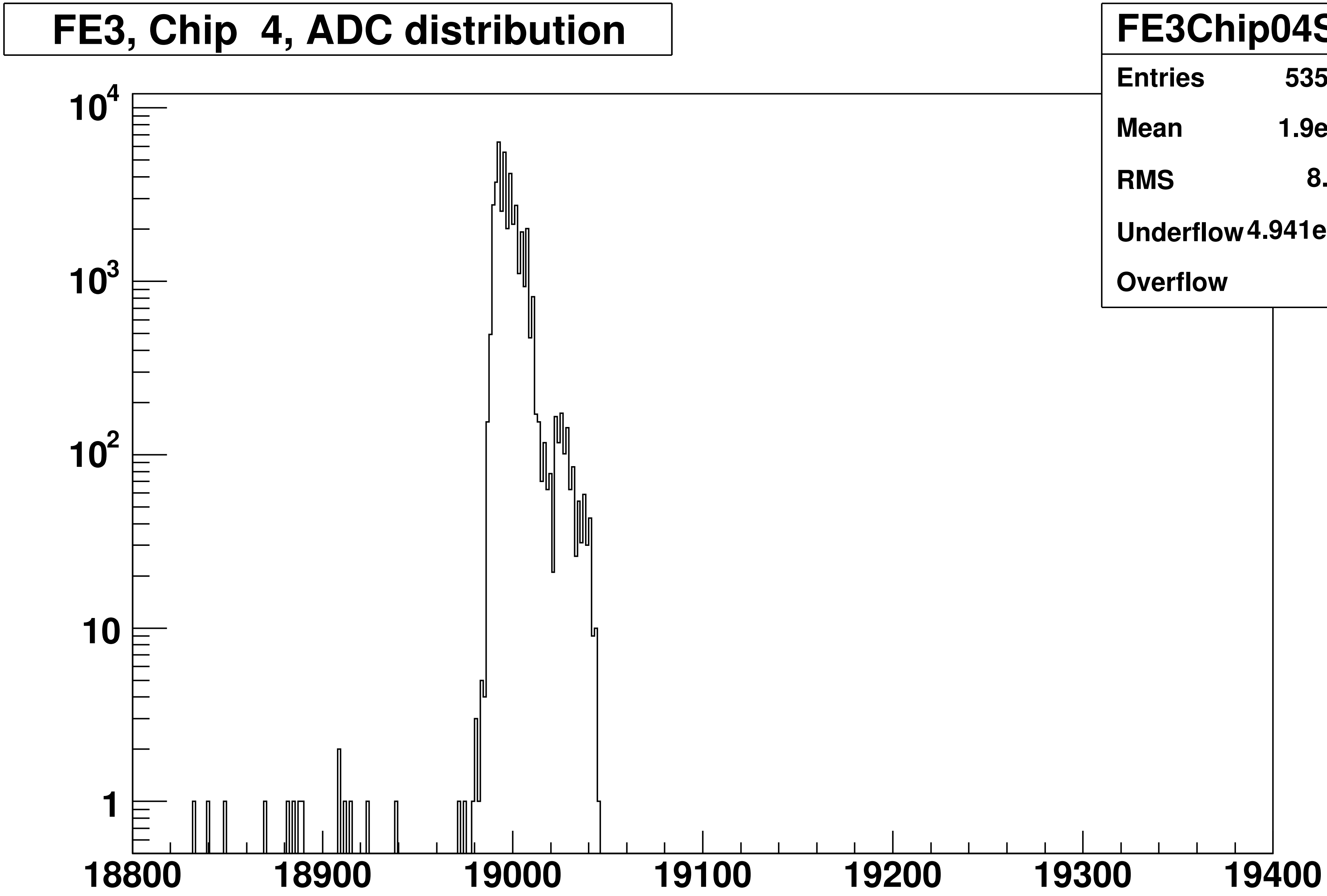
FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 535914
Mean 1.908e+04
RMS 9.146
Underflow 4.94e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

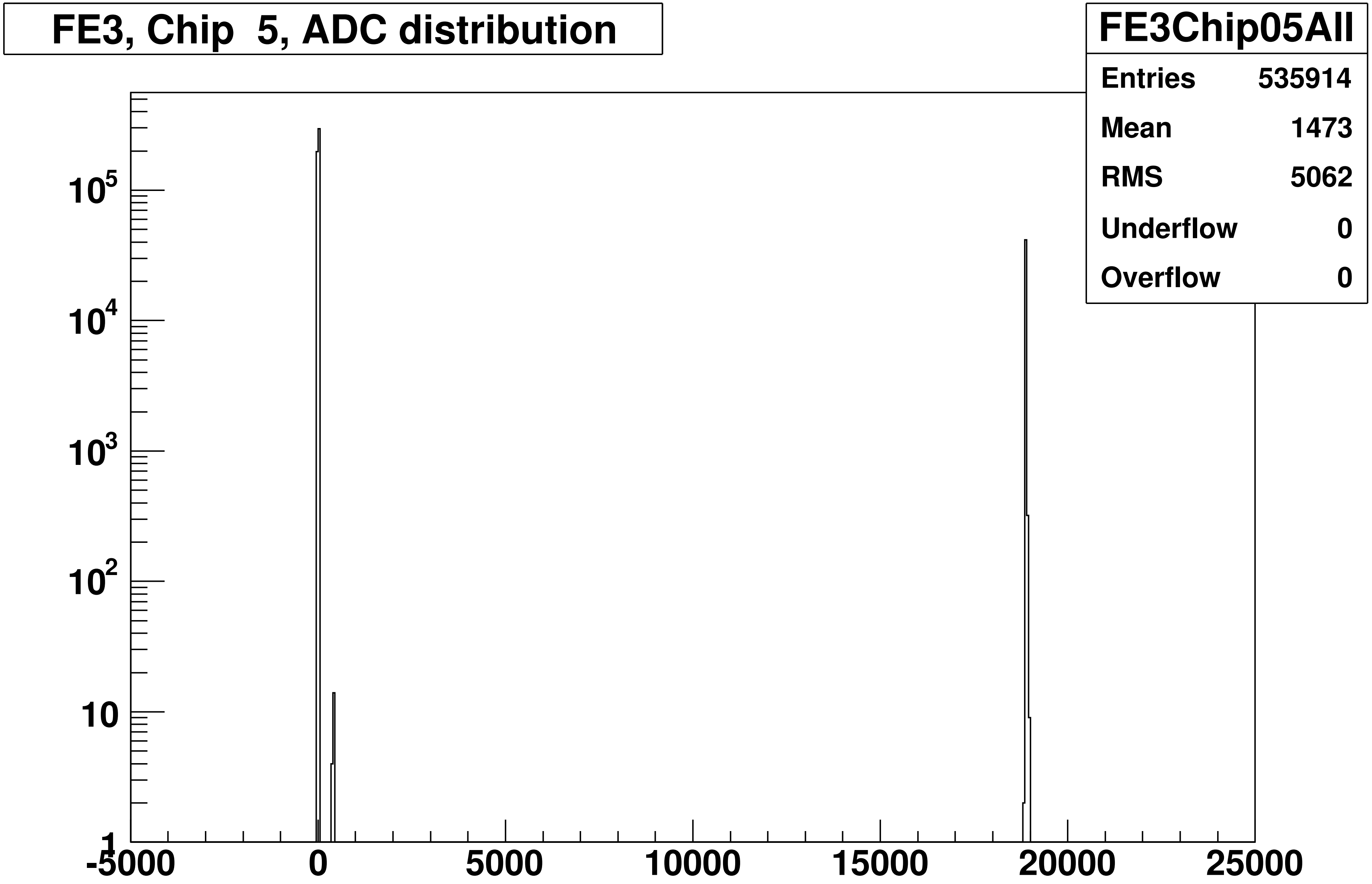


FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 535914
Mean 1487
RMS 5092
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

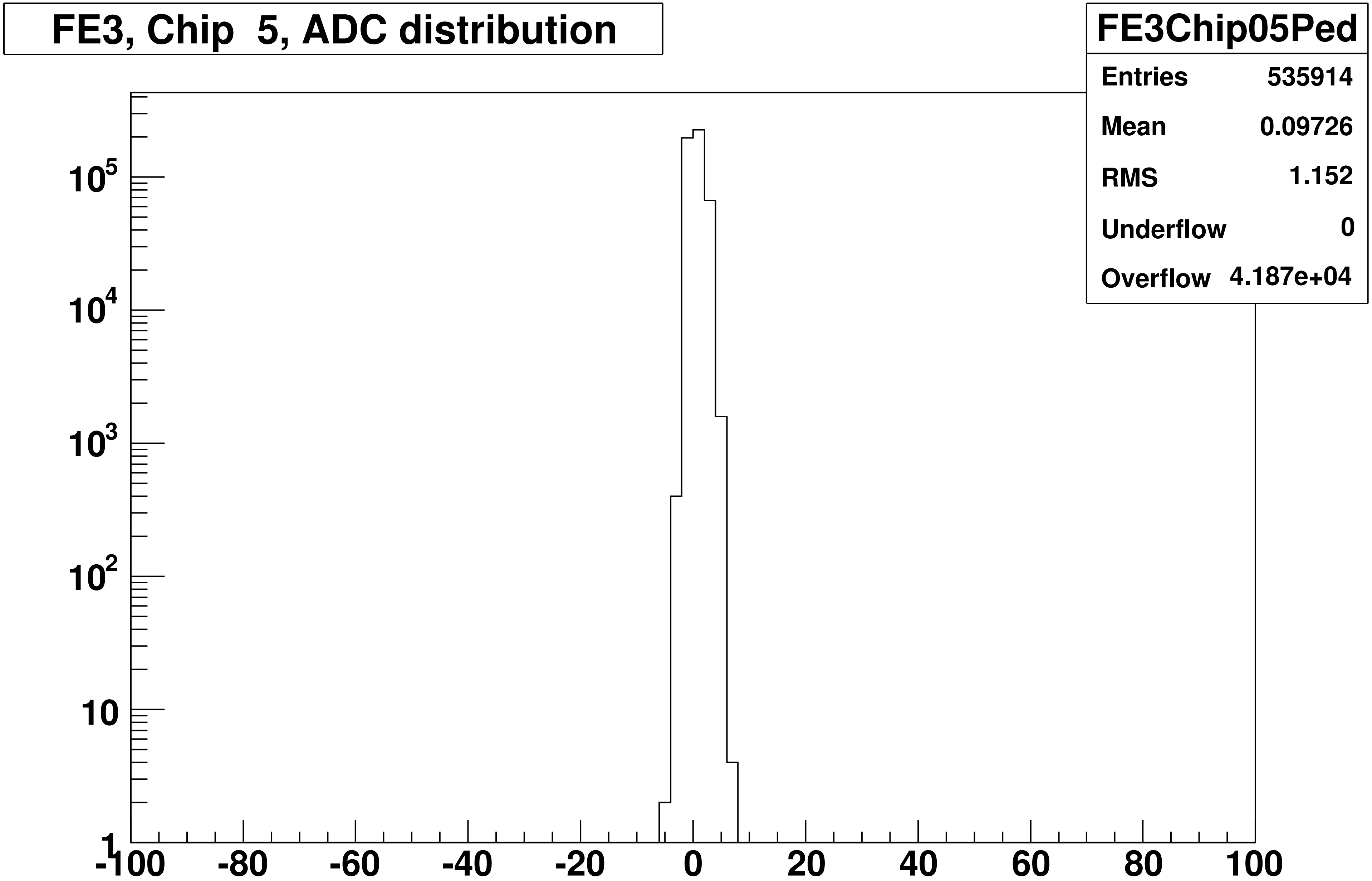


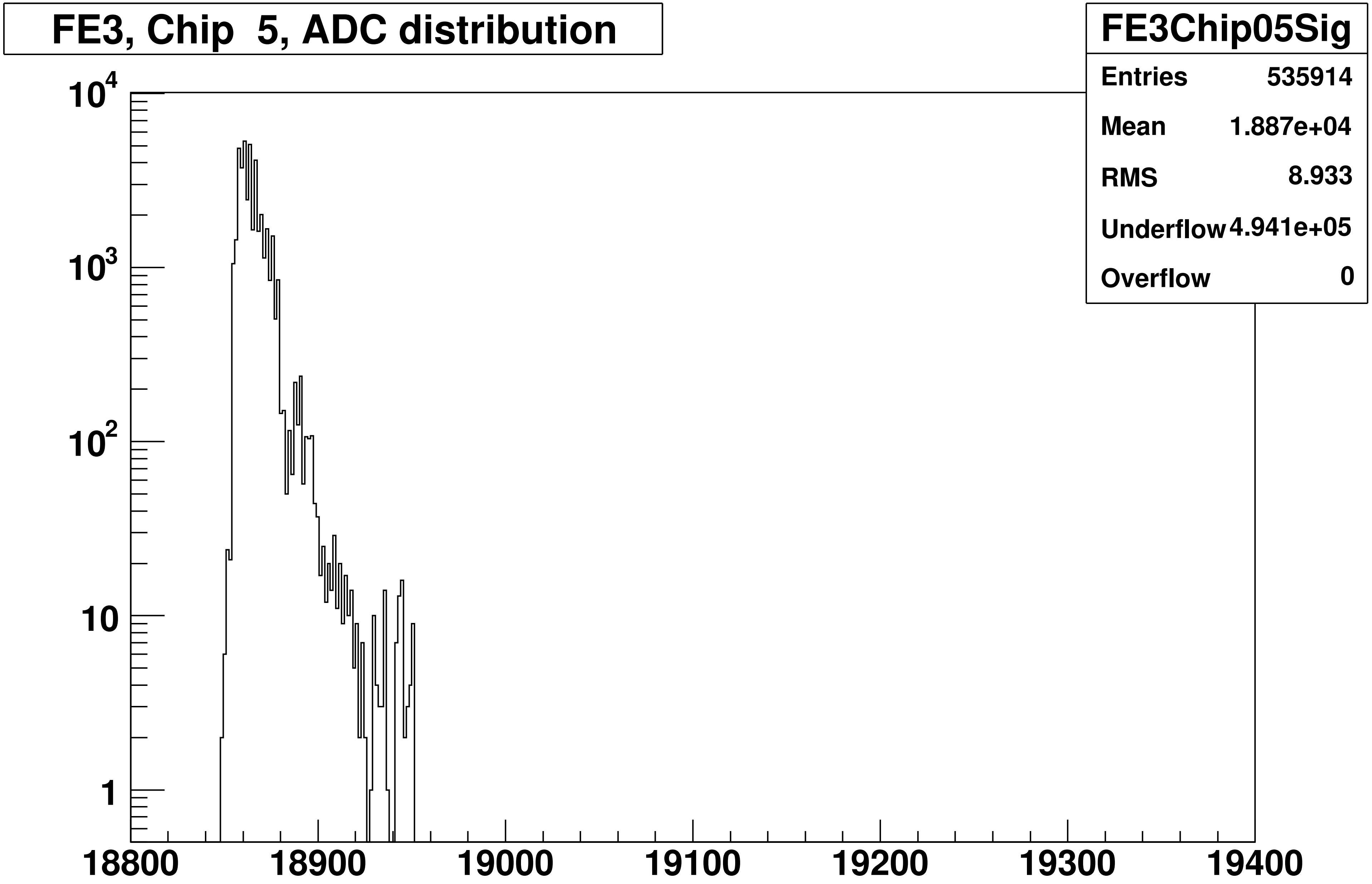
FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 535914
Mean 6.533
RMS 1.267
Underflow 0
Overflow 4.187e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 535914
Mean 1.9e+04
RMS 8.163
Underflow 4.941e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

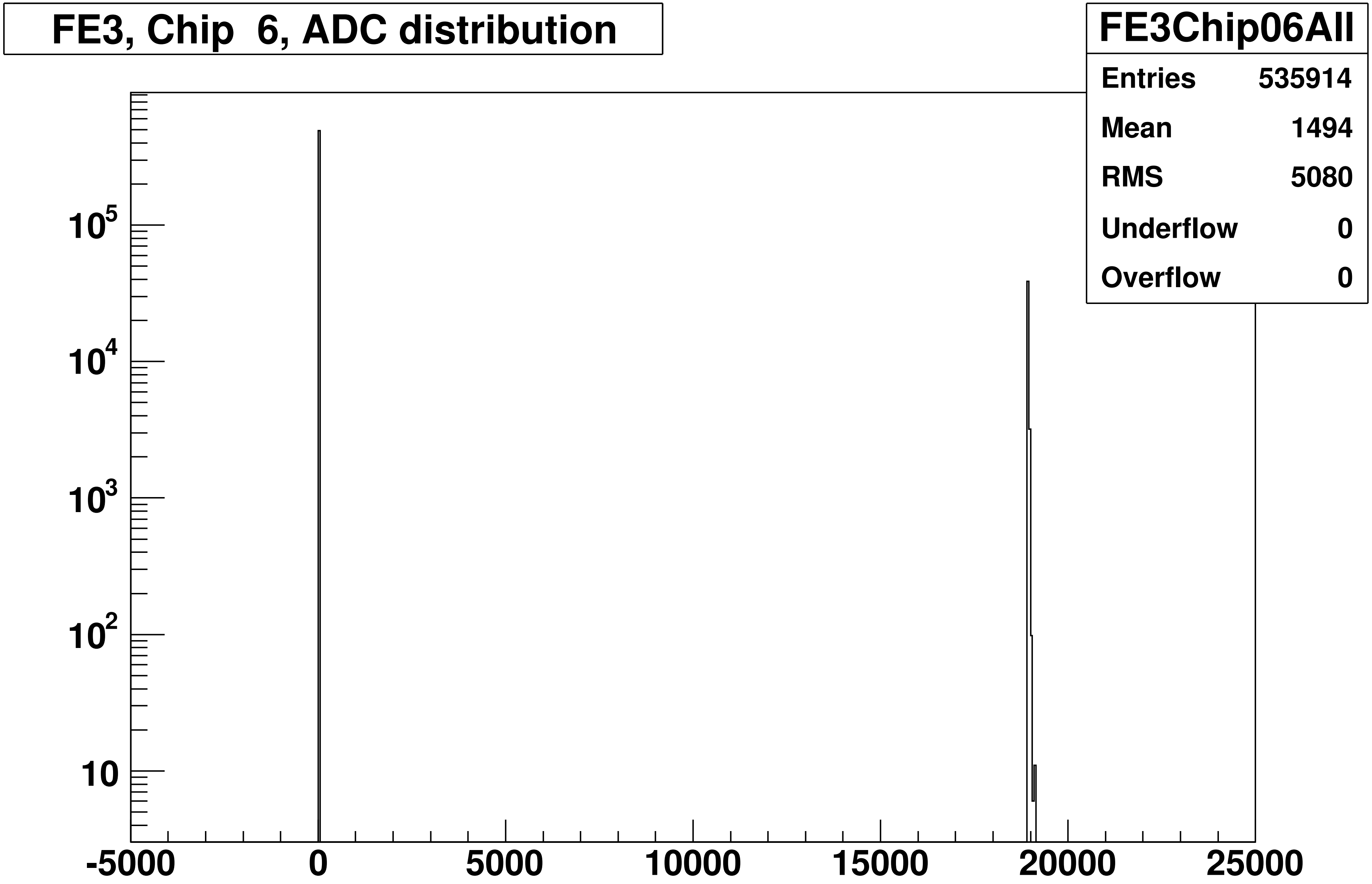


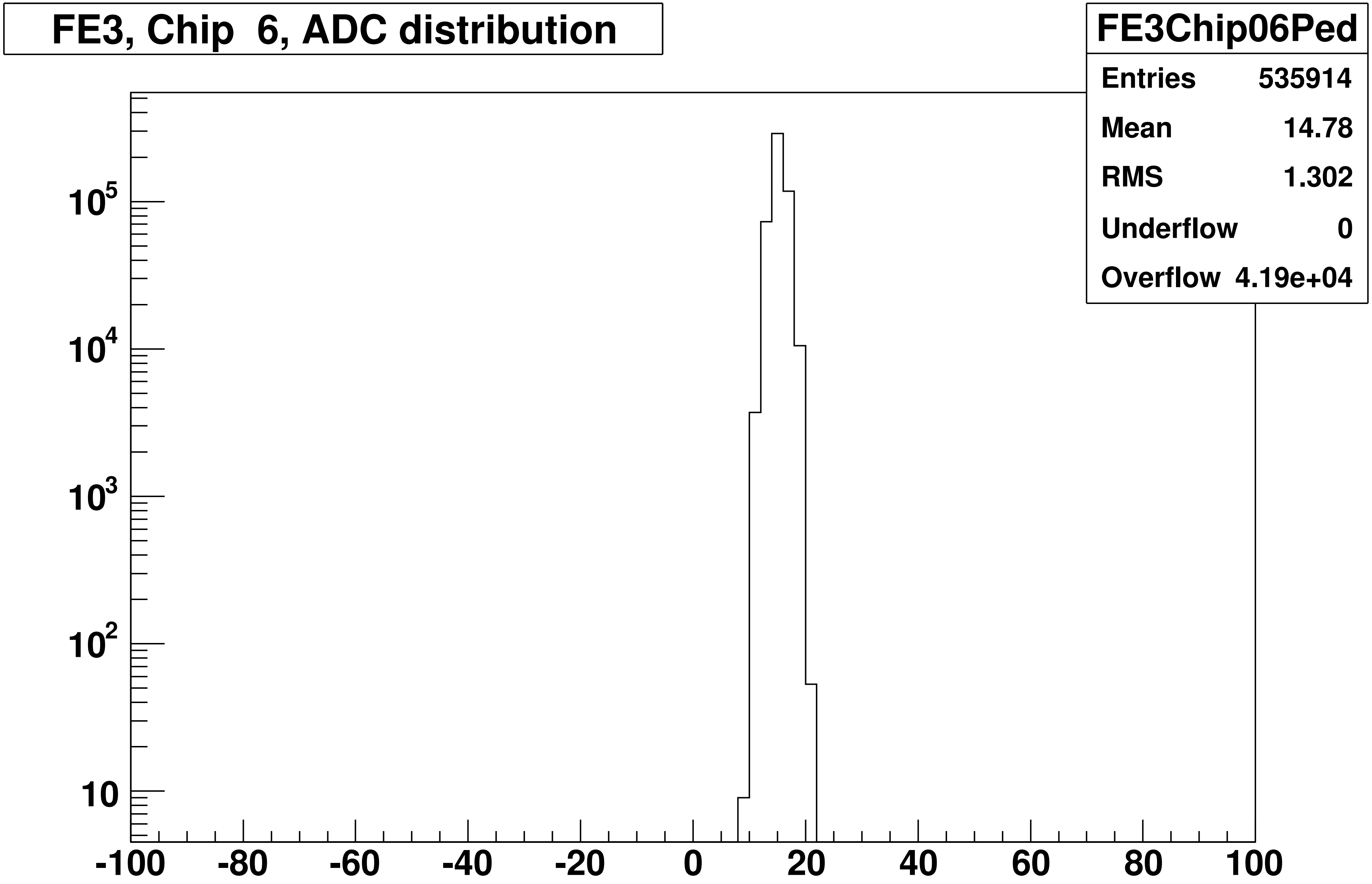
FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 535914
Mean 1473
RMS 5062
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 535914
Mean 0.09726
RMS 1.152
Underflow 0
Overflow 4.187e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

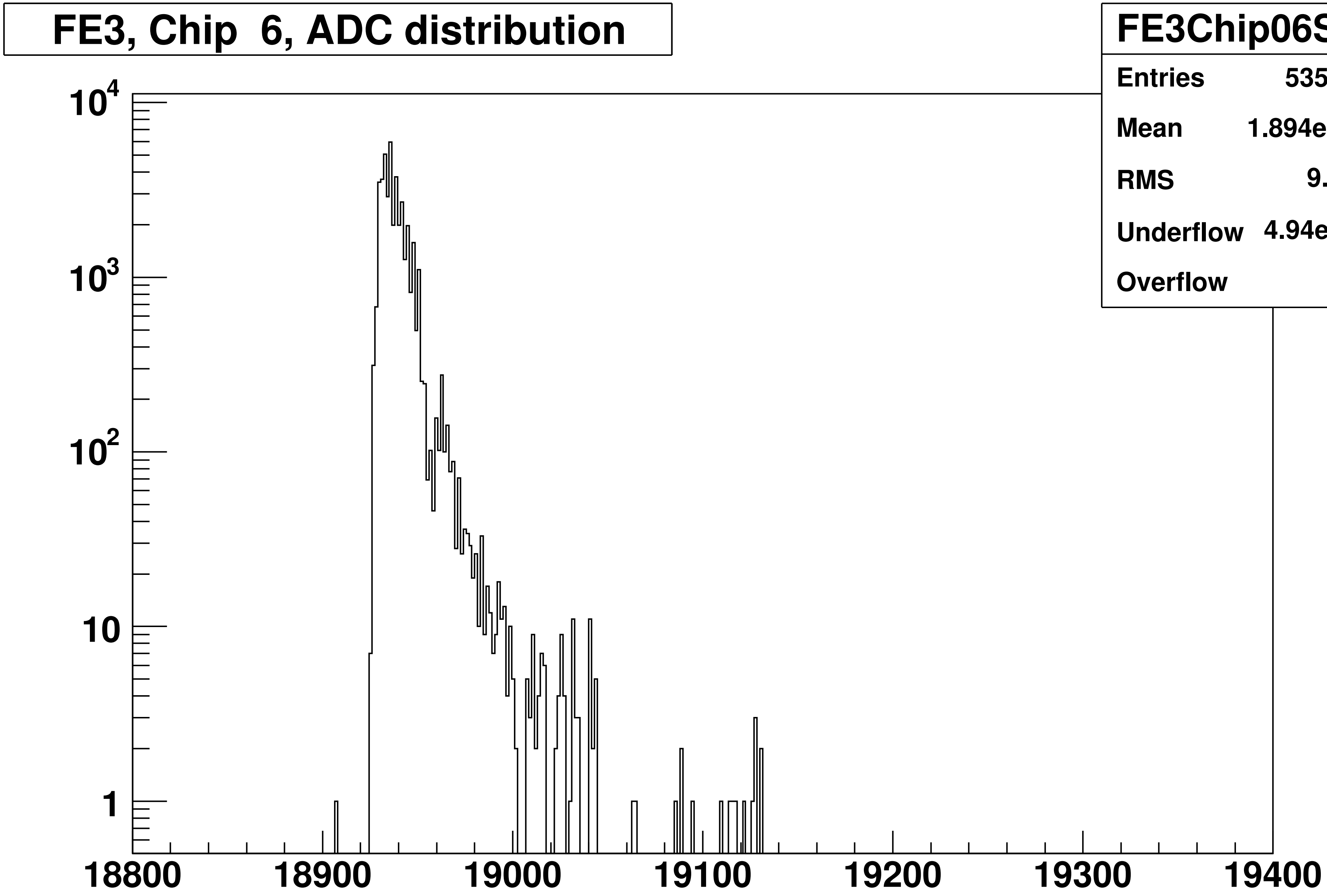


FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 535914
Mean 1.887e+04
RMS 8.933
Underflow 4.941e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

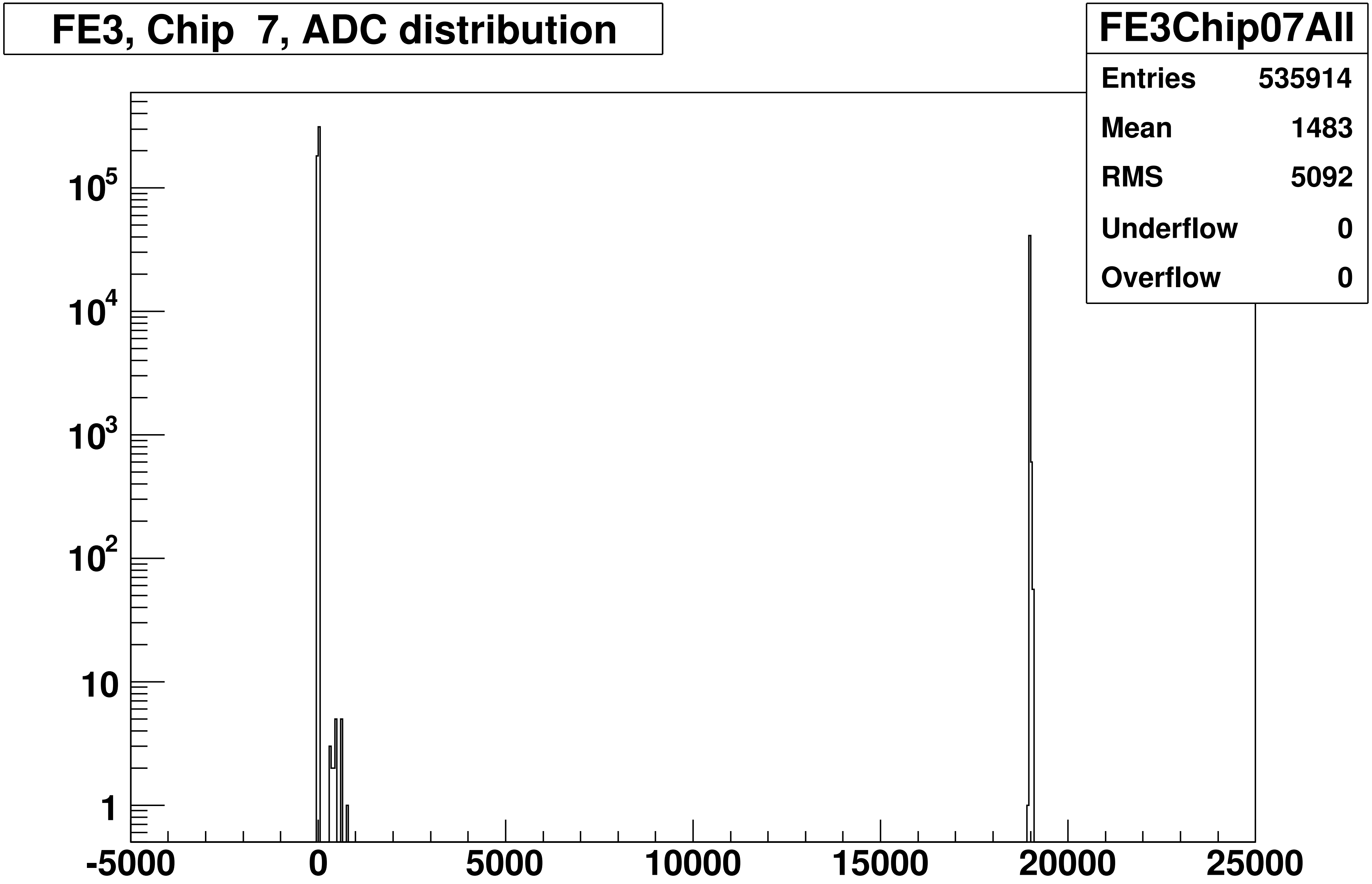

FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 535914
Mean 1494
RMS 5080
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

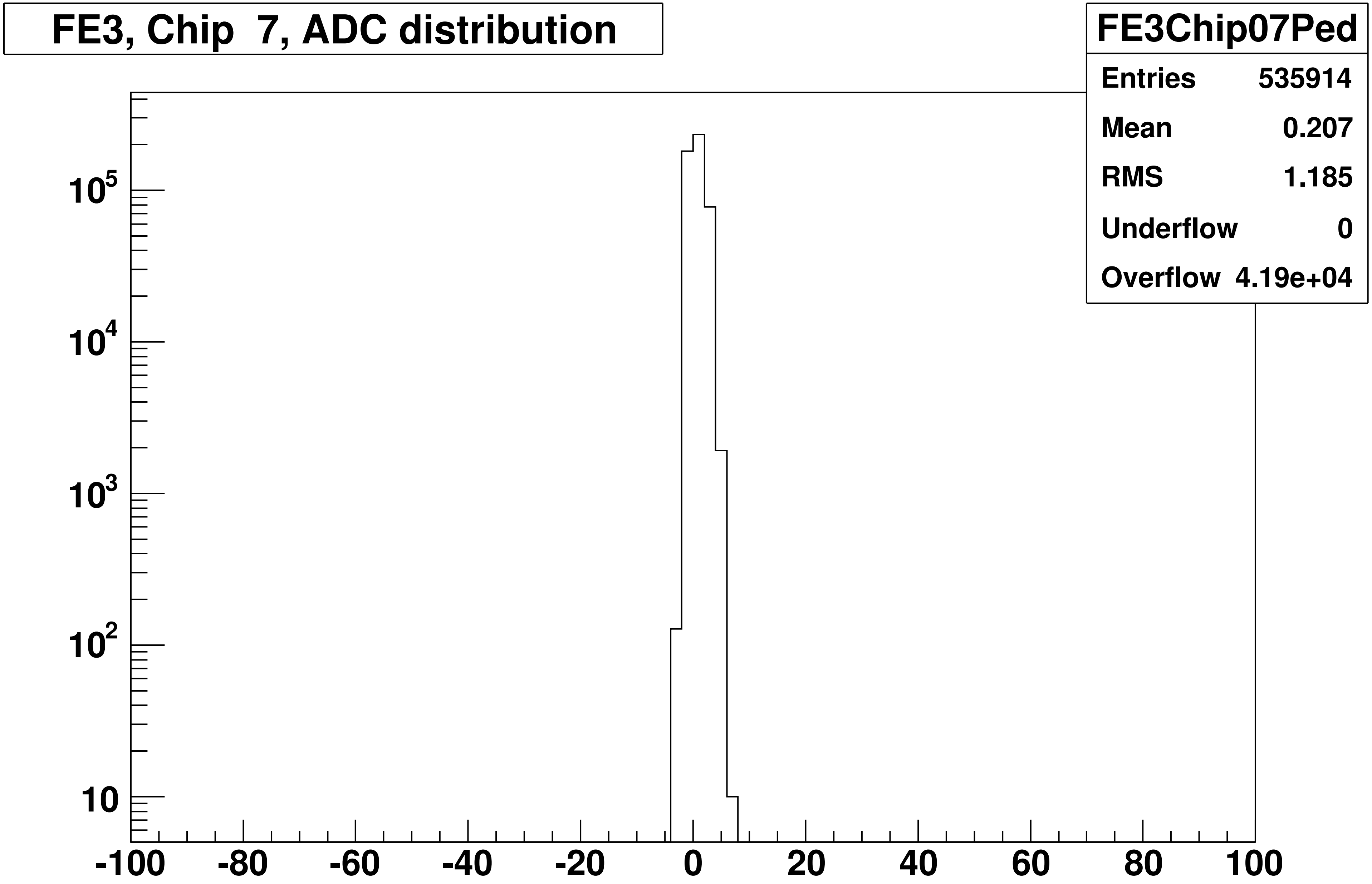


FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 535914
Mean 14.78
RMS 1.302
Underflow 0
Overflow 4.19e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

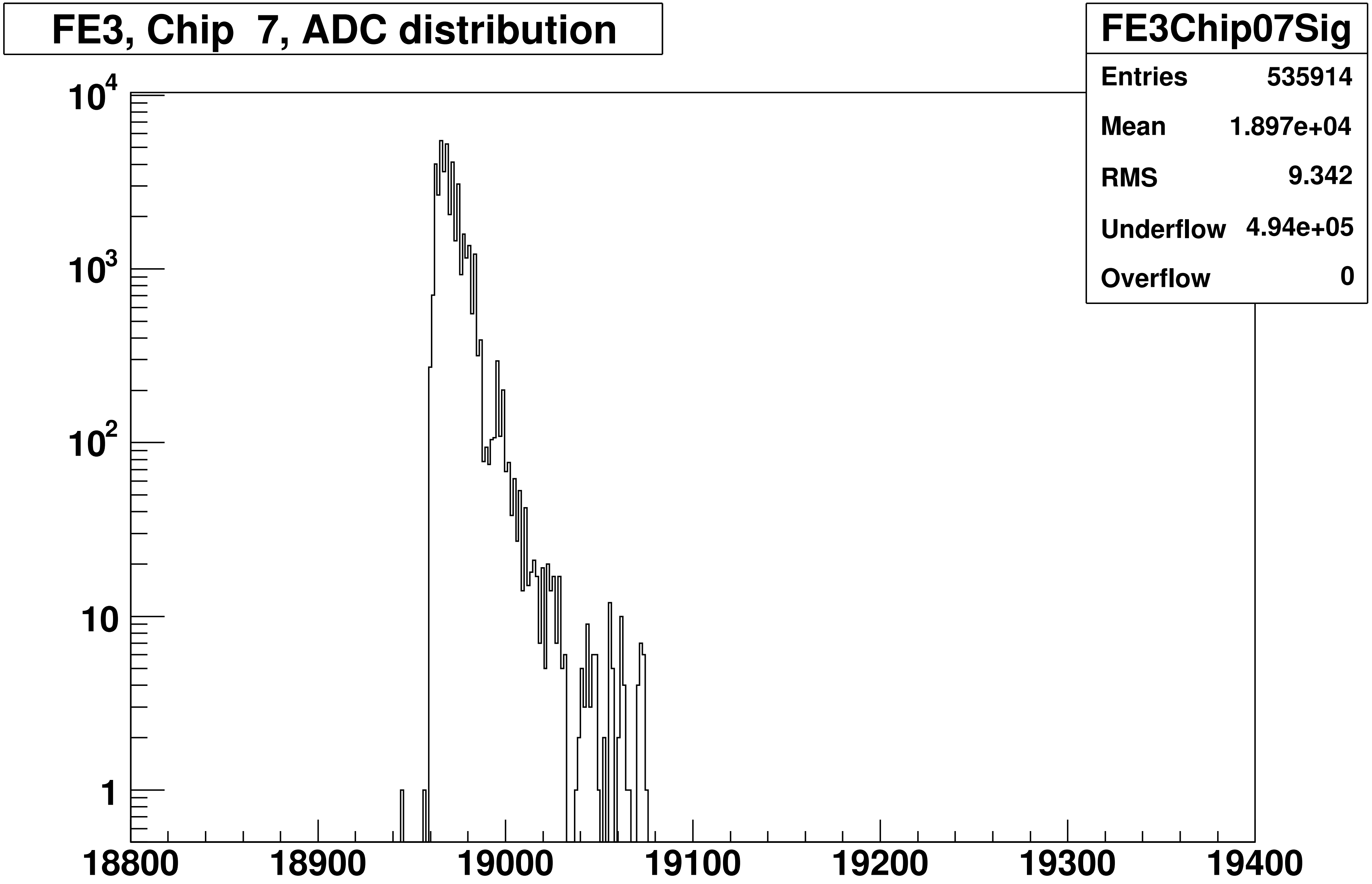


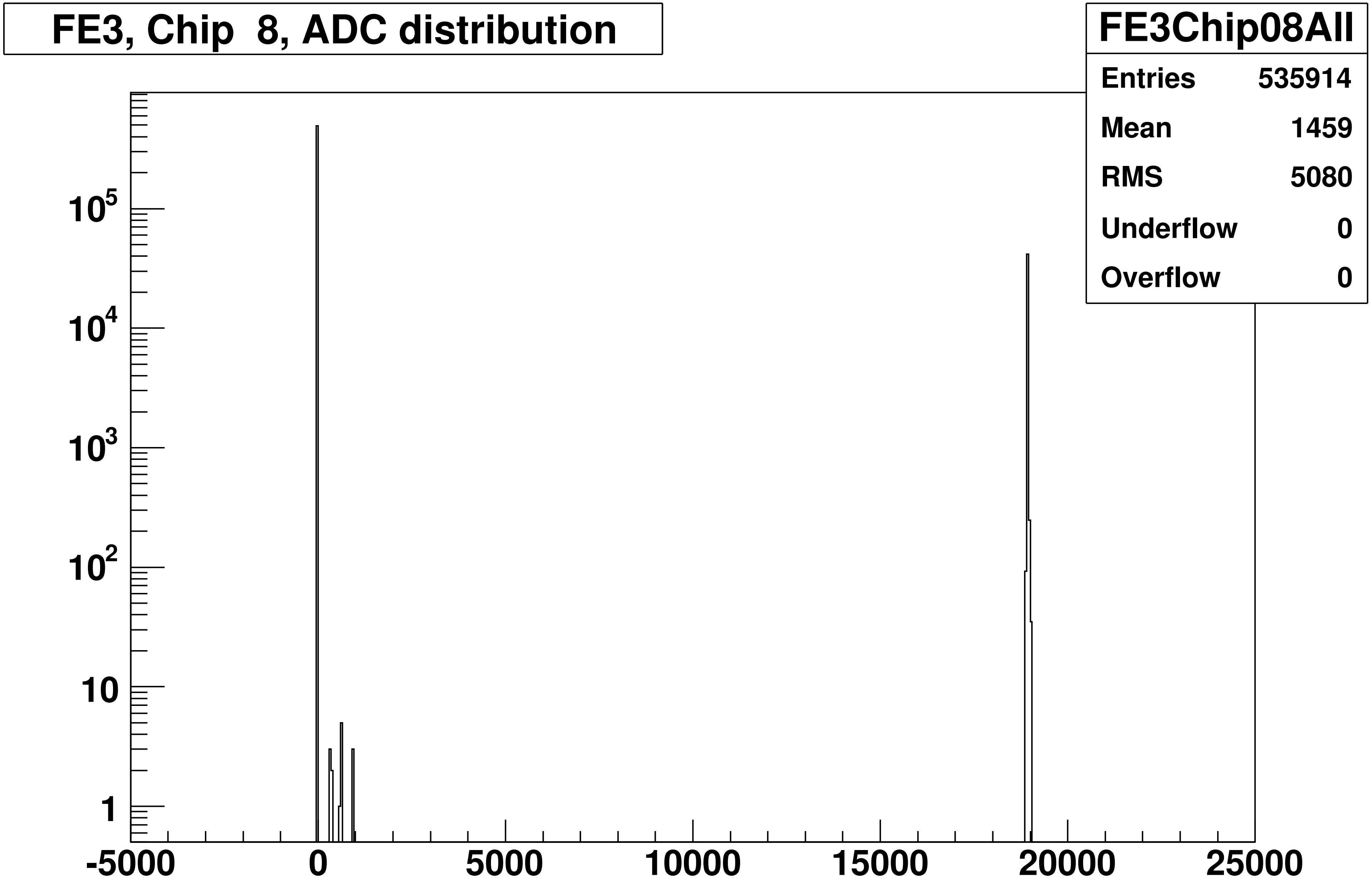
FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 535914
Mean 1.894e+04
RMS 9.934
Underflow 4.94e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 535914
Mean 1483
RMS 5092
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

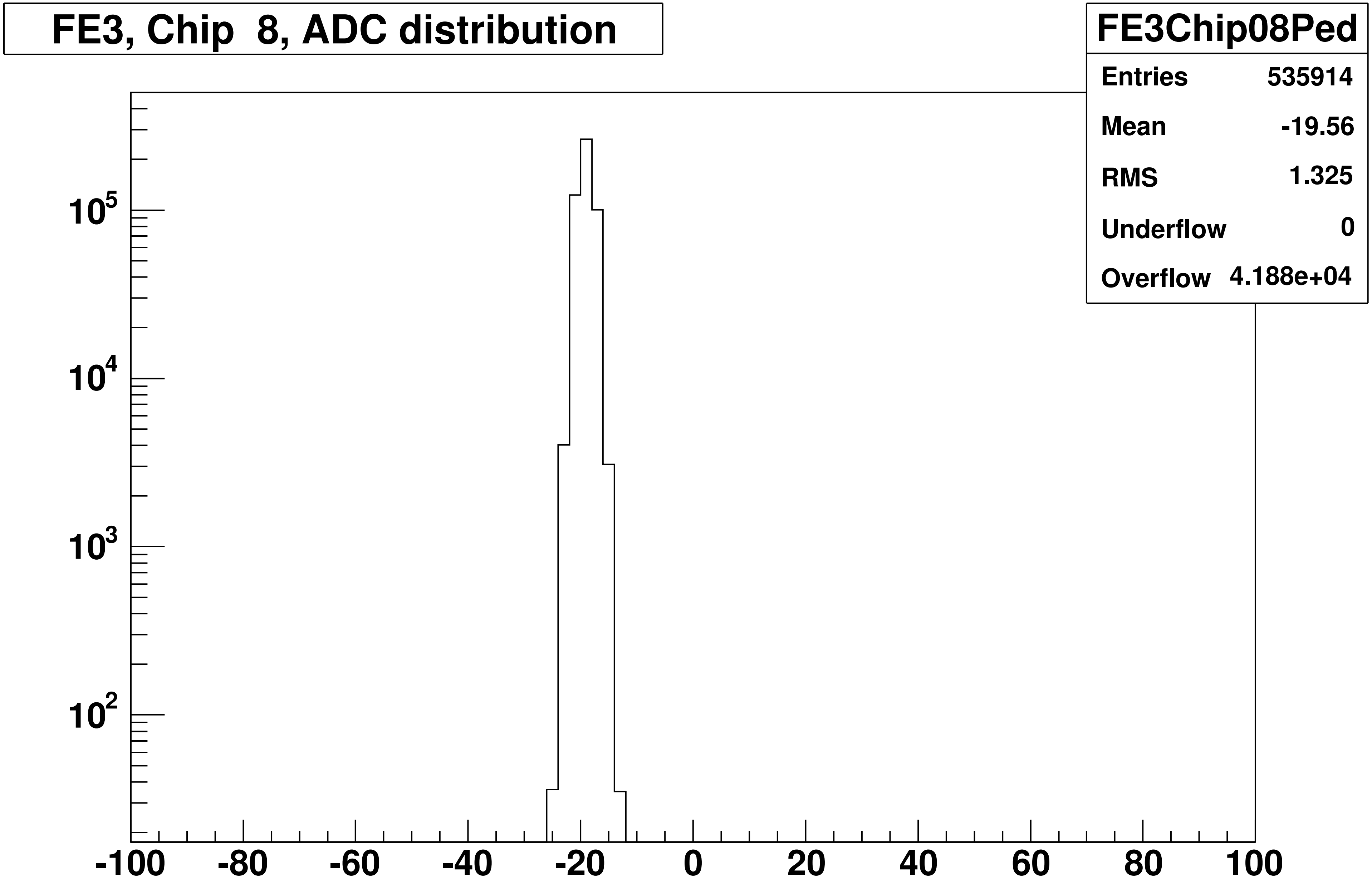


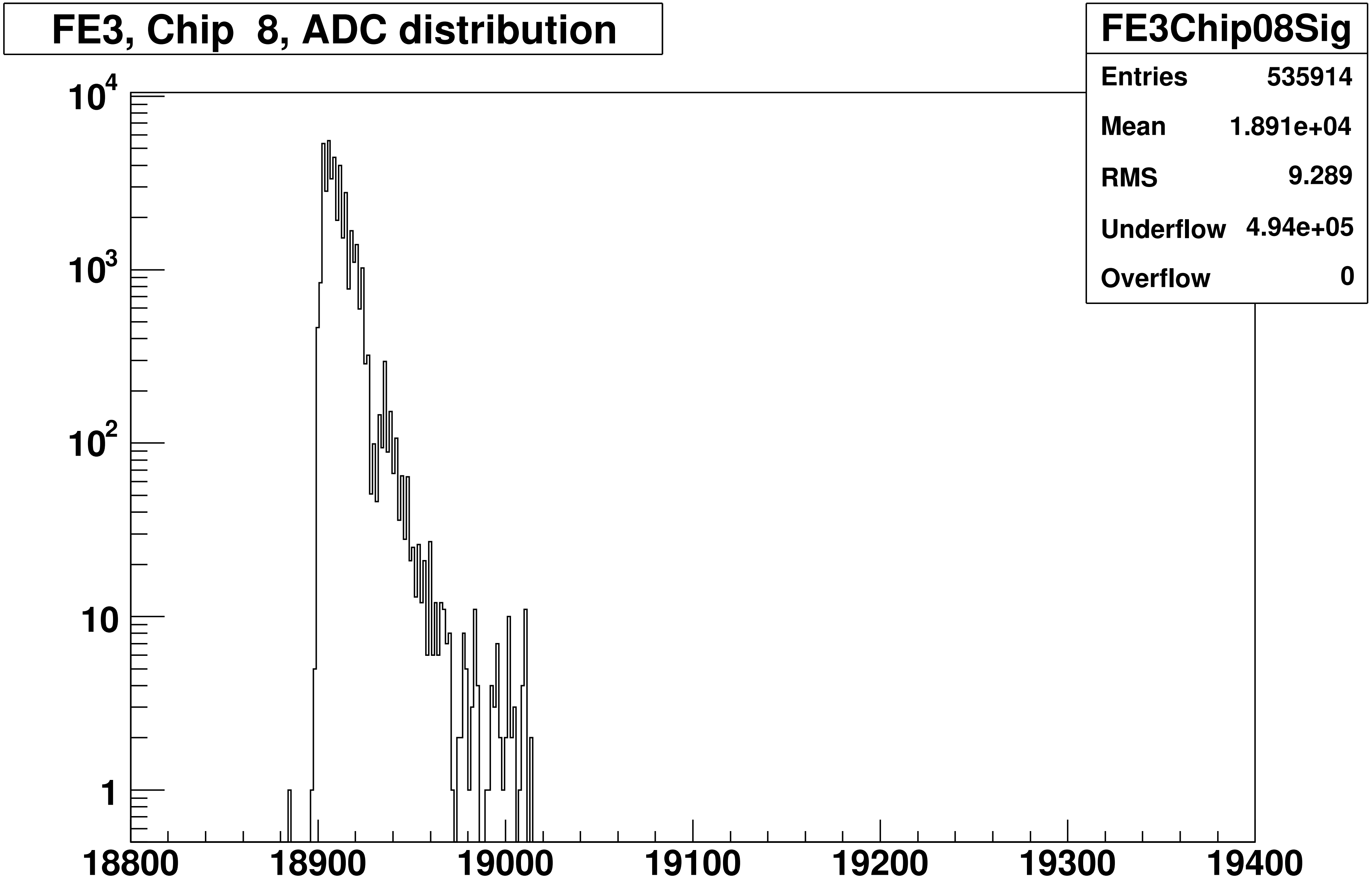
FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 535914
Mean 0.207
RMS 1.185
Underflow 0
Overflow 4.19e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

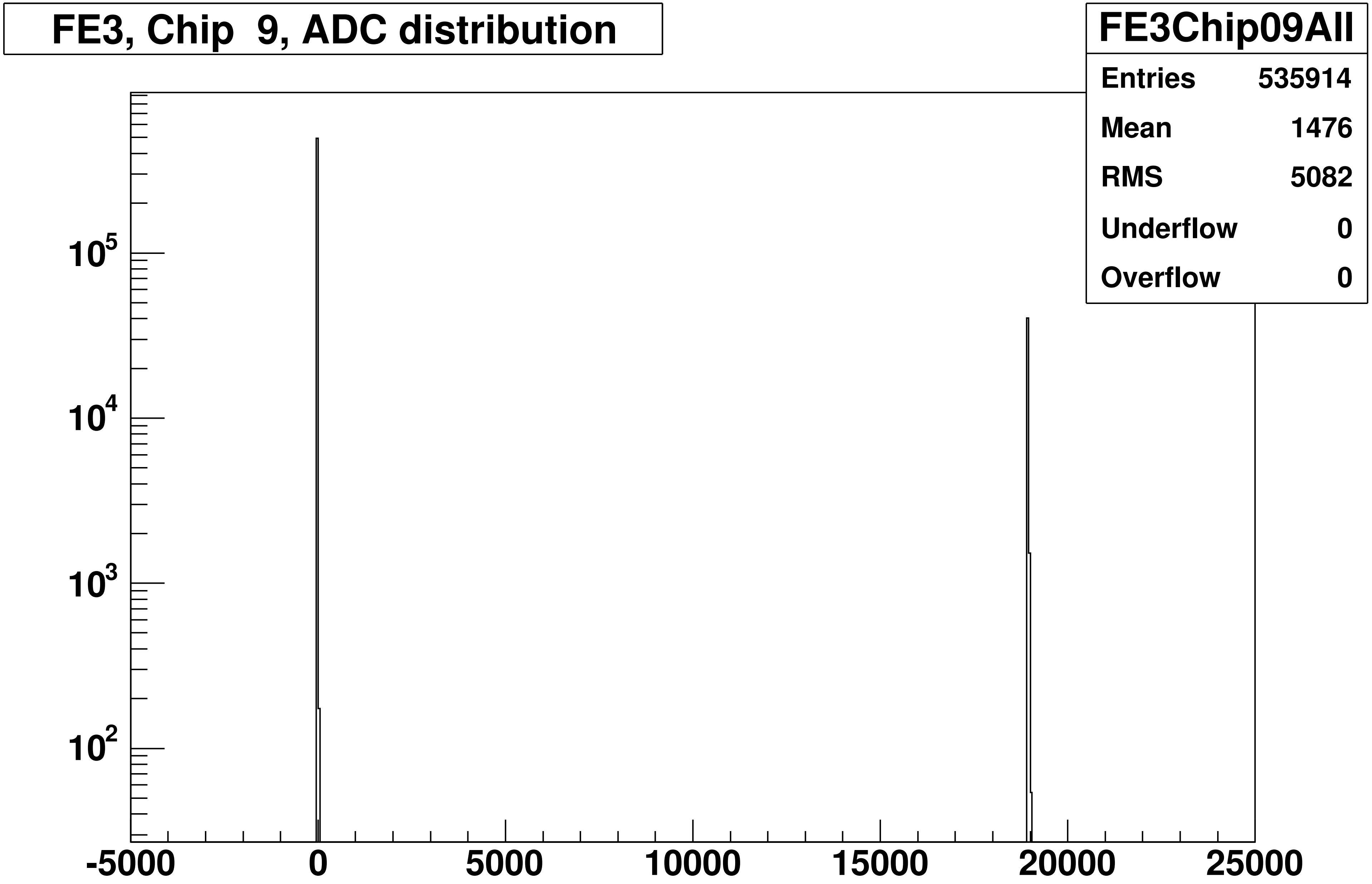

FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 535914
Mean 1.897e+04
RMS 9.342
Underflow 4.94e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

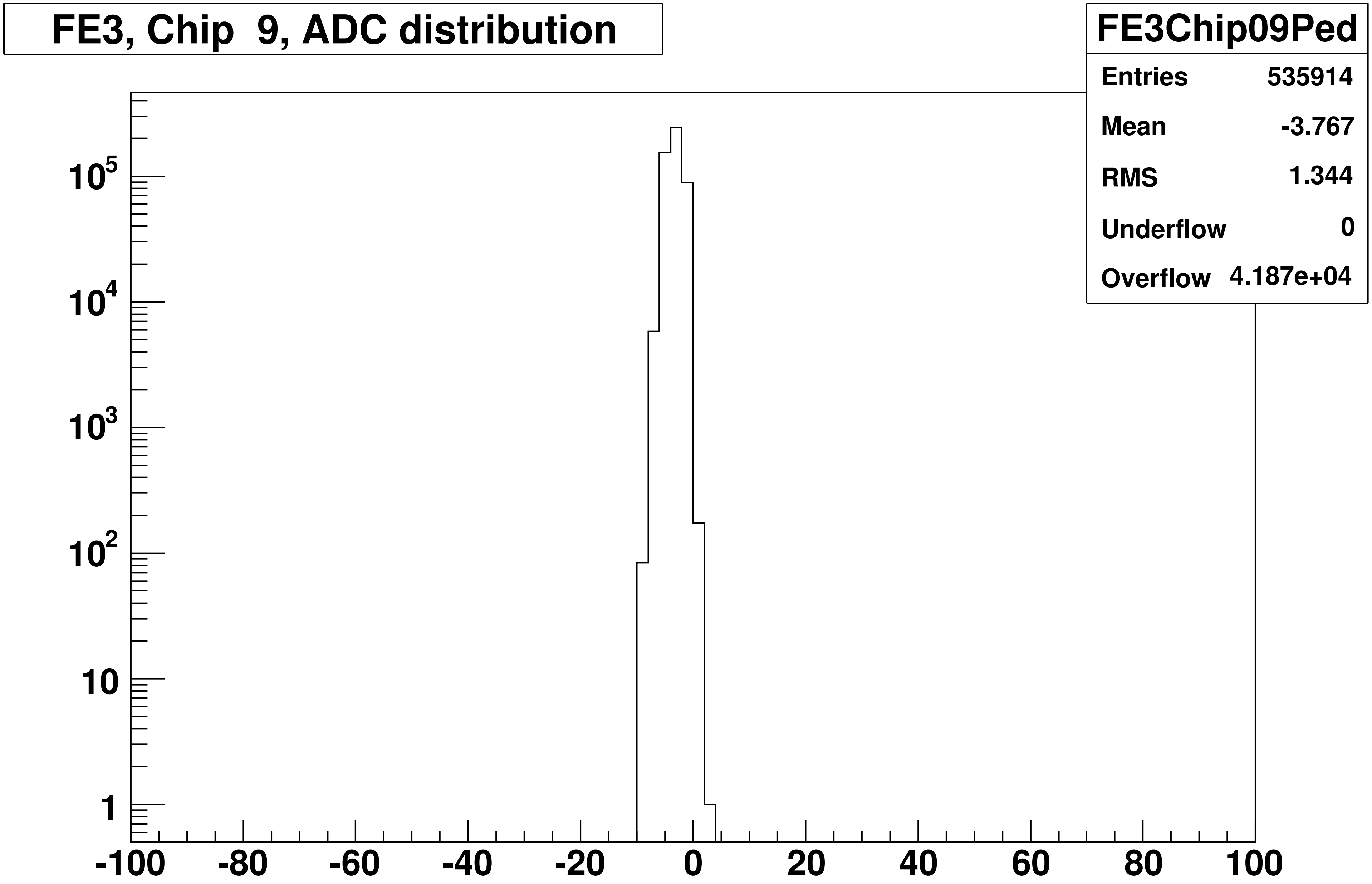


FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 535914
Mean 1459
RMS 5080
Underflow 0
Overflow 0

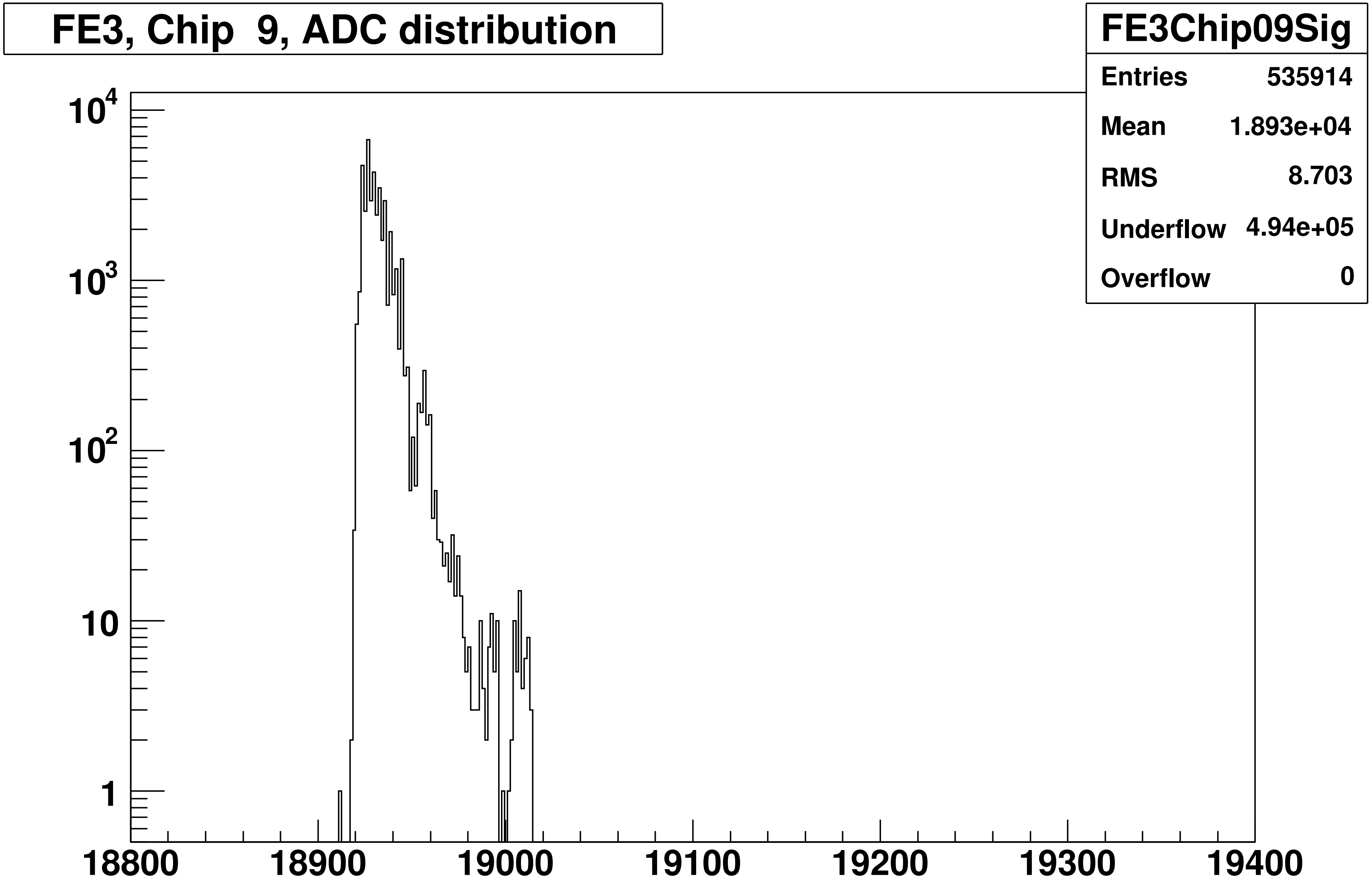

FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 535914
Mean -19.56
RMS 1.325
Underflow 0
Overflow 4.188e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

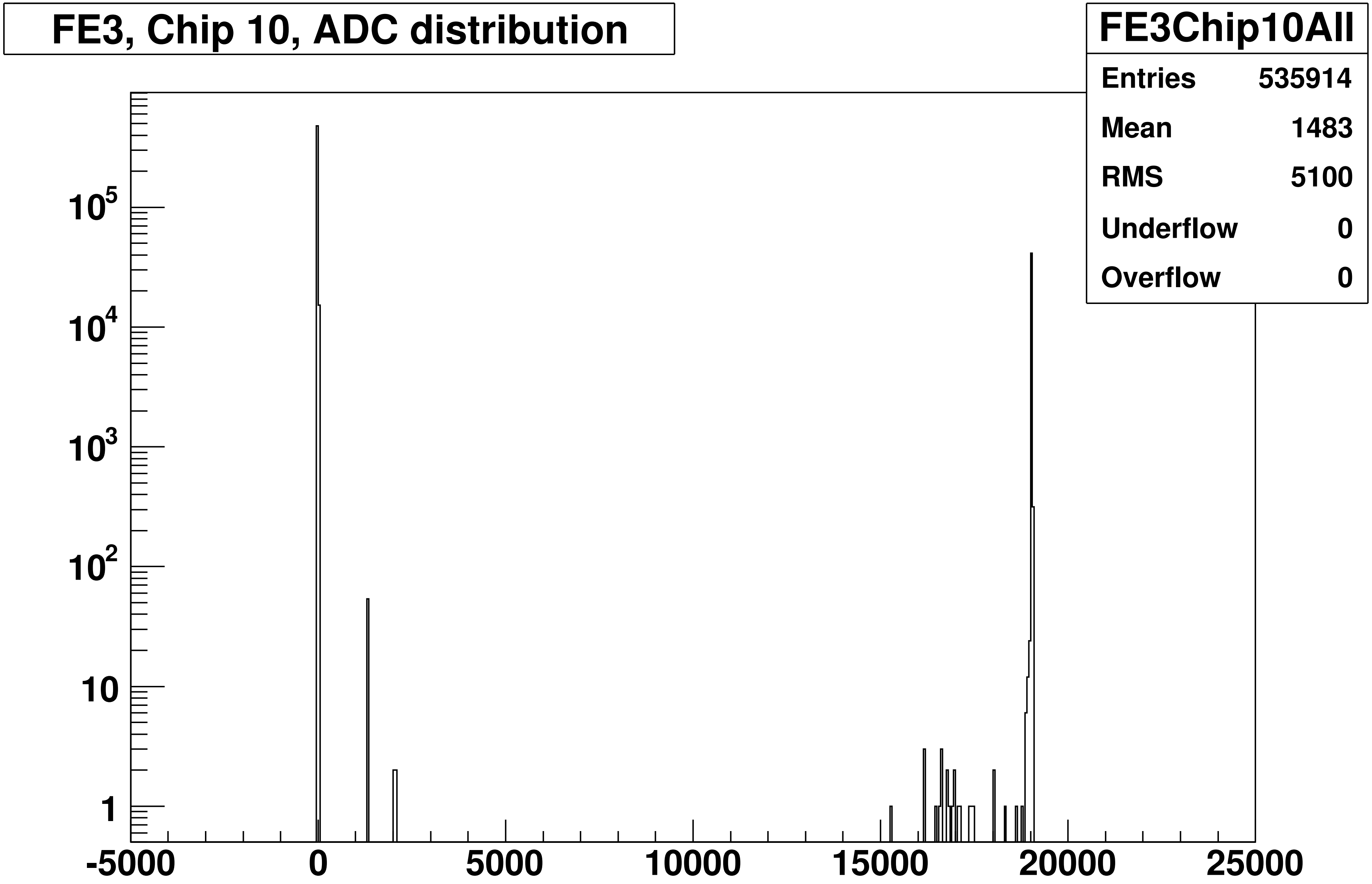

FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 535914
Mean 1.891e+04
RMS 9.289
Underflow 4.94e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 535914
Mean 1476
RMS 5082
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

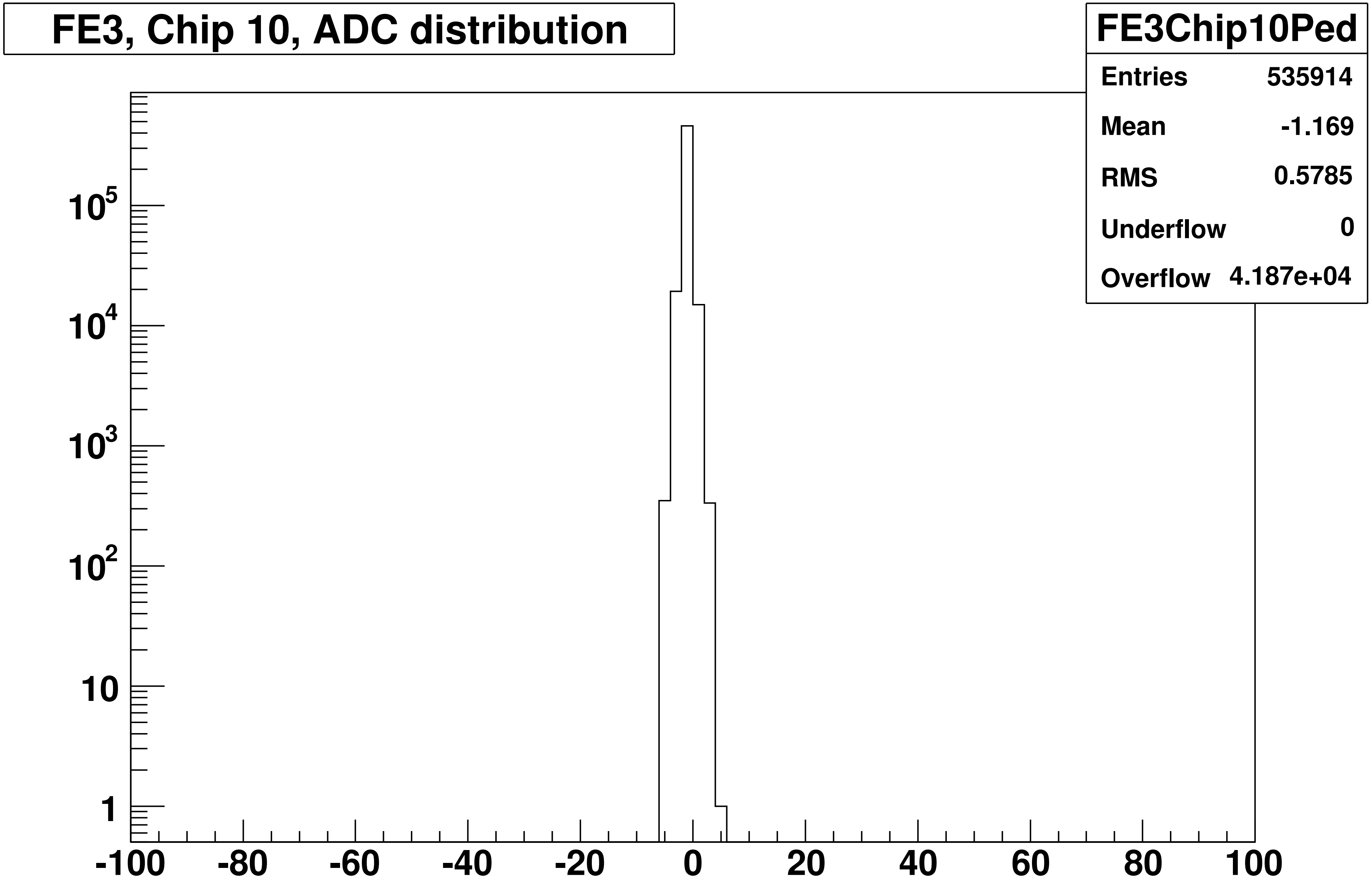


FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 535914
Mean -3.767
RMS 1.344
Underflow 0
Overflow 4.187e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

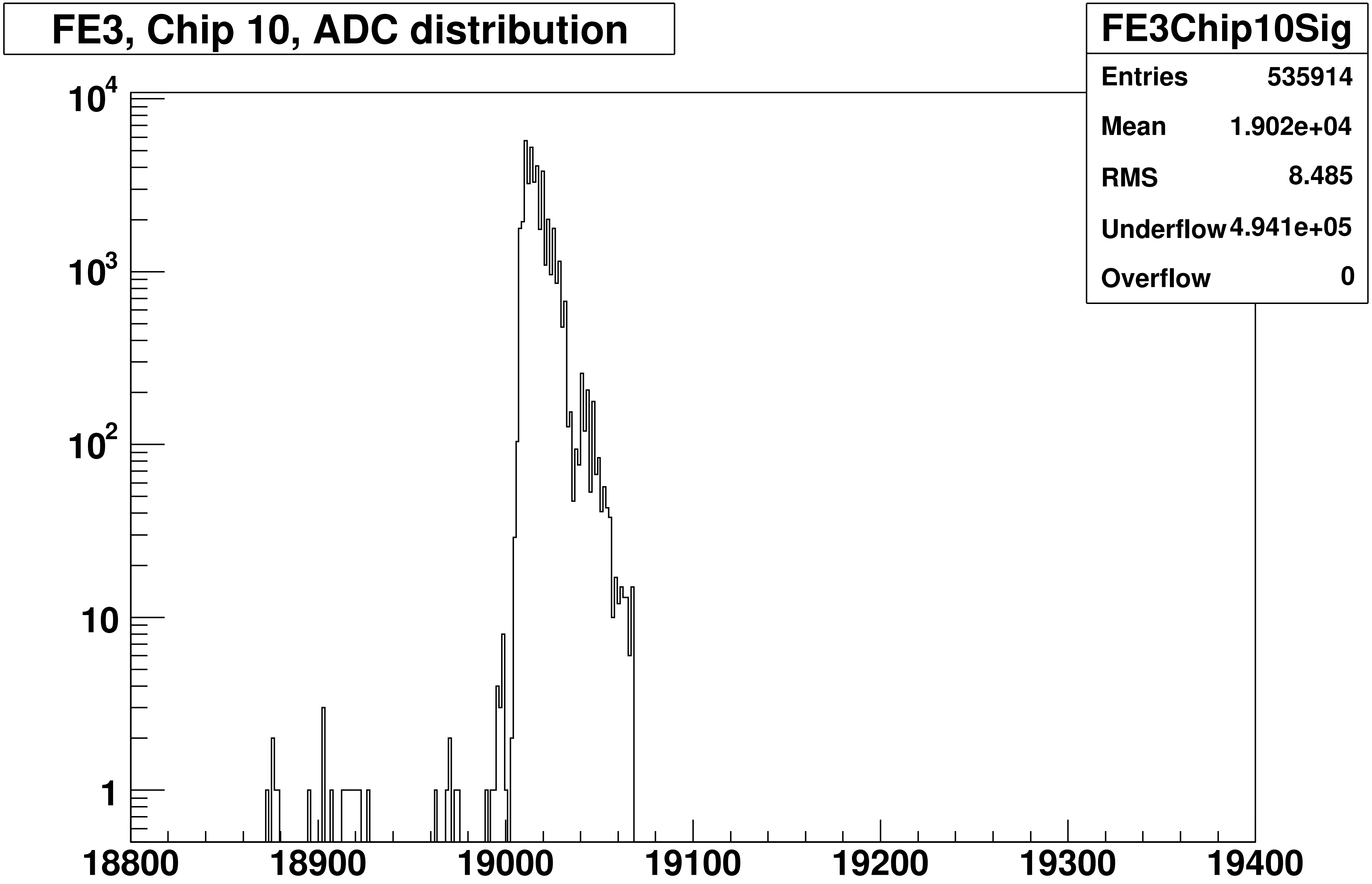

FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 535914
Mean 1.893e+04
RMS 8.703
Underflow 4.94e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



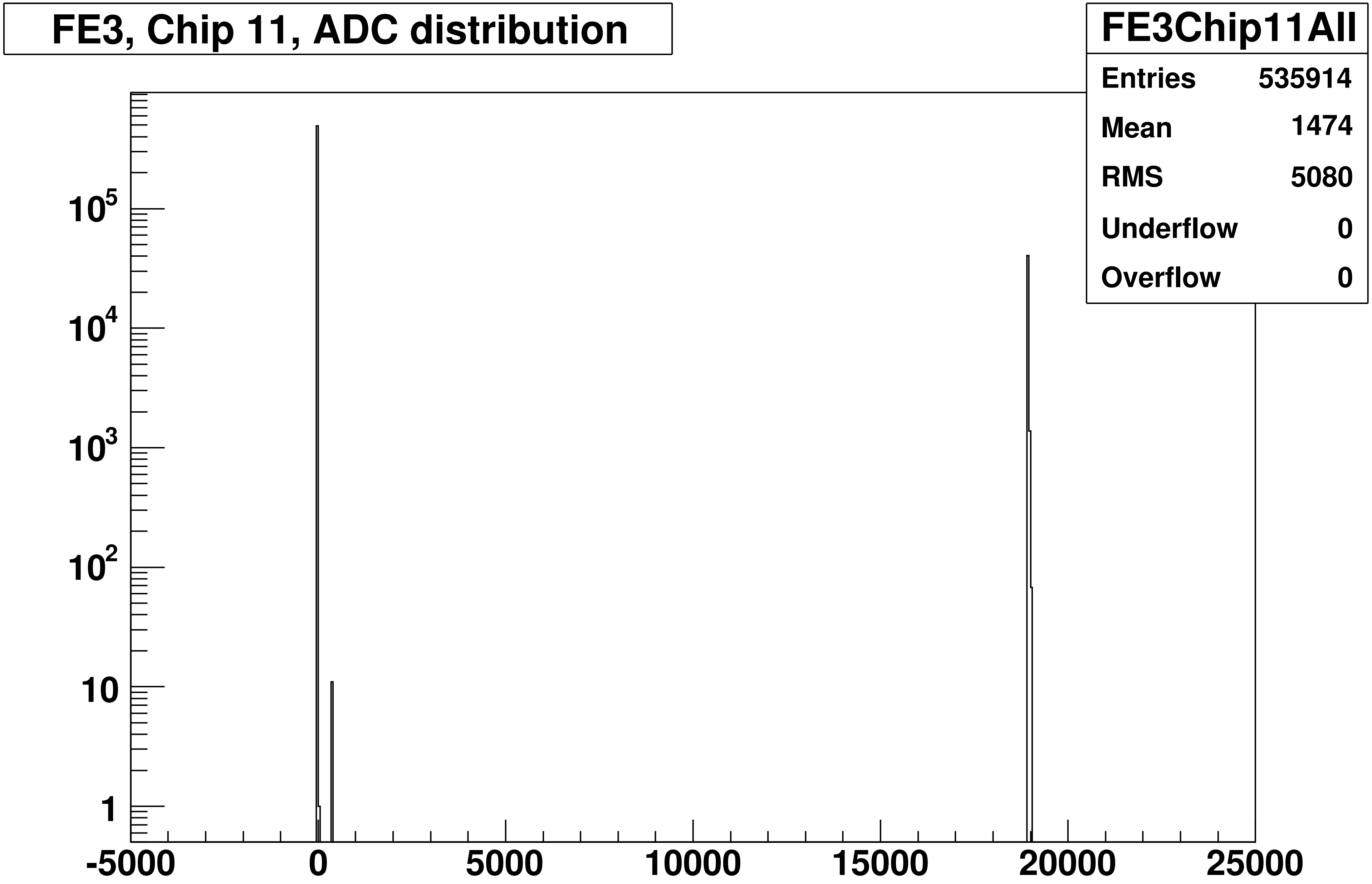
FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 535914
Mean 1483
RMS 5100
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

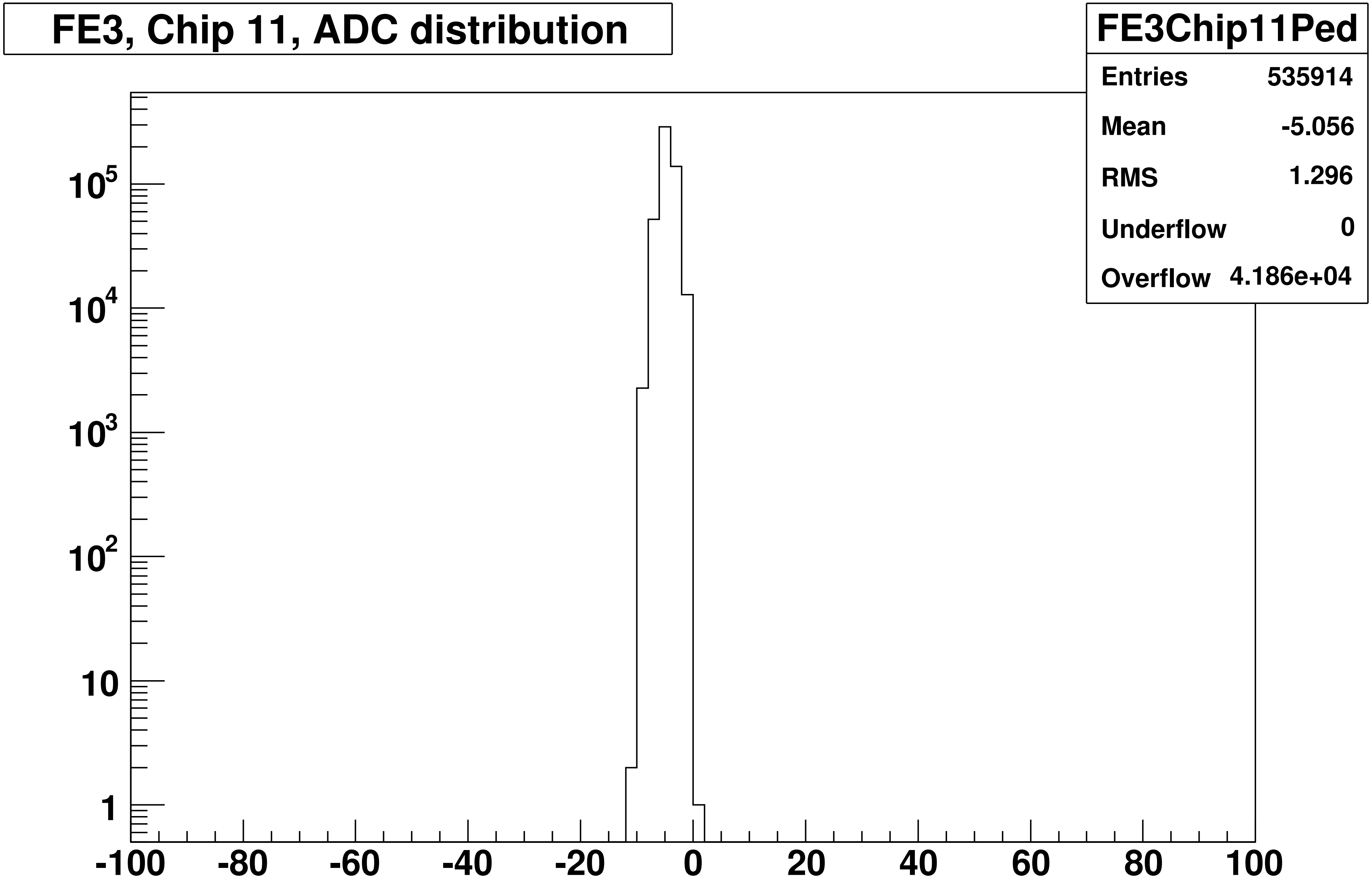


FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 535914
Mean -1.169
RMS 0.5785
Underflow 0
Overflow 4.187e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

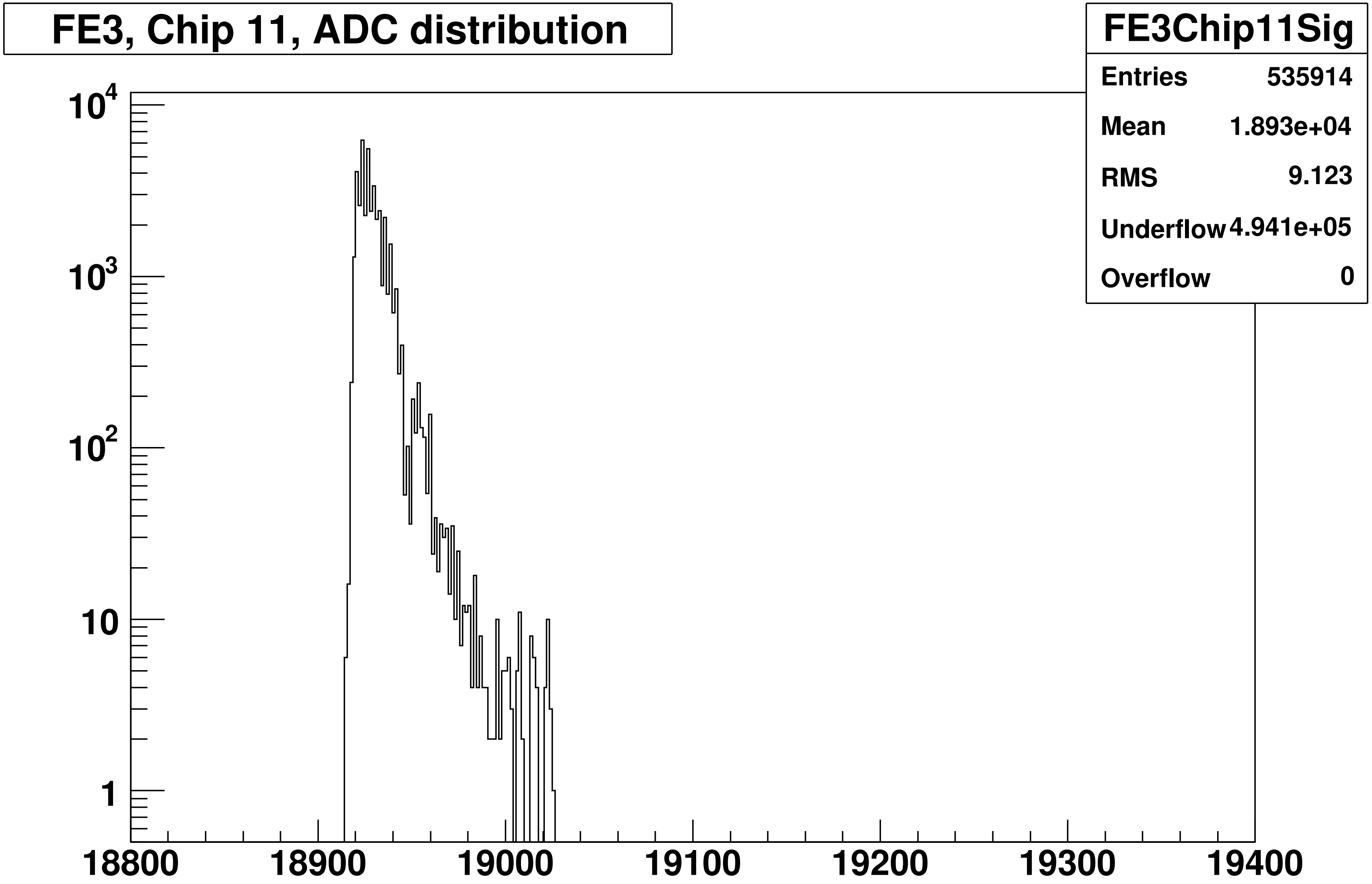


FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 535914
Mean 1.902e+04
RMS 8.485
Underflow 4.941e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

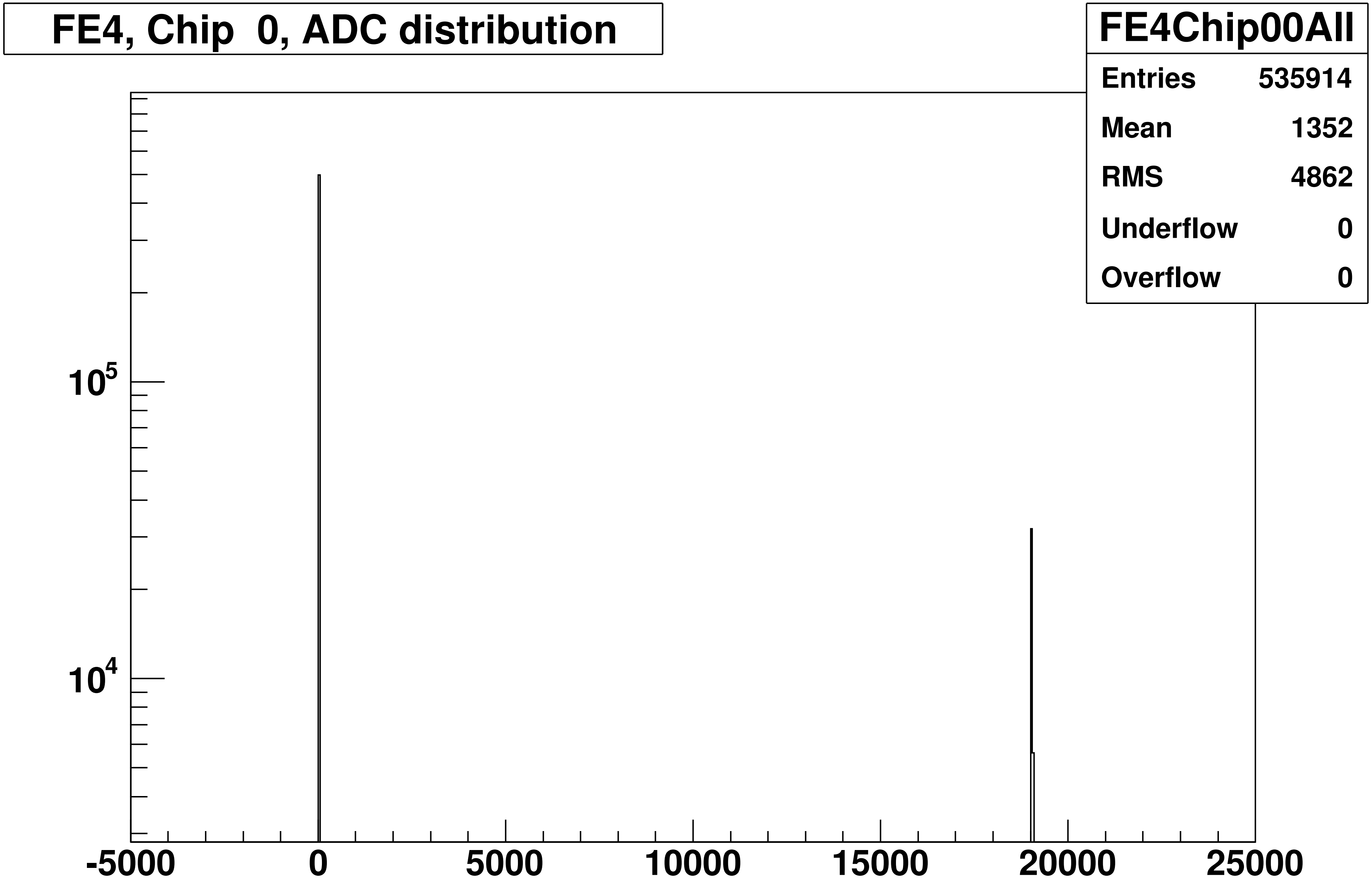

FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 535914
Mean 1474
RMS 5080
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



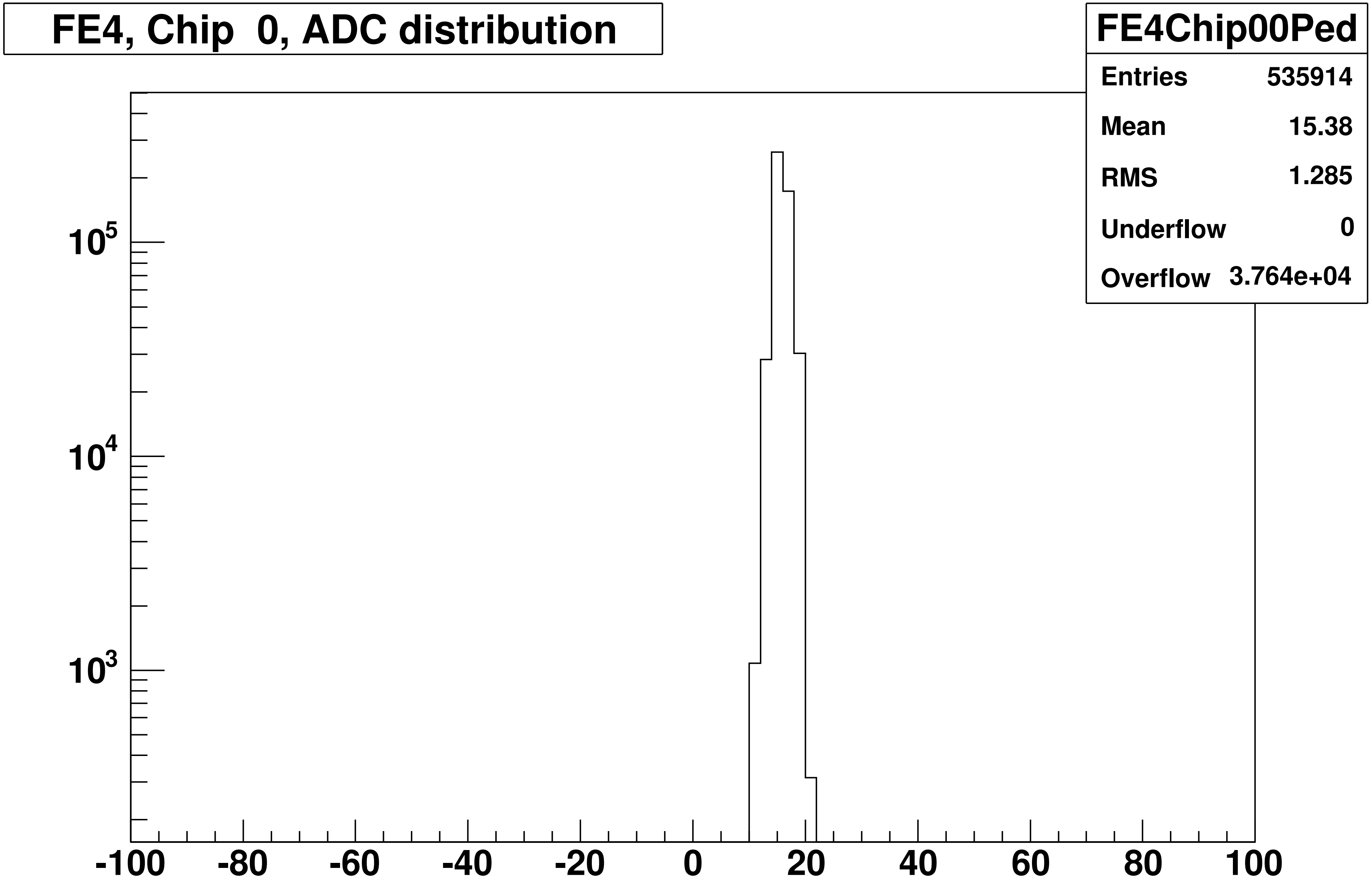
FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 535914
Mean -5.056
RMS 1.296
Underflow 0
Overflow 4.186e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

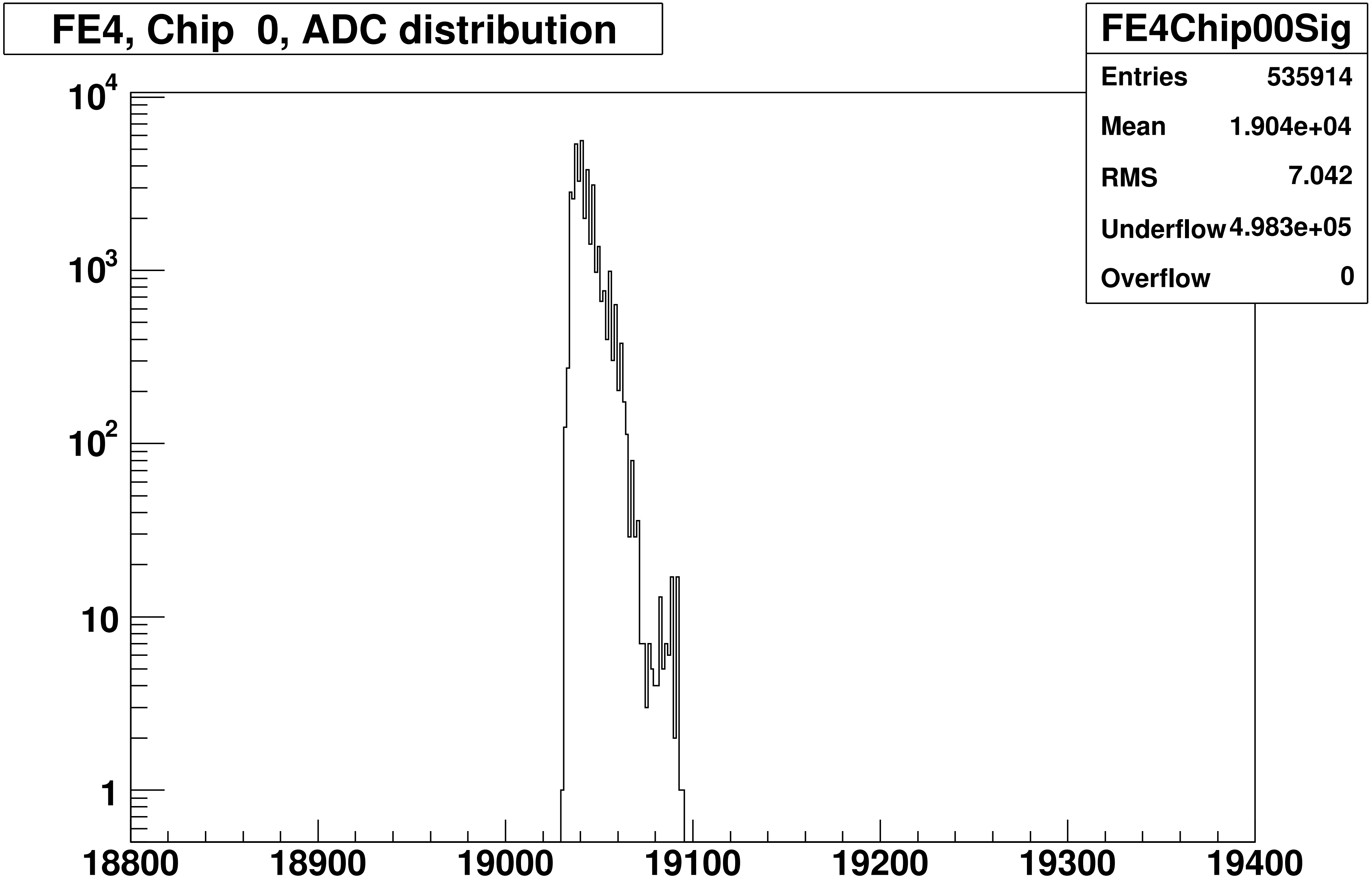


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 535914
Mean 1.893e+04
RMS 9.123
Underflow 4.941e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

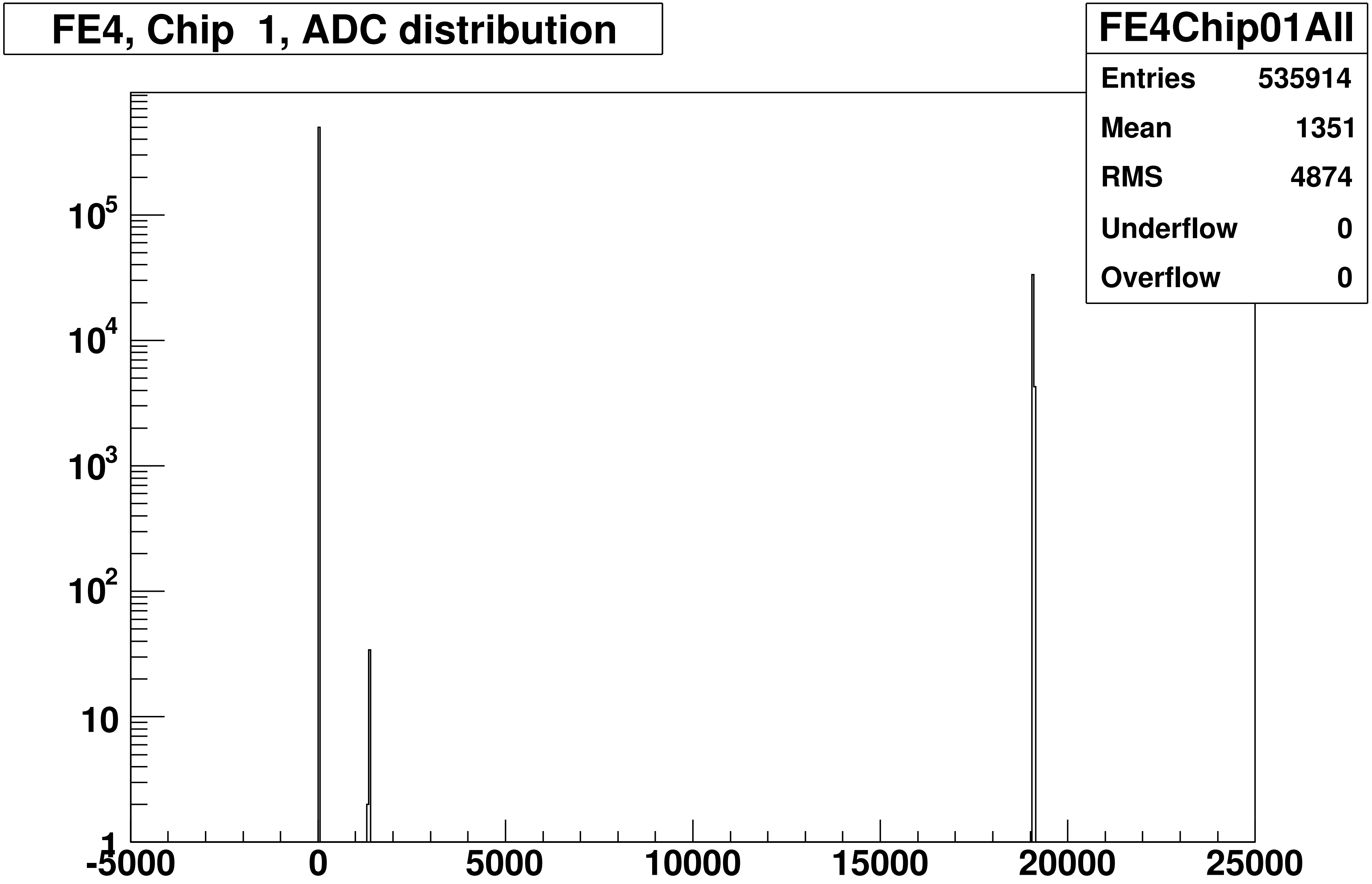


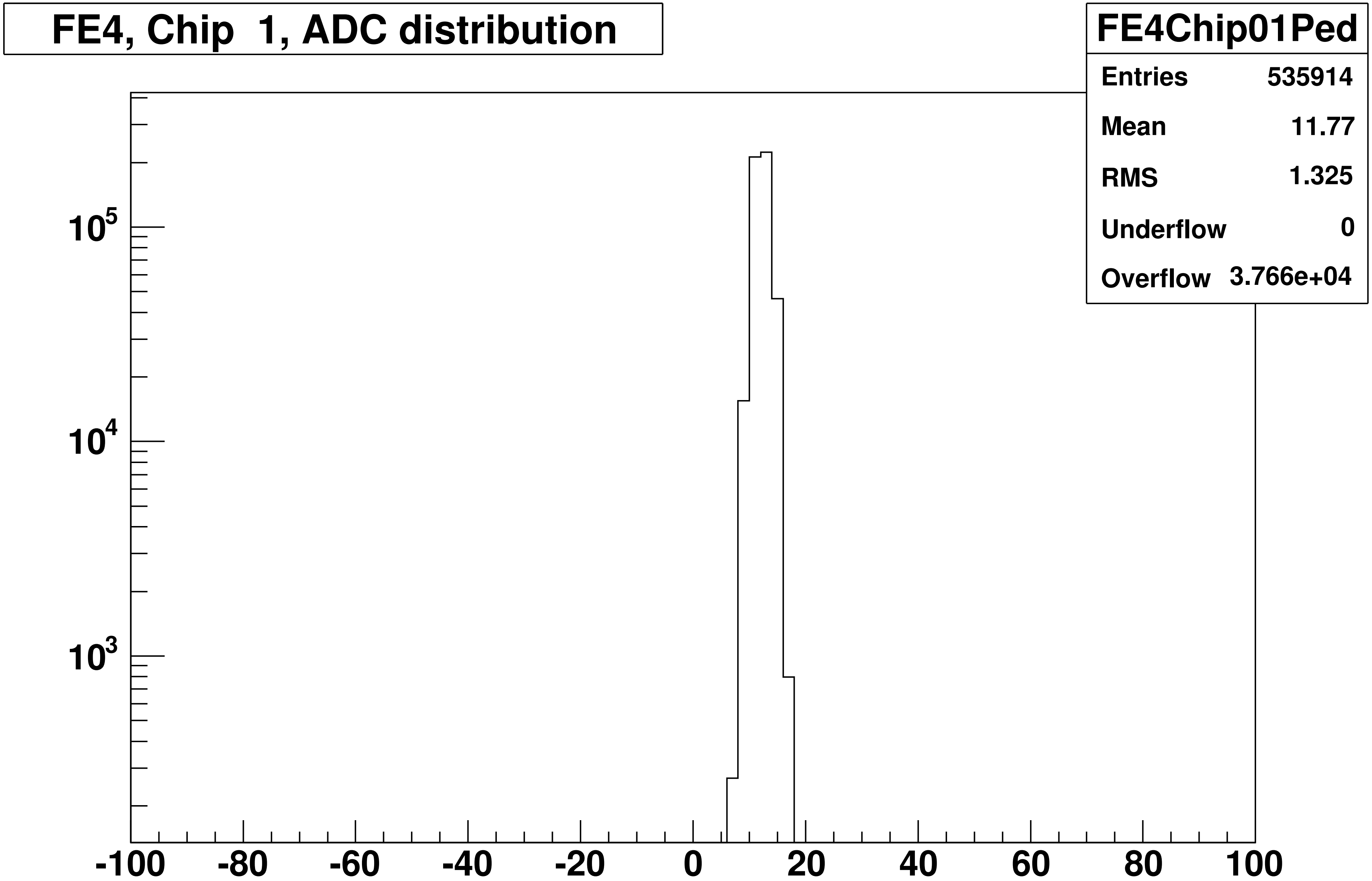
FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 535914
Mean 1352
RMS 4862
Underflow 0
Overflow 0
10^5
10^4
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 535914
Mean 15.38
RMS 1.285
Underflow 0
Overflow 3.764e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100

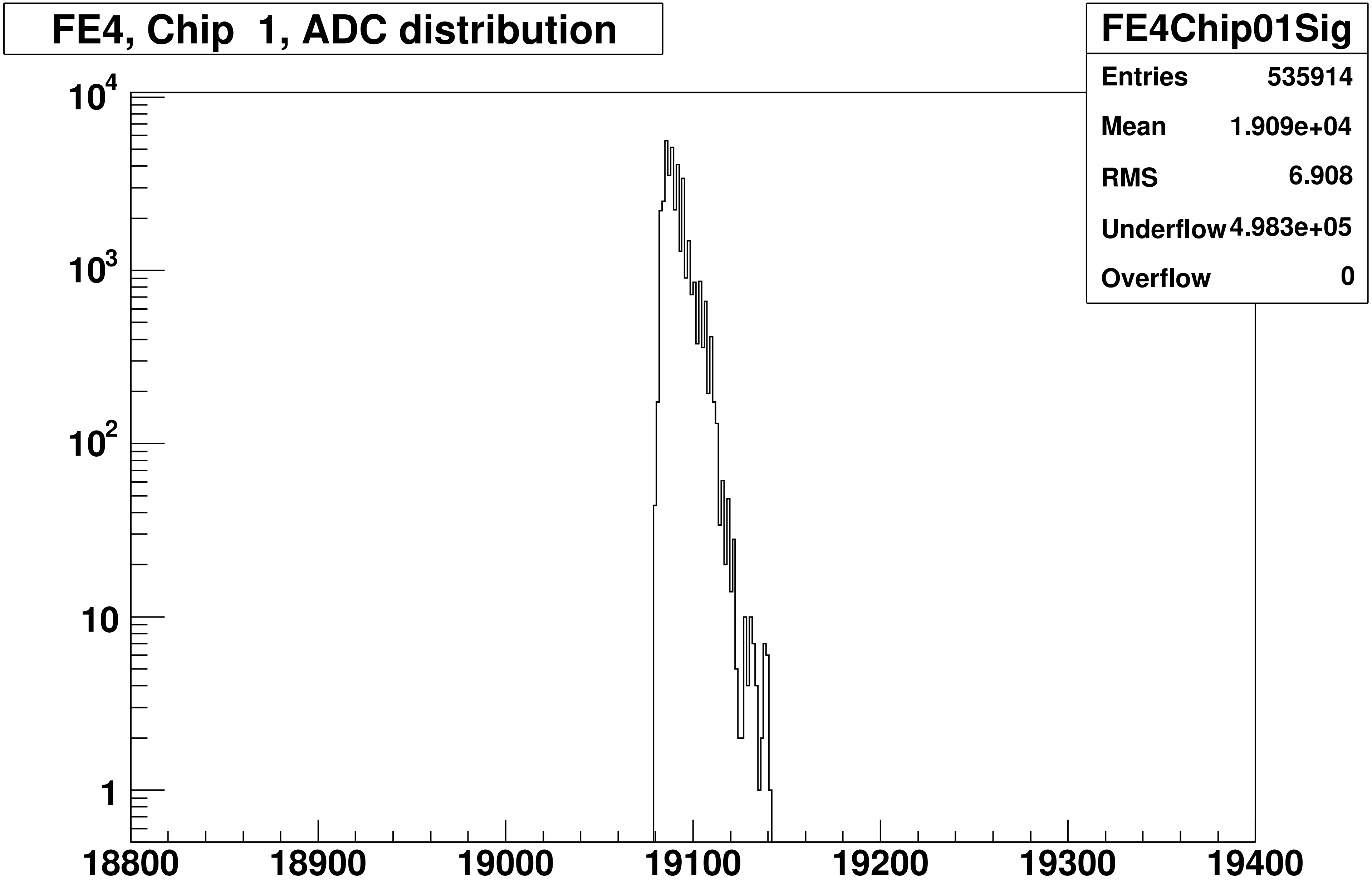


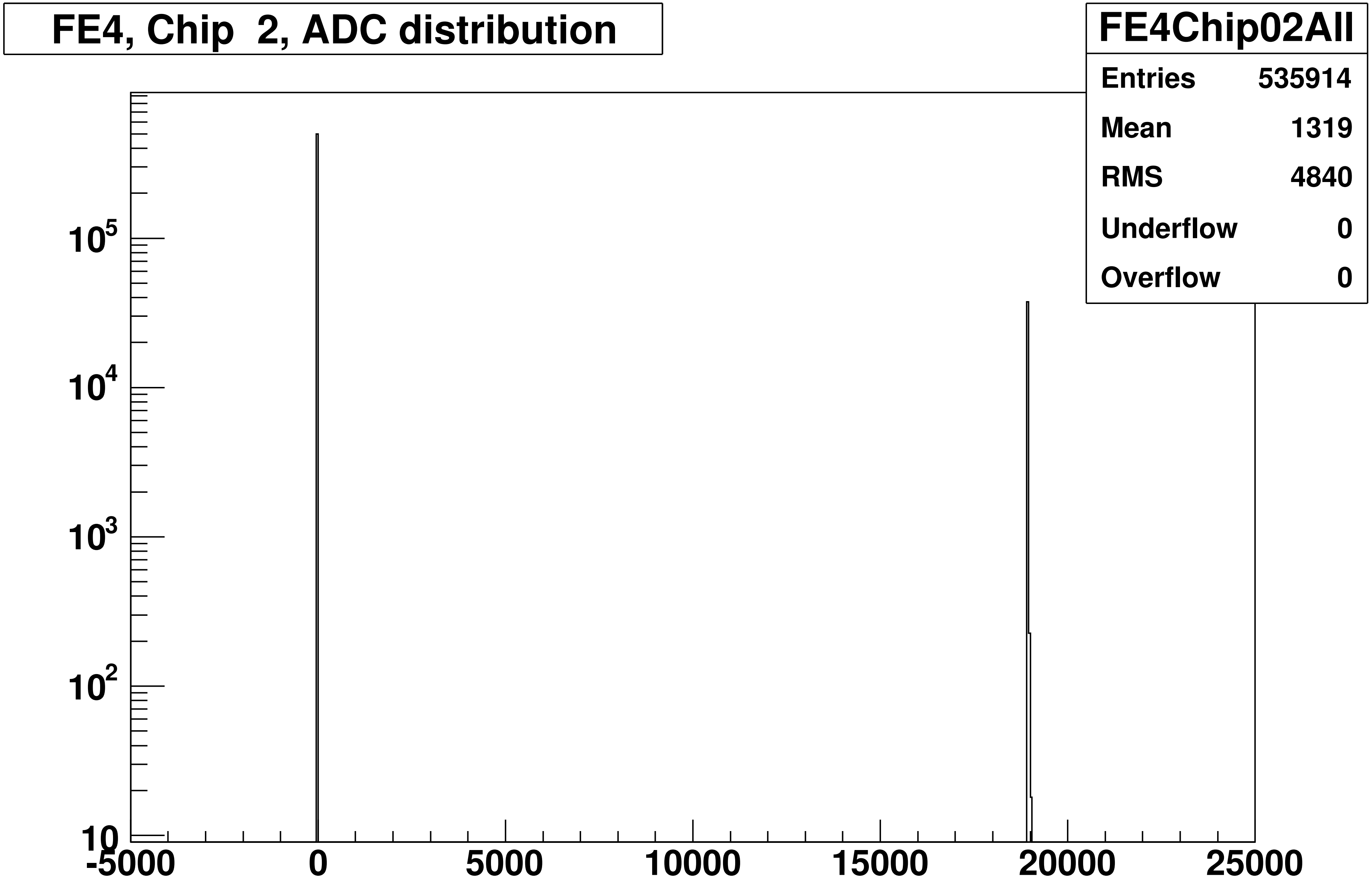
FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 535914
Mean 1.904e+04
RMS 7.042
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 535914
Mean 1351
RMS 4874
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

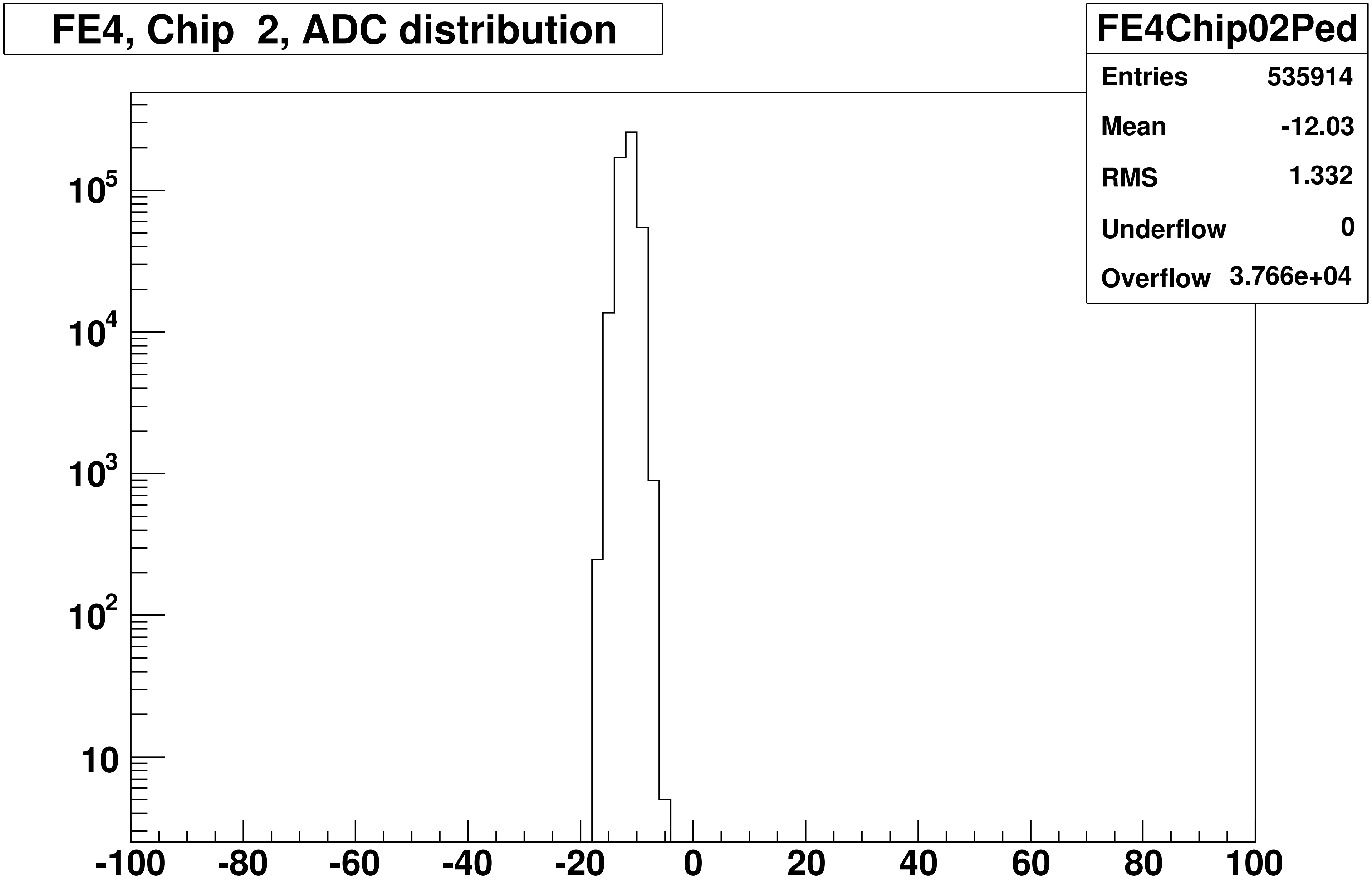


FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 535914
Mean 11.77
RMS 1.325
Underflow 0
Overflow 3.766e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100

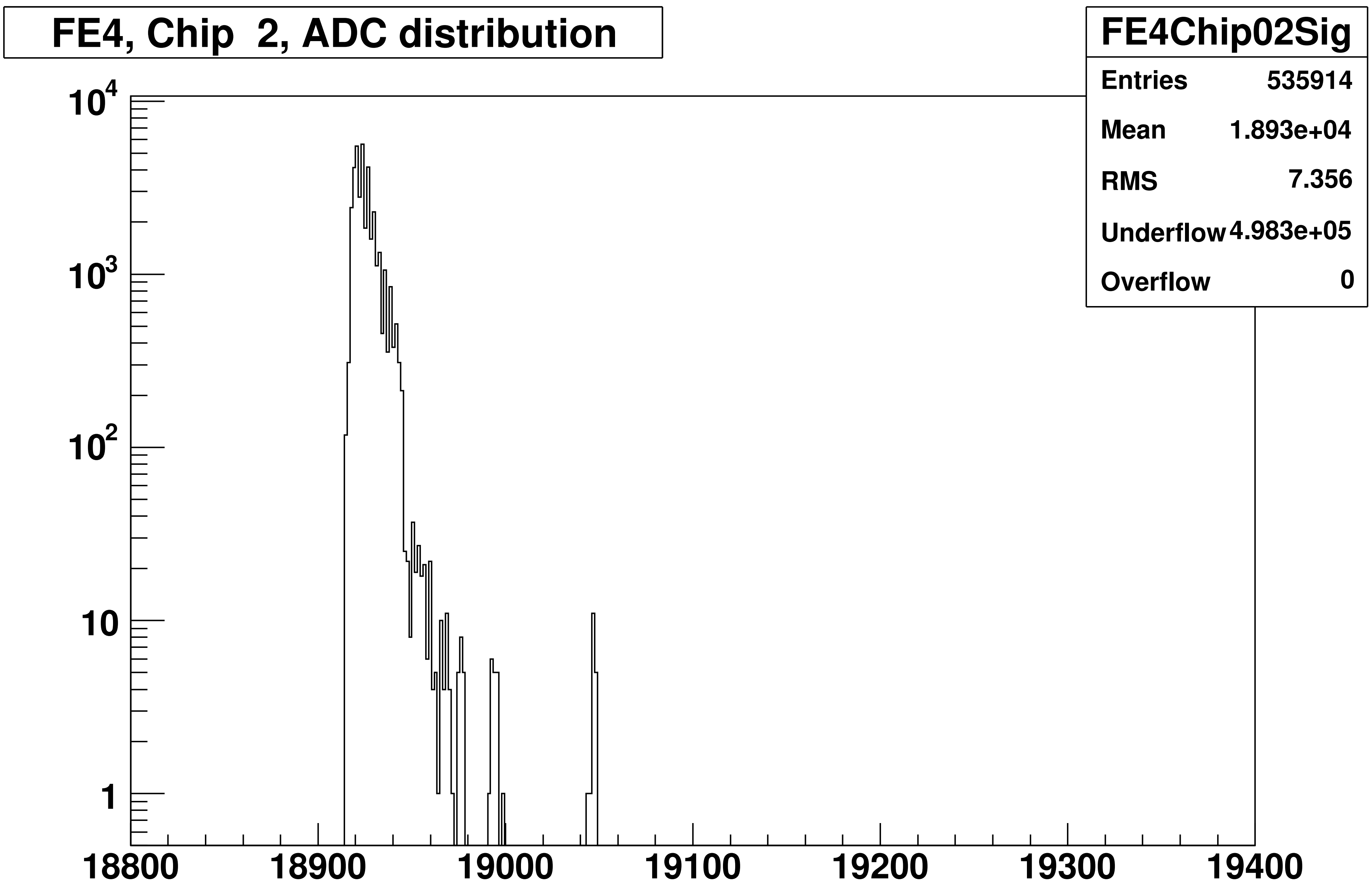

FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 535914
Mean 1.909e+04
RMS 6.908
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

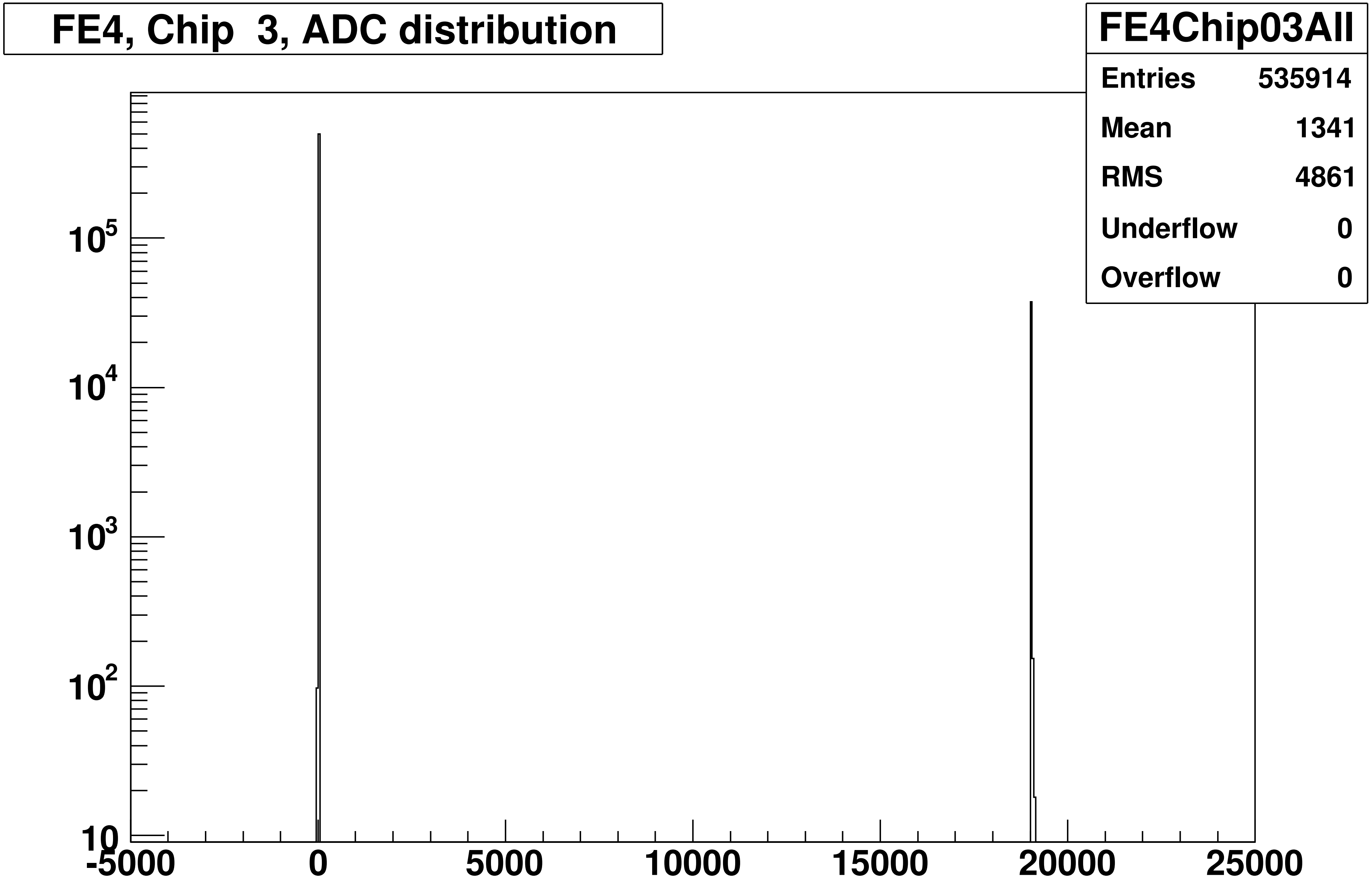


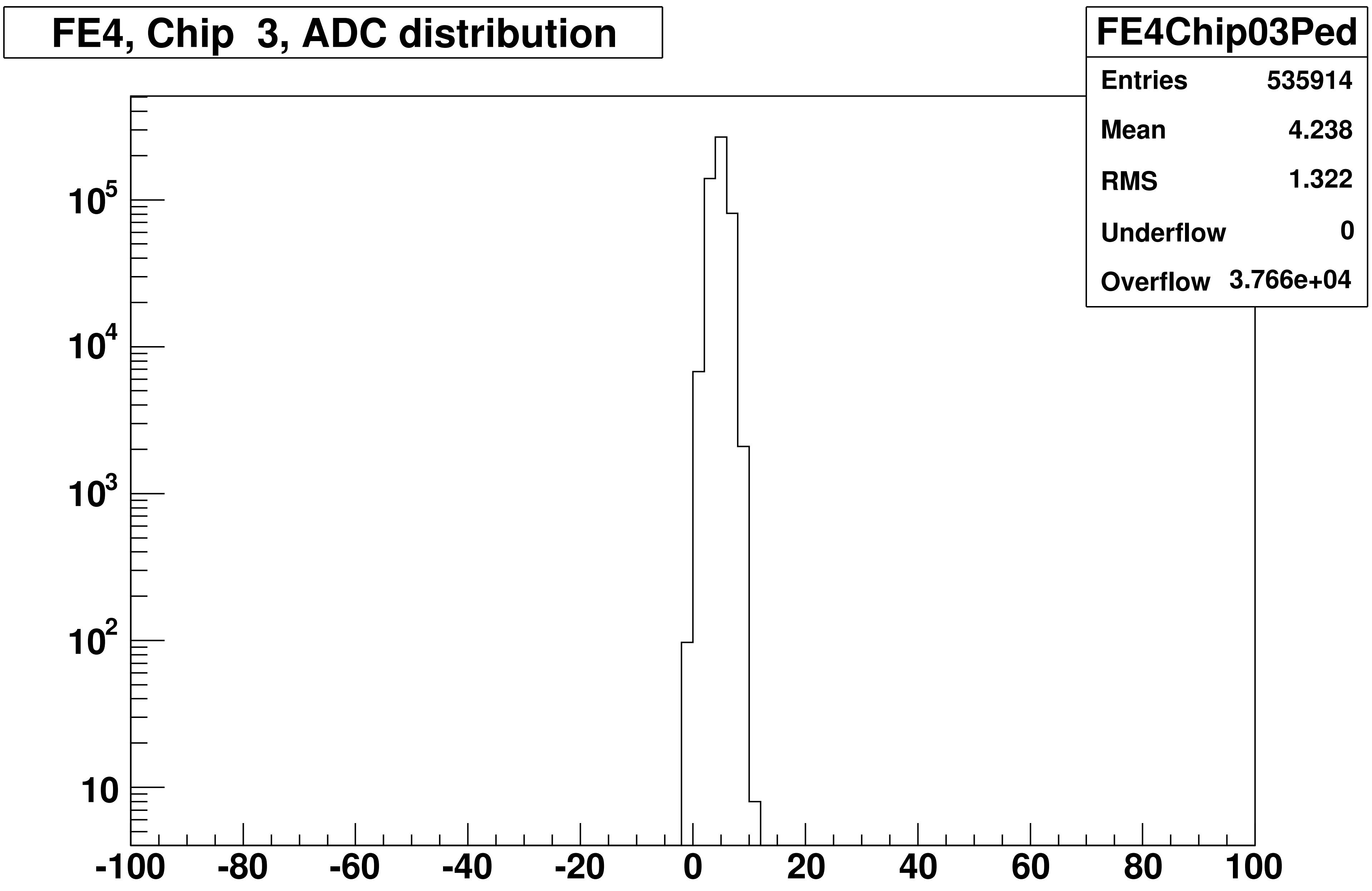
FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 535914
Mean 1319
RMS 4840
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

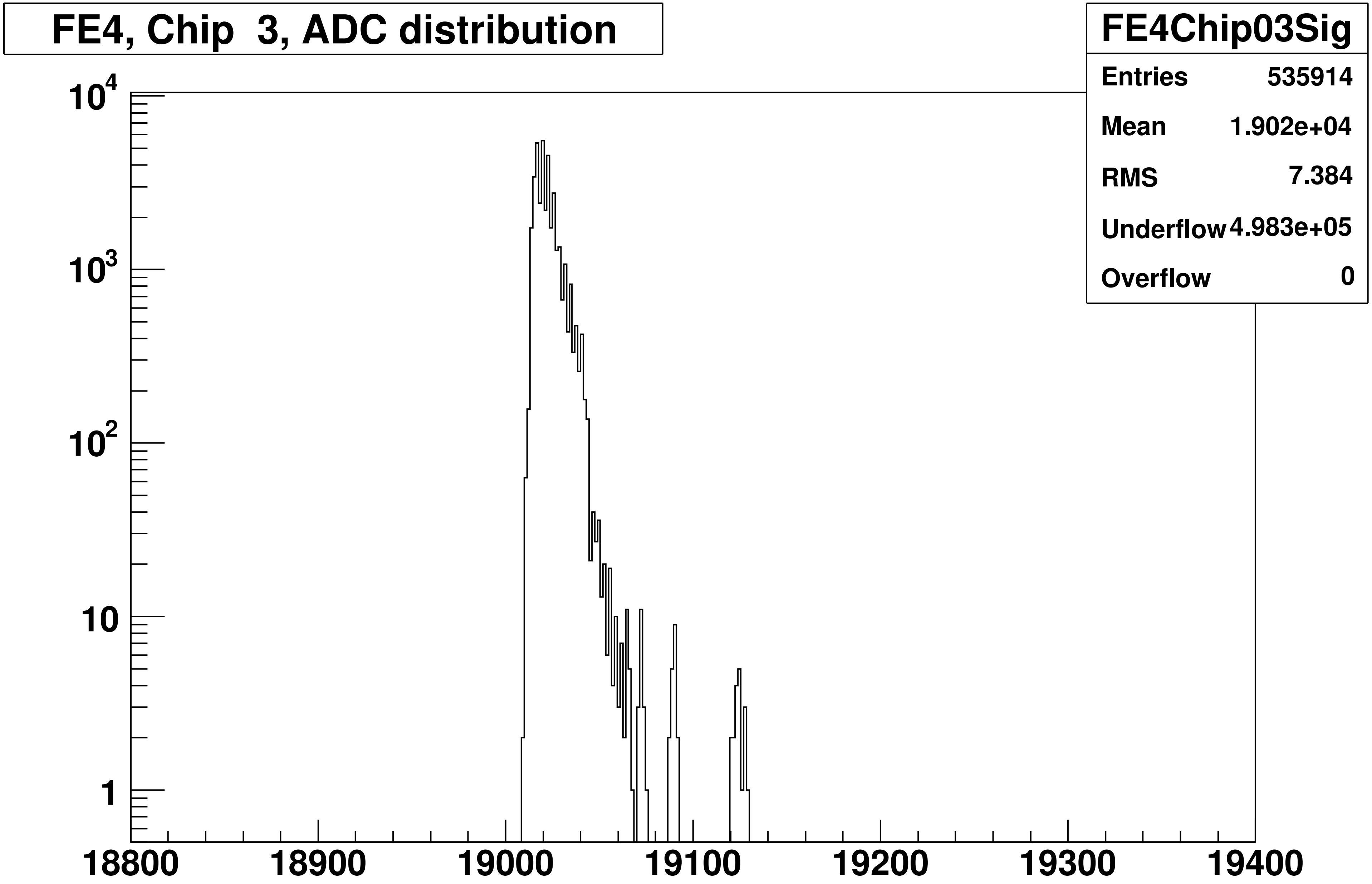


FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 535914
Mean -12.03
RMS 1.332
Underflow 0
Overflow 3.766e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

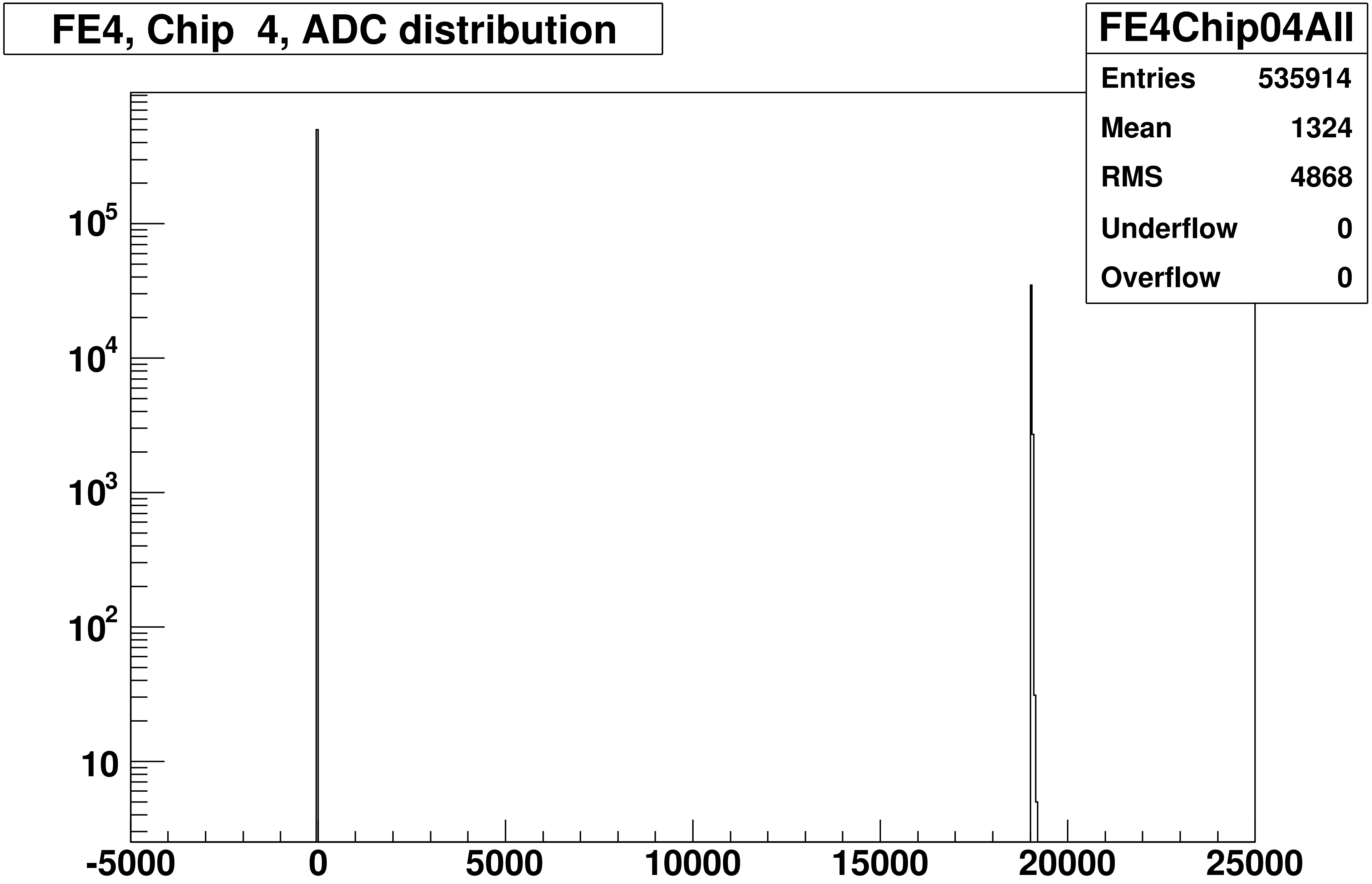

FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 535914
Mean 1.893e+04
RMS 7.356
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

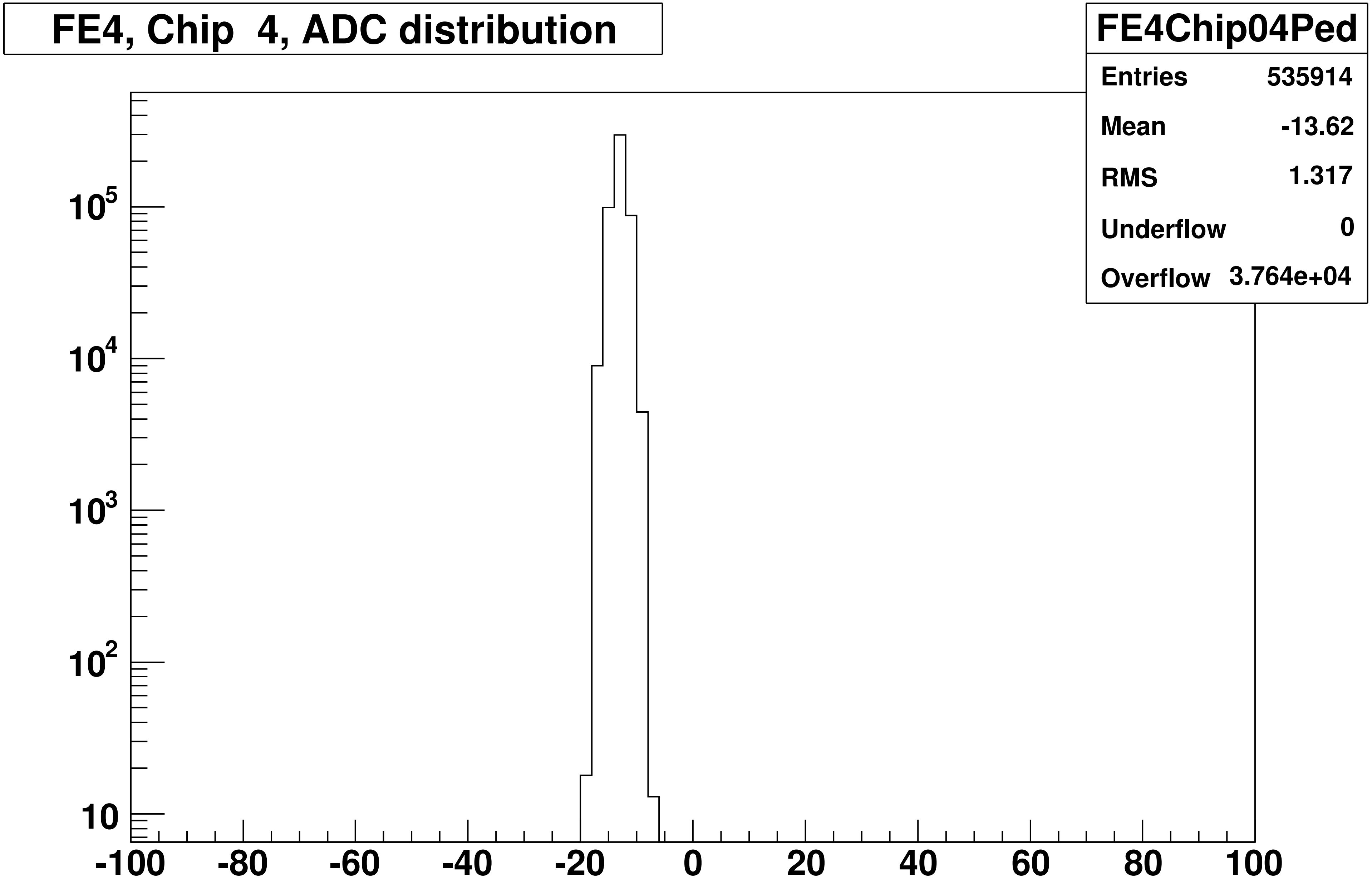

FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 535914
Mean 1341
RMS 4861
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 535914
Mean 4.238
RMS 1.322
Underflow 0
Overflow 3.766e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

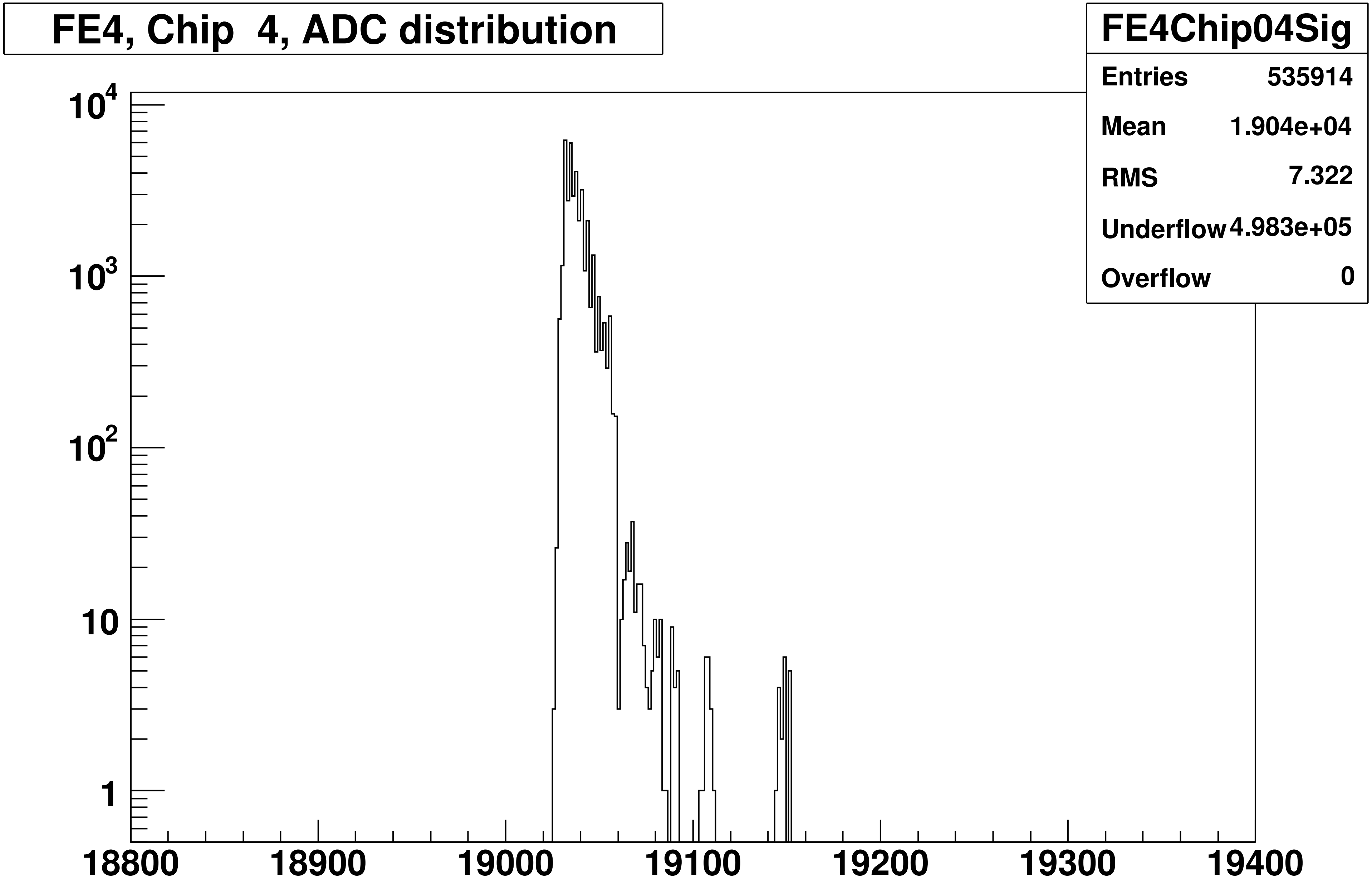


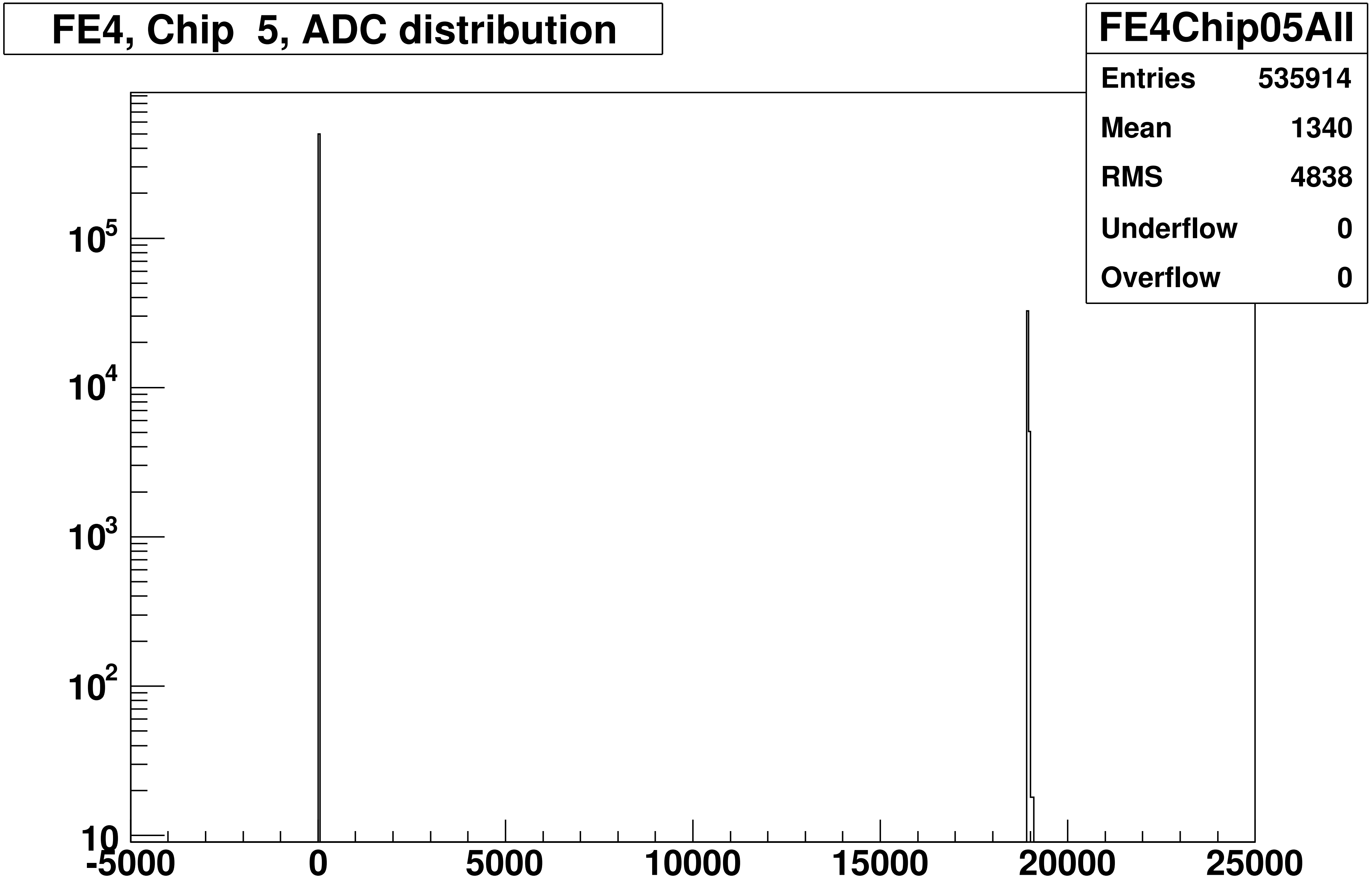
FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 535914
Mean 1.902e+04
RMS 7.384
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

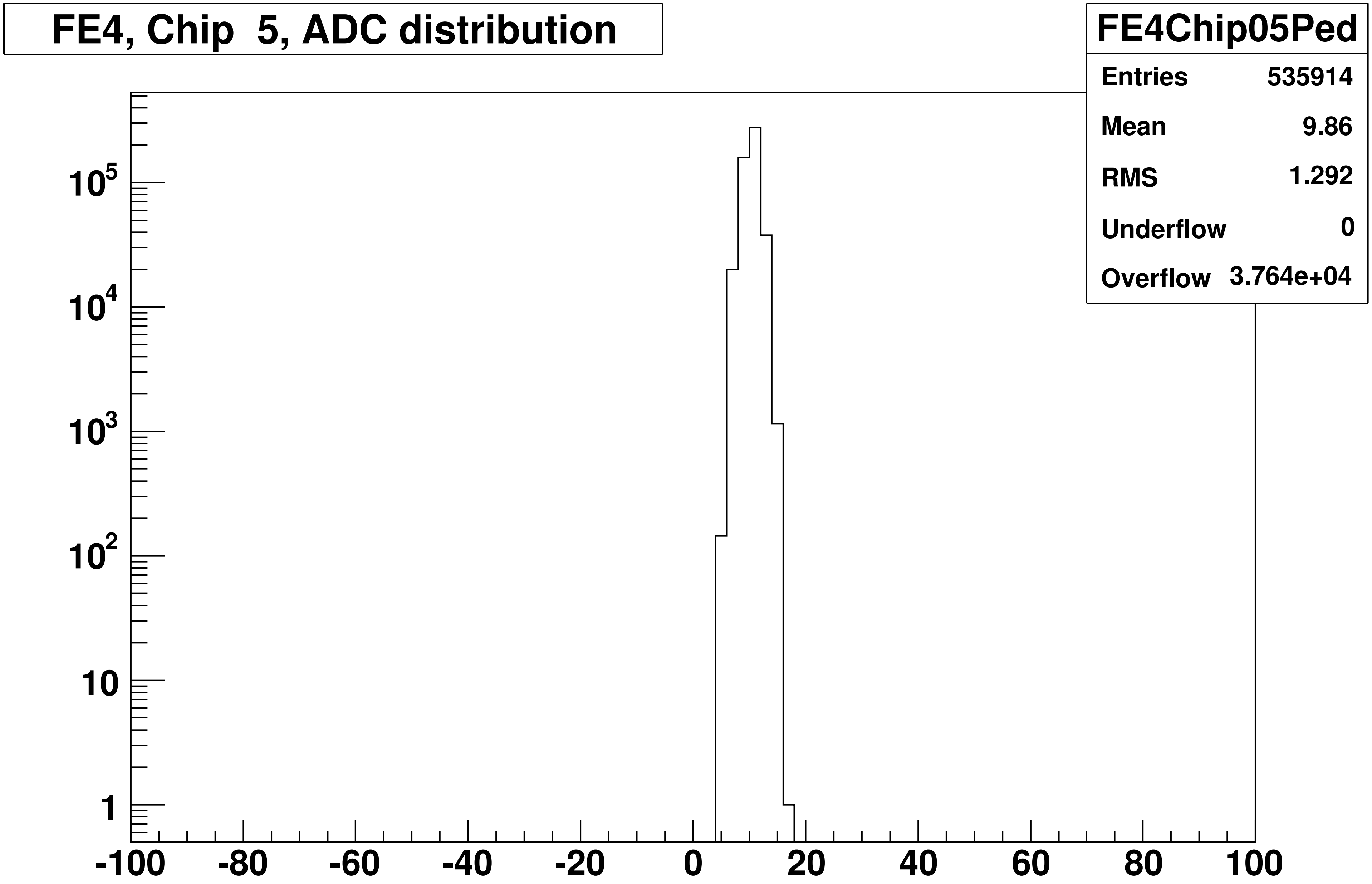

FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 535914
Mean 1324
RMS 4868
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

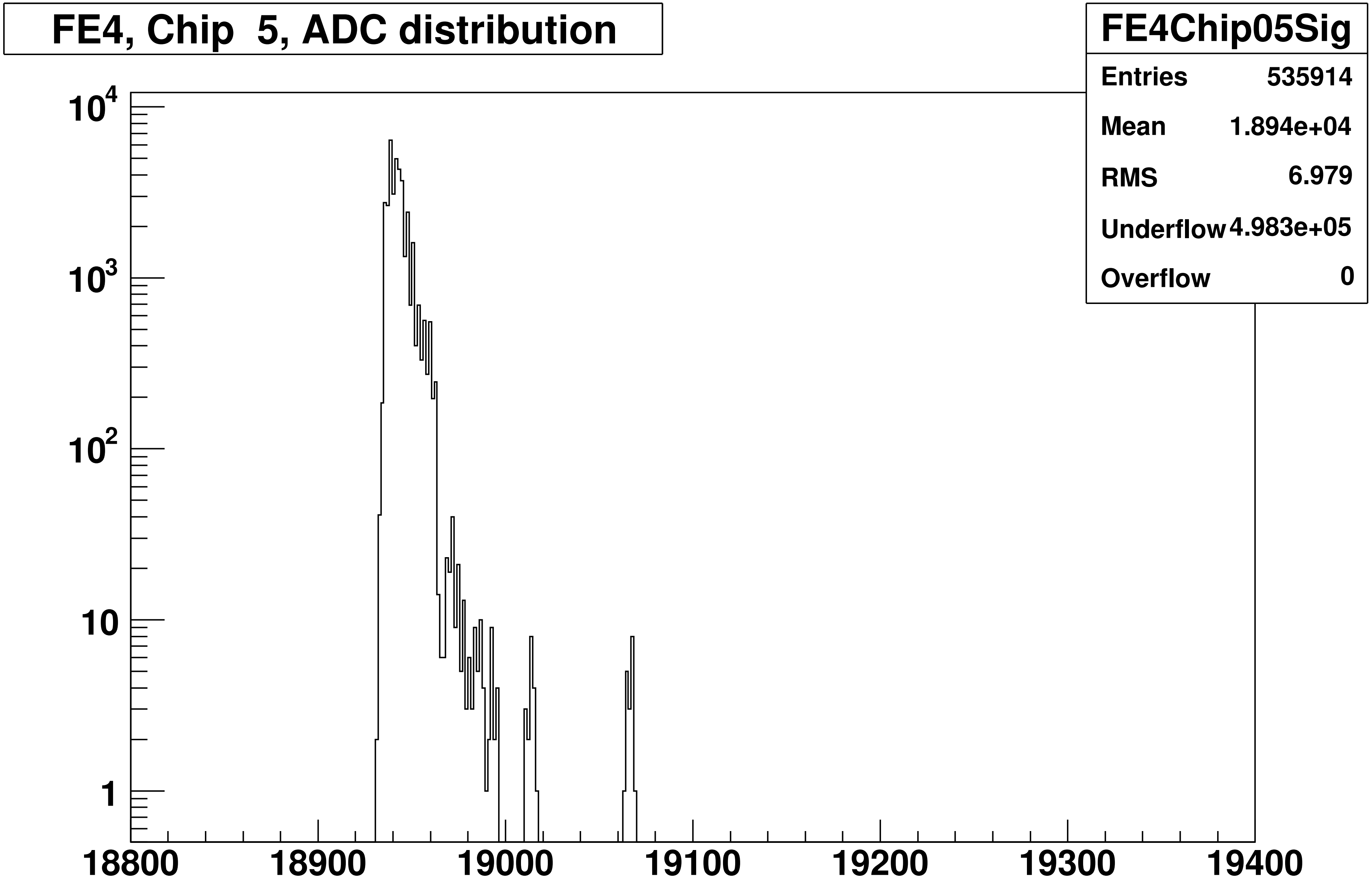


FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 535914
Mean -13.62
RMS 1.317
Underflow 0
Overflow 3.764e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

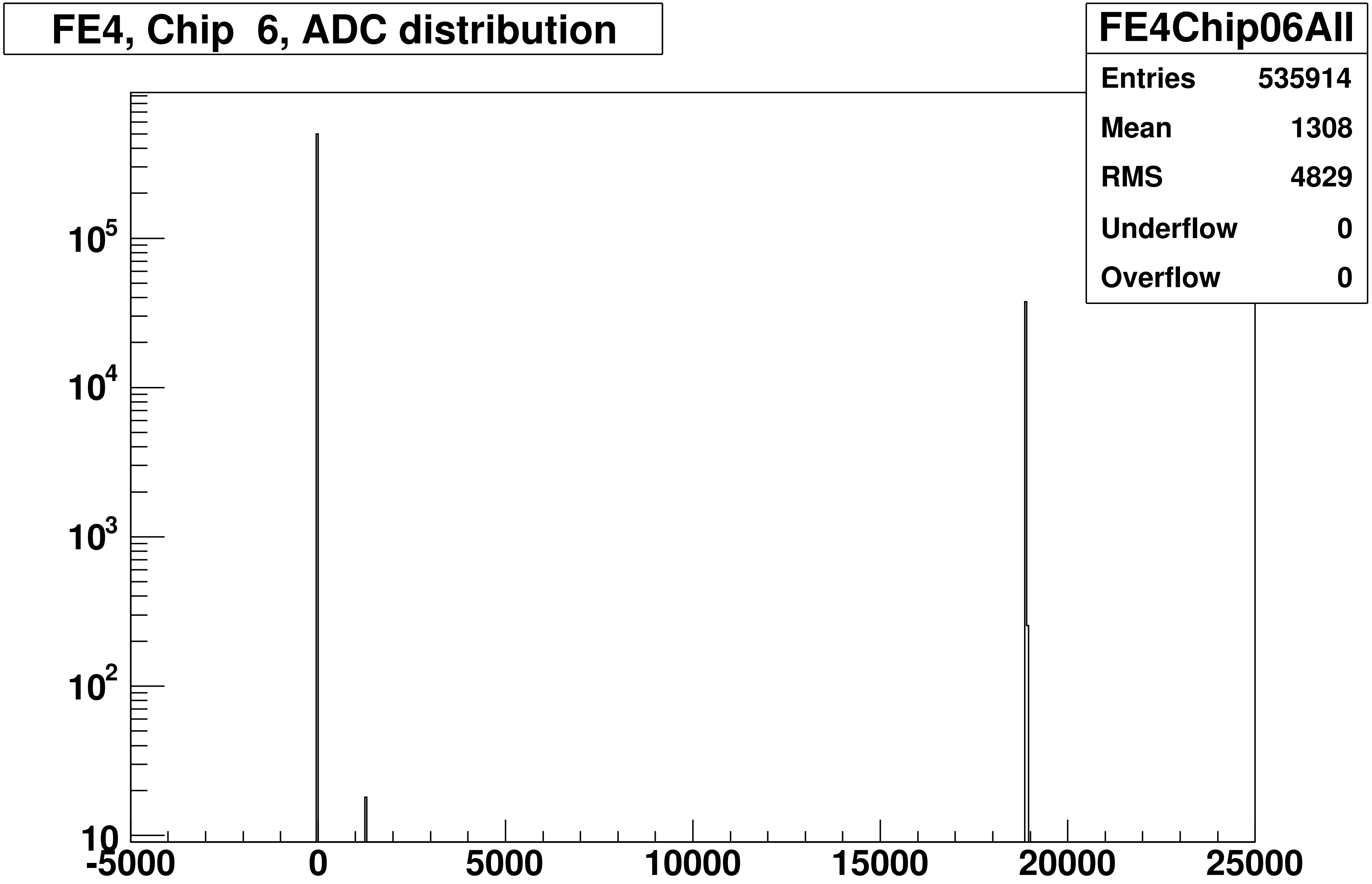

FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 535914
Mean 1.904e+04
RMS 7.322
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

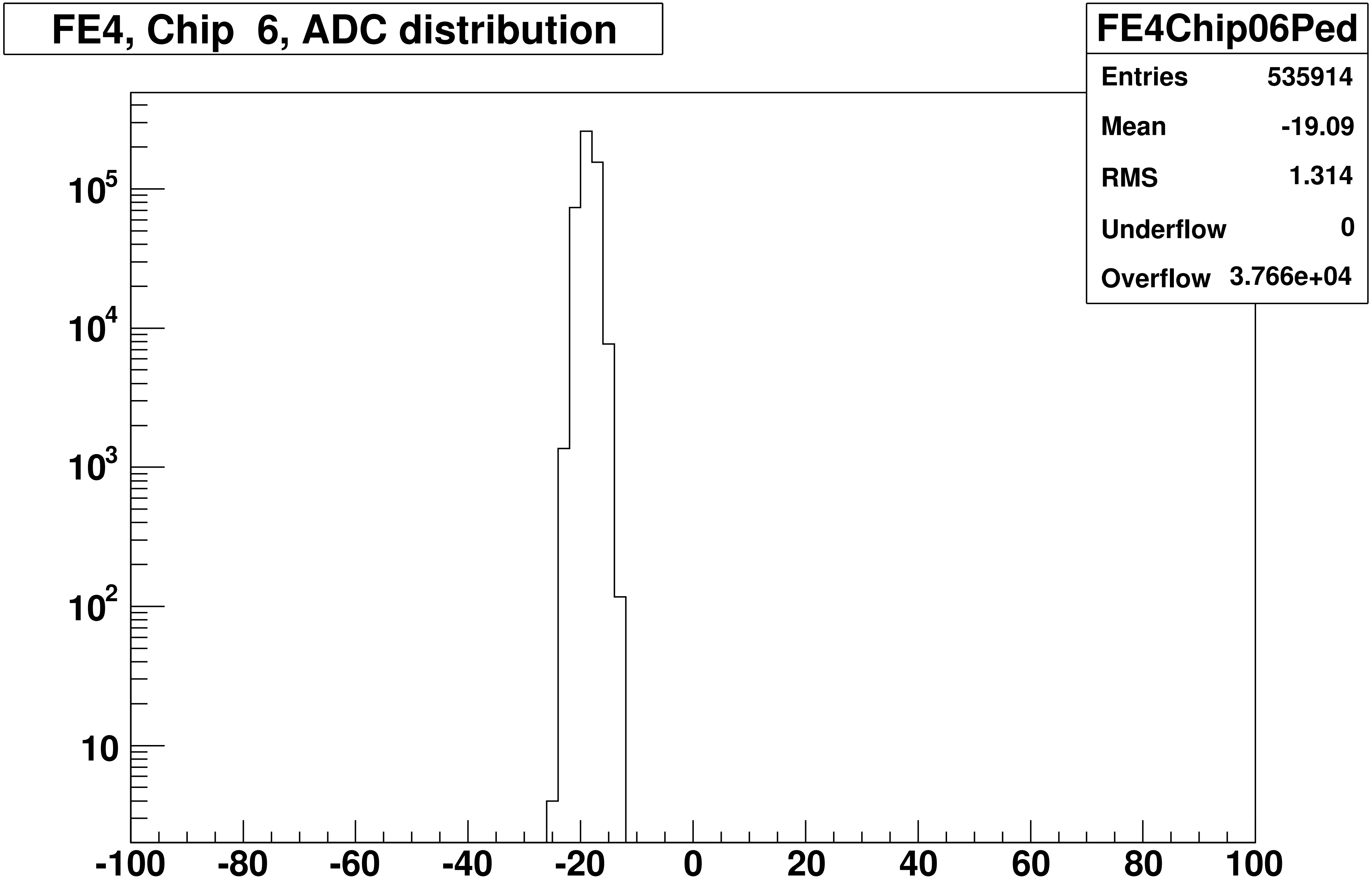

FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 535914
Mean 1340
RMS 4838
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

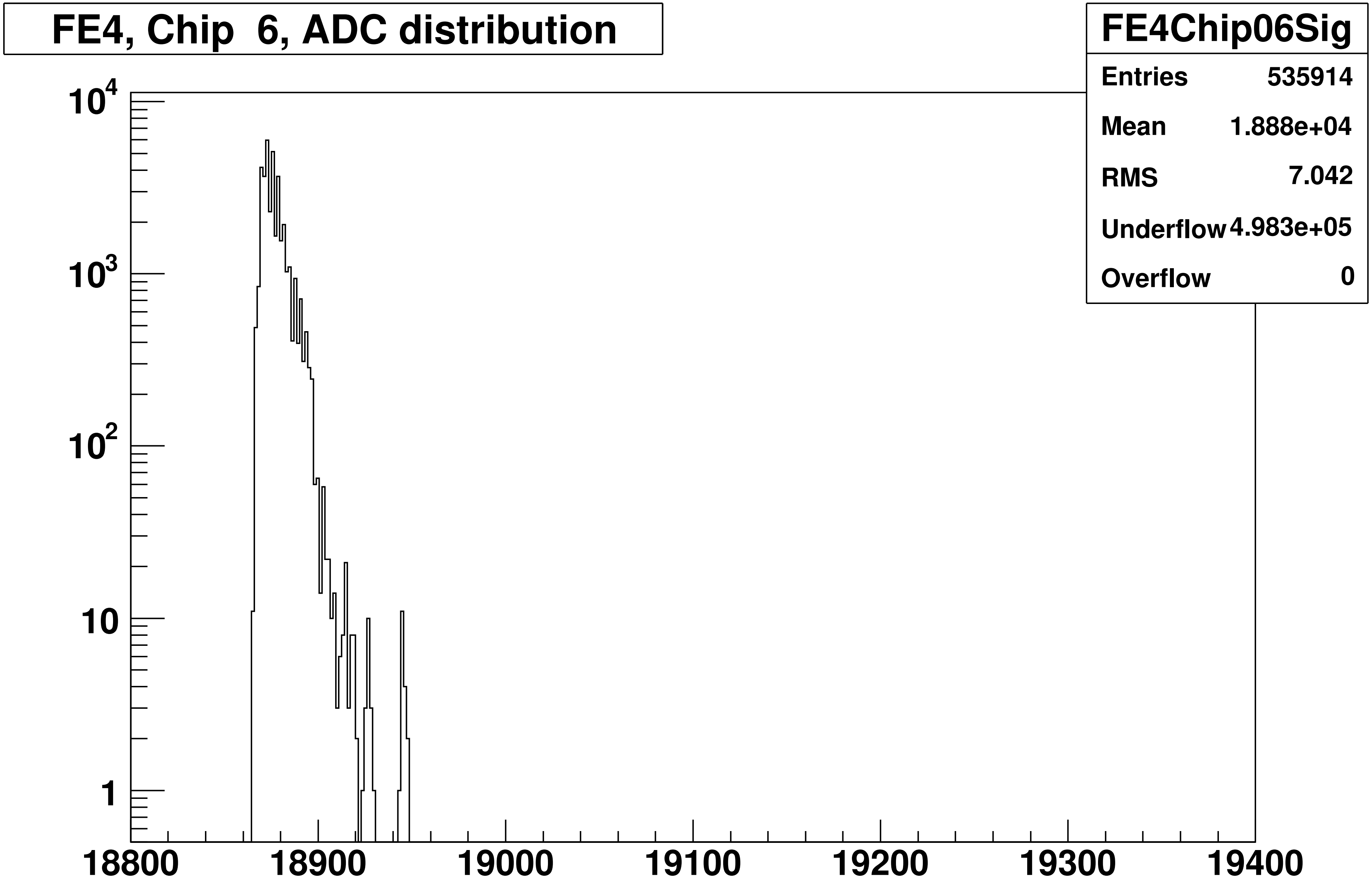

FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 535914
Mean 9.86
RMS 1.292
Underflow 0
Overflow 3.764e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



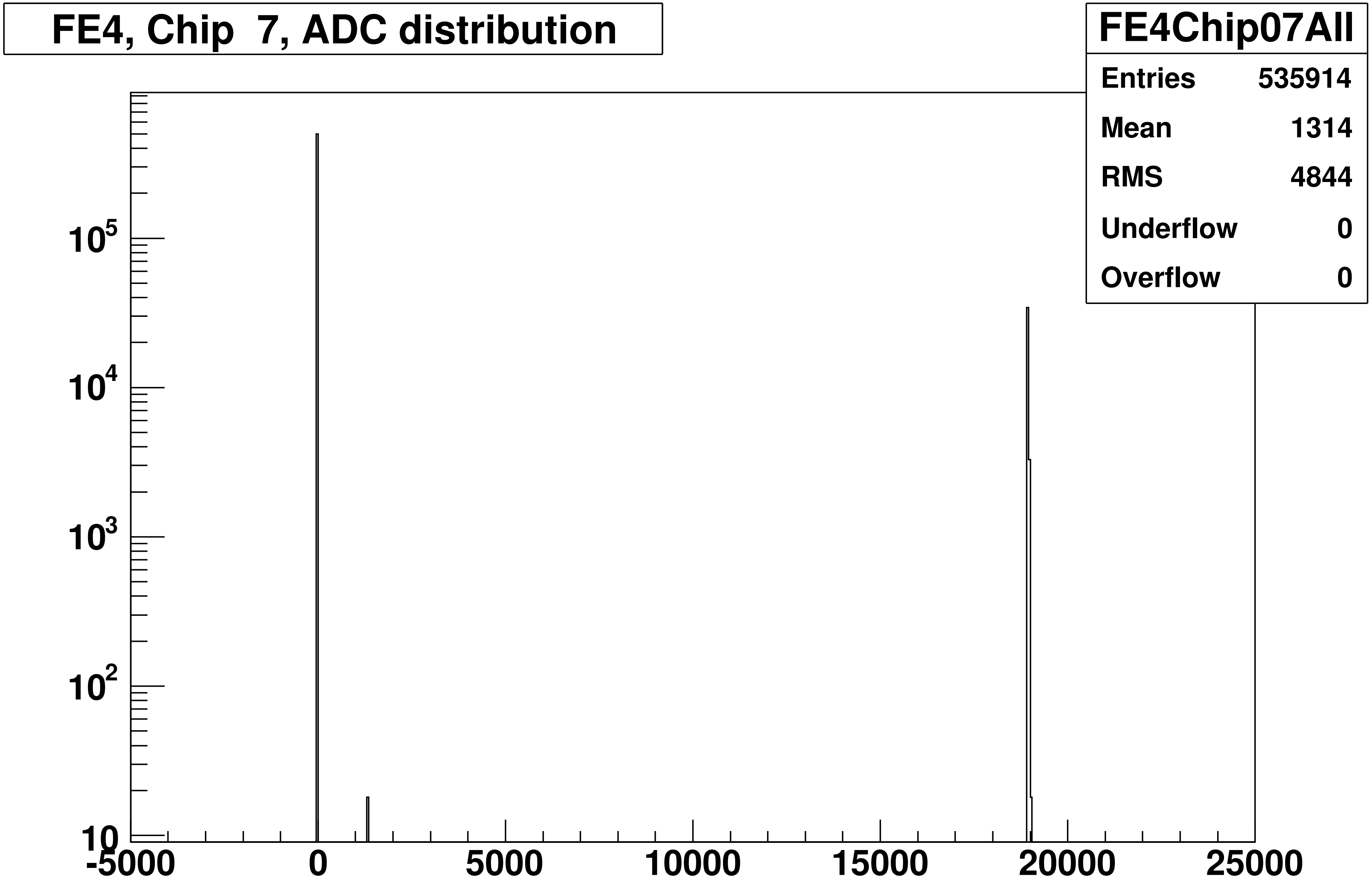
FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 535914
Mean 1.894e+04
RMS 6.979
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 535914
Mean 1308
RMS 4829
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

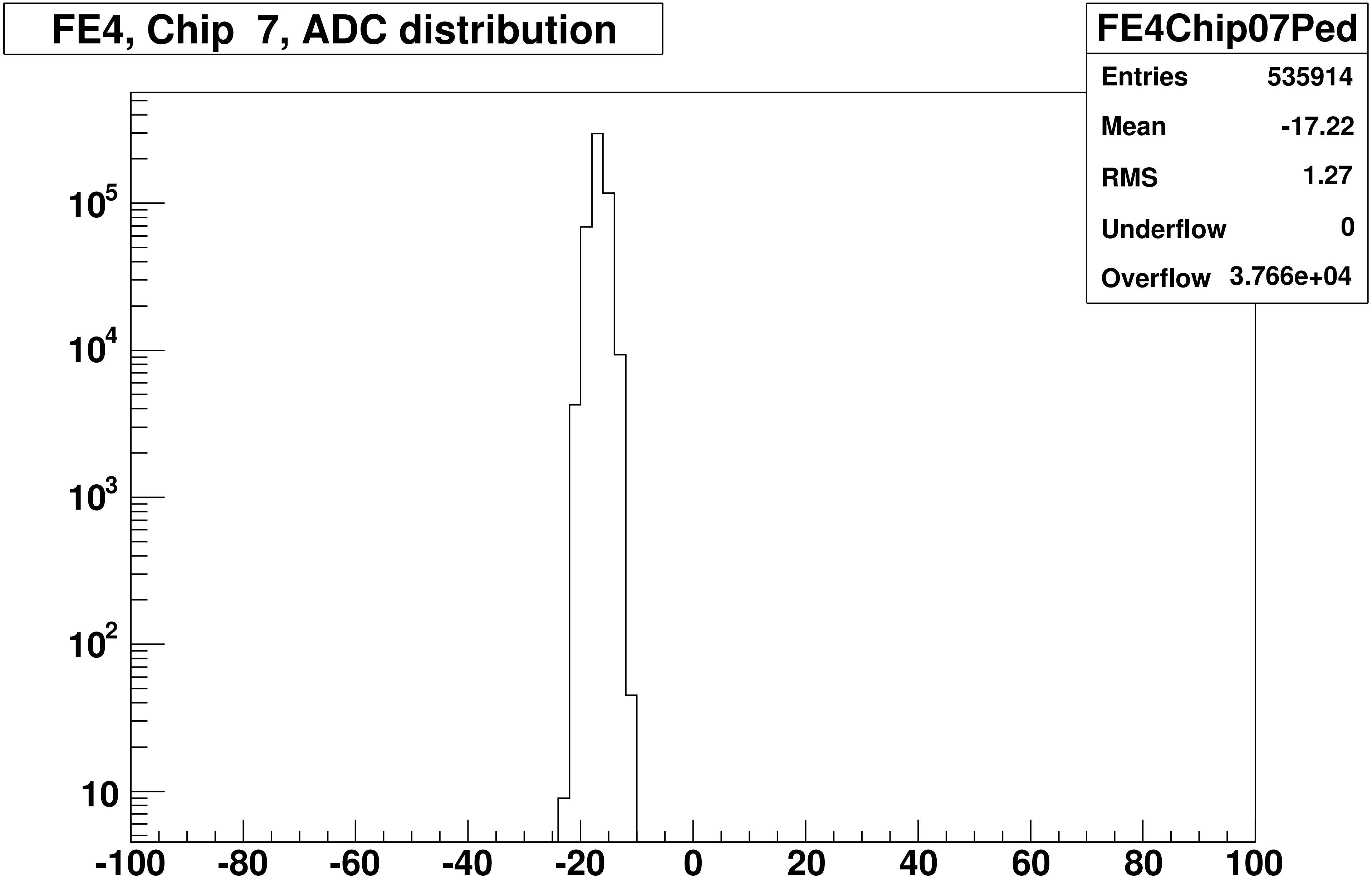

FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 535914
Mean -19.09
RMS 1.314
Underflow 0
Overflow 3.766e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

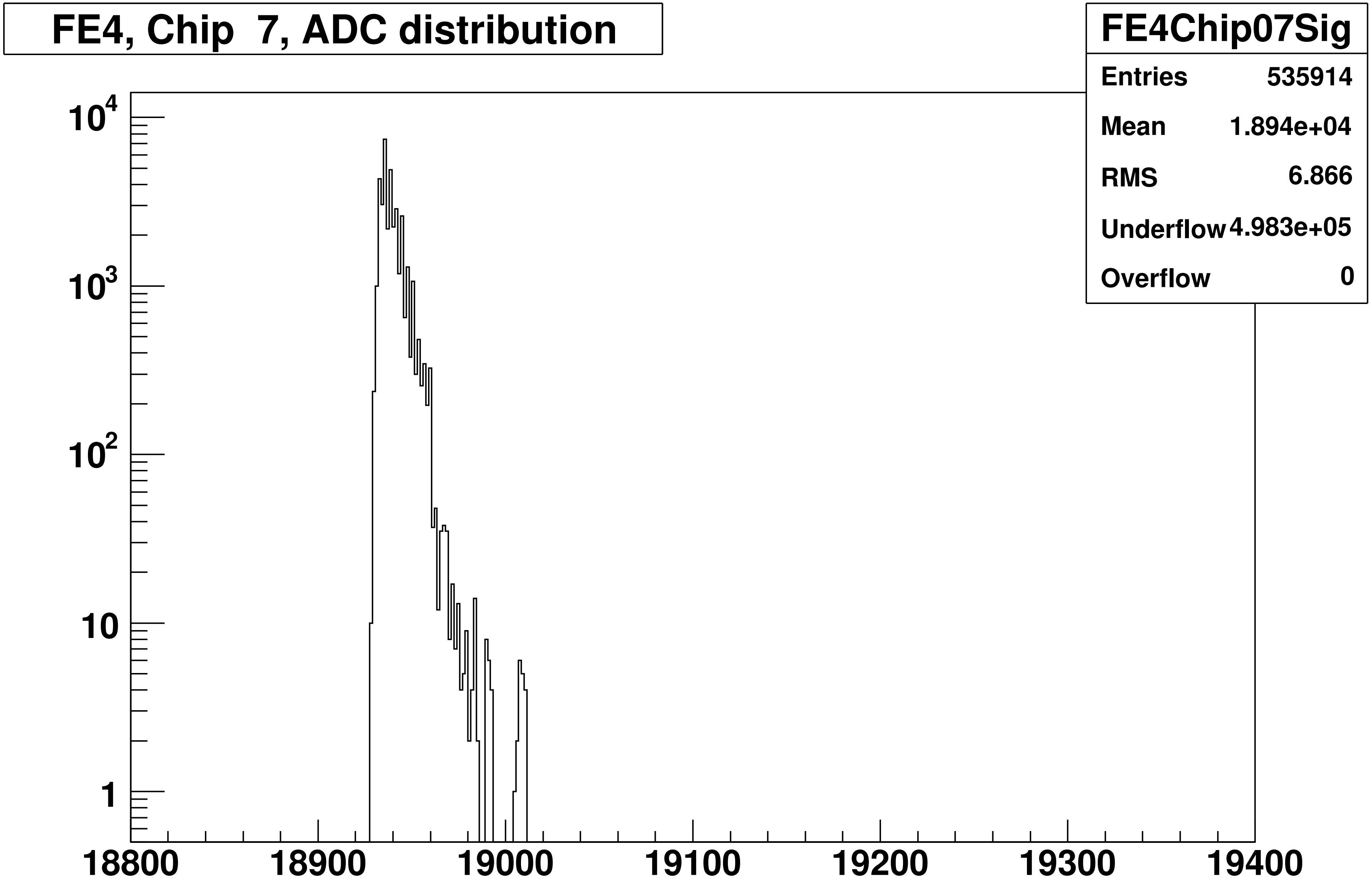


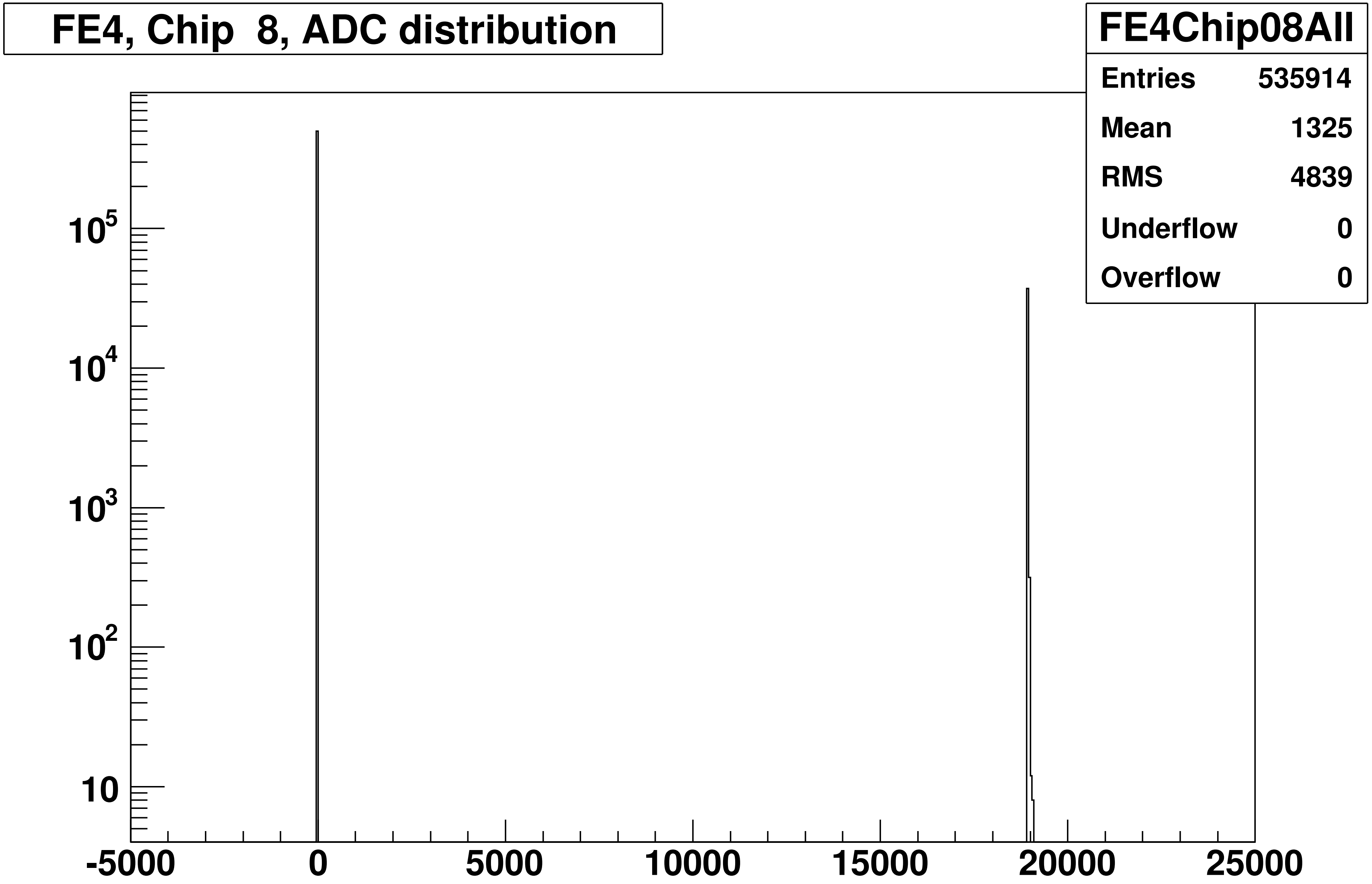
FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 535914
Mean 1.888e+04
RMS 7.042
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

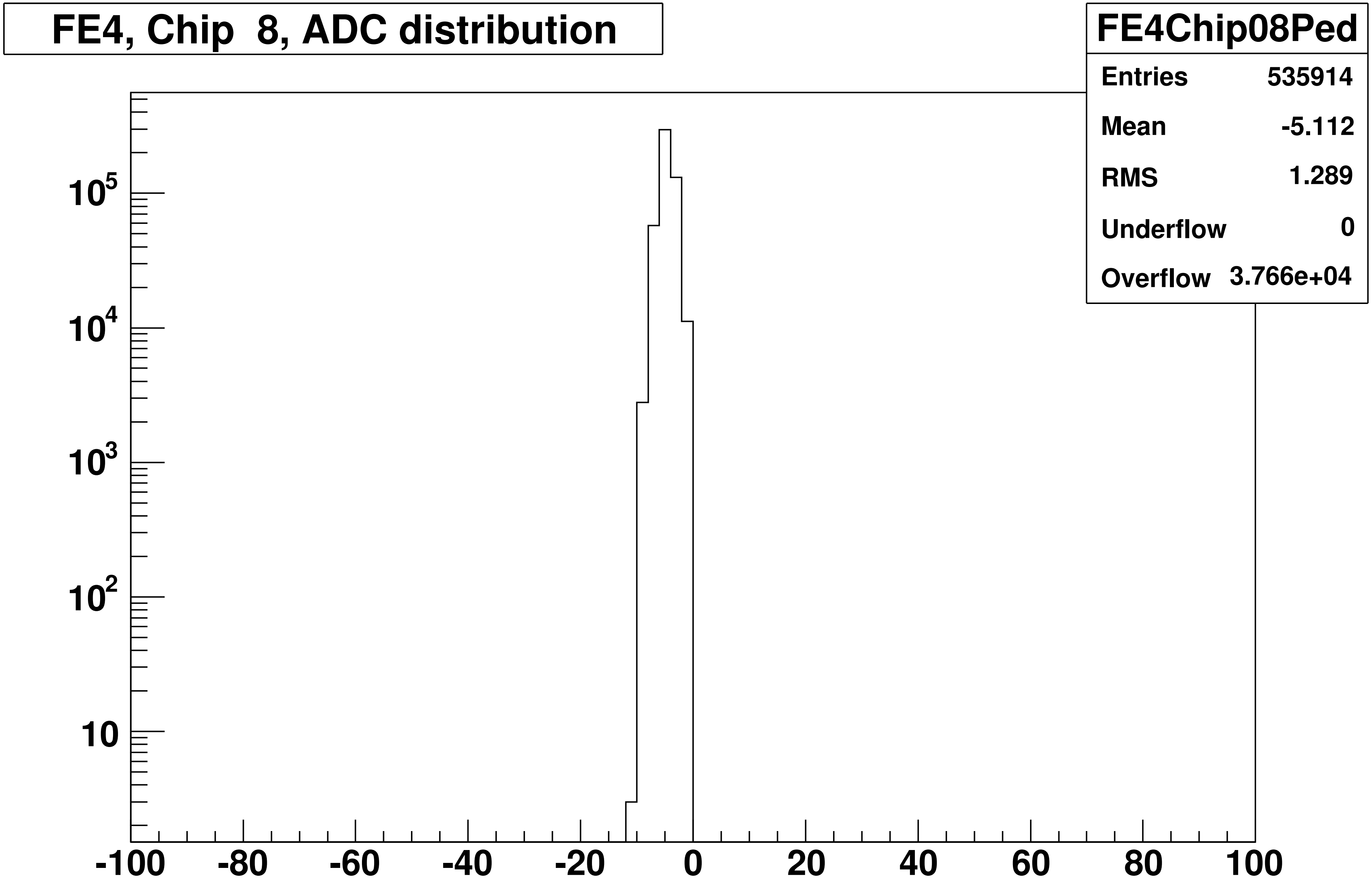


FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 535914
Mean 1314
RMS 4844
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

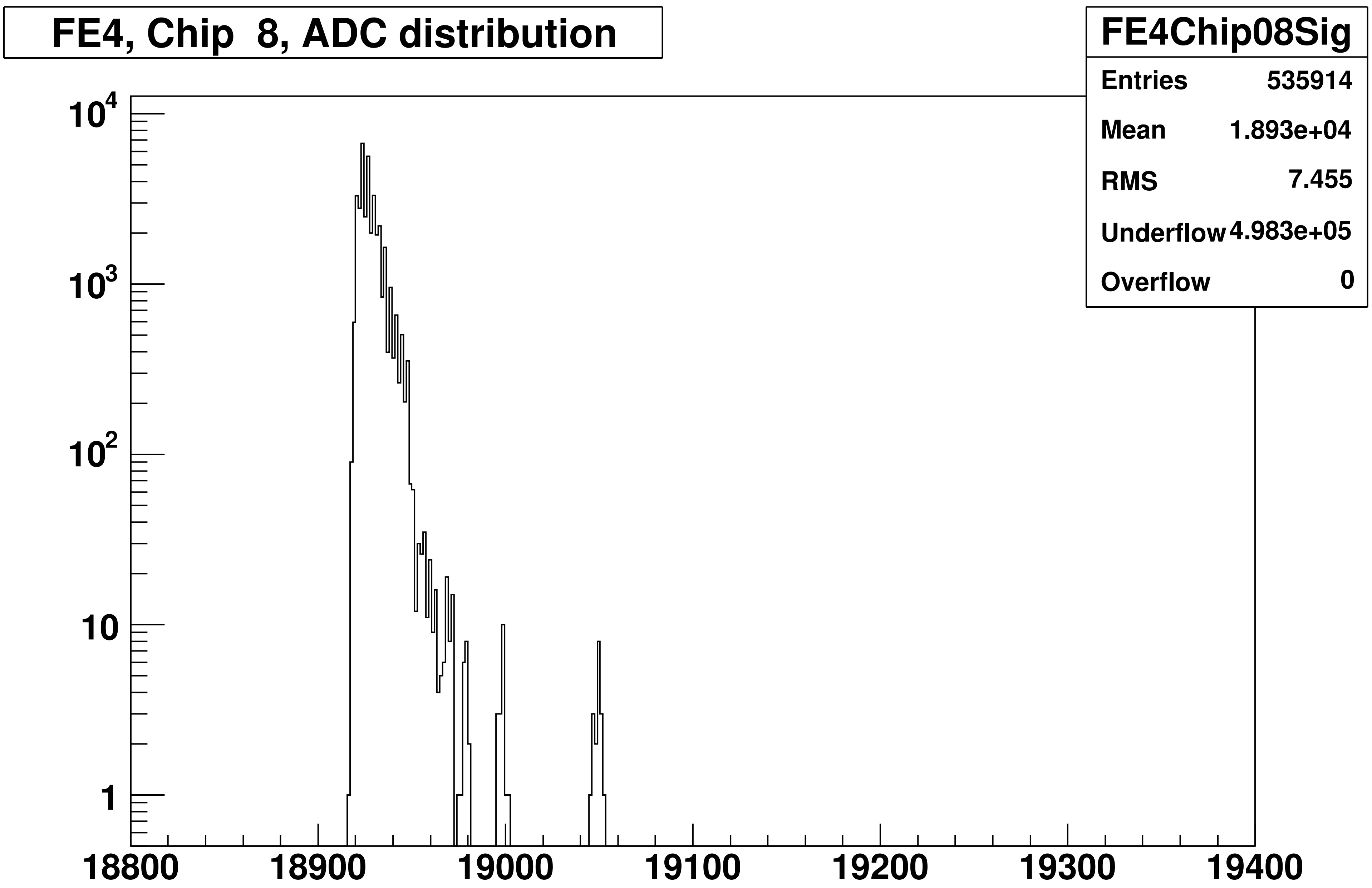

FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 535914
Mean -17.22
RMS 1.27
Underflow 0
Overflow 3.766e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

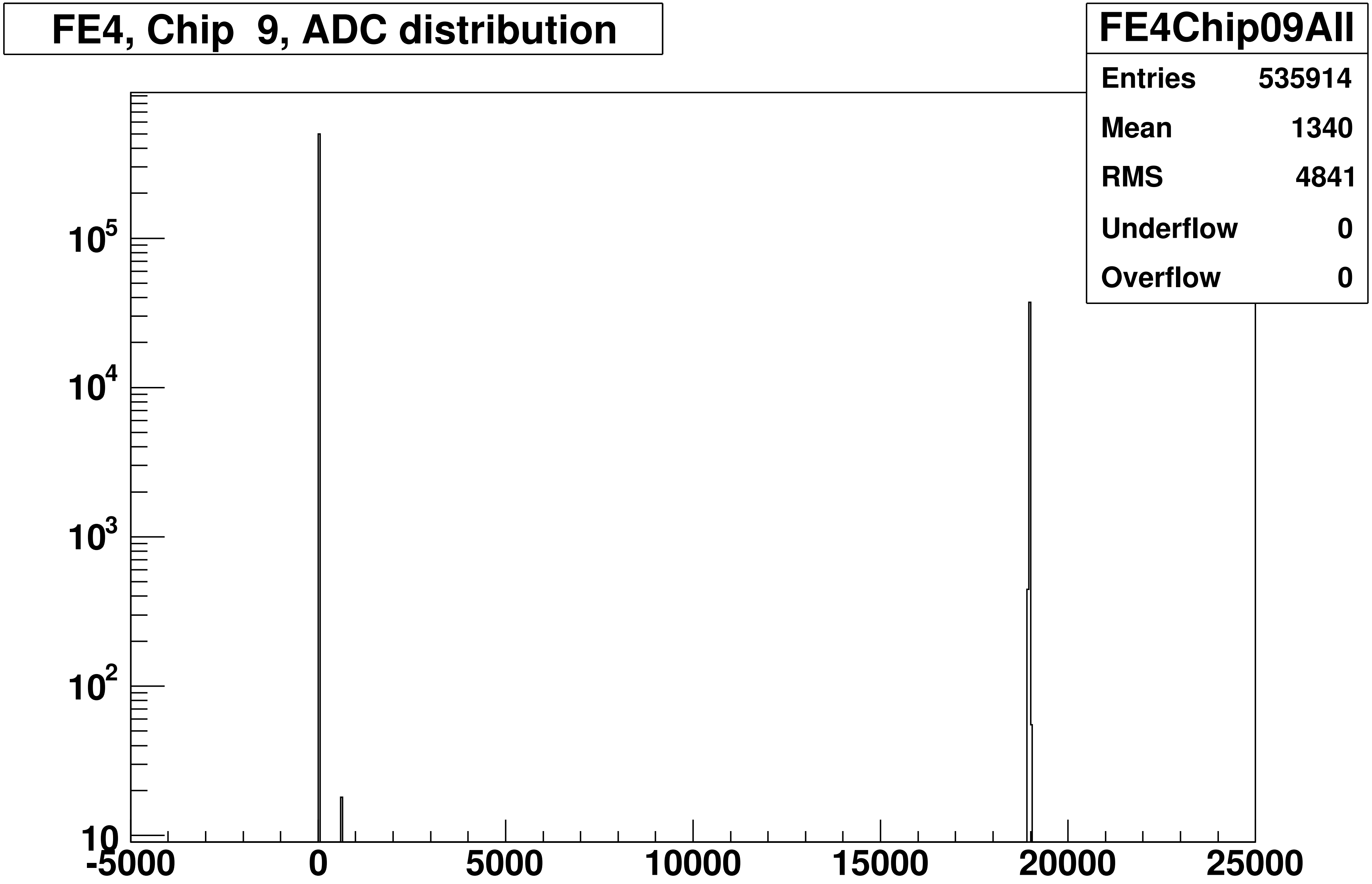

FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 535914
Mean 1.894e+04
RMS 6.866
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 535914
Mean 1325
RMS 4839
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

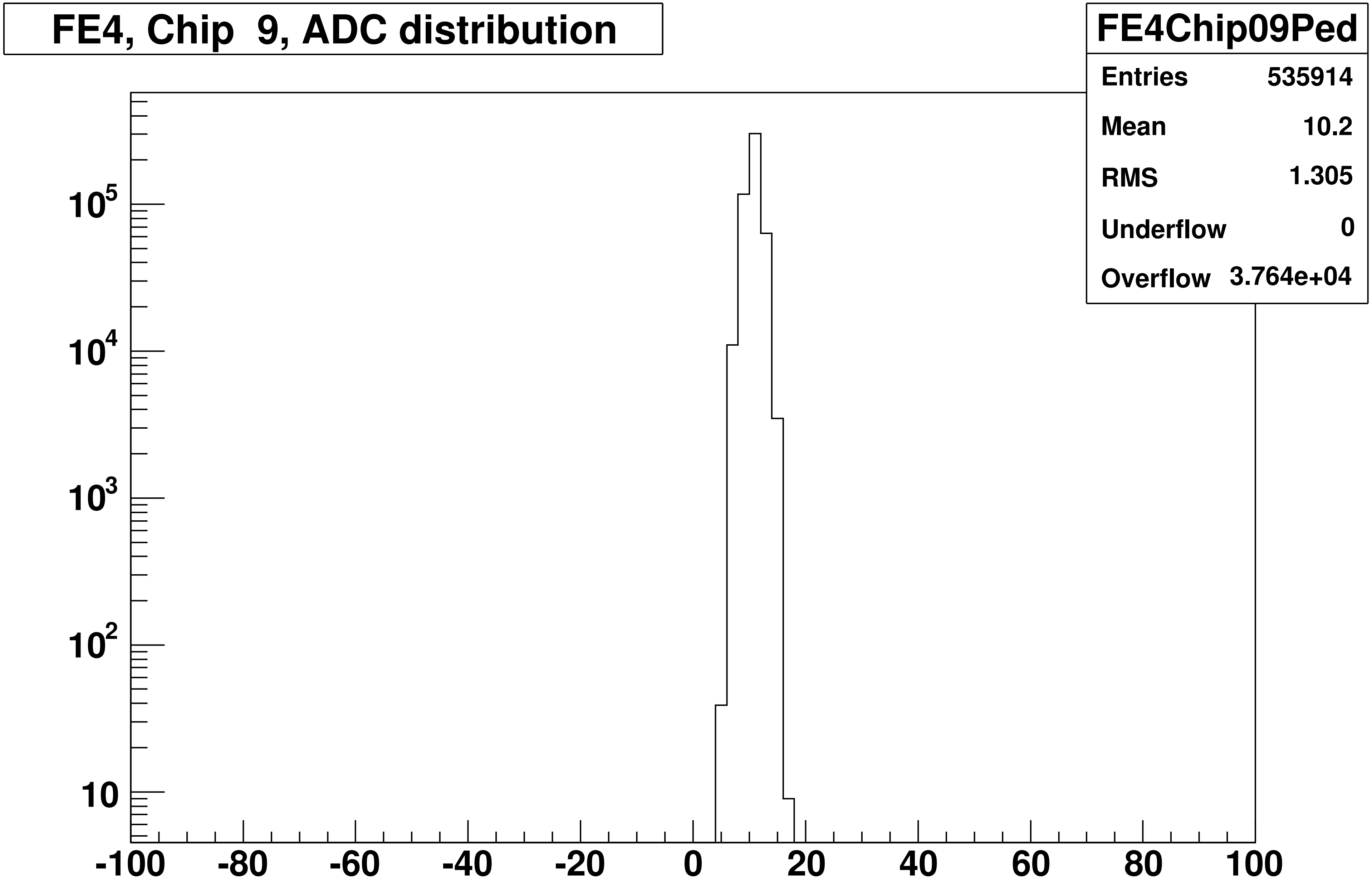


FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 535914
Mean -5.112
RMS 1.289
Underflow 0
Overflow 3.766e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

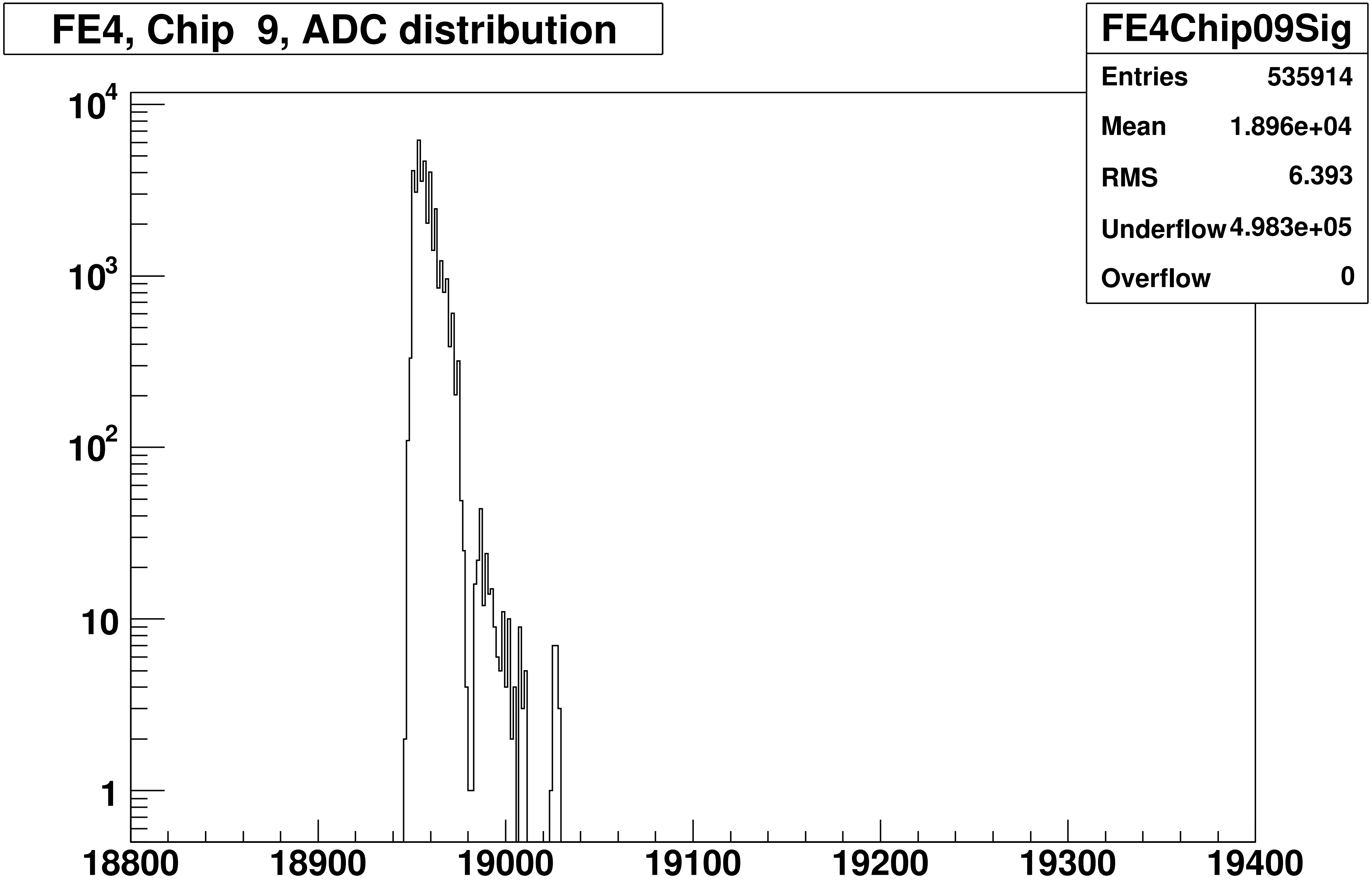

FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 535914
Mean 1.893e+04
RMS 7.455
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

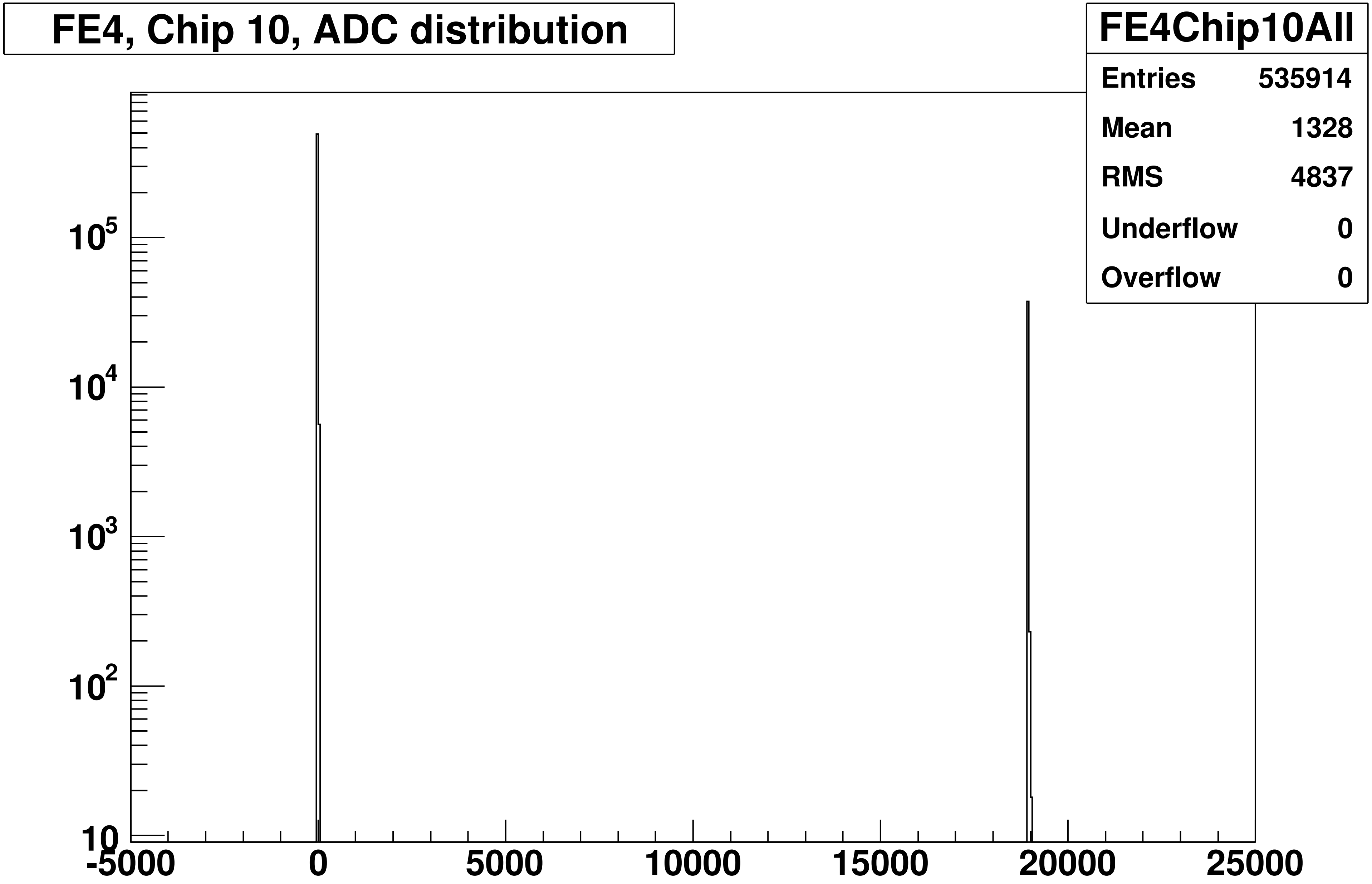


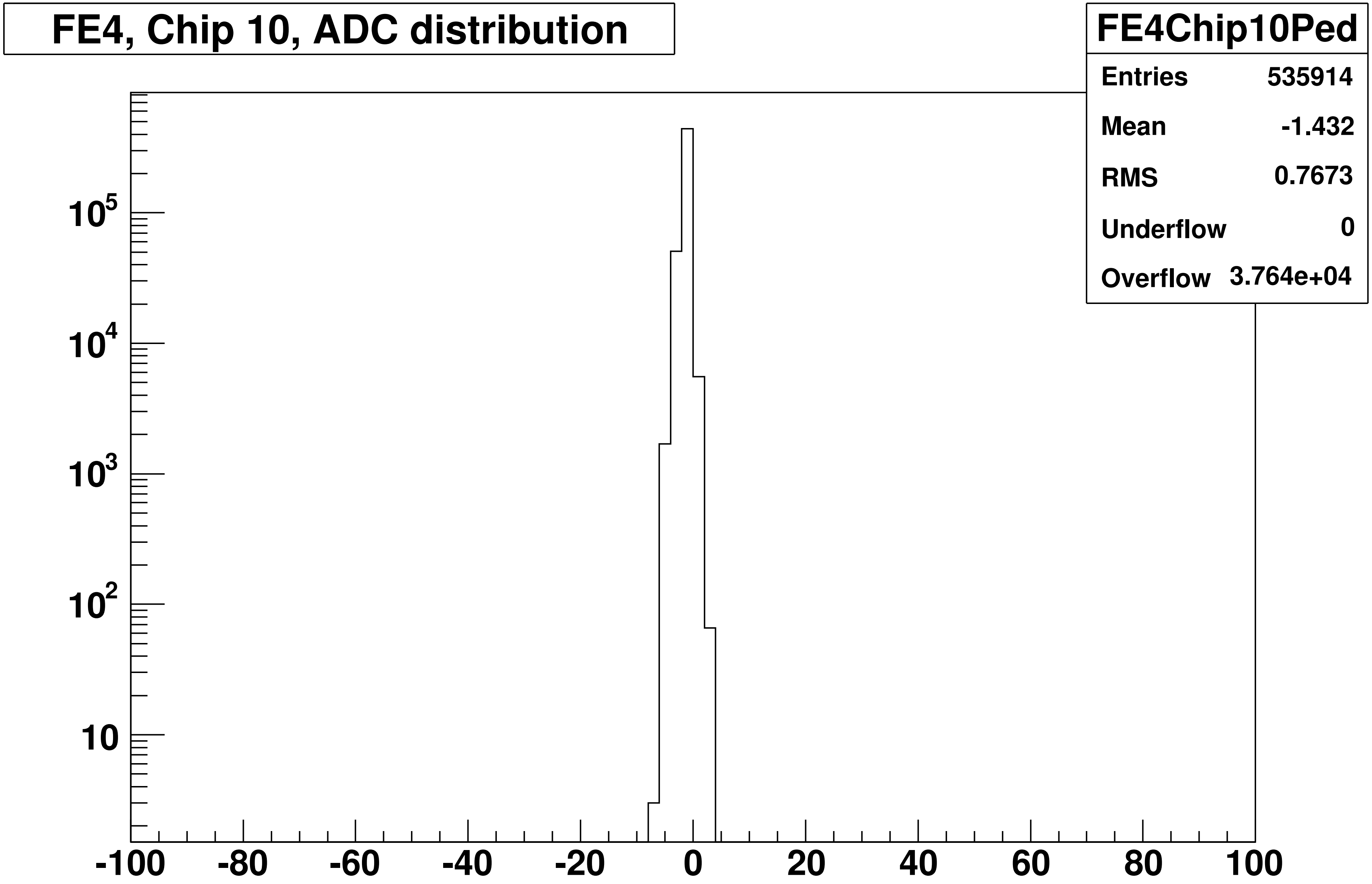
FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 535914
Mean 1340
RMS 4841
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000



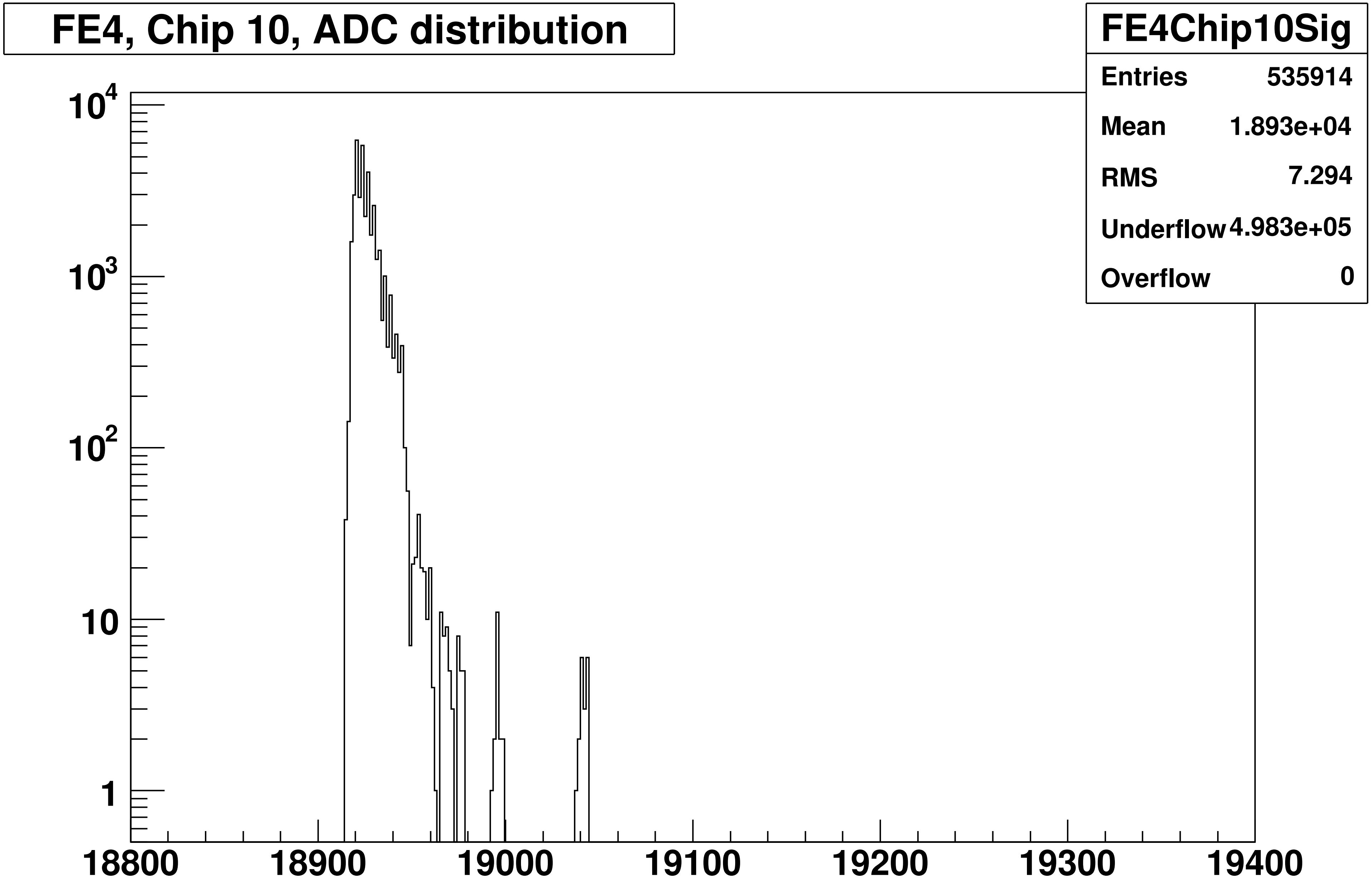
FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 535914
Mean 10.2
RMS 1.305
Underflow 0
Overflow 3.764e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 535914
Mean 1.896e+04
RMS 6.393
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

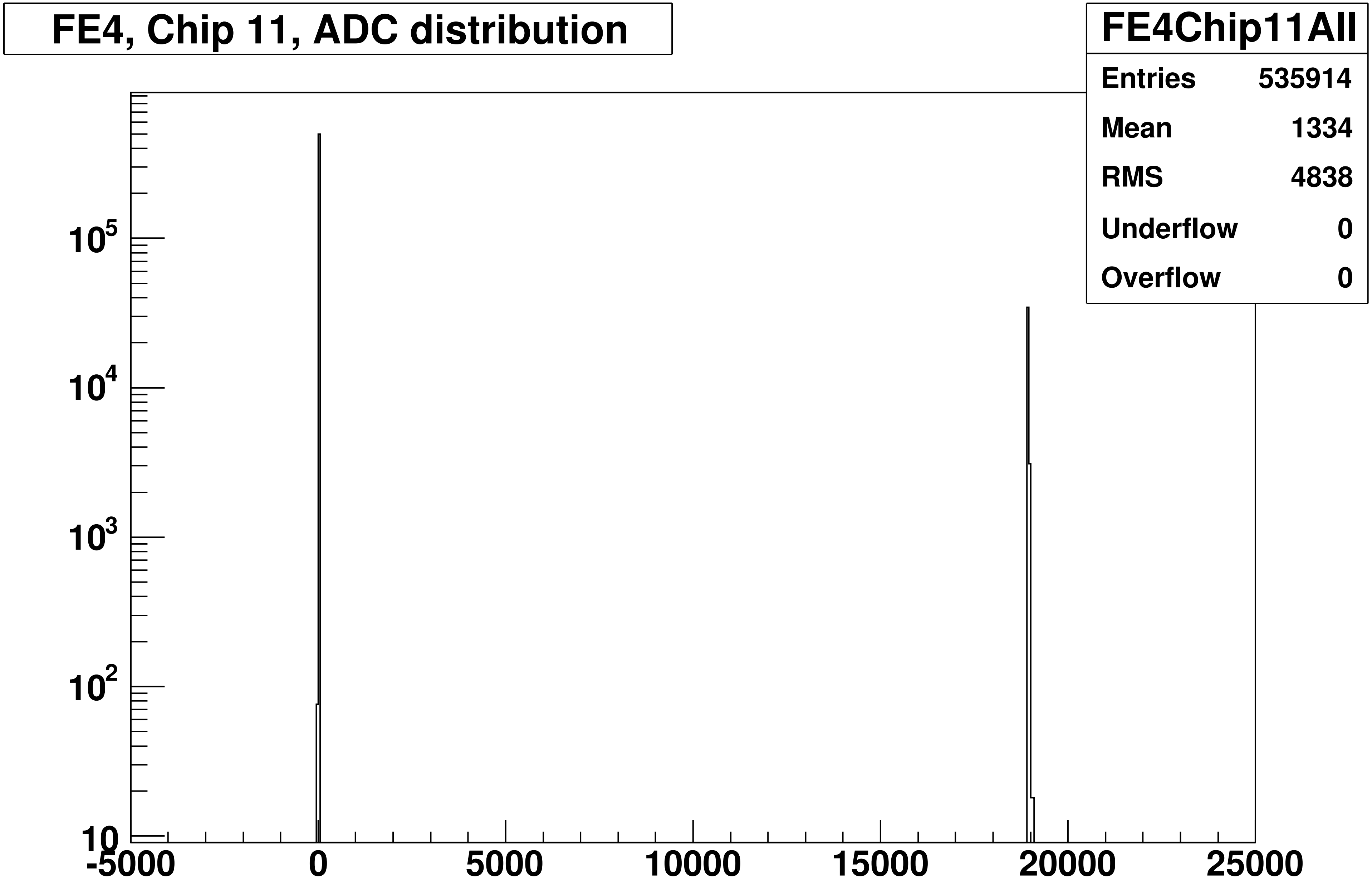

FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 535914
Mean 1328
RMS 4837
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

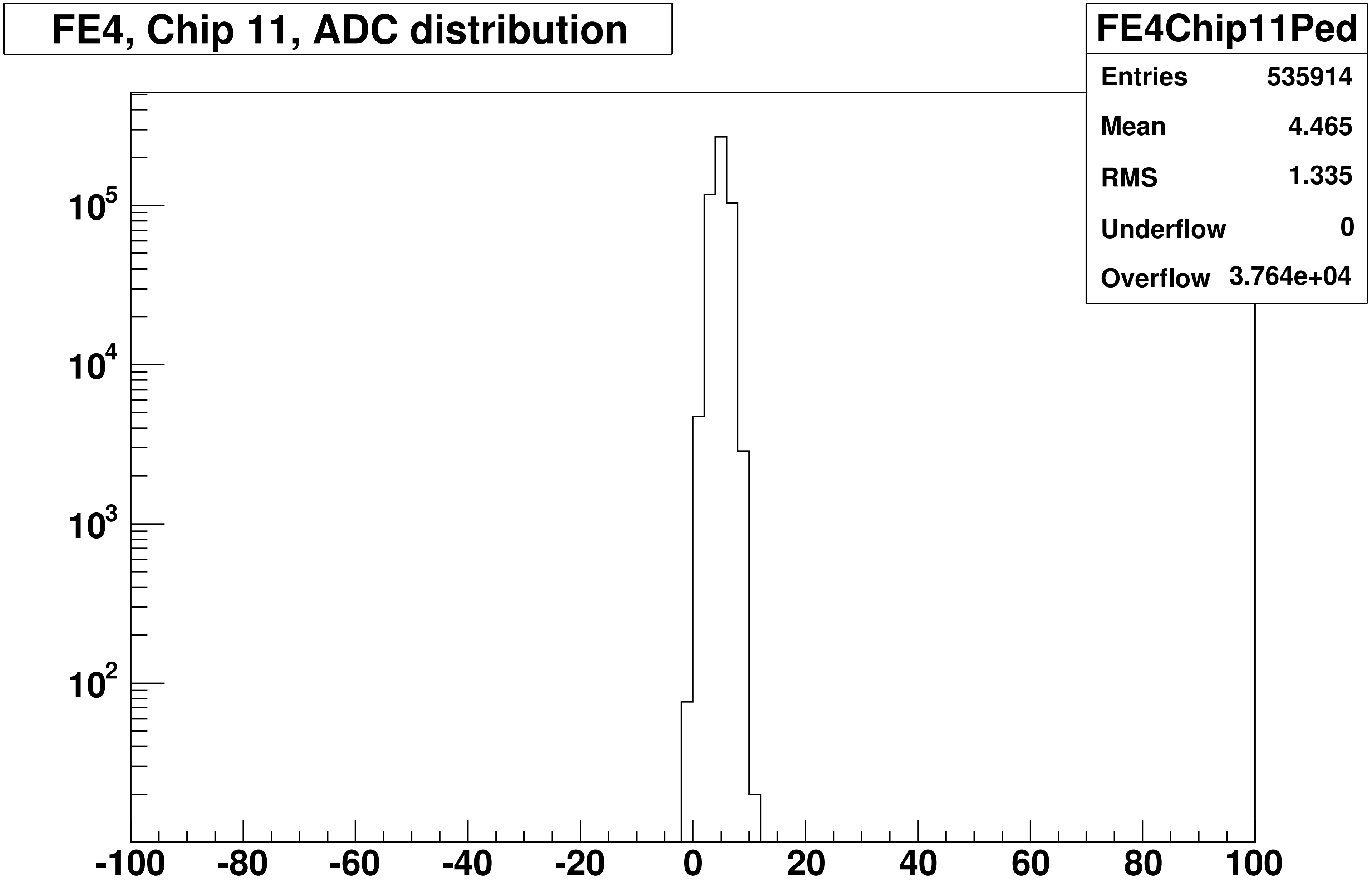


FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 535914
Mean -1.432
RMS 0.7673
Underflow 0
Overflow 3.764e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

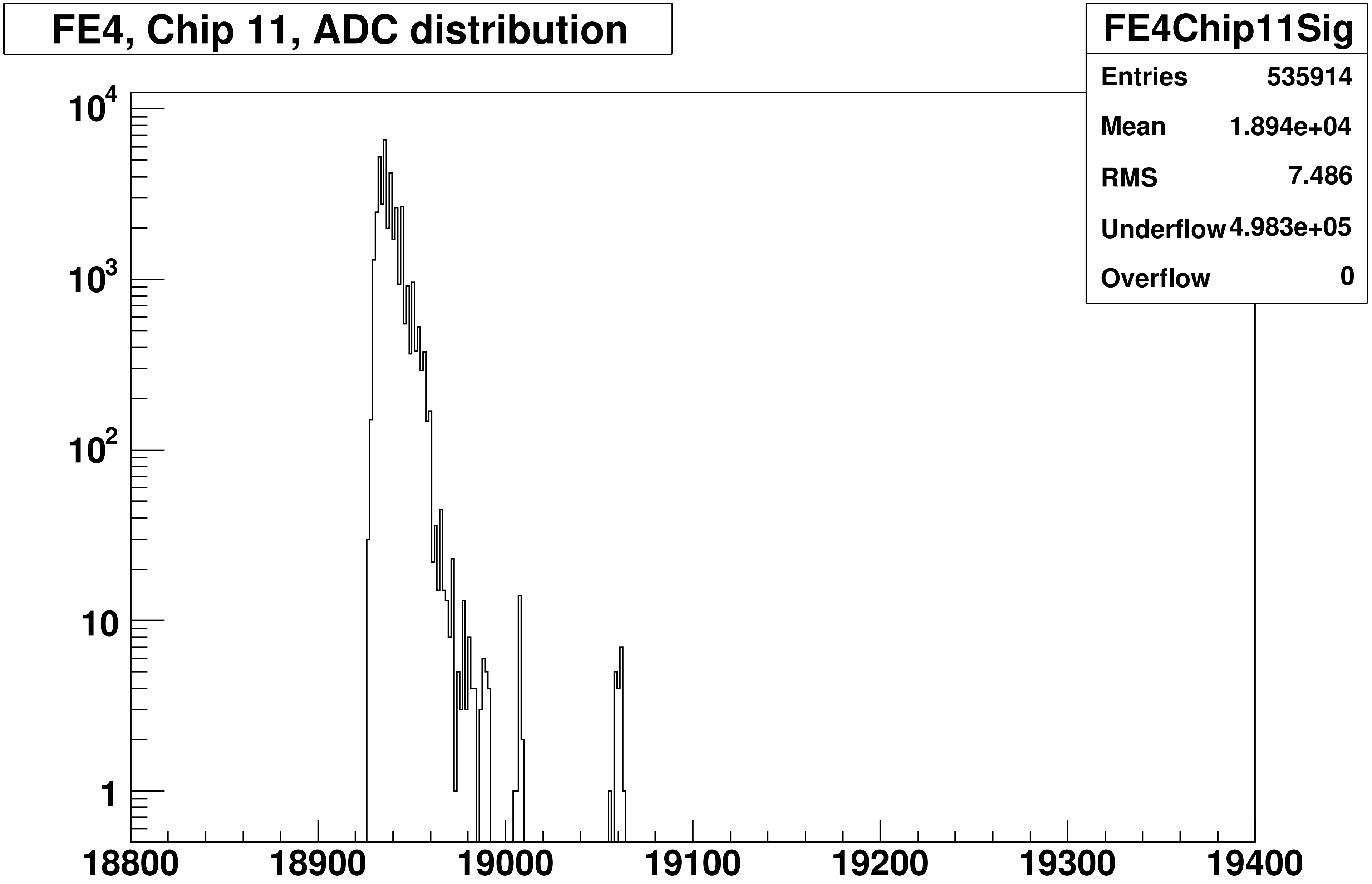


FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 535914
Mean 1.893e+04
RMS 7.294
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

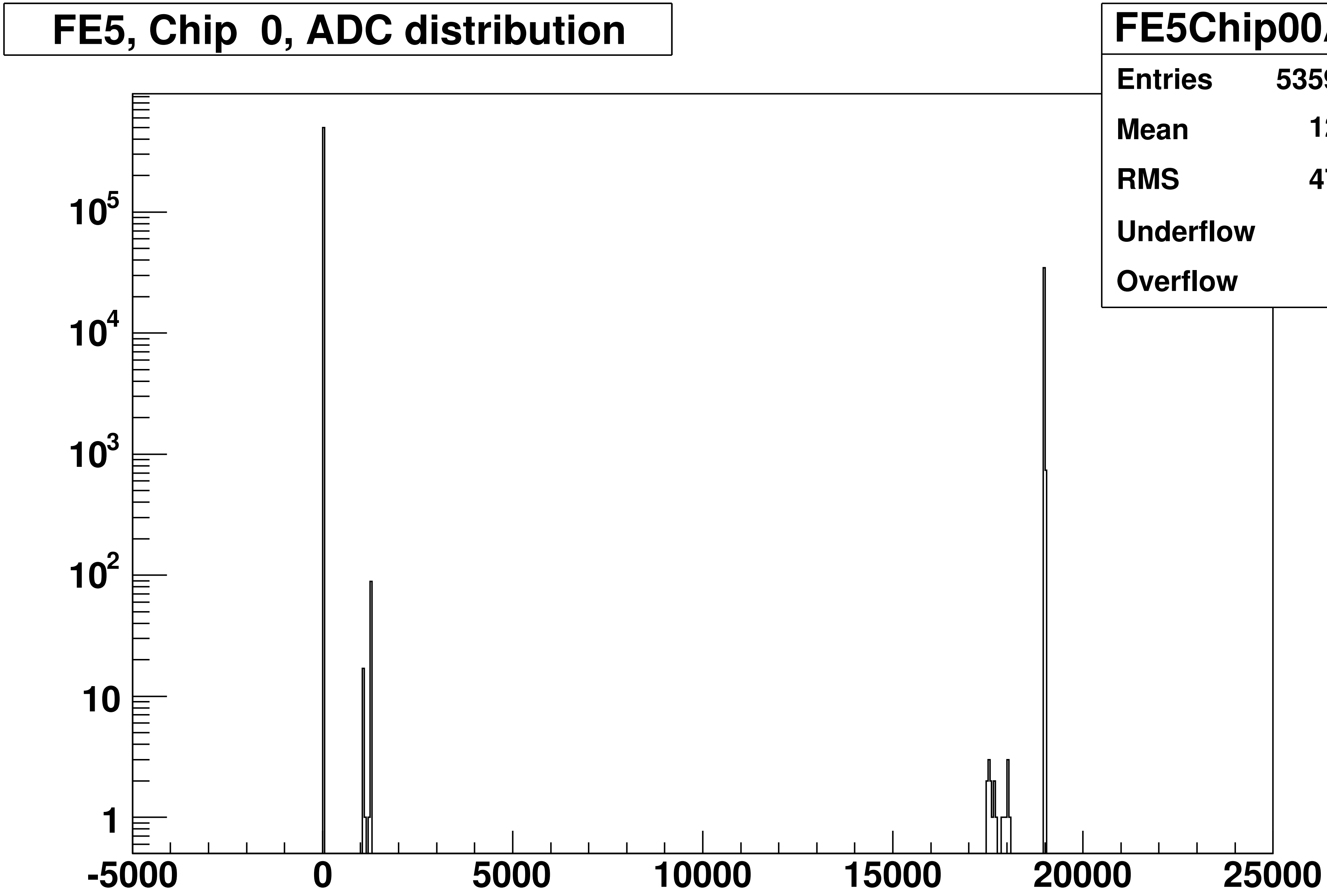

FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 535914
Mean 1334
RMS 4838
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

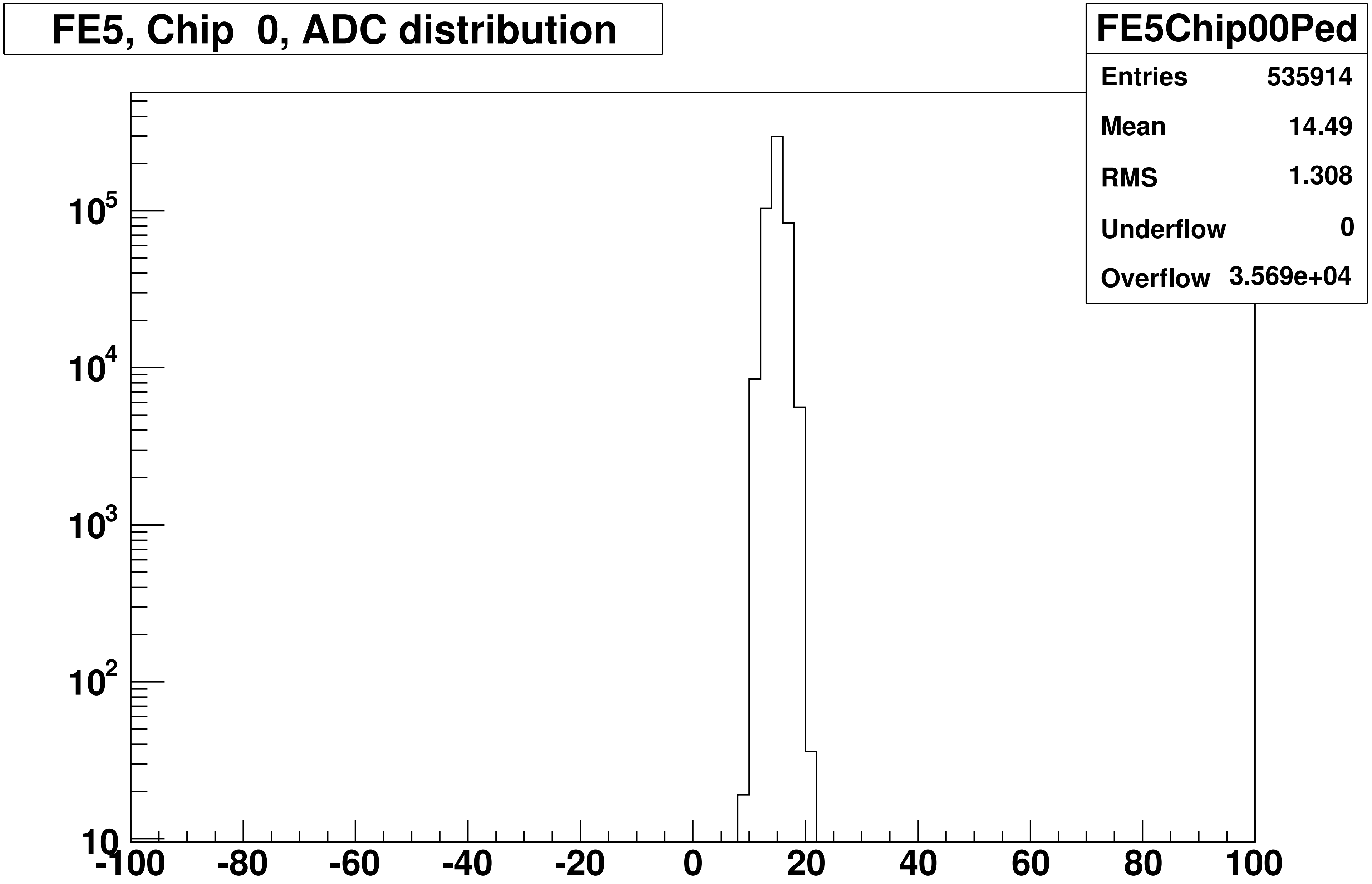


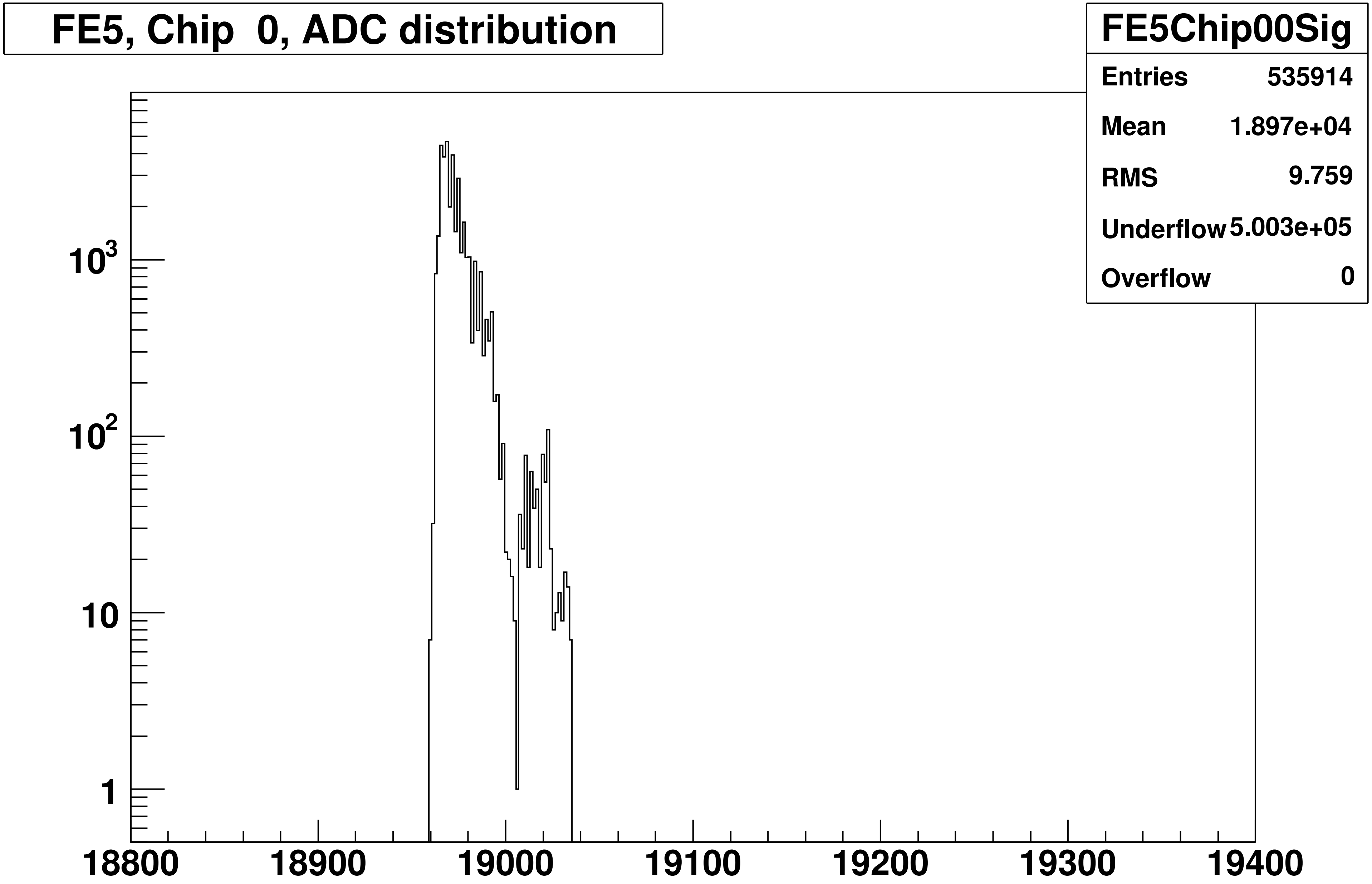
FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 535914
Mean 4.465
RMS 1.335
Underflow 0
Overflow 3.764e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

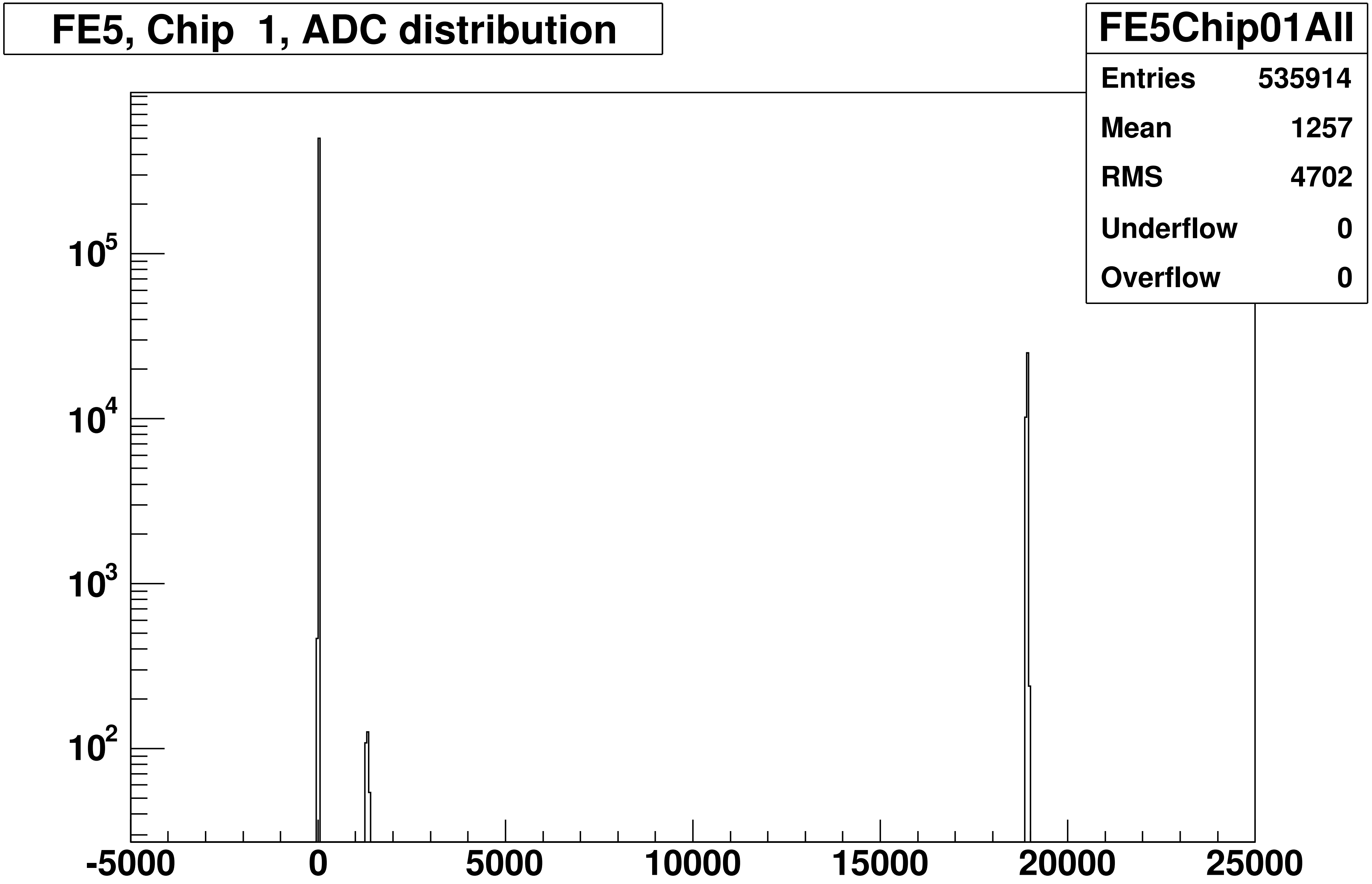


FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 535914
Mean 1.894e+04
RMS 7.486
Underflow 4.983e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

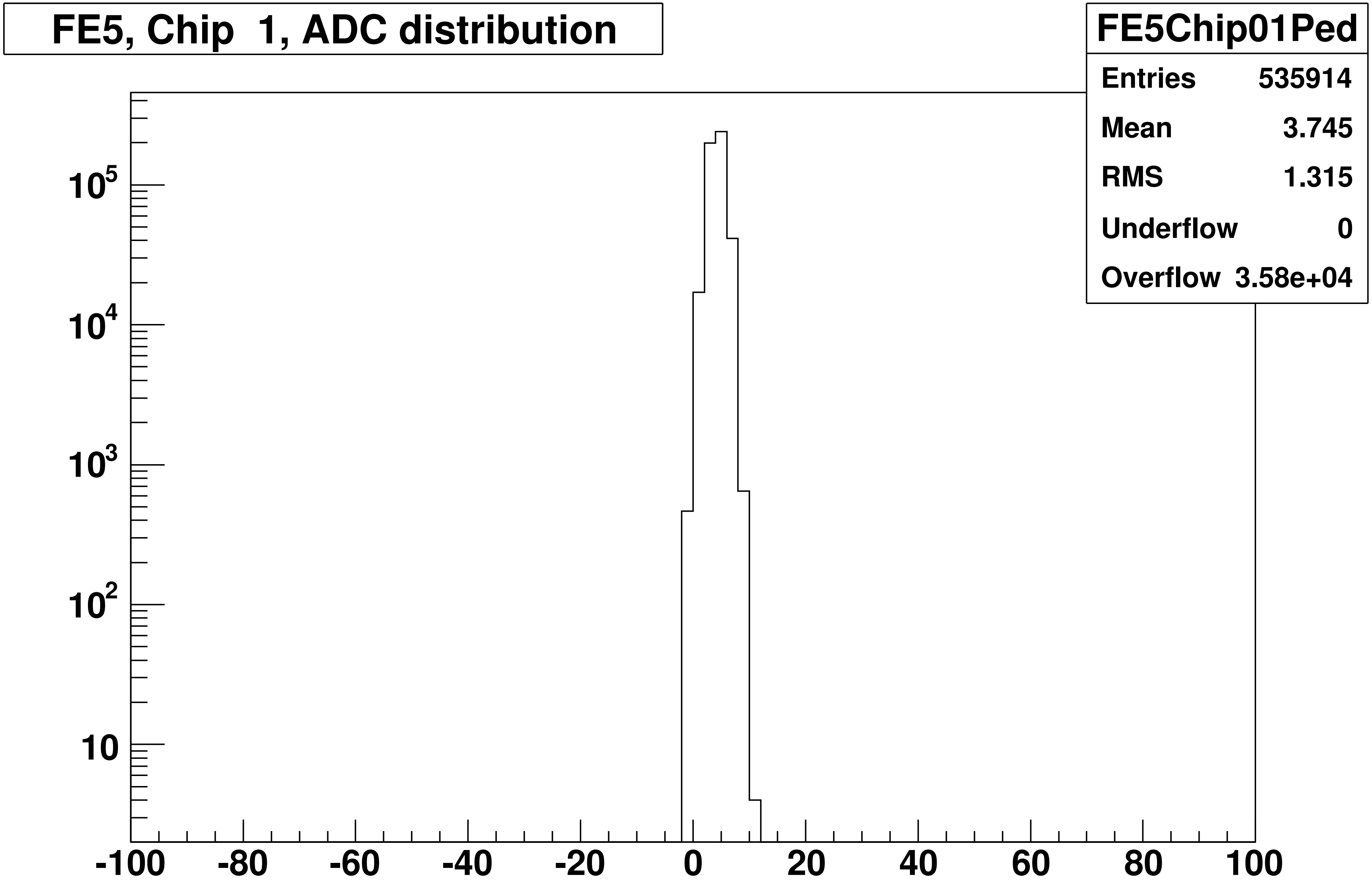

FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 535914
Mean 1274
RMS 4720
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

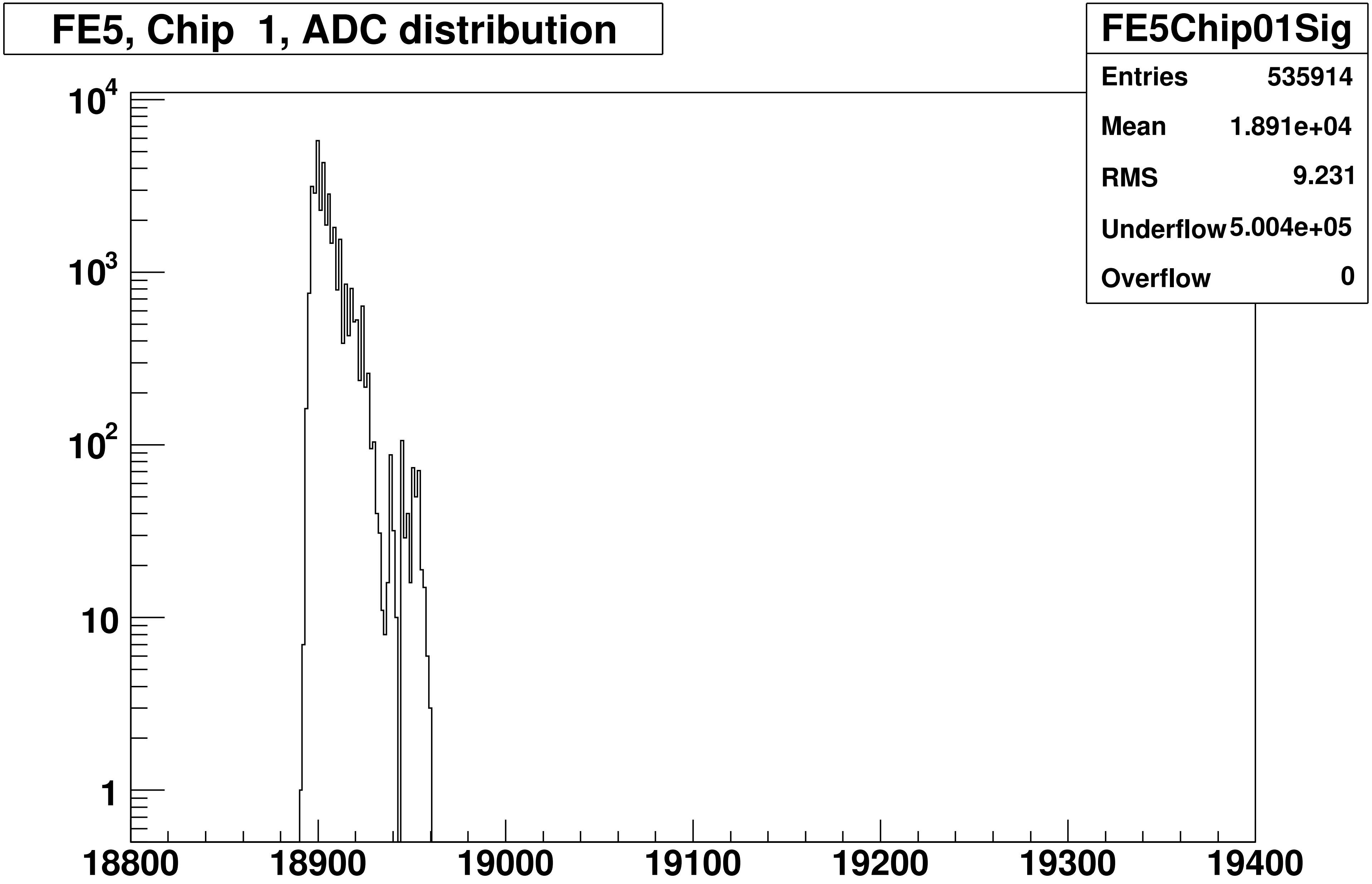

FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 535914
Mean 14.49
RMS 1.308
Underflow 0
Overflow 3.569e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 535914
Mean 1.897e+04
RMS 9.759
Underflow 5.003e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

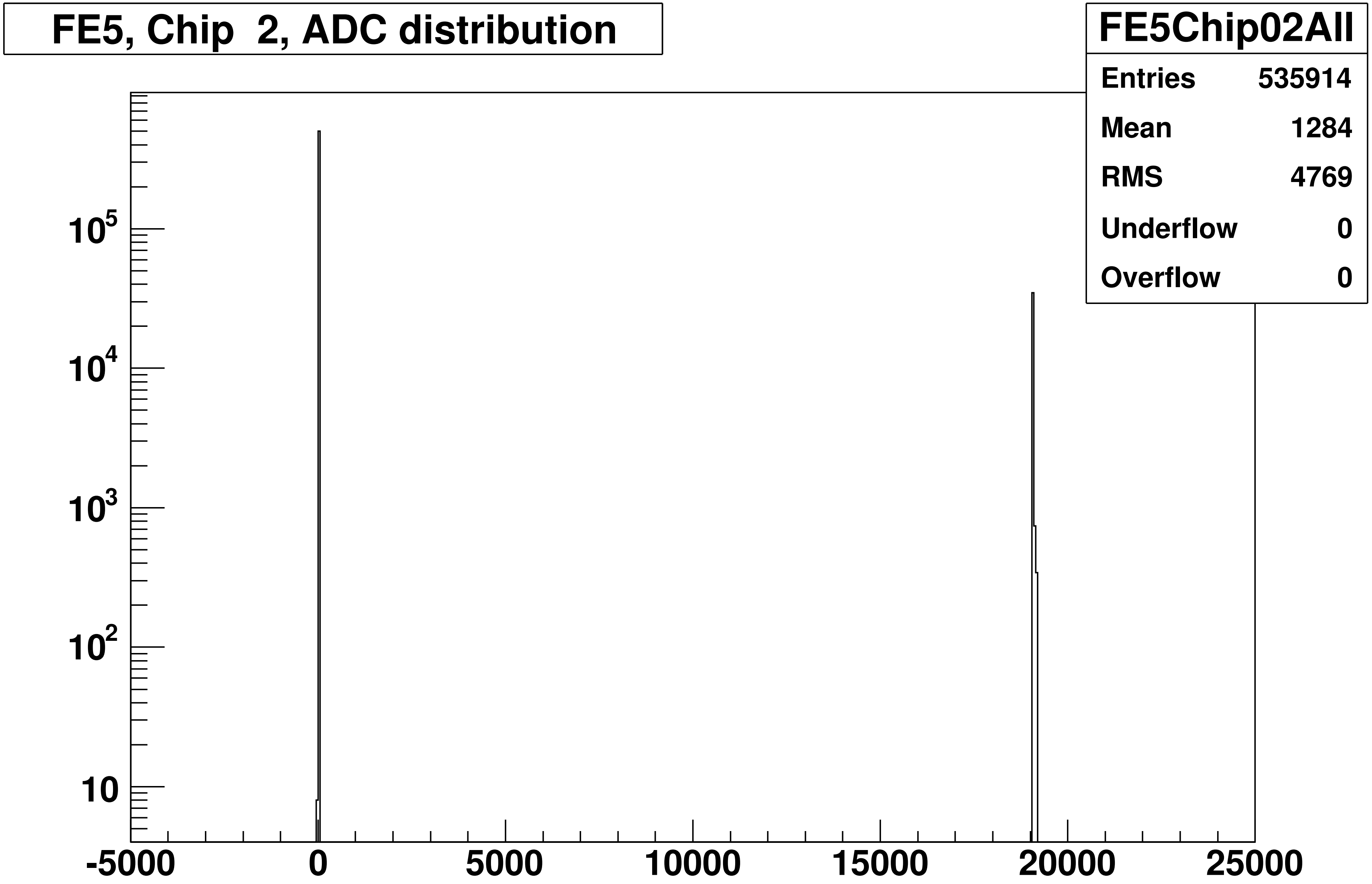


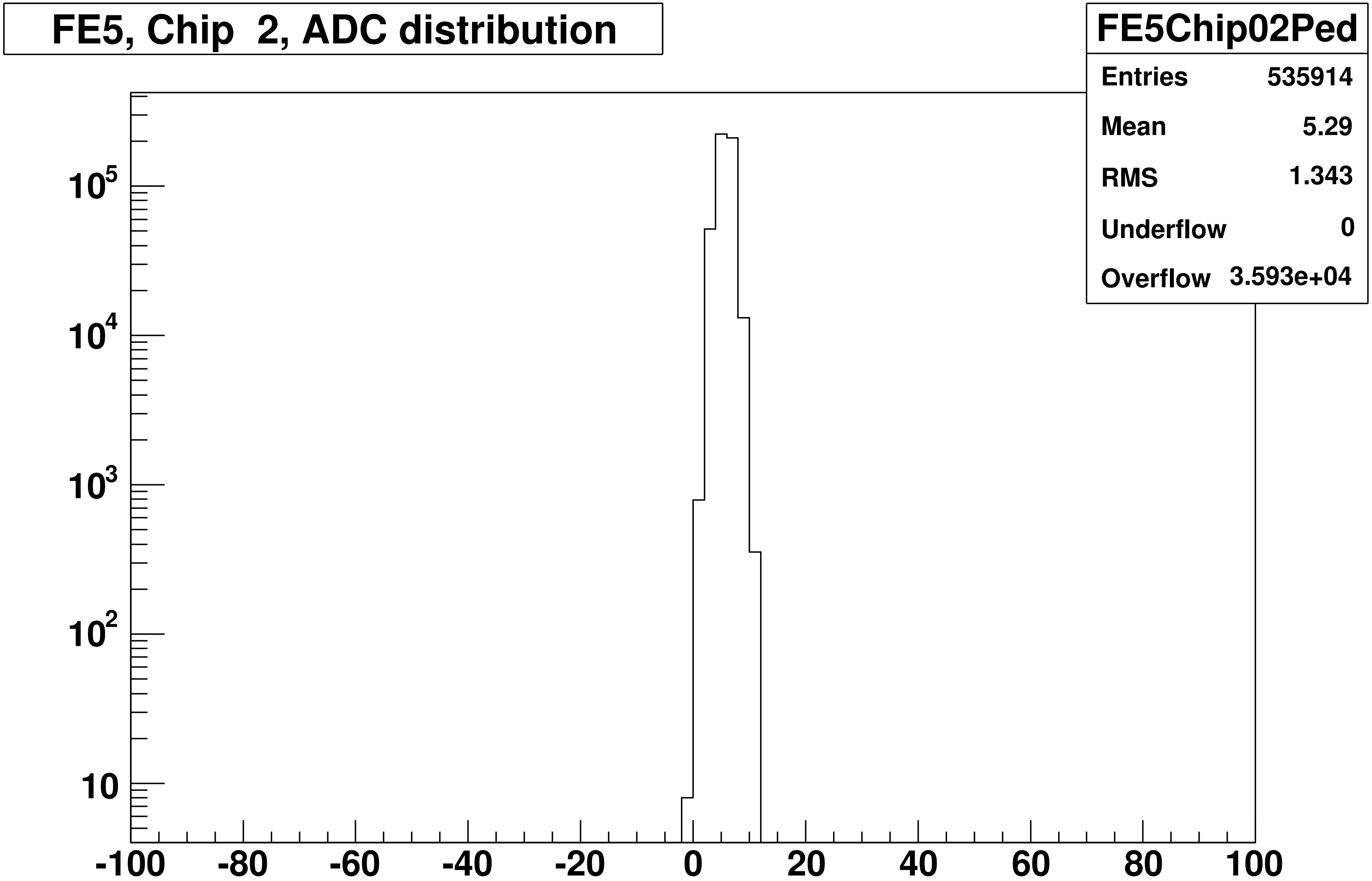
FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 535914
Mean 1257
RMS 4702
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

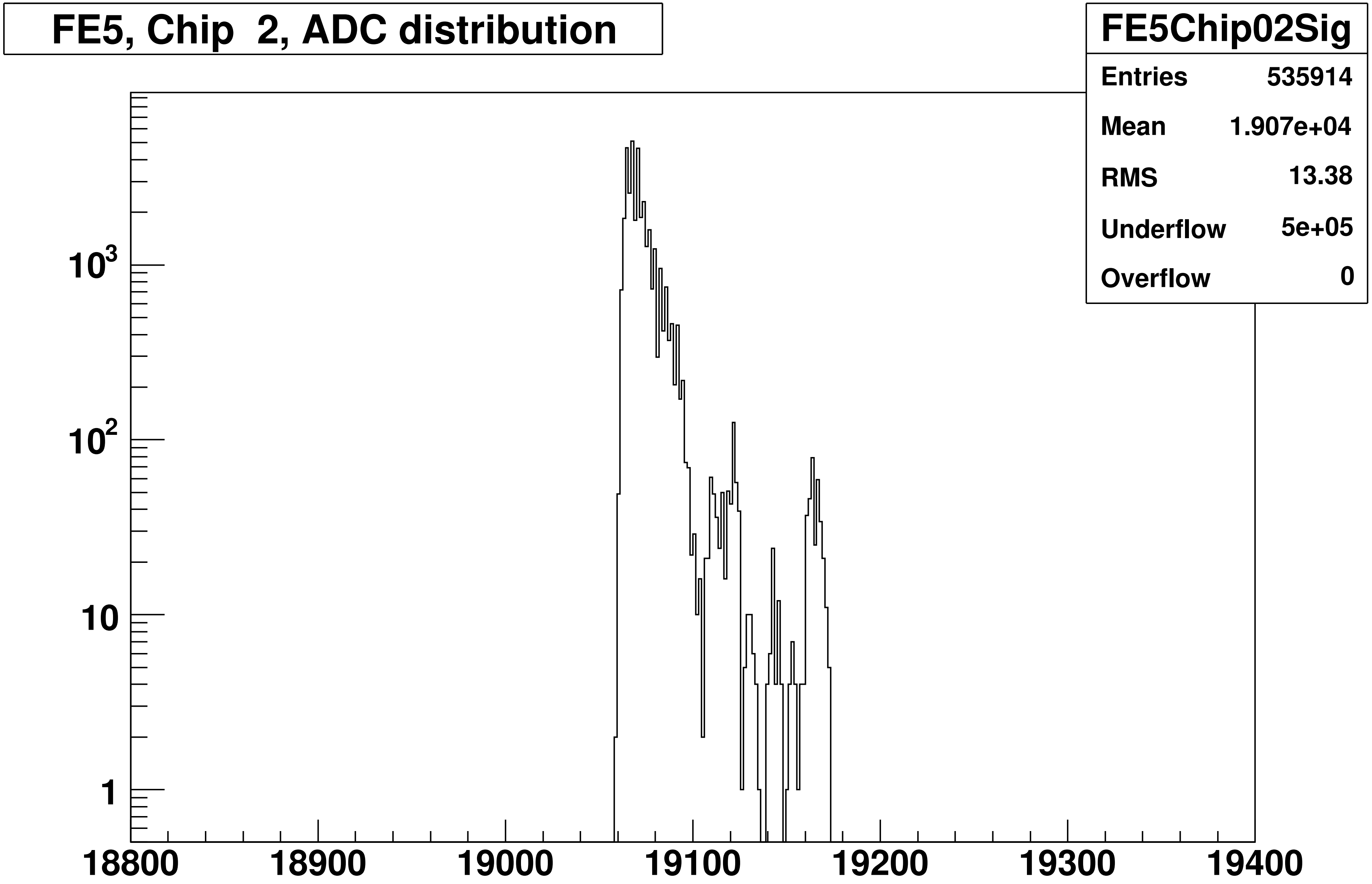

FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 535914
Mean 3.745
RMS 1.315
Underflow 0
Overflow 3.58e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



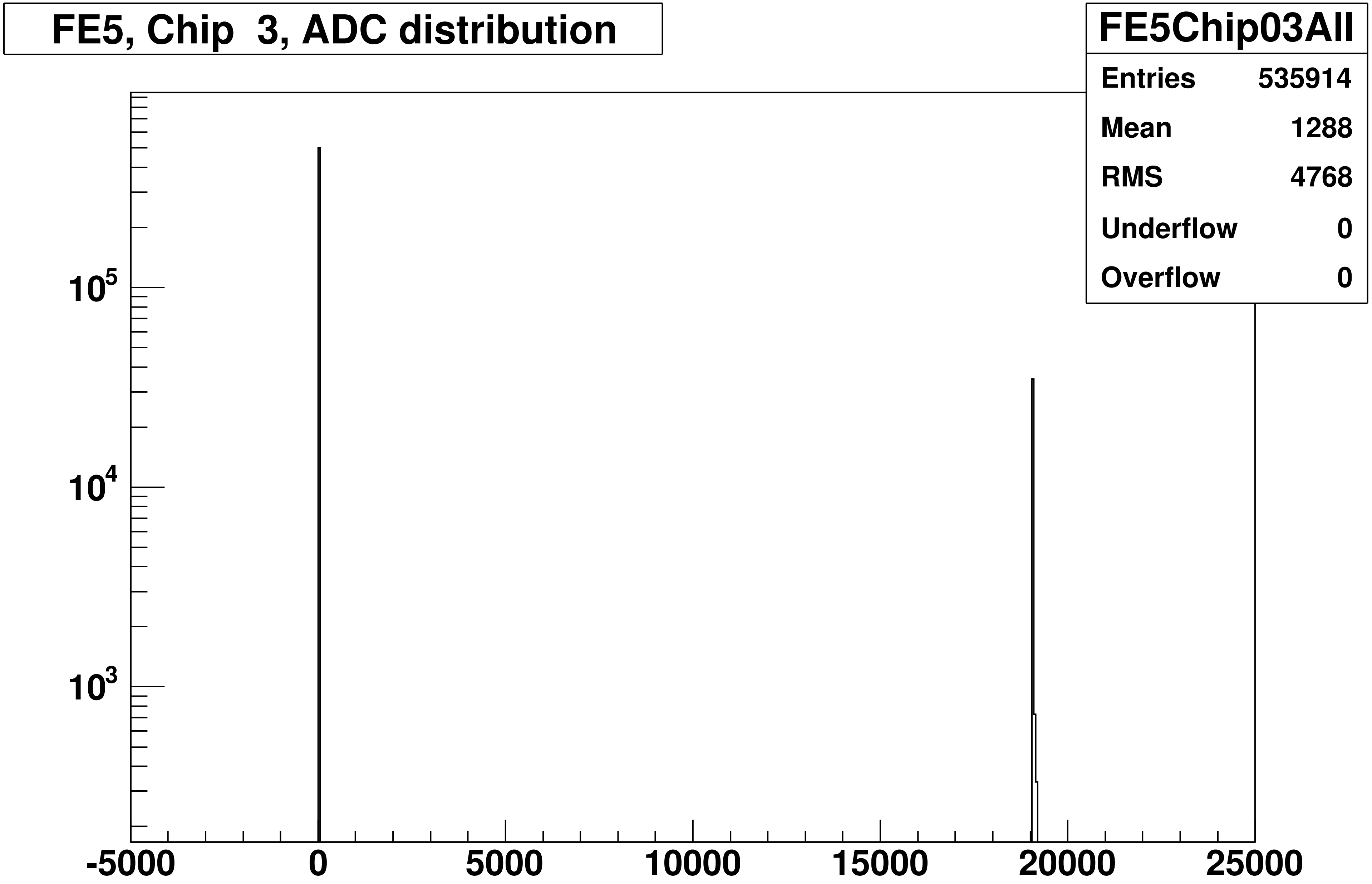
FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 535914
Mean 1.891e+04
RMS 9.231
Underflow 5.004e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

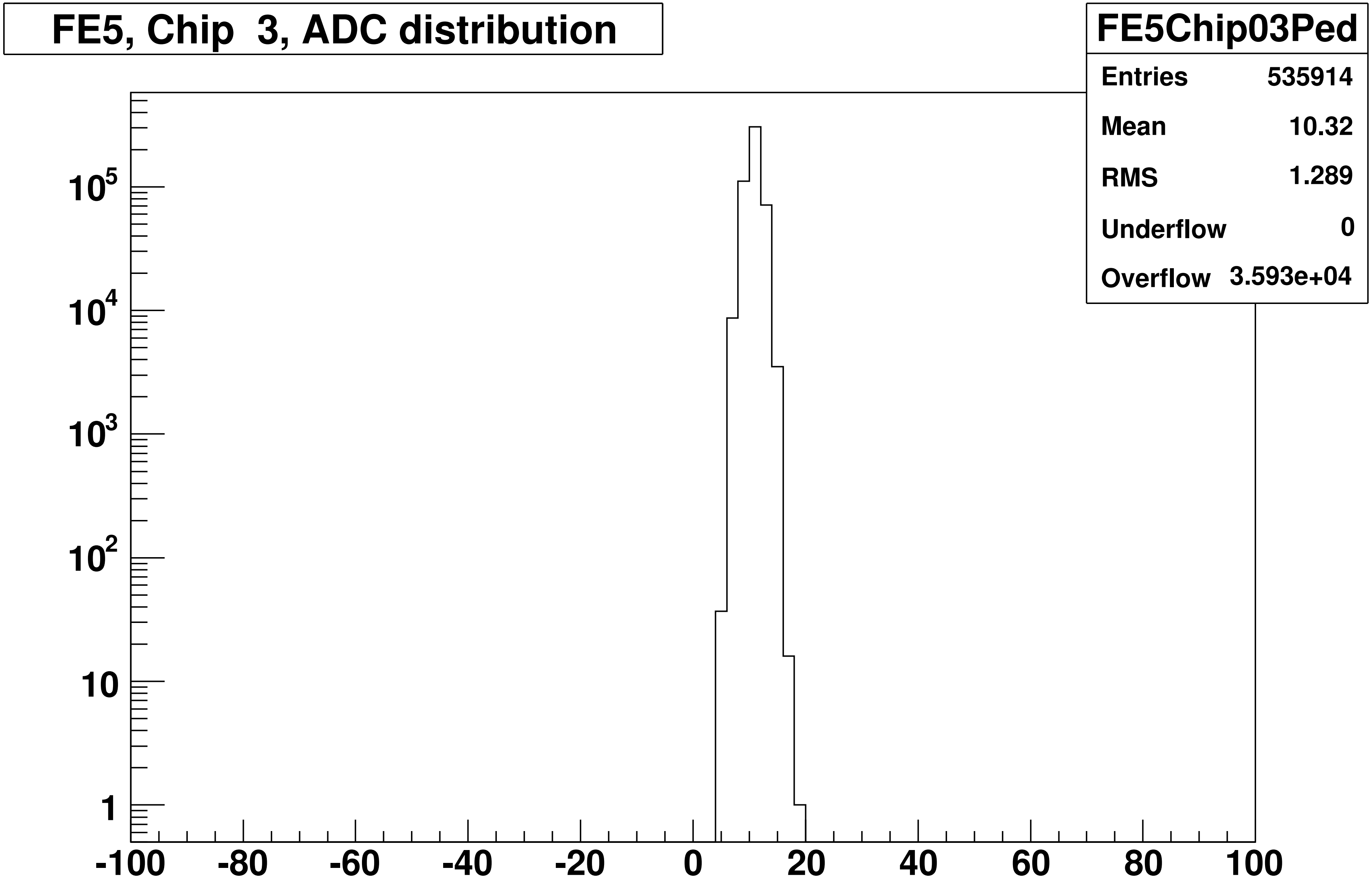

FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 535914
Mean 1284
RMS 4769
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 535914
Mean 5.29
RMS 1.343
Underflow 0
Overflow 3.593e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

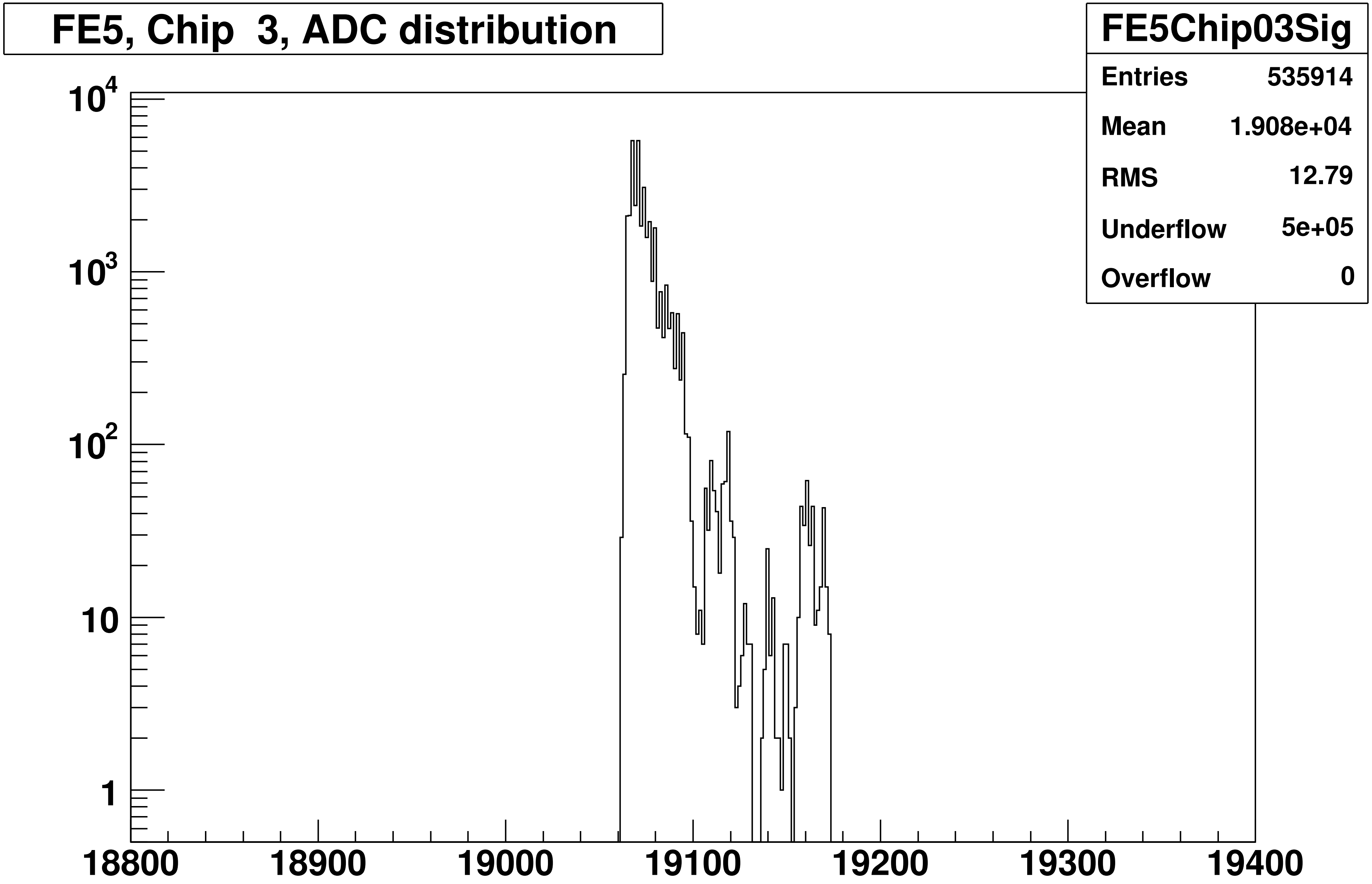


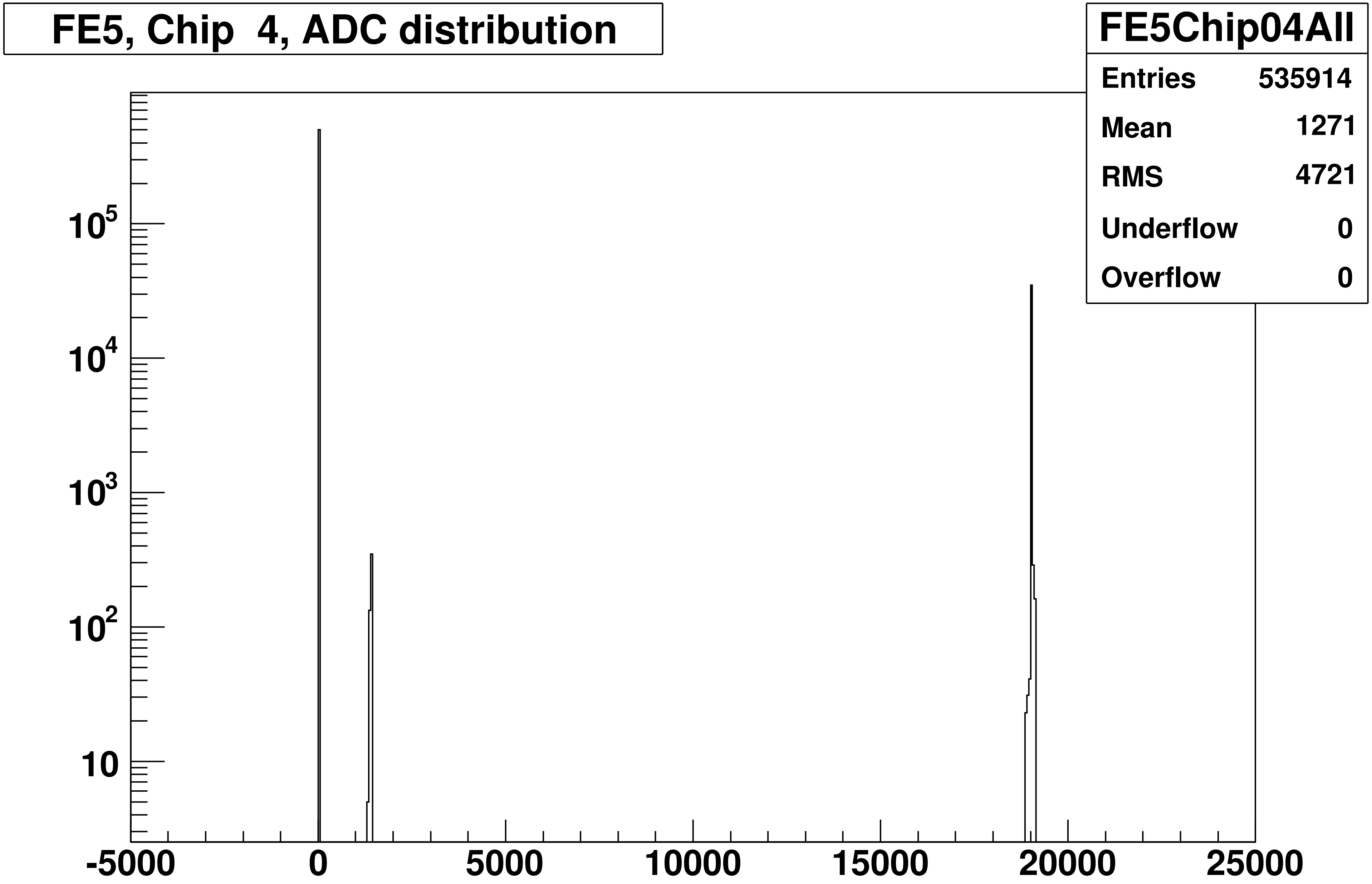
FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 535914
Mean 1.907e+04
RMS 13.38
Underflow 5e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 535914
Mean 1288
RMS 4768
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

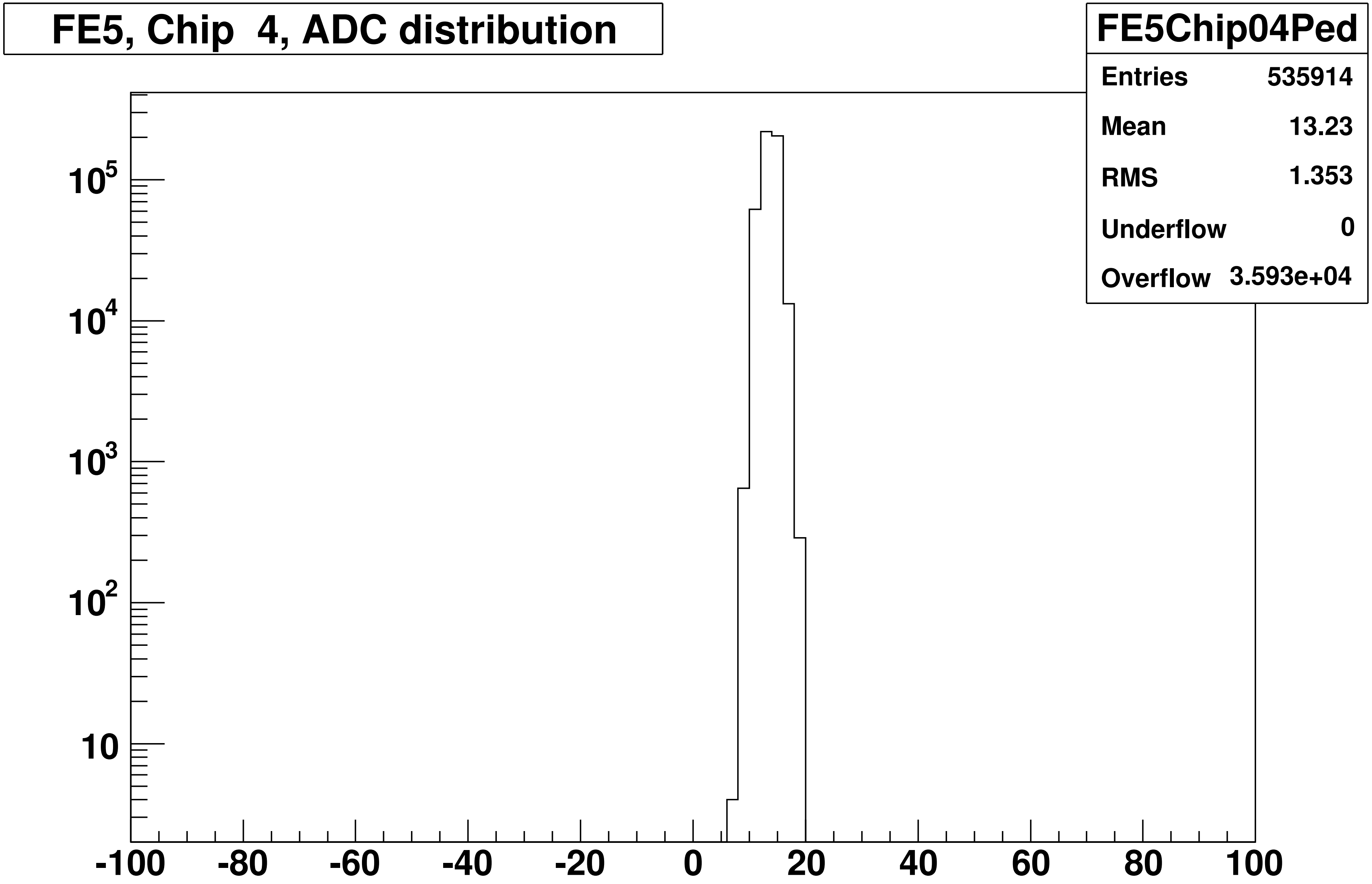


FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 535914
Mean 10.32
RMS 1.289
Underflow 0
Overflow 3.593e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

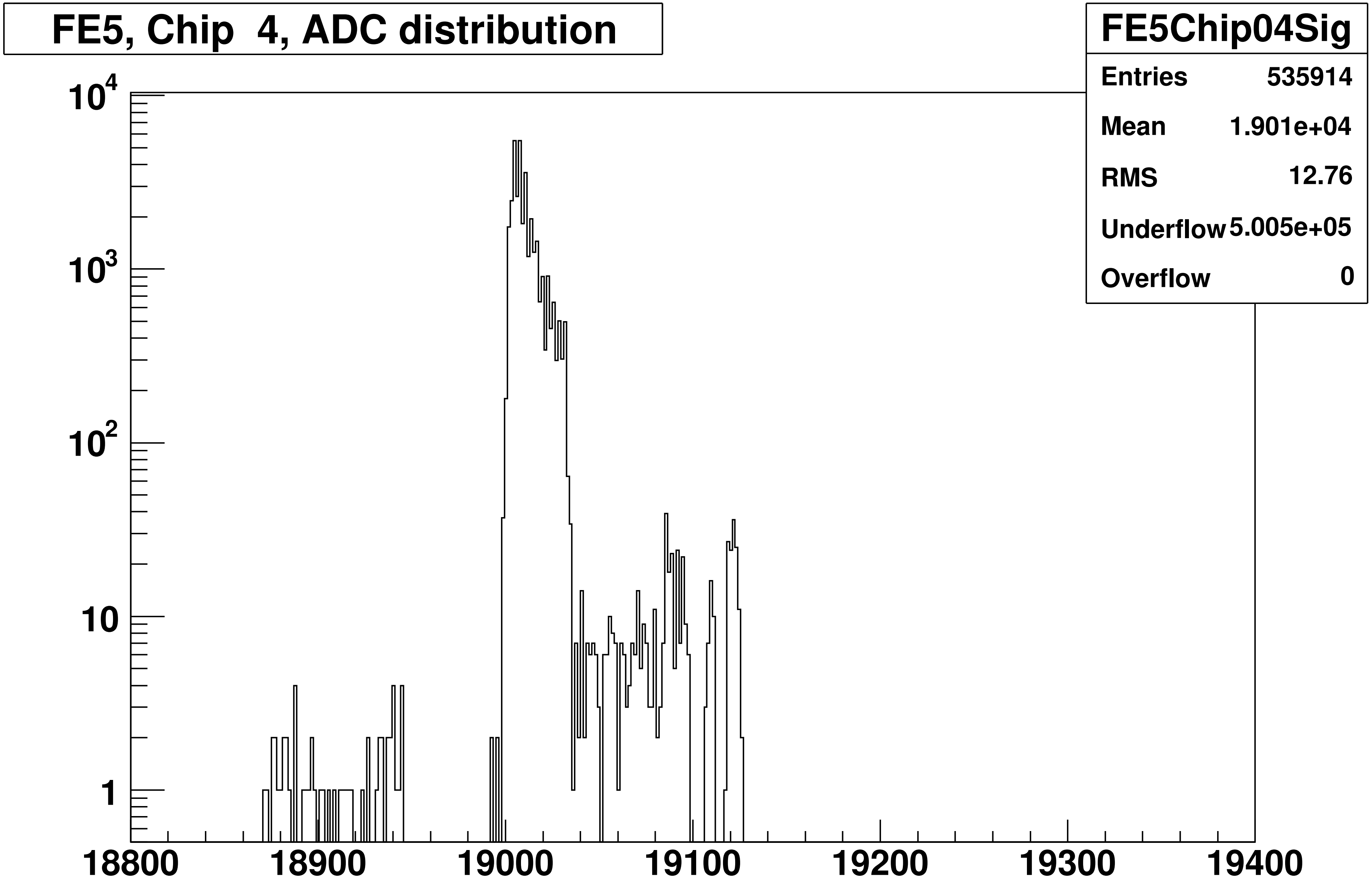

FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 535914
Mean 1.908e+04
RMS 12.79
Underflow 5e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

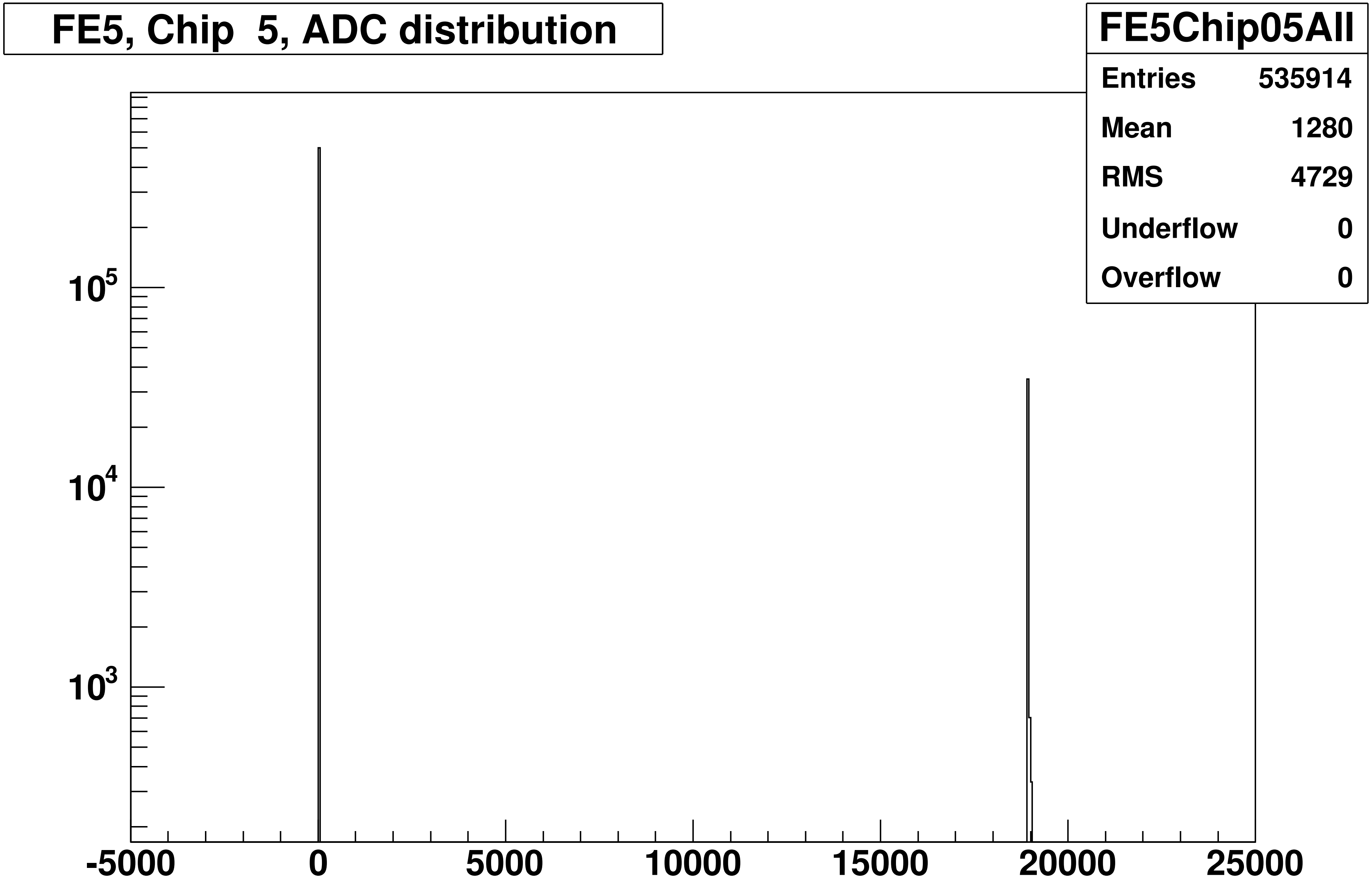


FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 535914
Mean 1271
RMS 4721
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

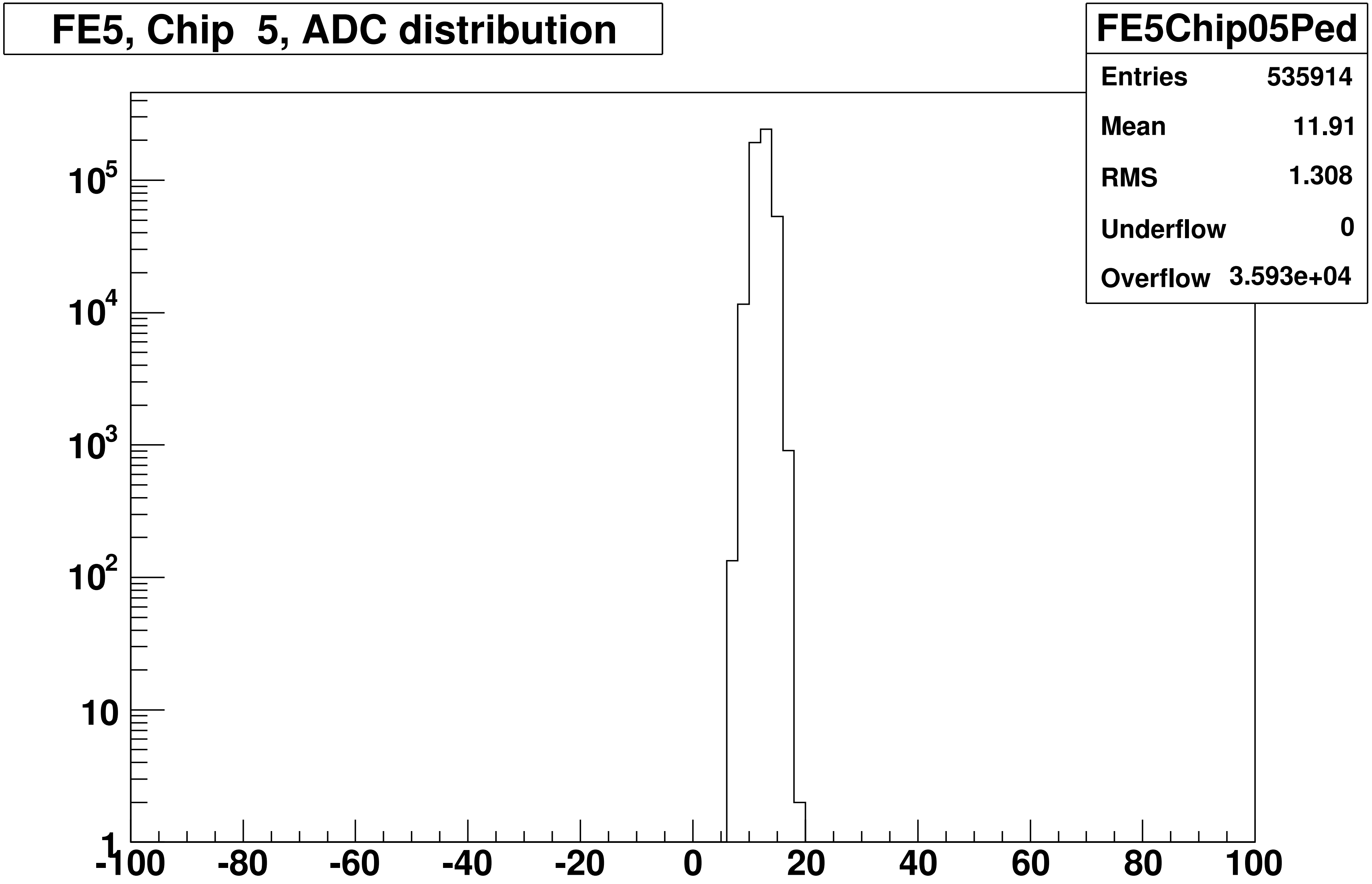


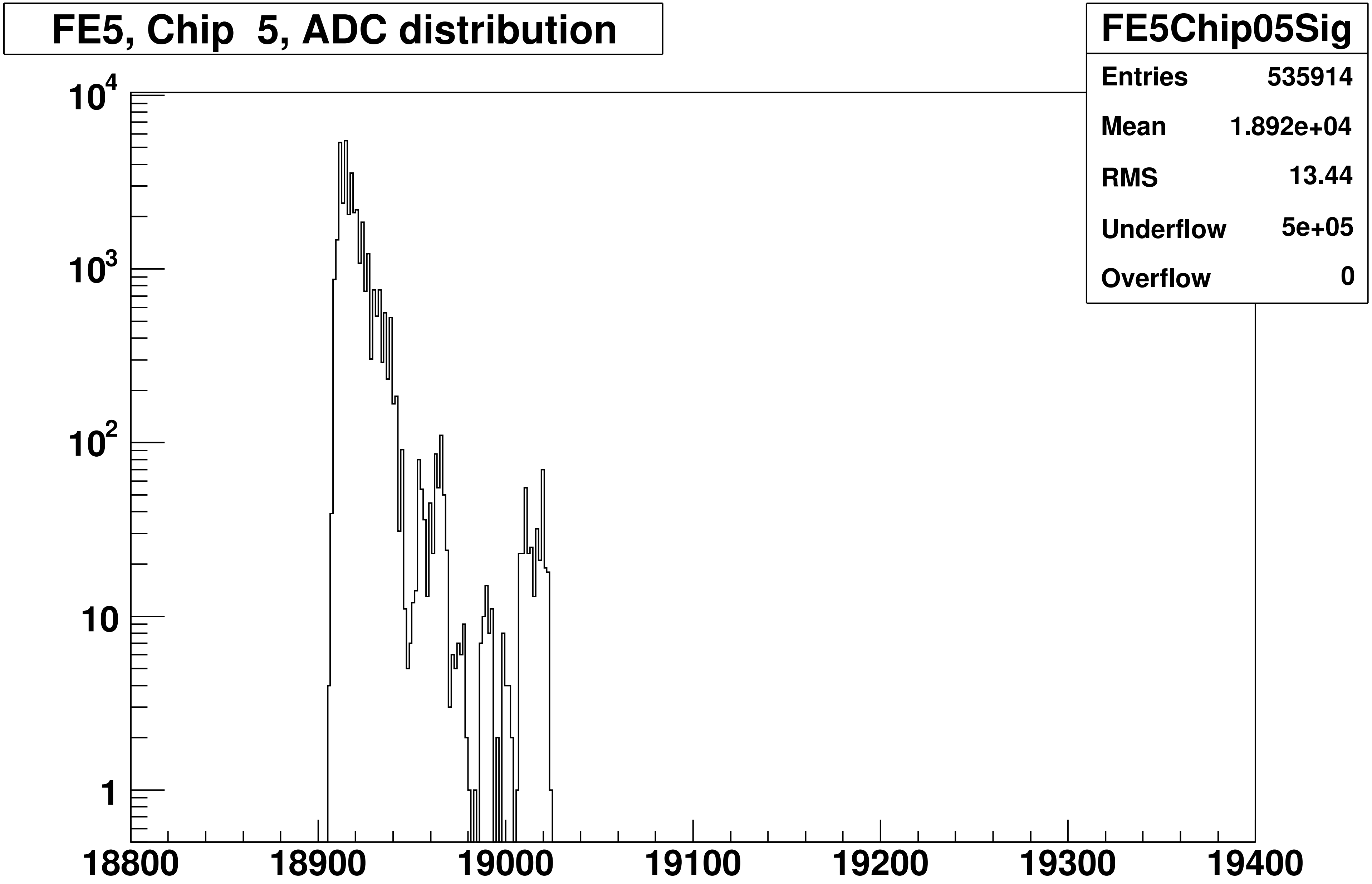
FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 535914
Mean 13.23
RMS 1.353
Underflow 0
Overflow 3.593e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

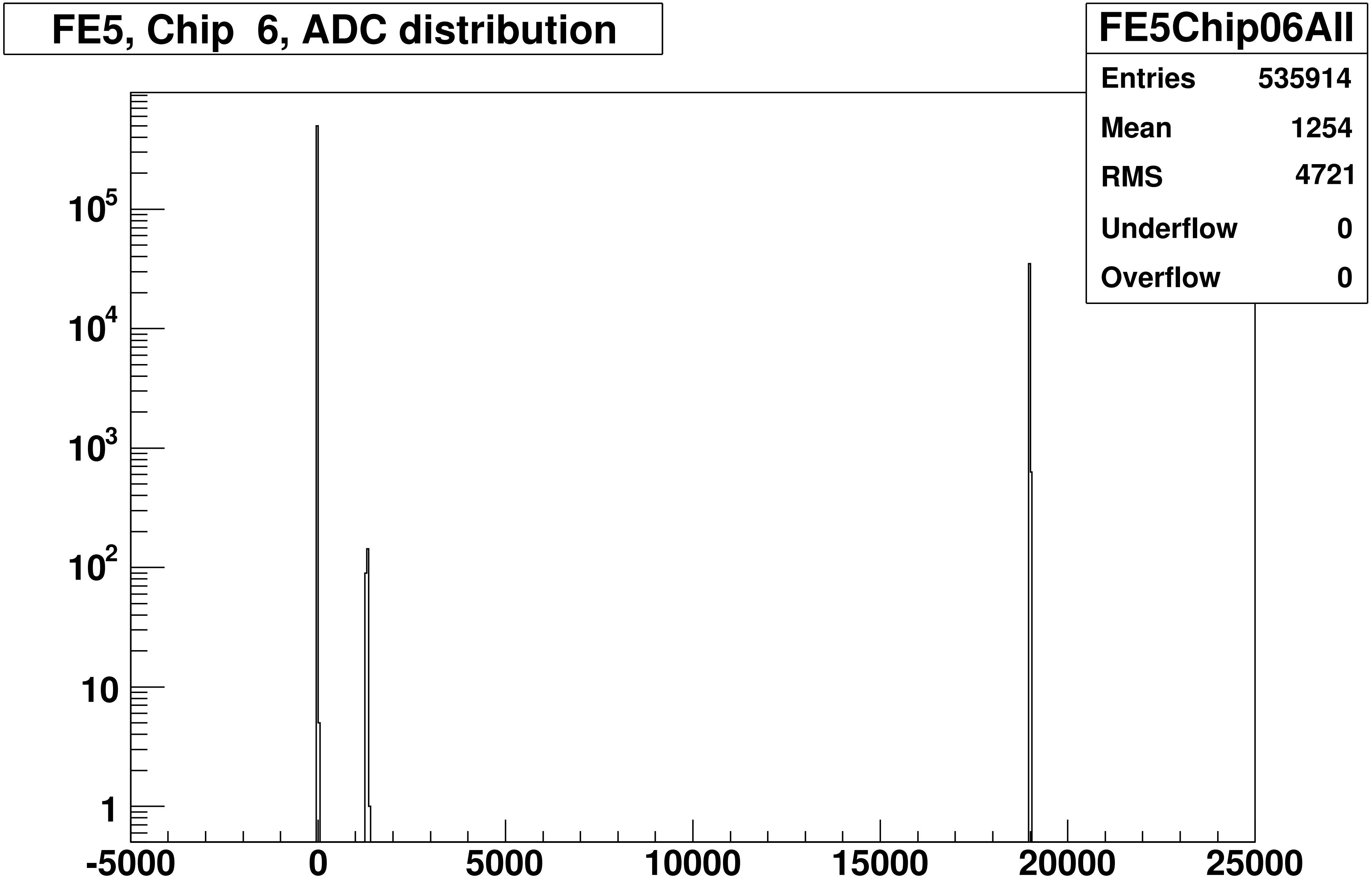

FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 535914
Mean 1.901e+04
RMS 12.76
Underflow 5.005e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

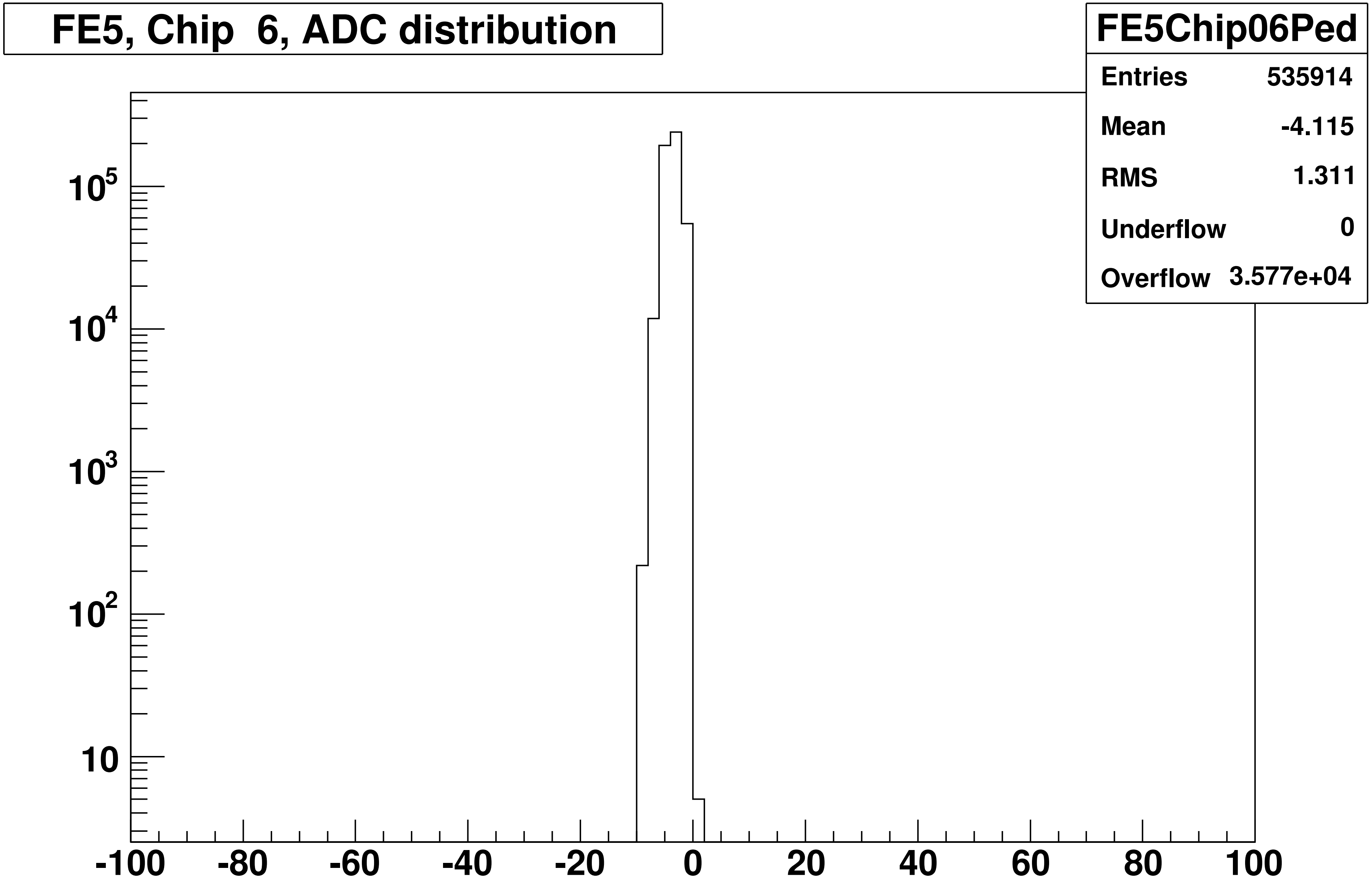


FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 535914
Mean 1280
RMS 4729
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

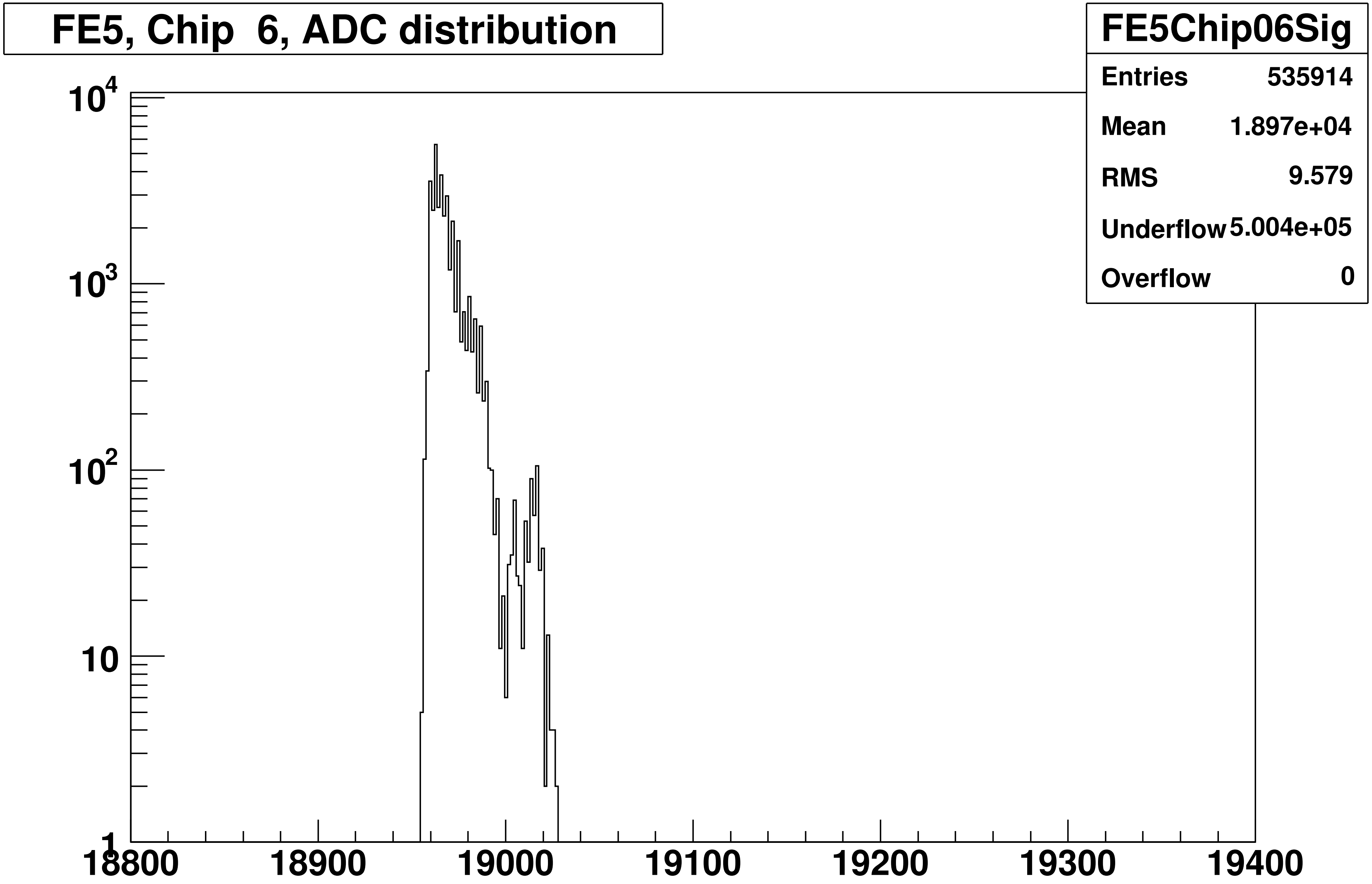

FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 535914
Mean 11.91
RMS 1.308
Underflow 0
Overflow 3.593e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

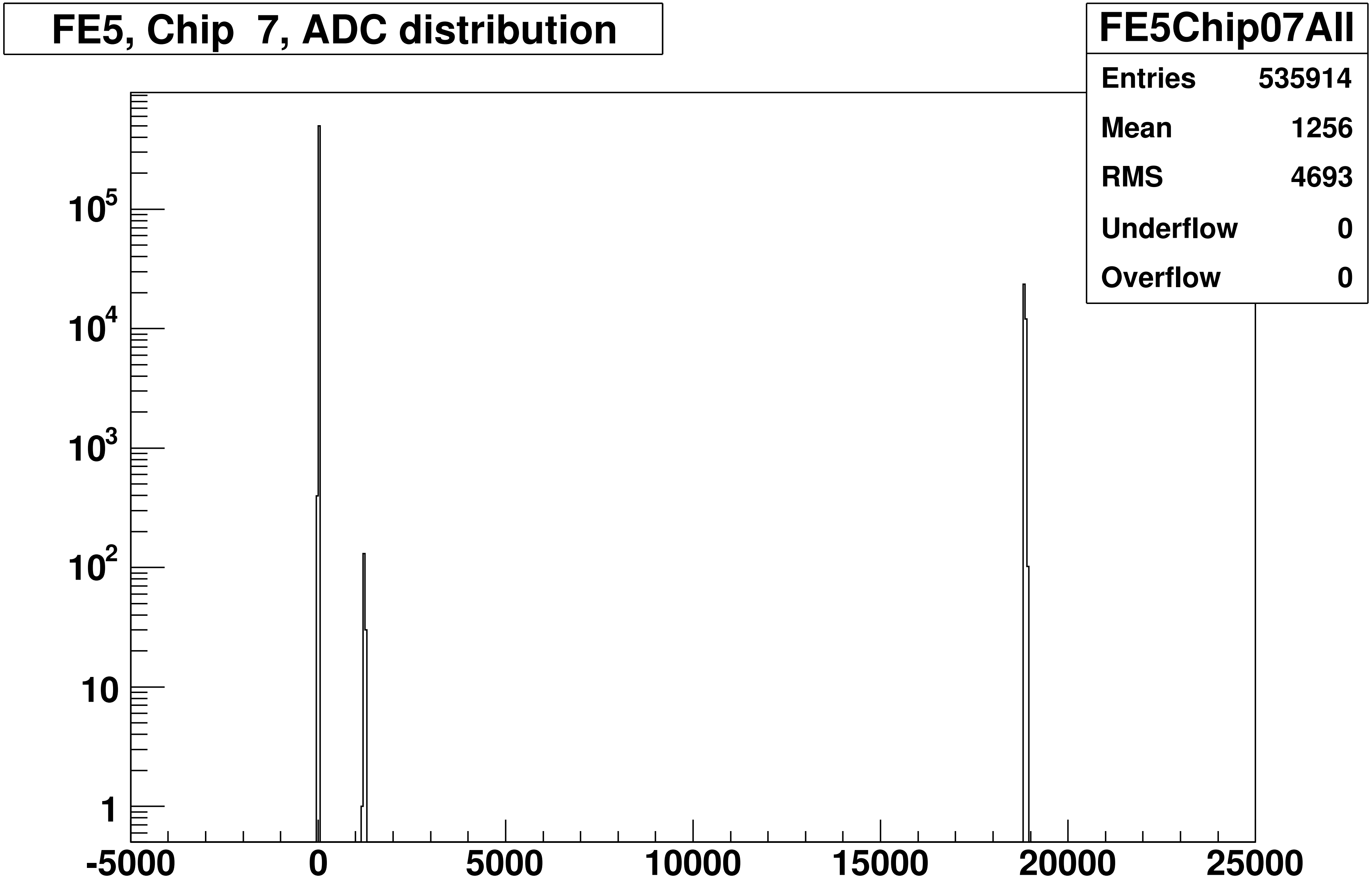

FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 535914
Mean 1.892e+04
RMS 13.44
Underflow 5e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

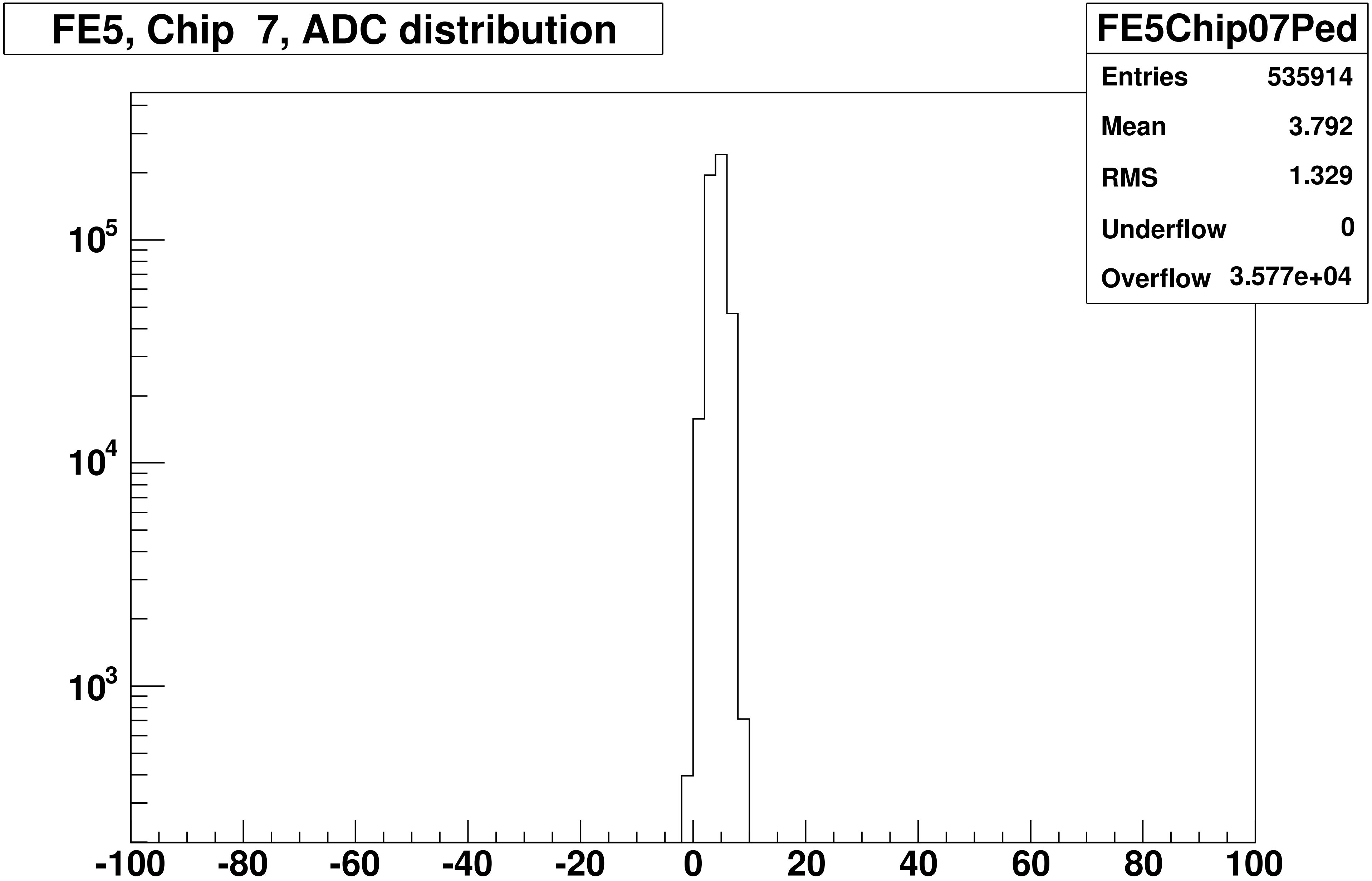

FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 535914
Mean 1254
RMS 4721
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

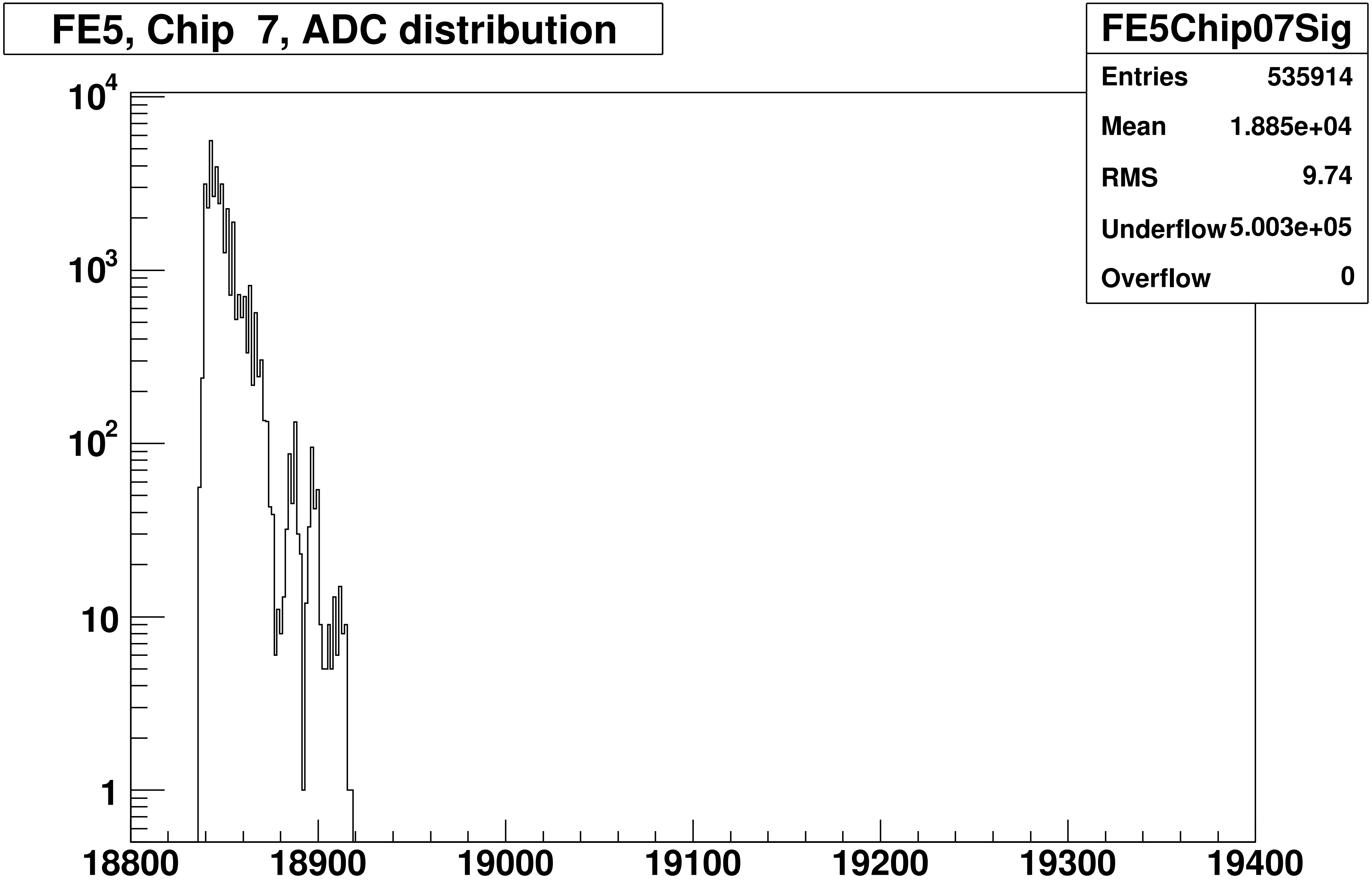


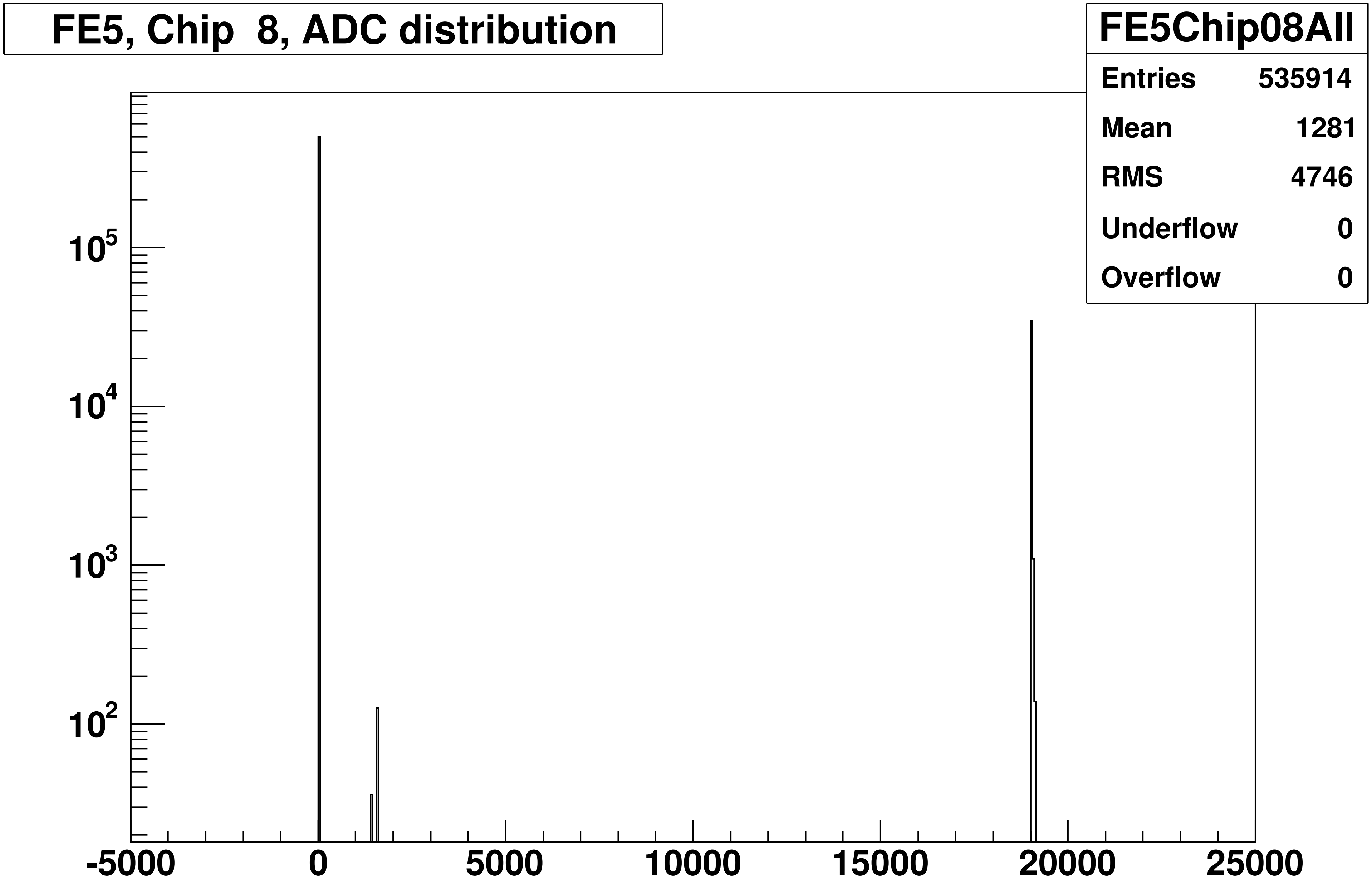
FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 535914
Mean -4.115
RMS 1.311
Underflow 0
Overflow 3.577e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

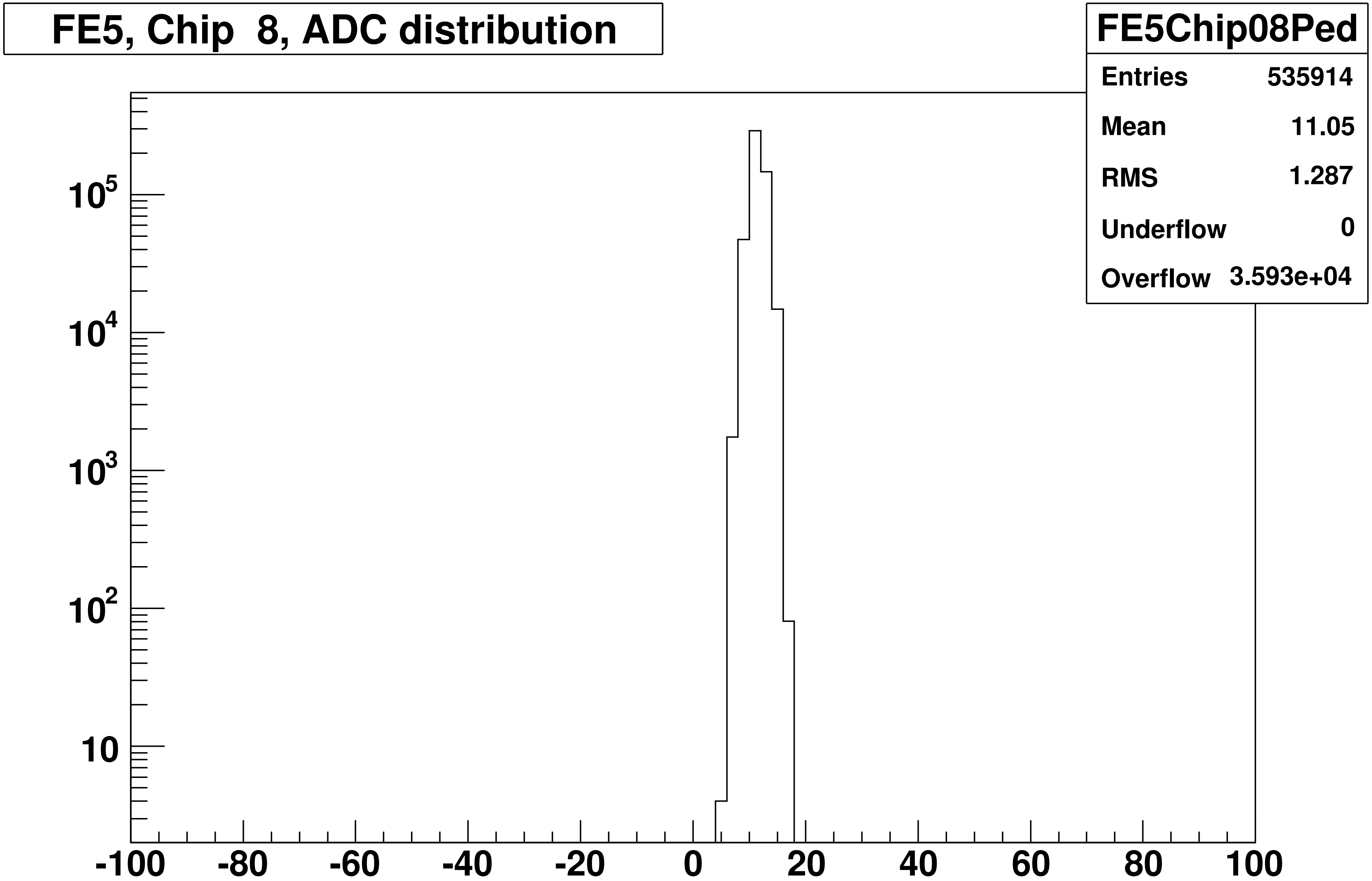

FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 535914
Mean 1.897e+04
RMS 9.579
Underflow 5.004e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

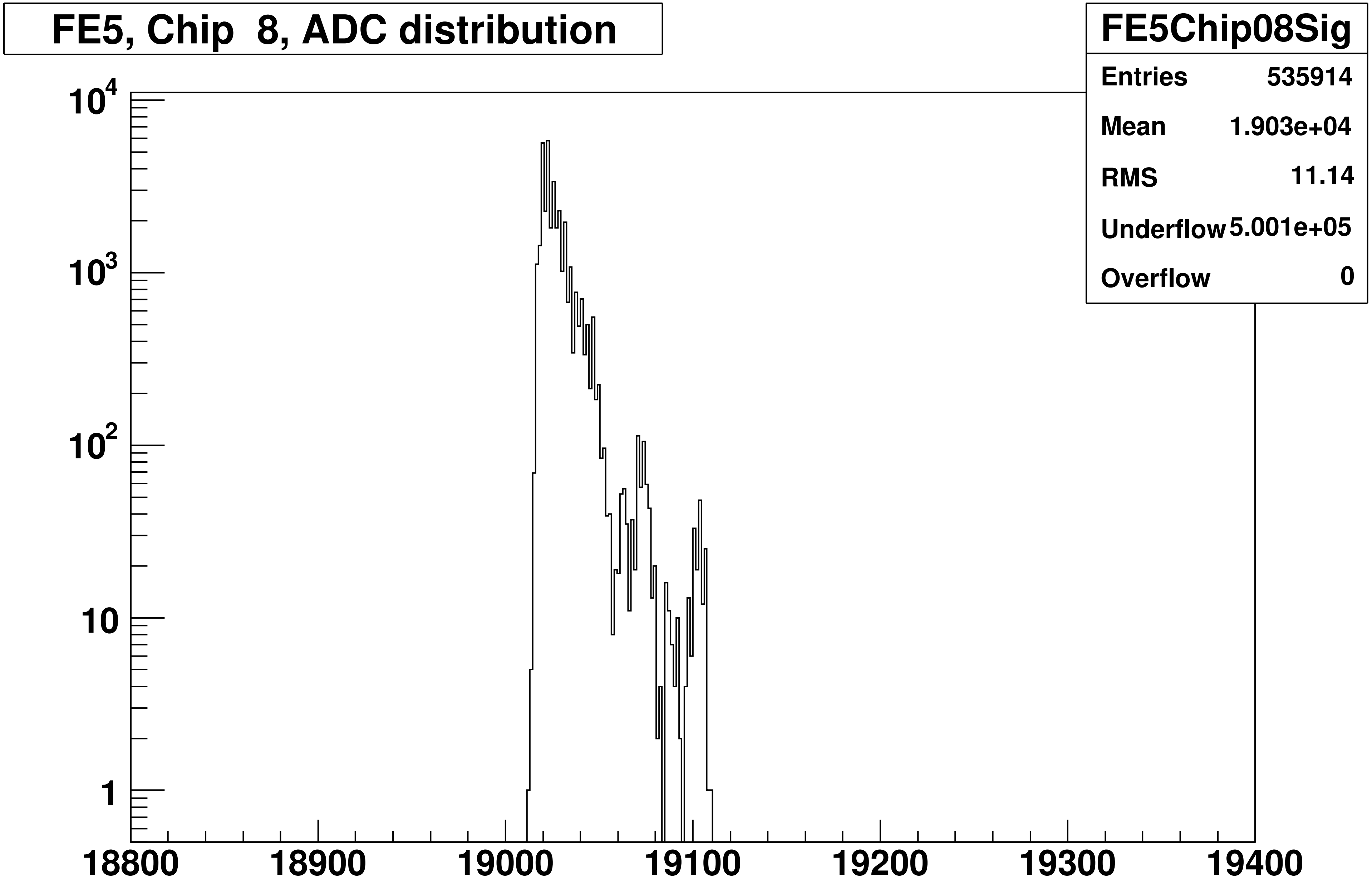

FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 535914
Mean 1256
RMS 4693
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 535914
Mean 3.792
RMS 1.329
Underflow 0
Overflow 3.577e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100

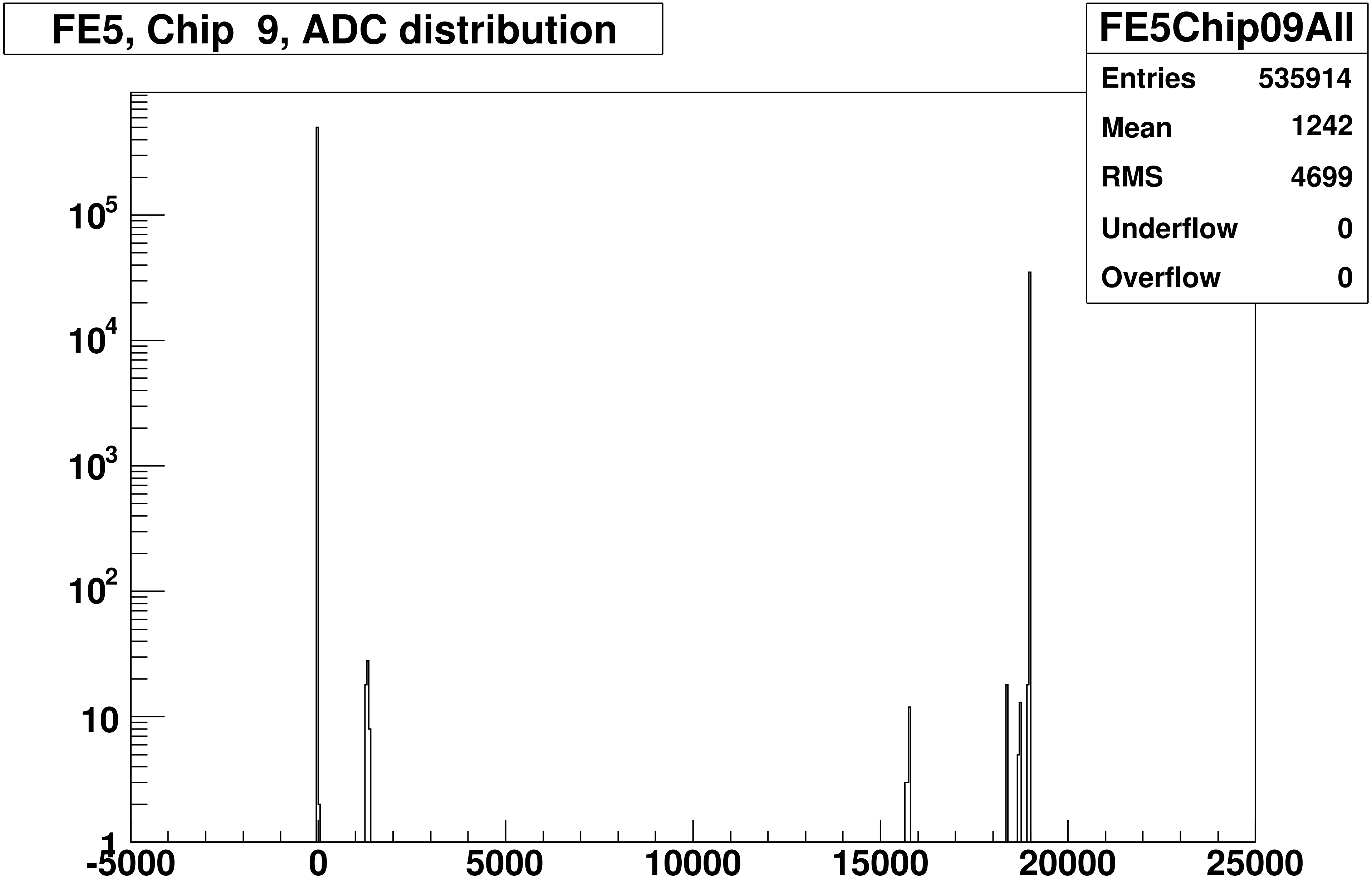

FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 535914
Mean 1.885e+04
RMS 9.74
Underflow 5.003e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 535914
Mean 1281
RMS 4746
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

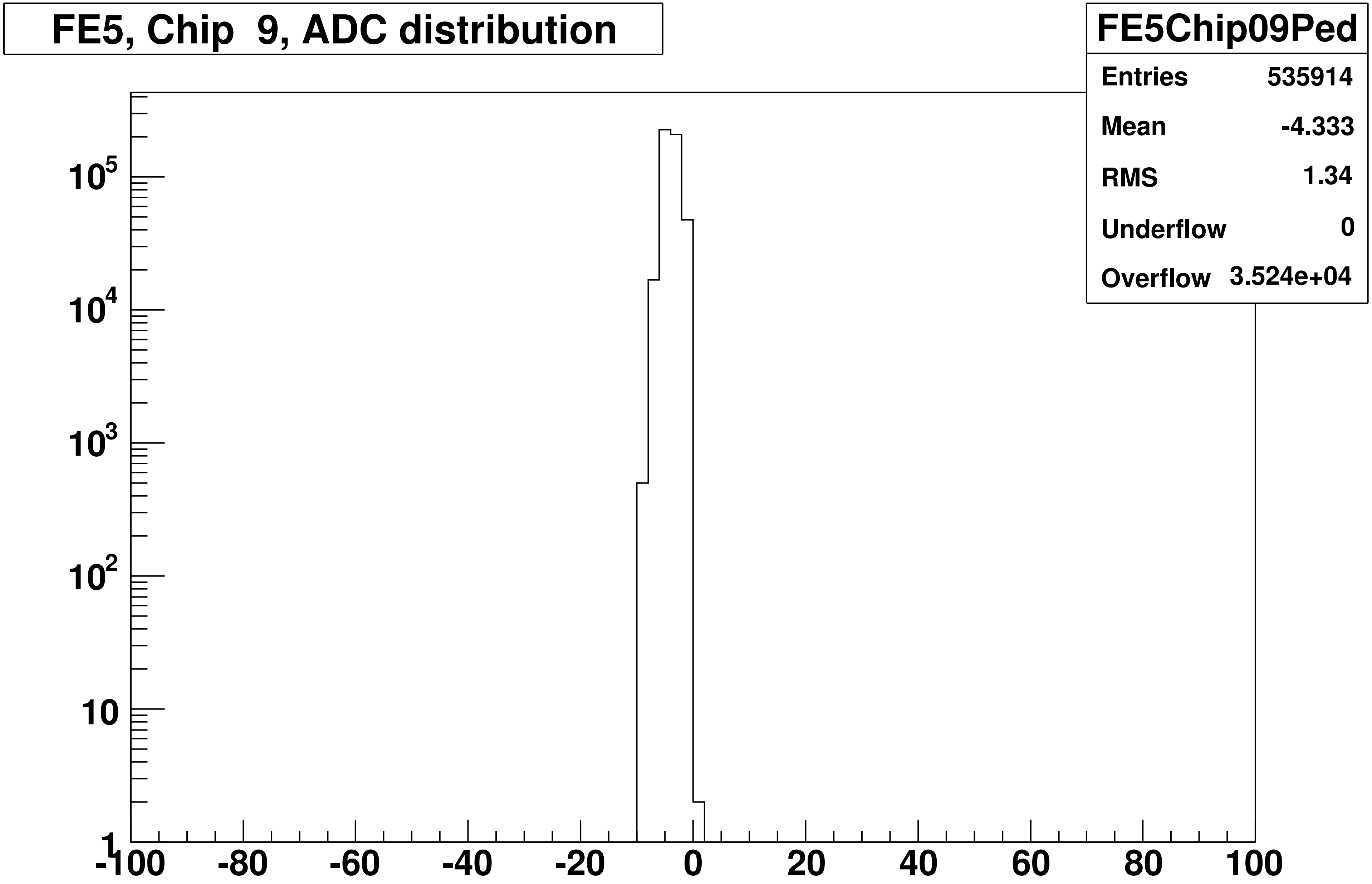

FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 535914
Mean 11.05
RMS 1.287
Underflow 0
Overflow 3.593e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

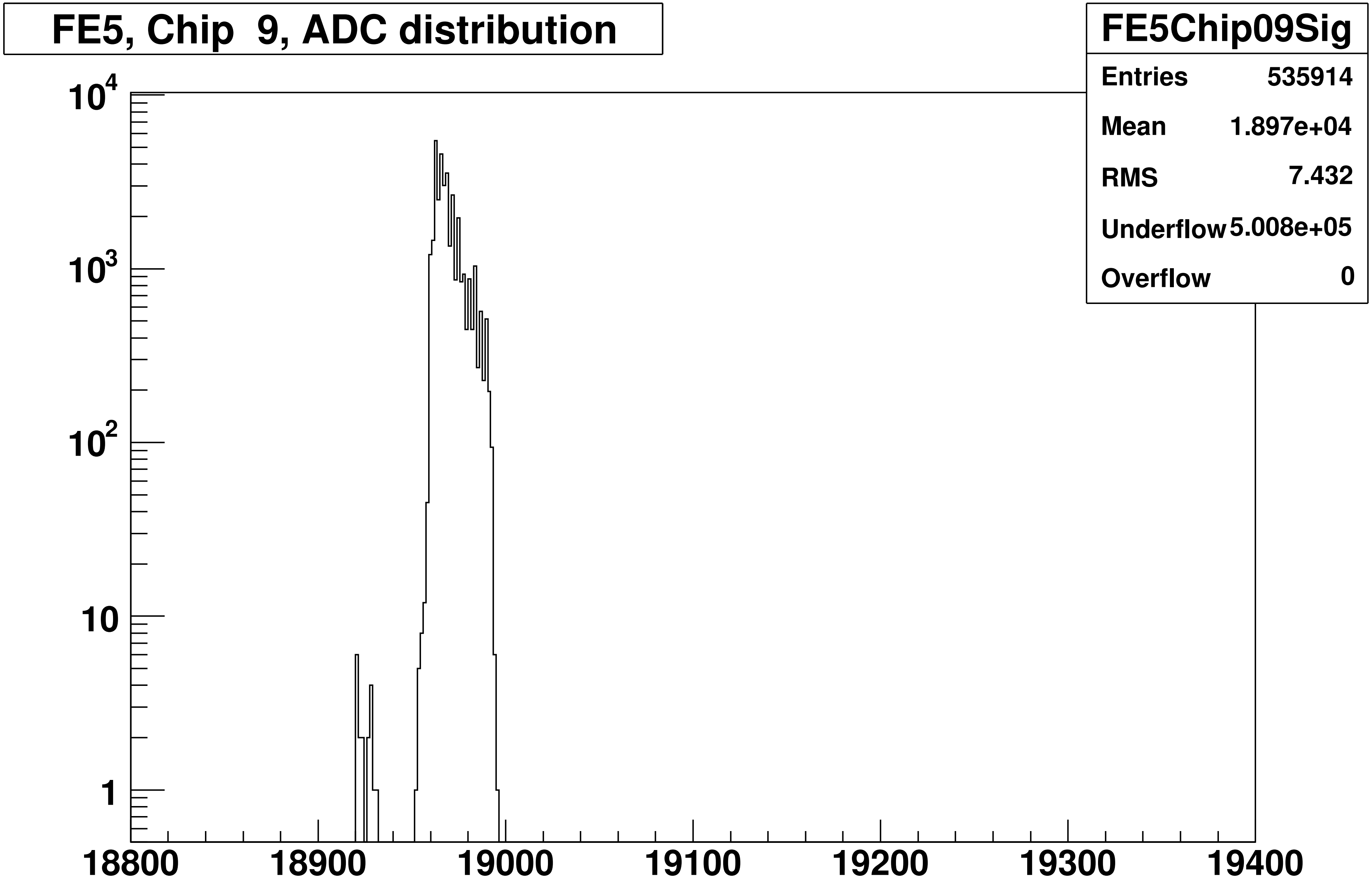


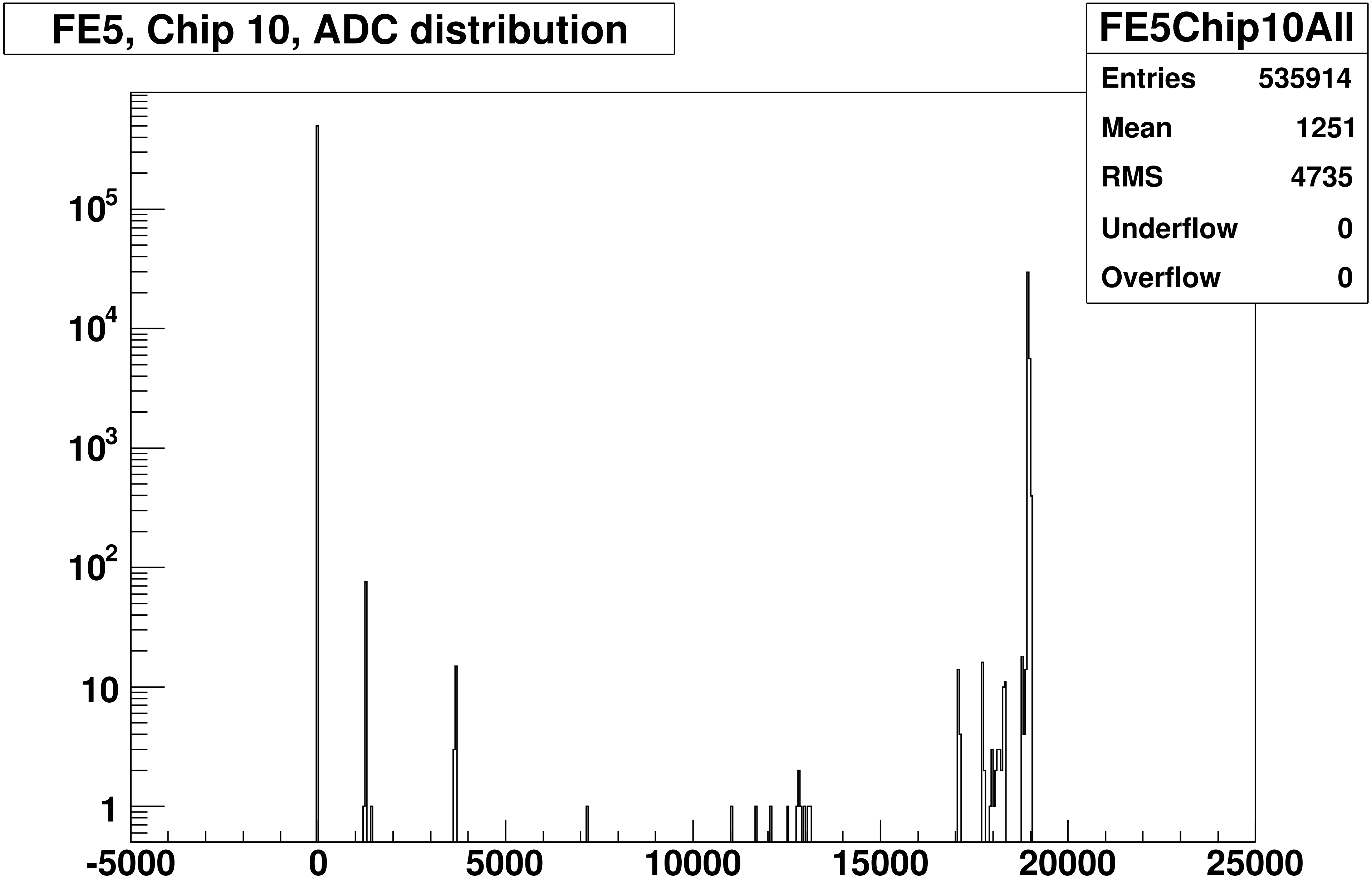
FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 535914
Mean 1.903e+04
RMS 11.14
Underflow 5.001e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

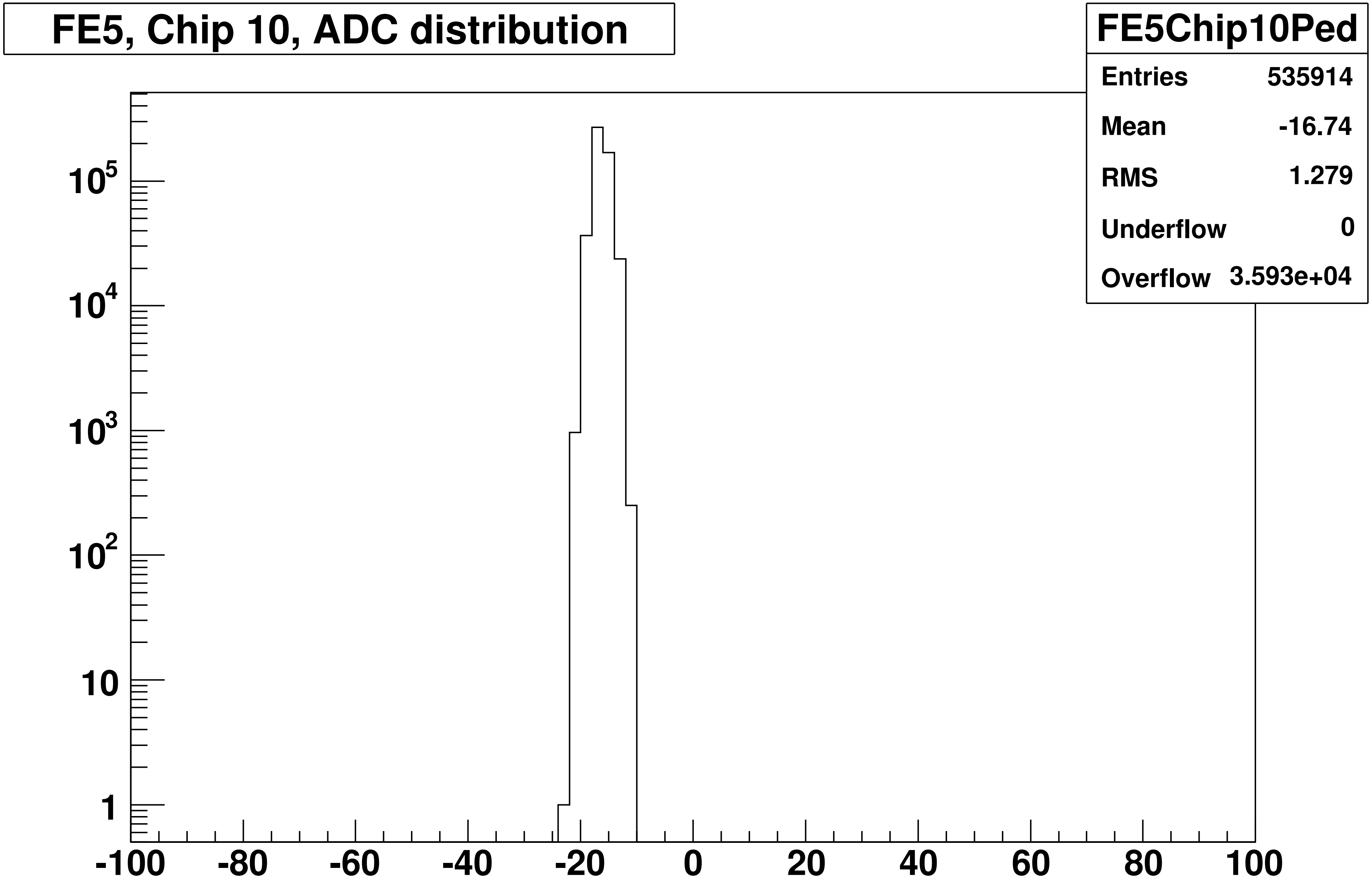


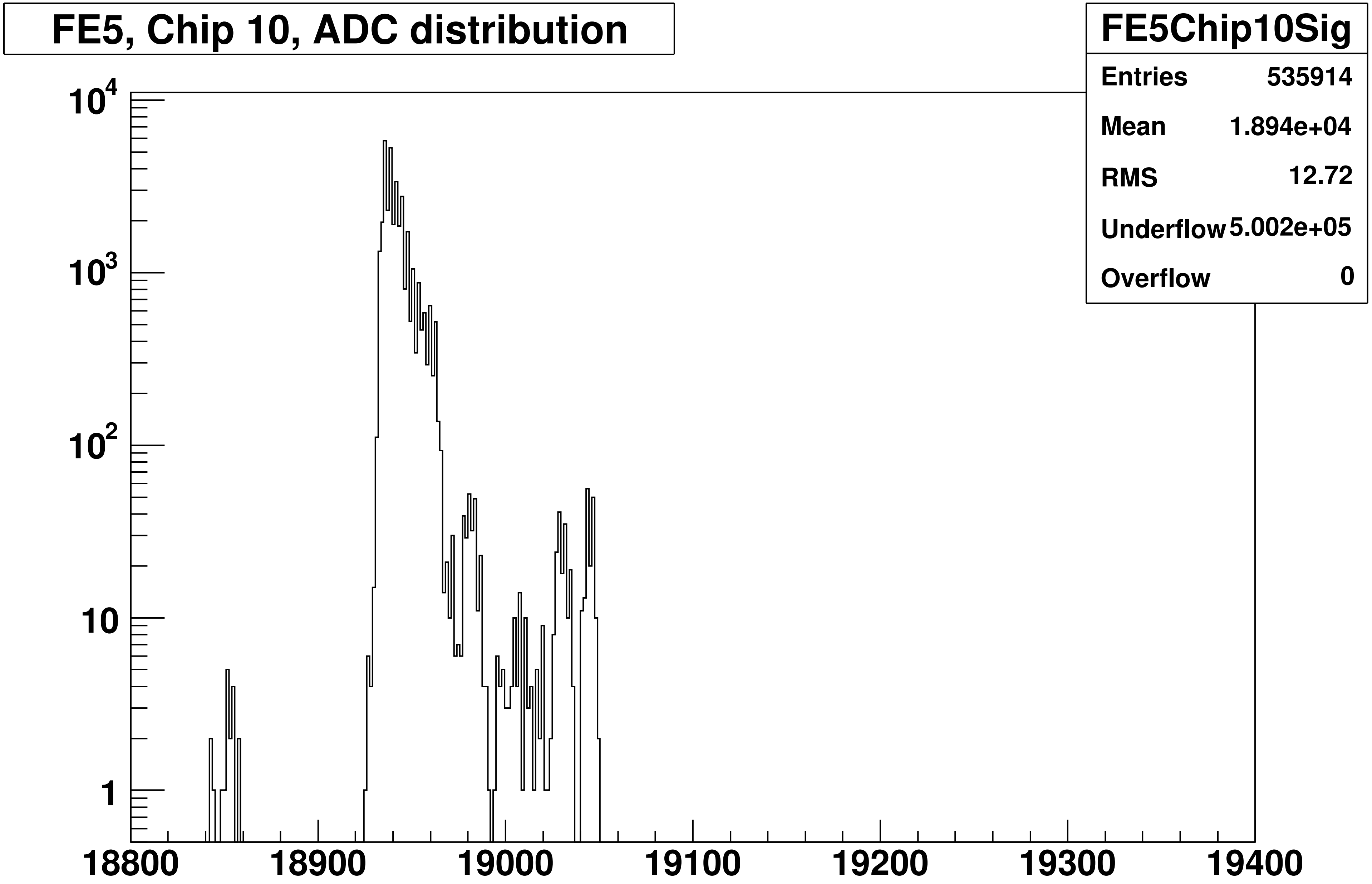
FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 535914
Mean 1242
RMS 4699
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

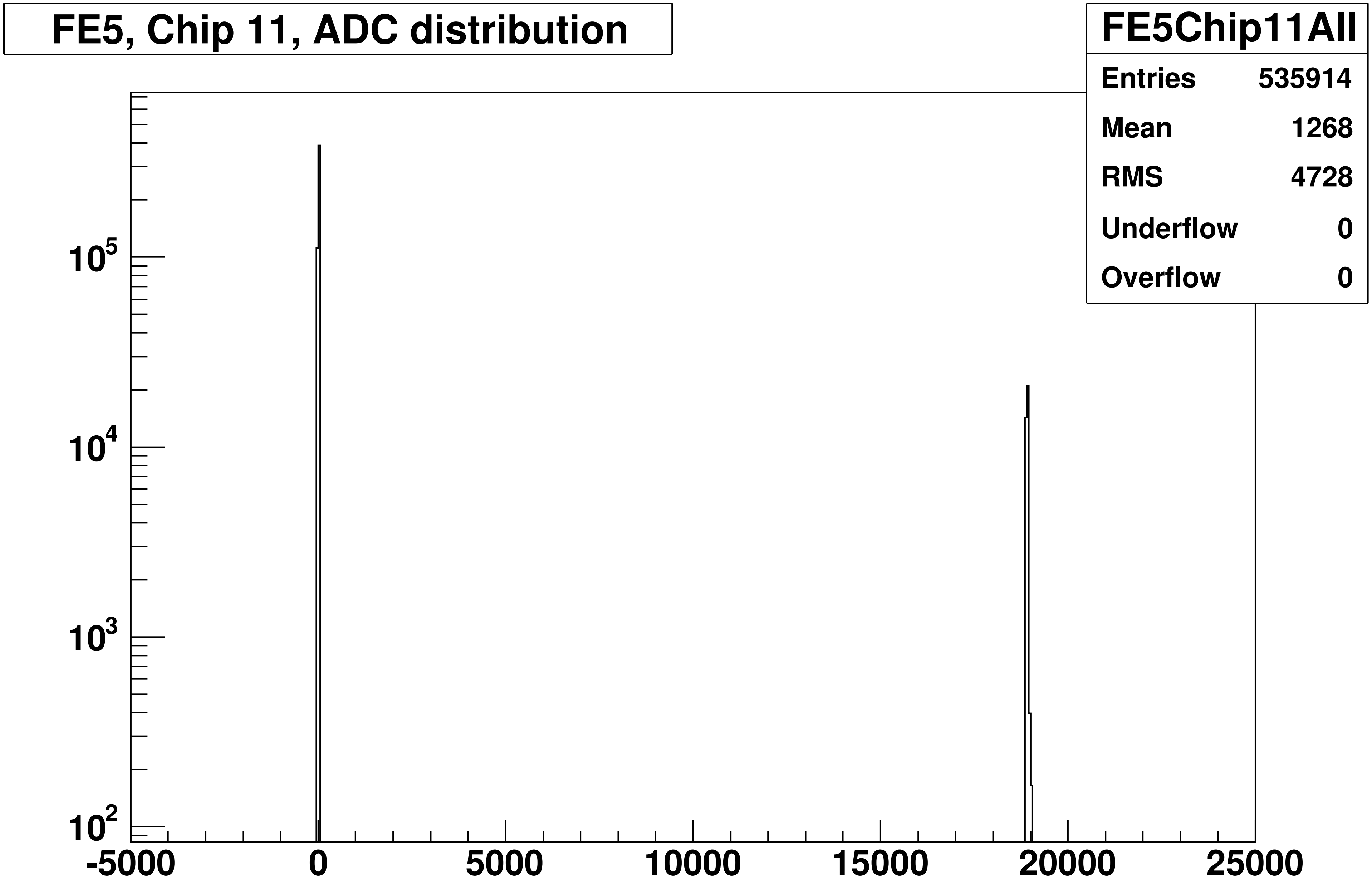

FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 535914
Mean -4.333
RMS 1.34
Underflow 0
Overflow 3.524e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 535914
Mean 1.897e+04
RMS 7.432
Underflow 5.008e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

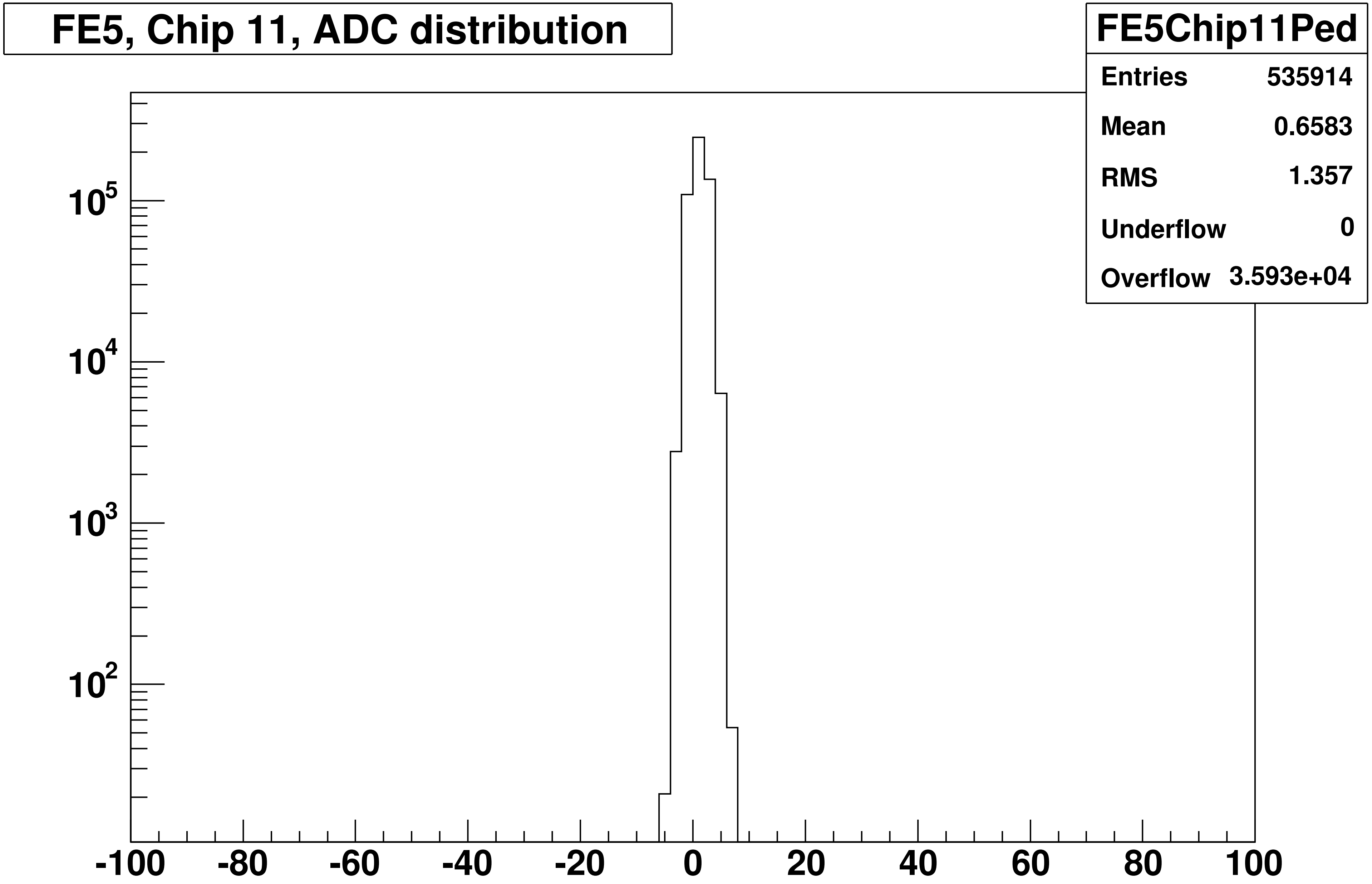

FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 535914
Mean 1251
RMS 4735
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 535914
Mean -16.74
RMS 1.279
Underflow 0
Overflow 3.593e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

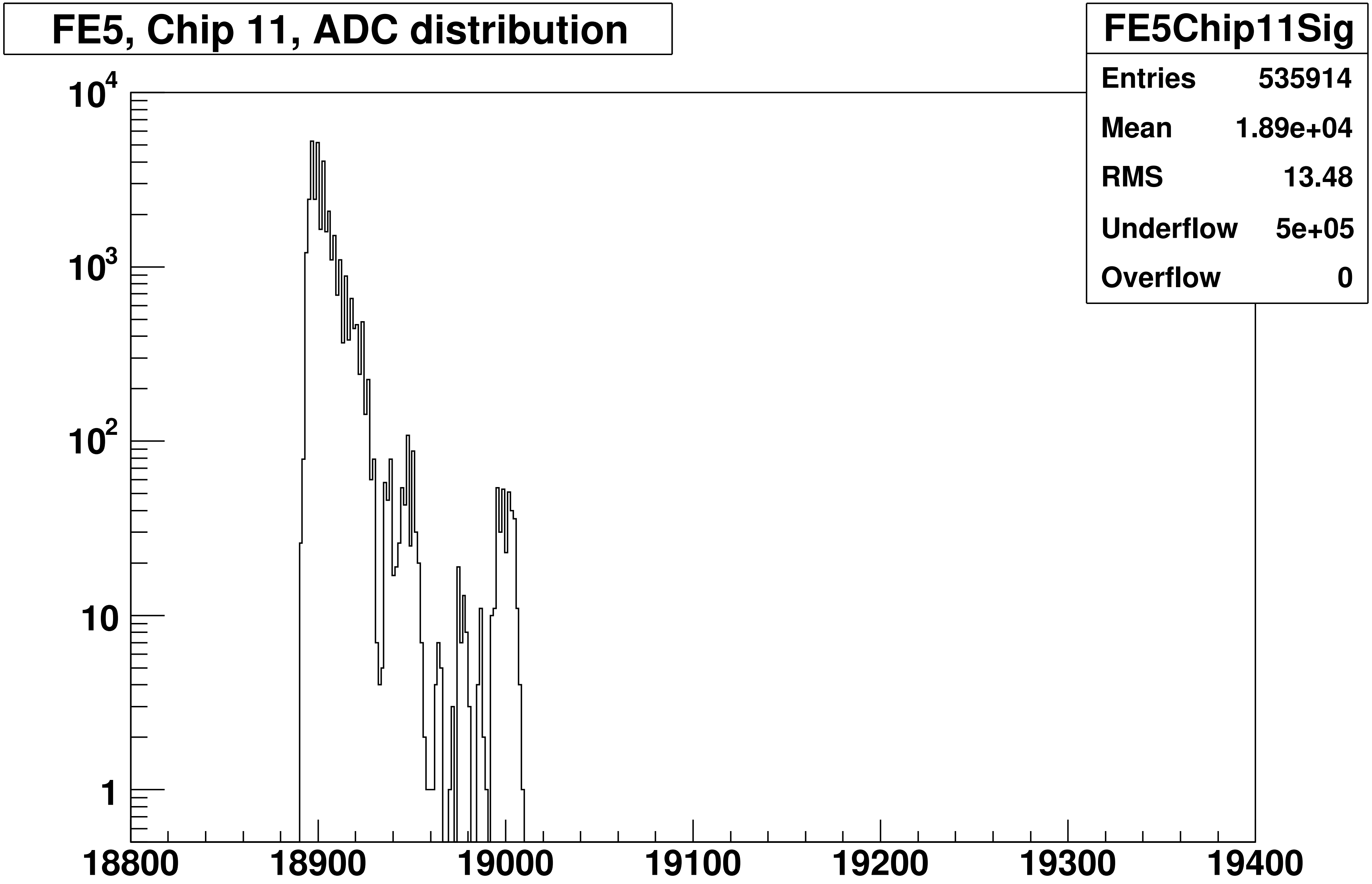

FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 535914
Mean 1.894e+04
RMS 12.72
Underflow 5.002e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

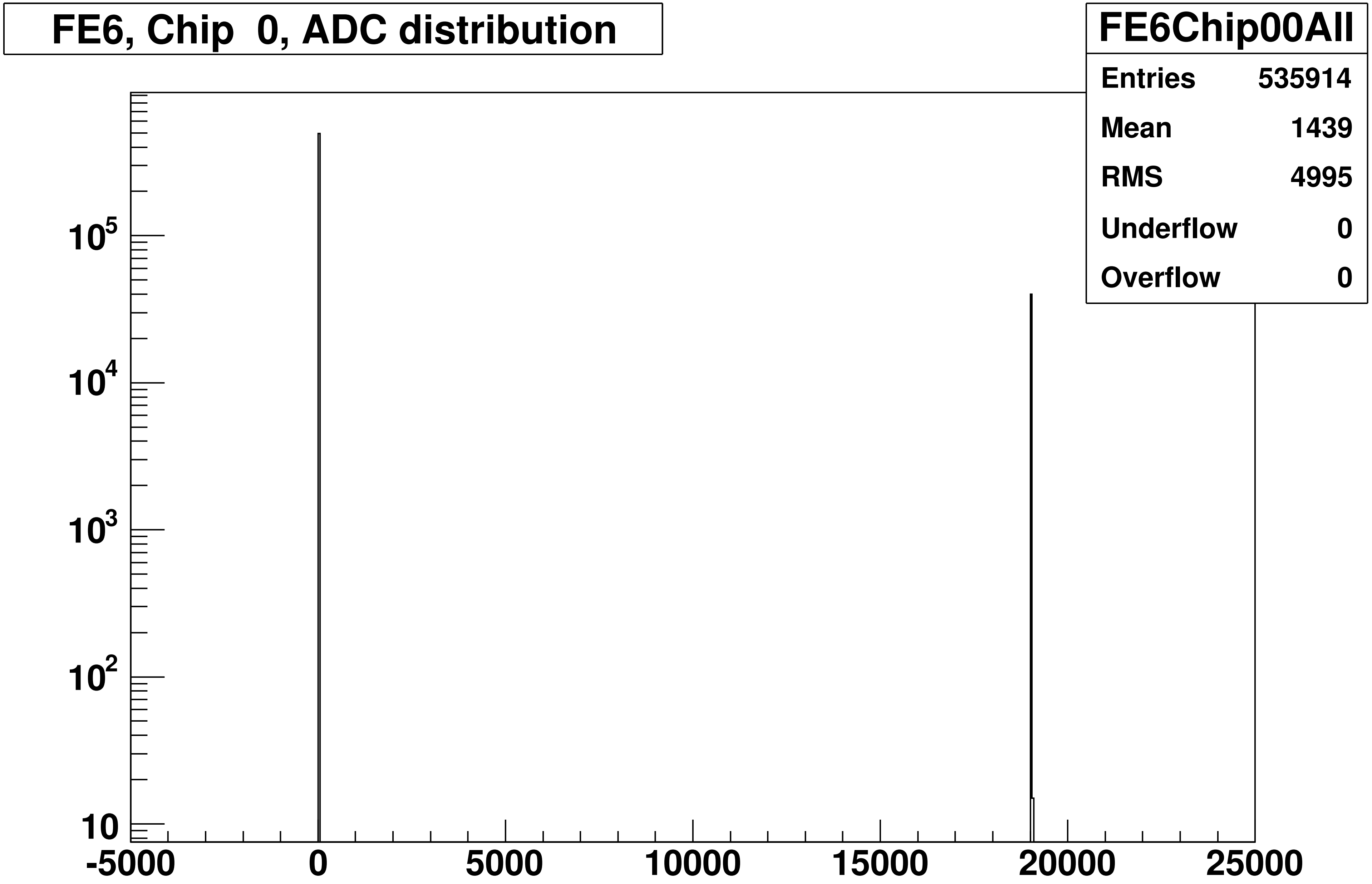


FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 535914
Mean 1268
RMS 4728
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

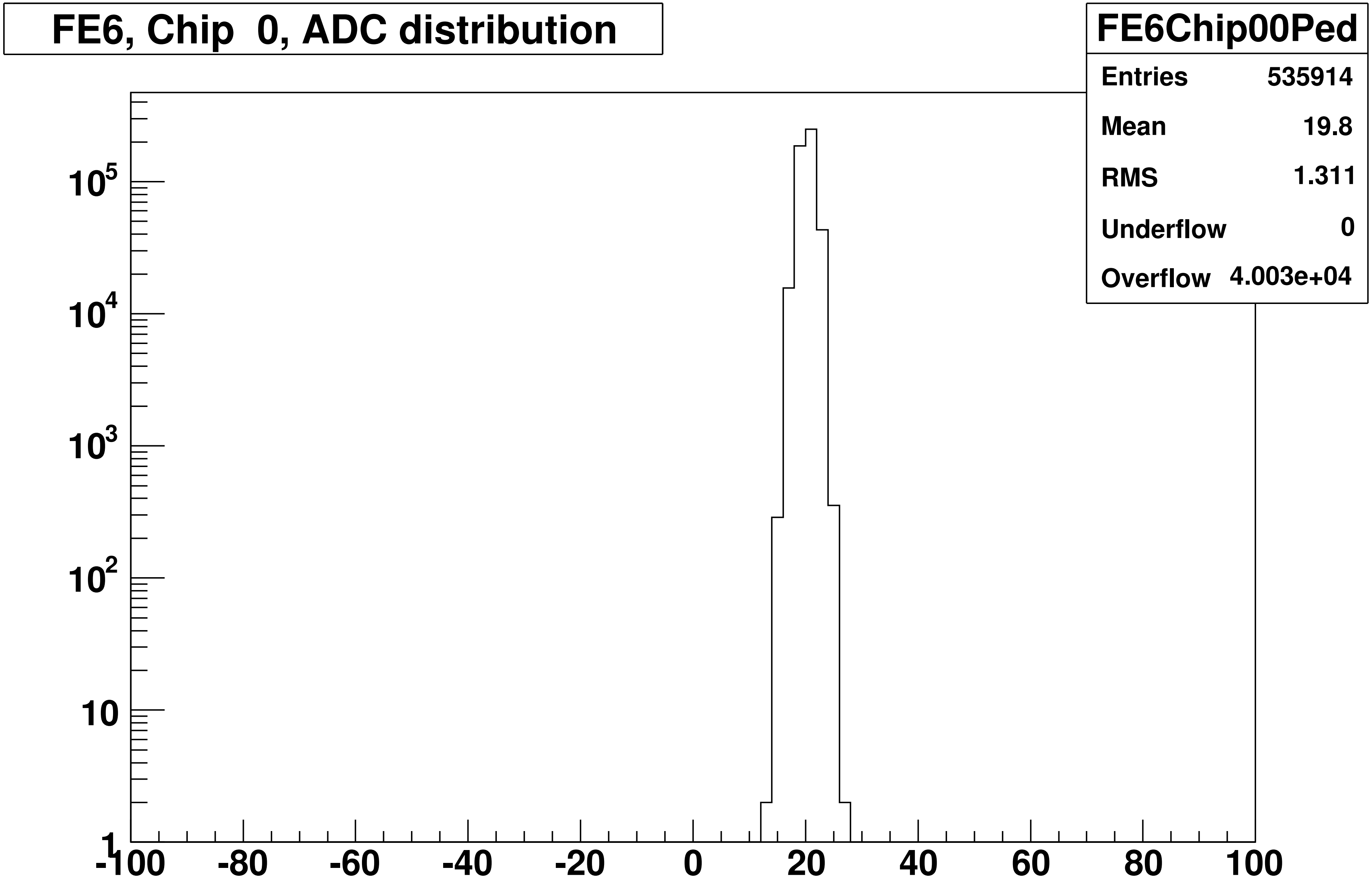


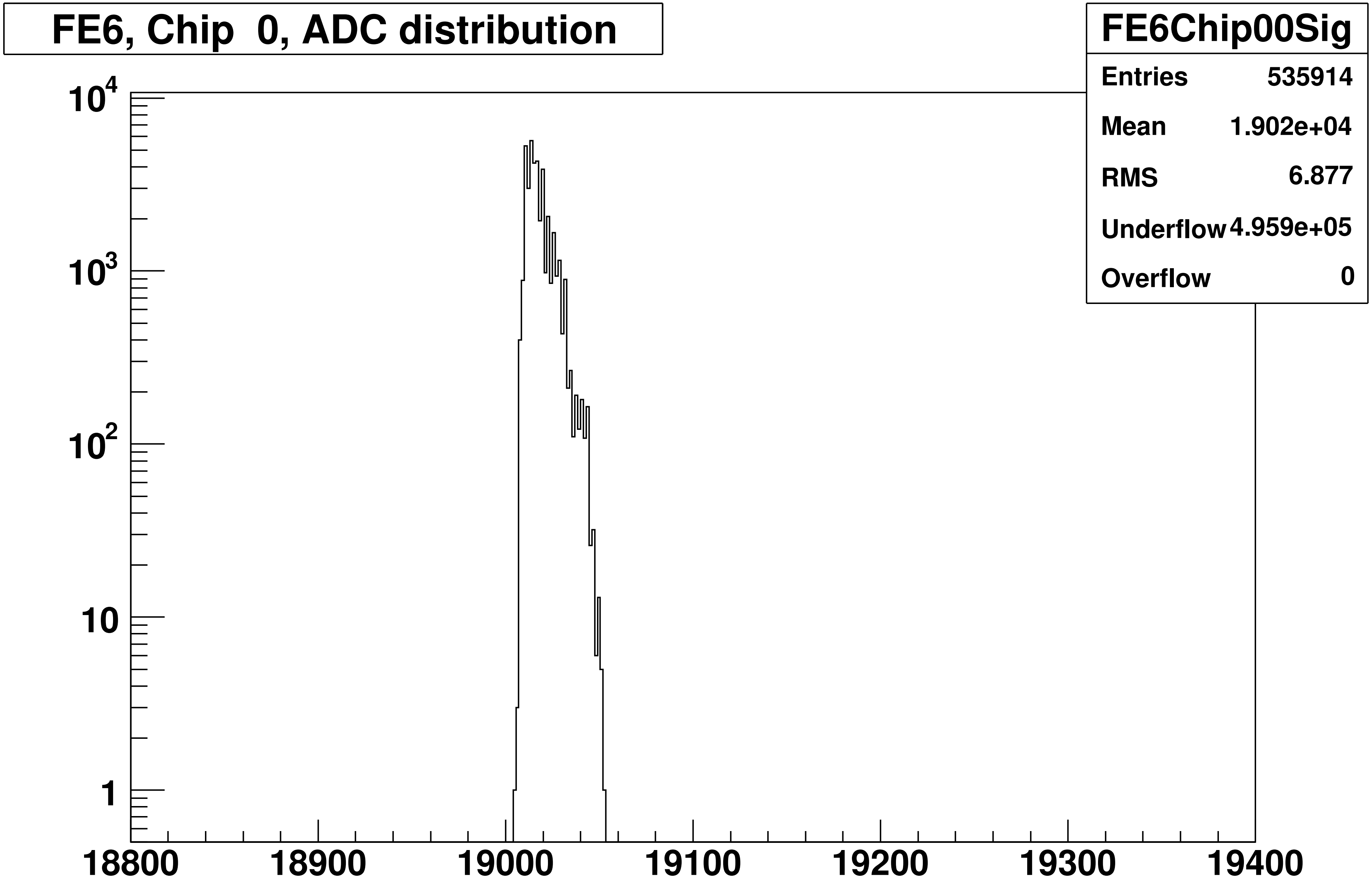
FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 535914
Mean 0.6583
RMS 1.357
Underflow 0
Overflow 3.593e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

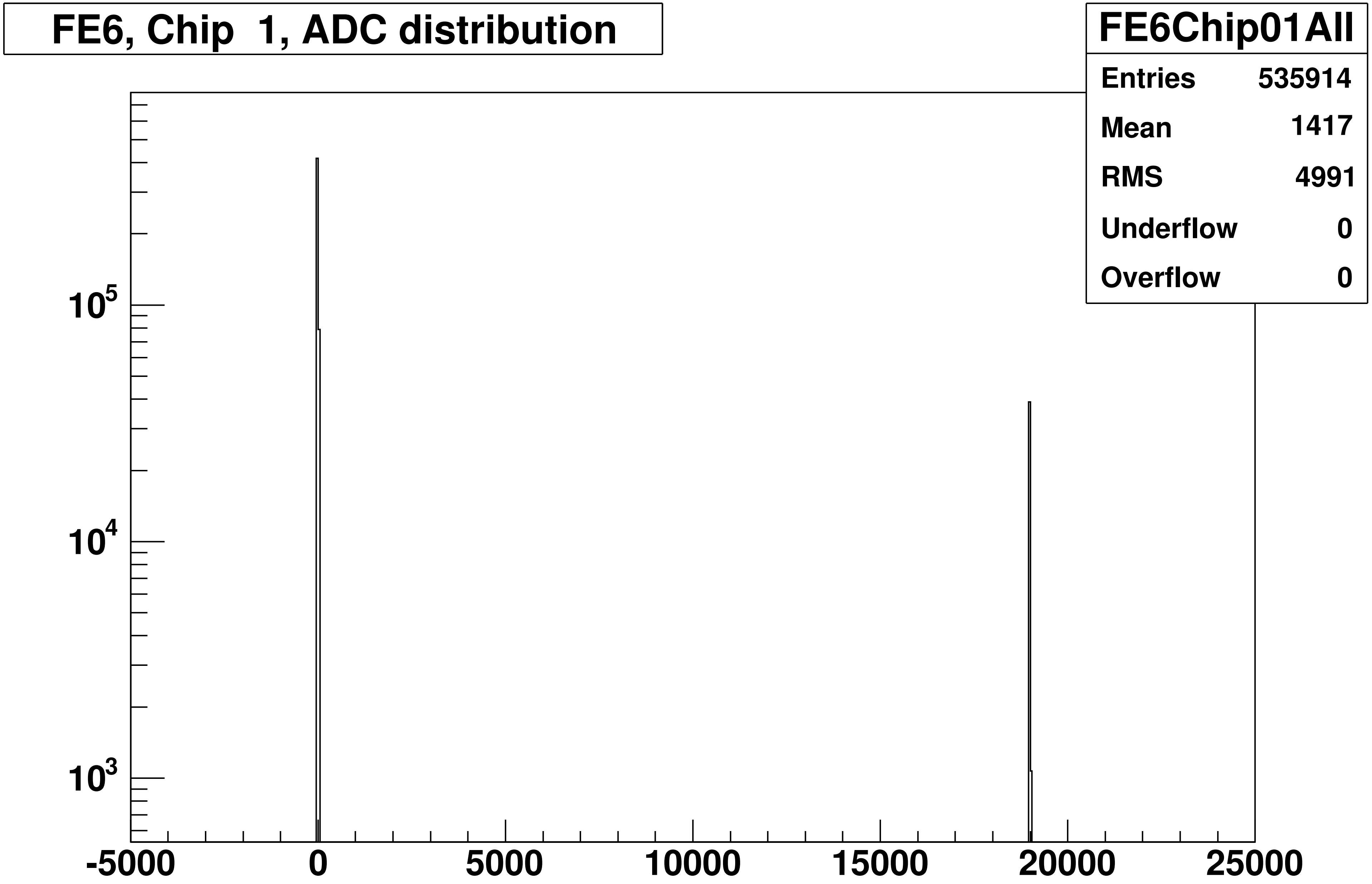

FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 535914
Mean 1.89e+04
RMS 13.48
Underflow 5e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

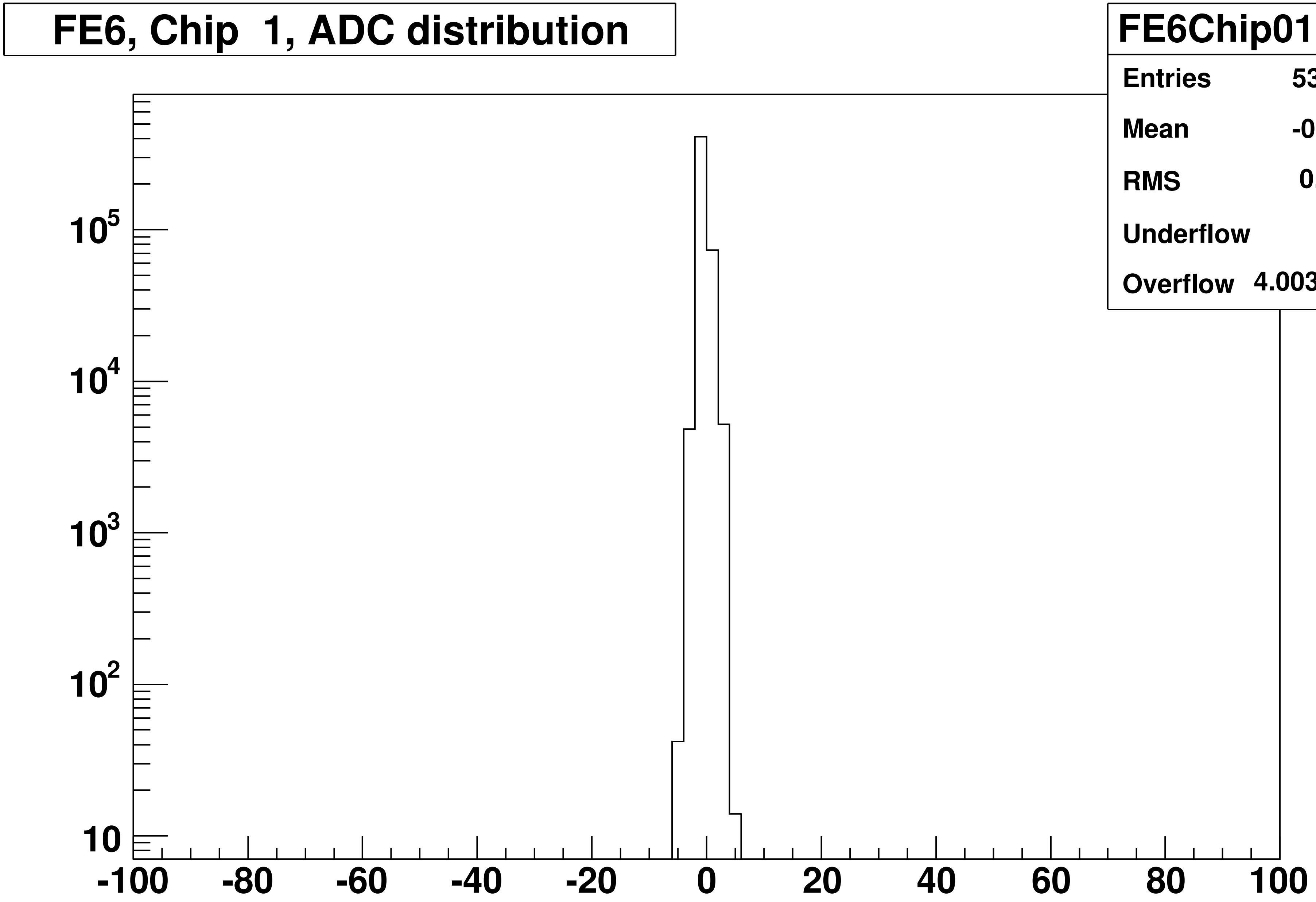


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 535914
Mean 1439
RMS 4995
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

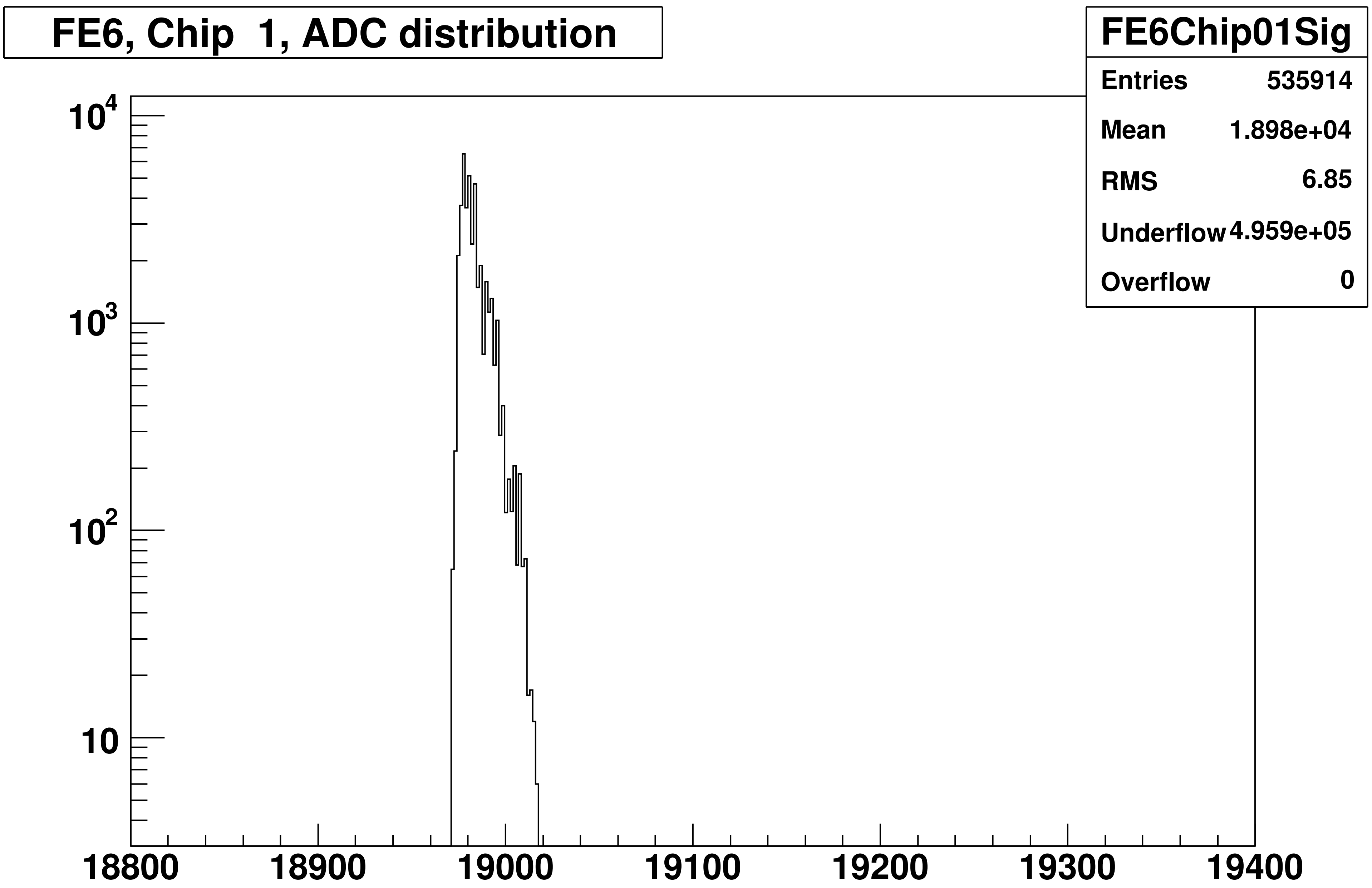

FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 535914
Mean 19.8
RMS 1.311
Underflow 0
Overflow 4.003e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

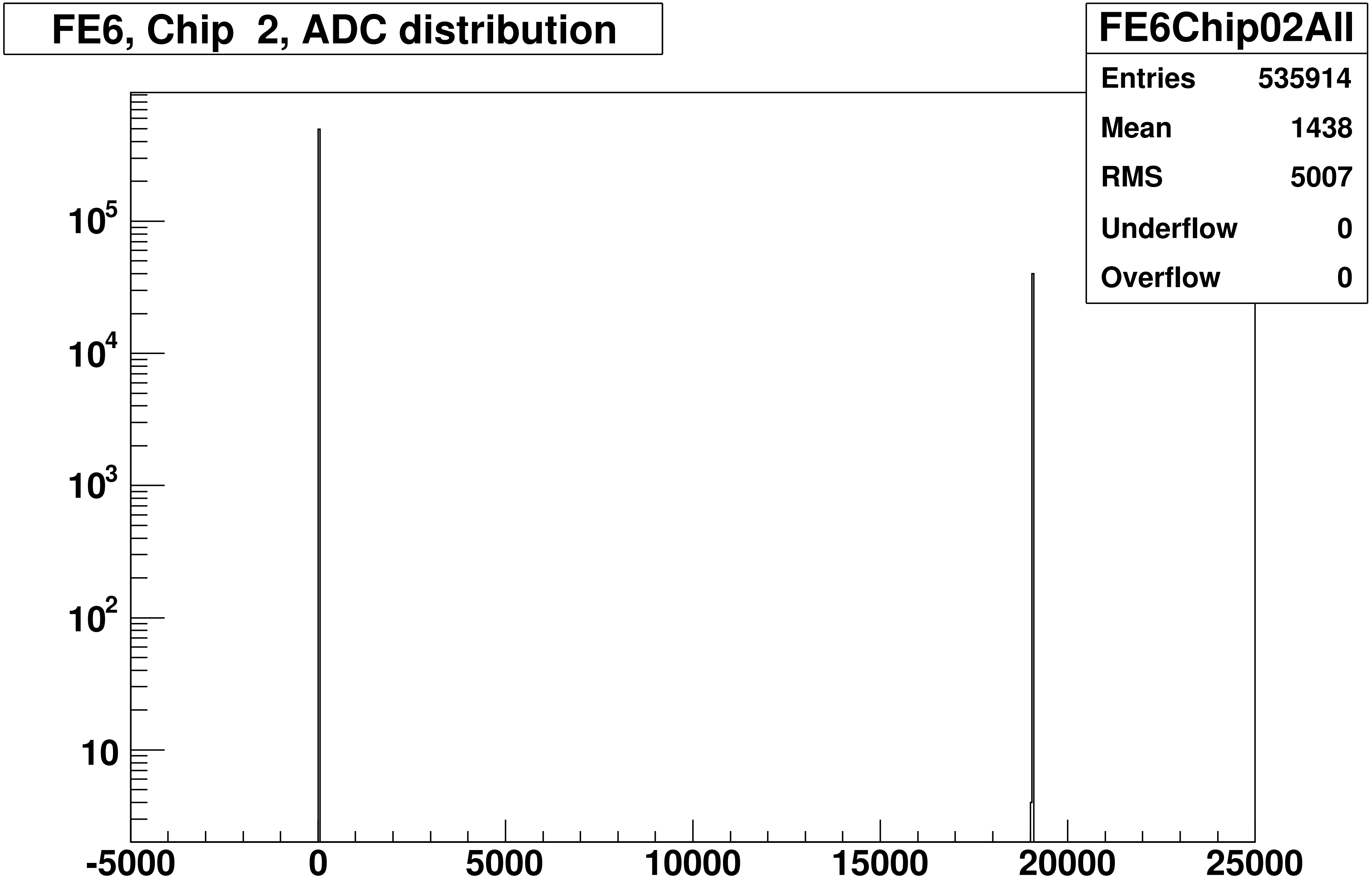

FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 535914
Mean 1.902e+04
RMS 6.877
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 535914
Mean 1417
RMS 4991
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

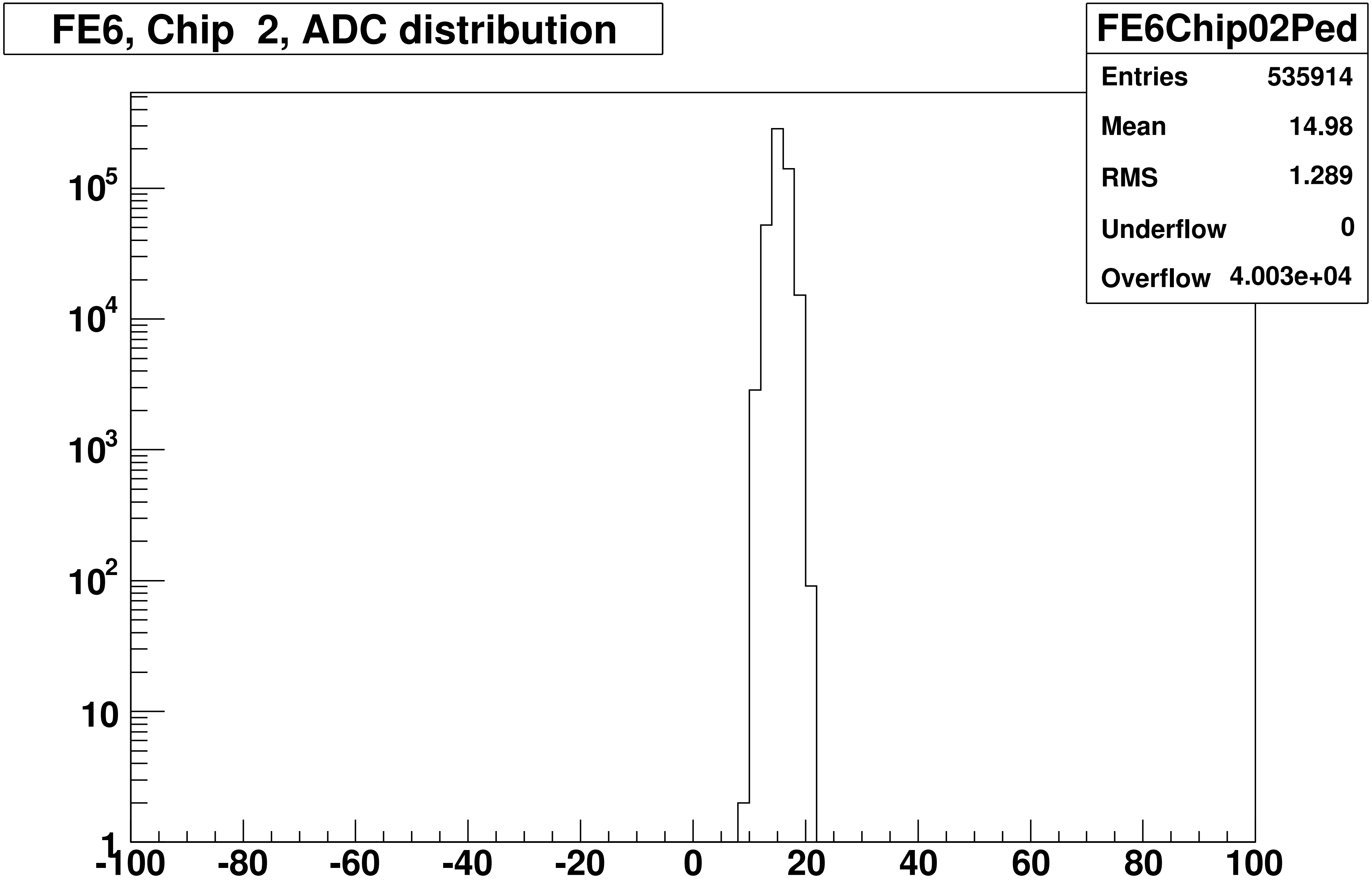


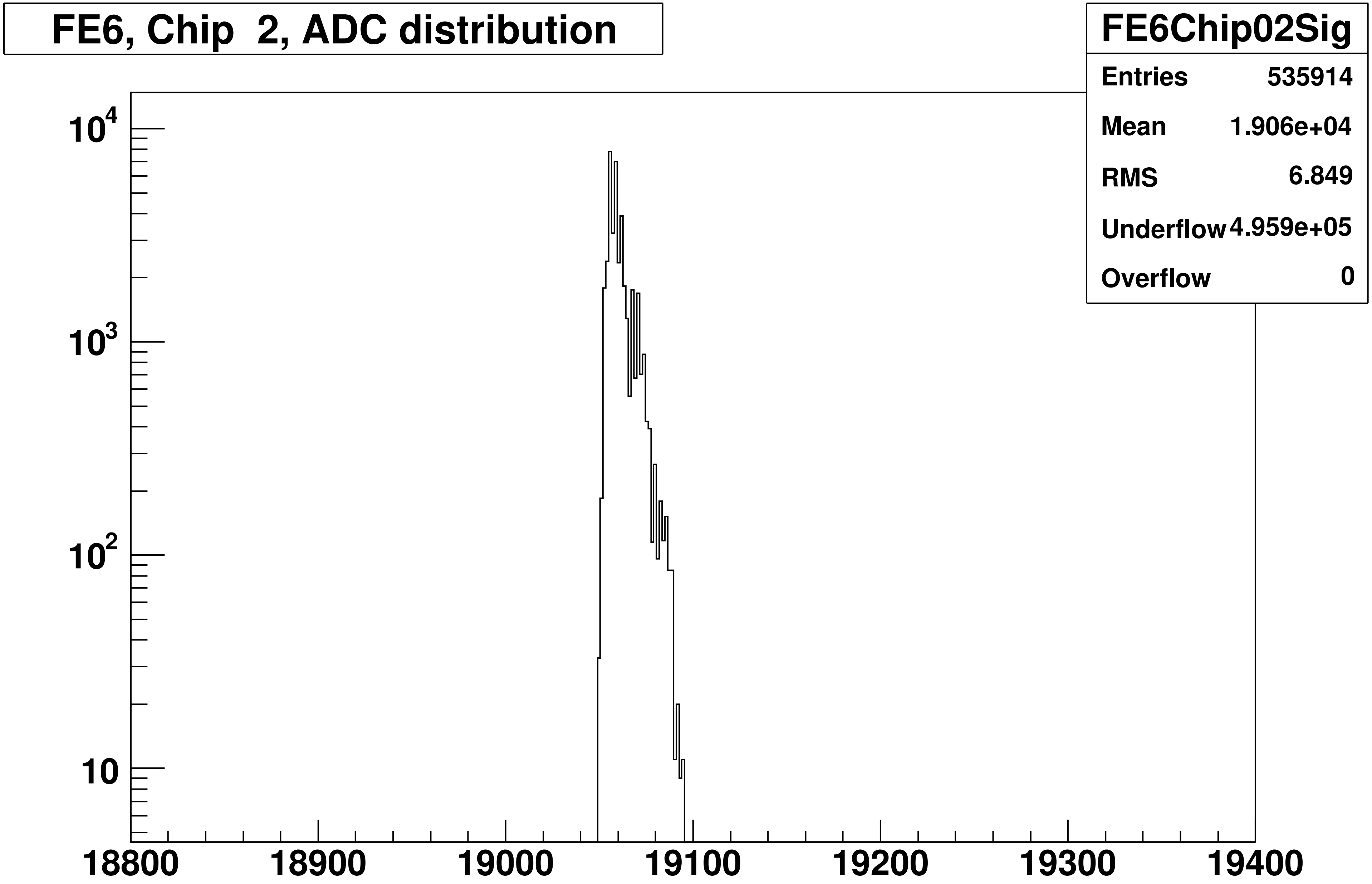
FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 535914
Mean -0.8493
RMS 0.6653
Underflow 0
Overflow 4.003e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

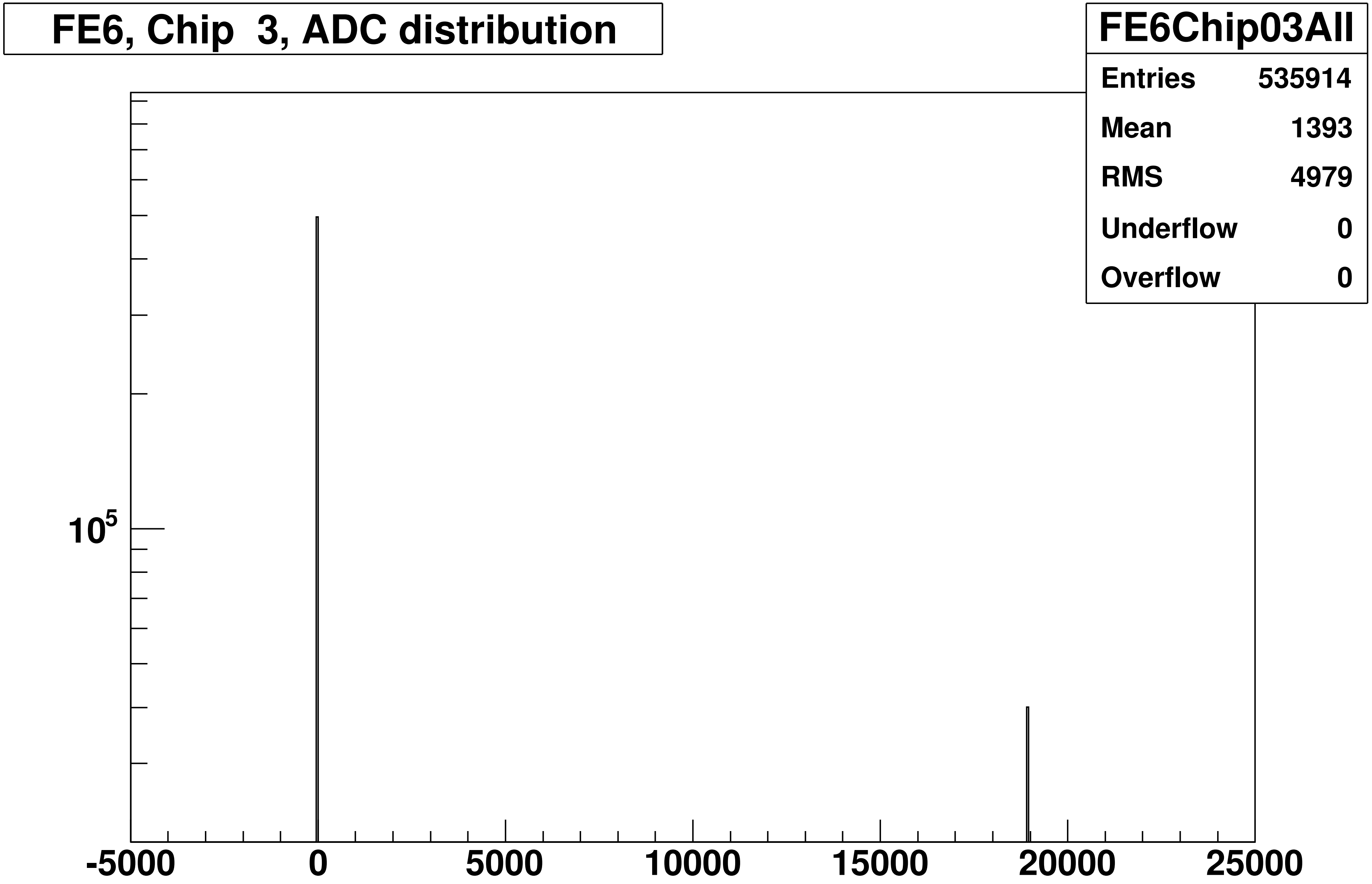

FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 535914
Mean 1.898e+04
RMS 6.85
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400

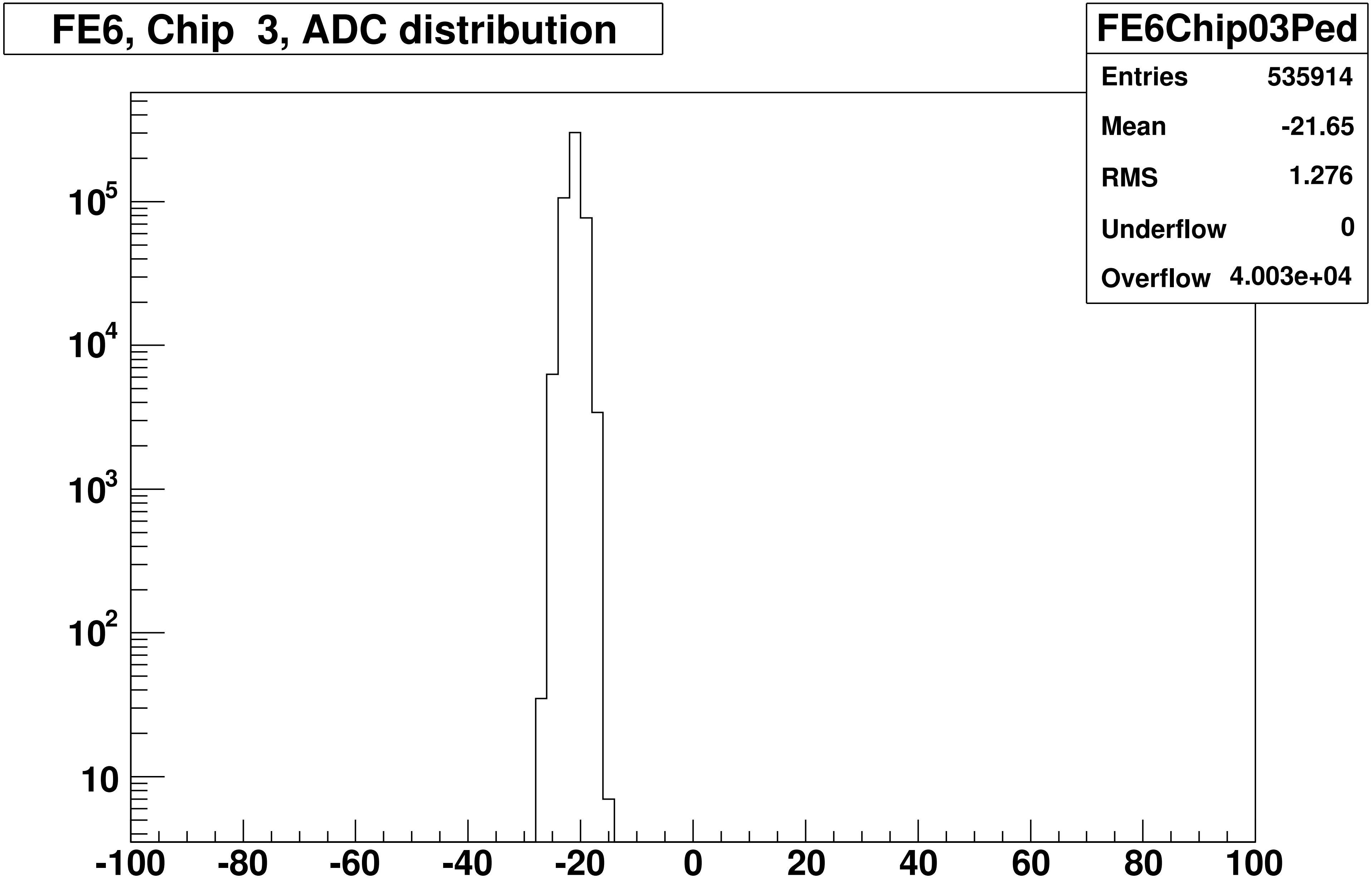


FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 535914
Mean 1438
RMS 5007
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

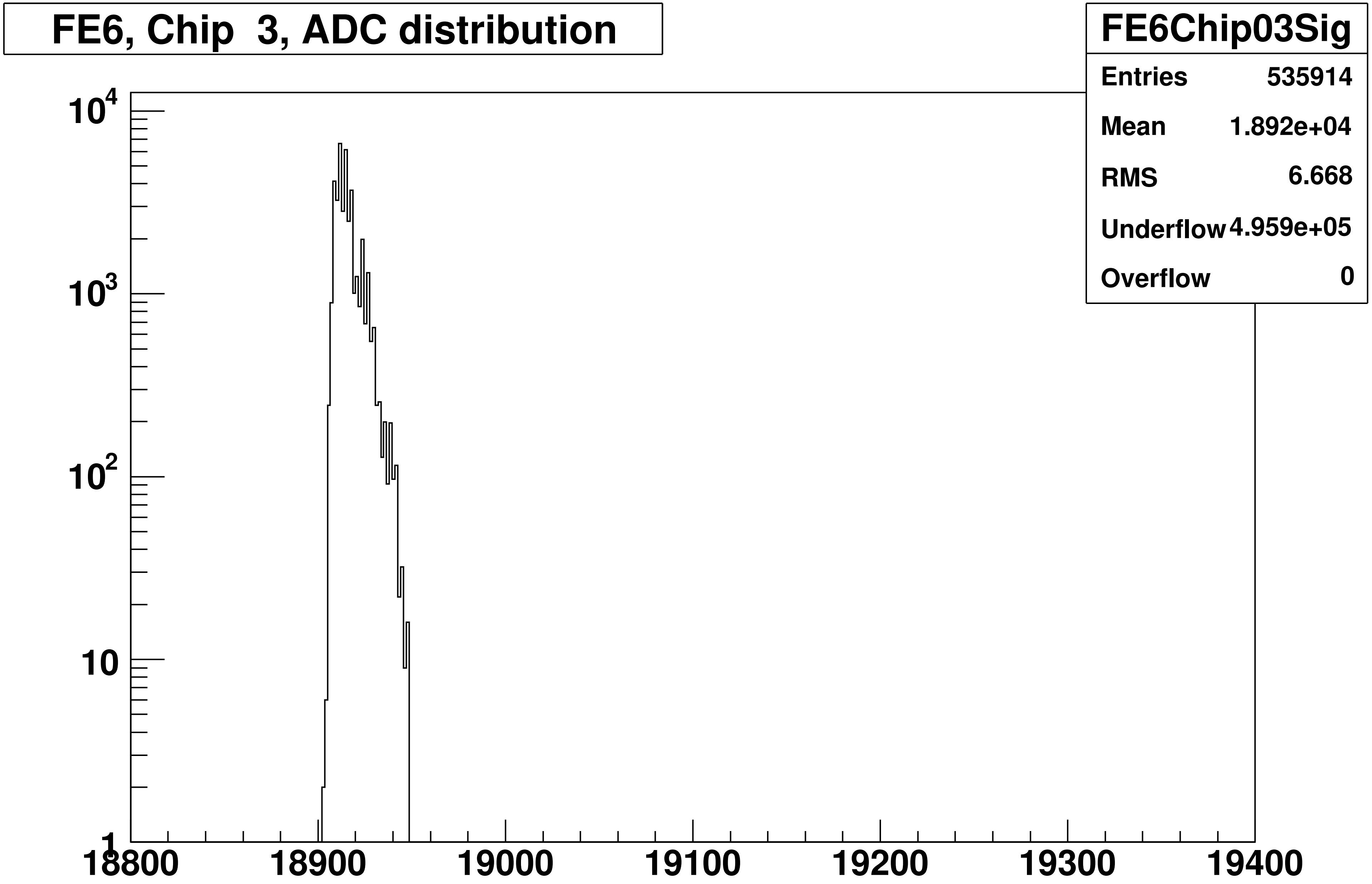

FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 535914
Mean 14.98
RMS 1.289
Underflow 0
Overflow 4.003e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

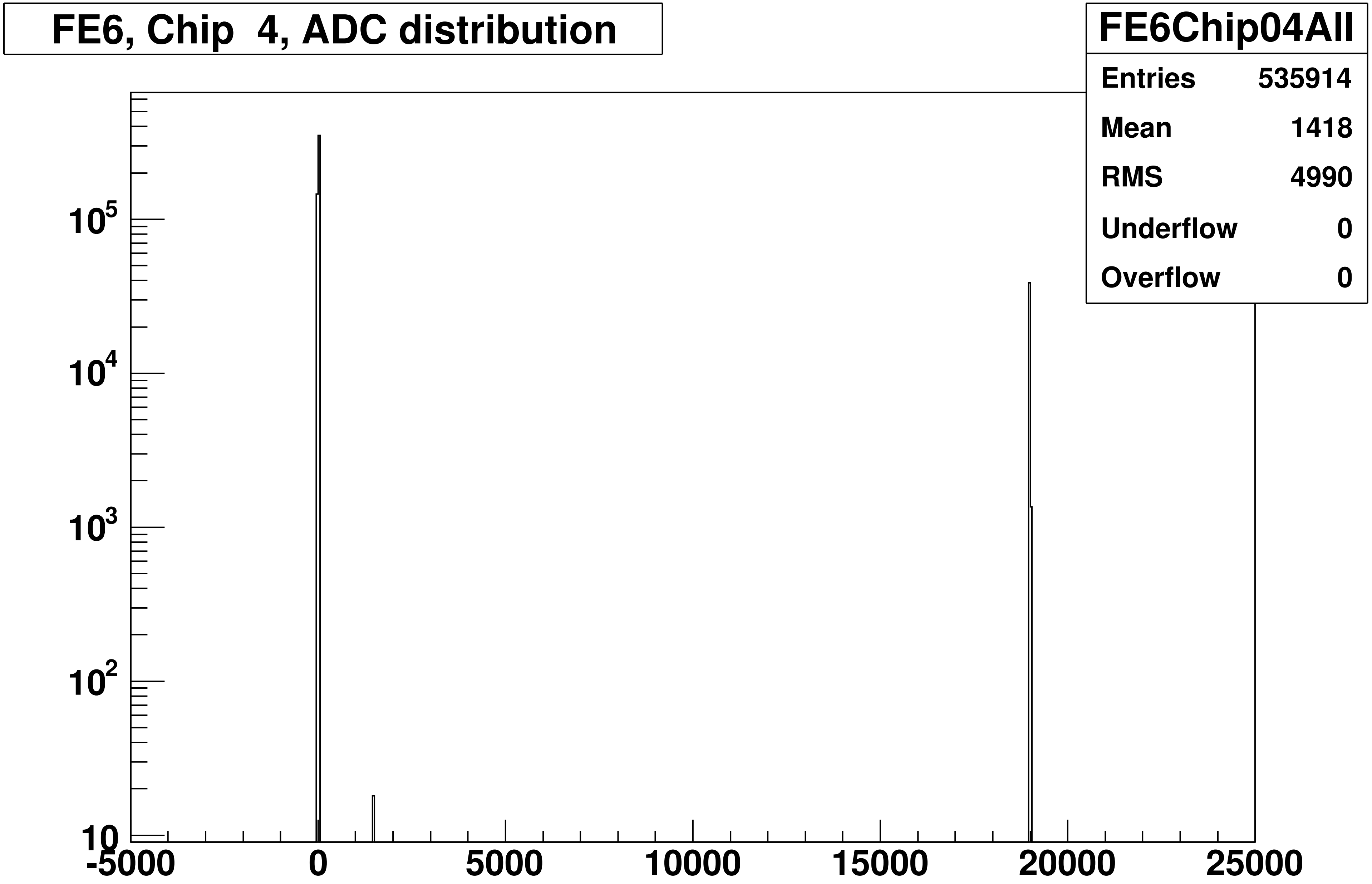

FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 535914
Mean 1.906e+04
RMS 6.849
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400

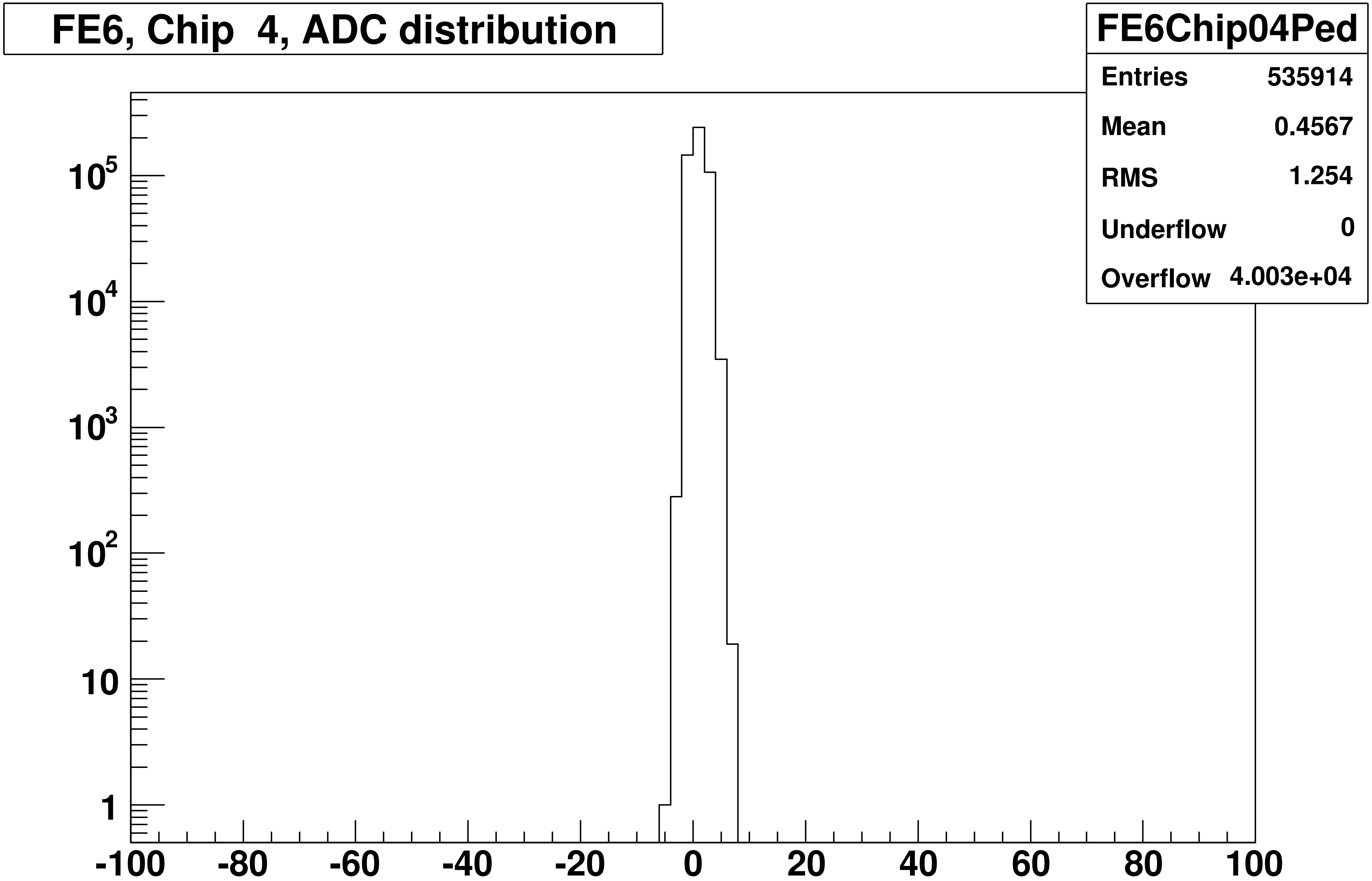

FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 535914
Mean 1393
RMS 4979
Underflow 0
Overflow 0
10^5
-5000
0
5000
10000
15000
20000
25000



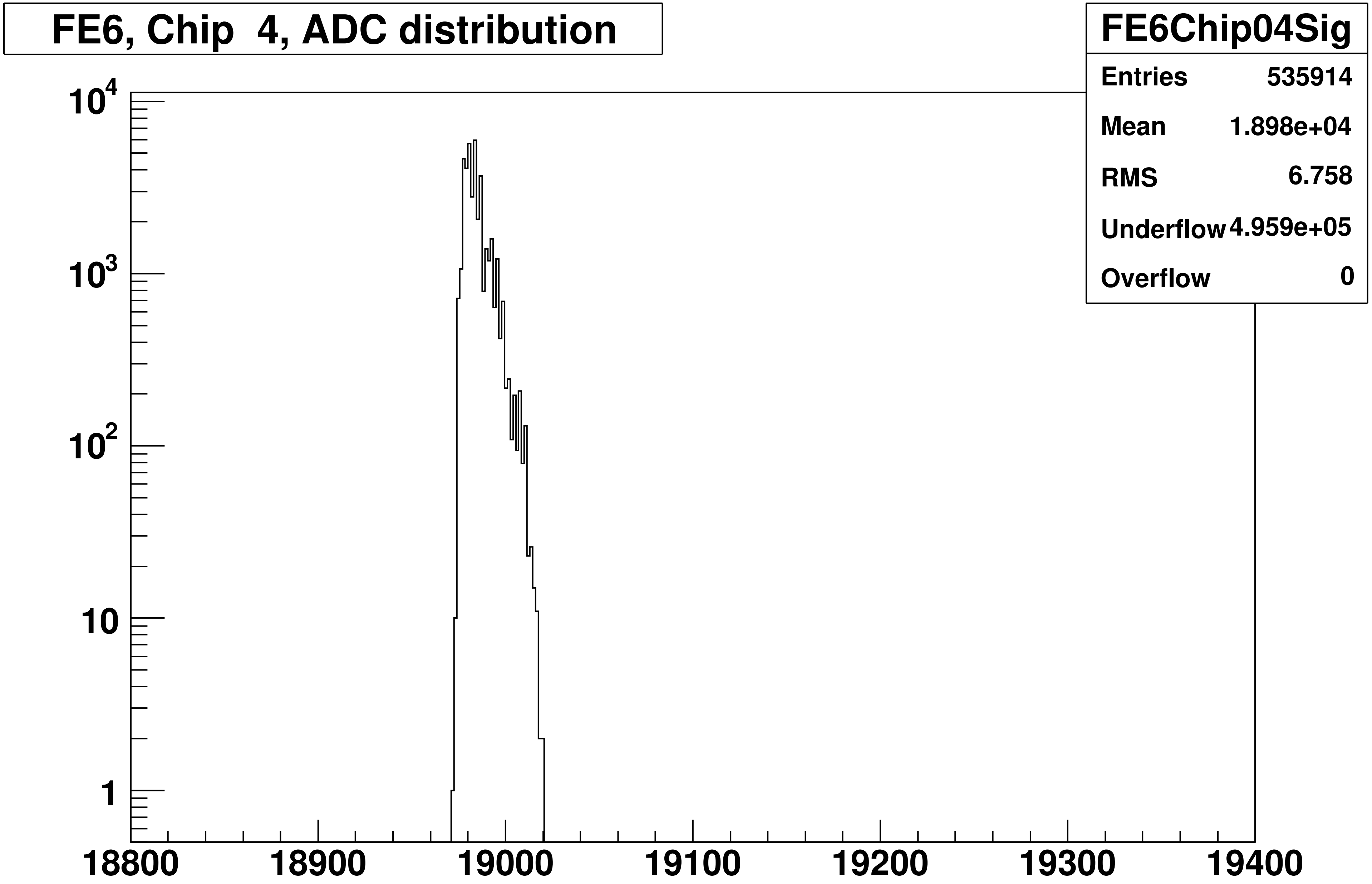
FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 535914
Mean -21.65
RMS 1.276
Underflow 0
Overflow 4.003e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

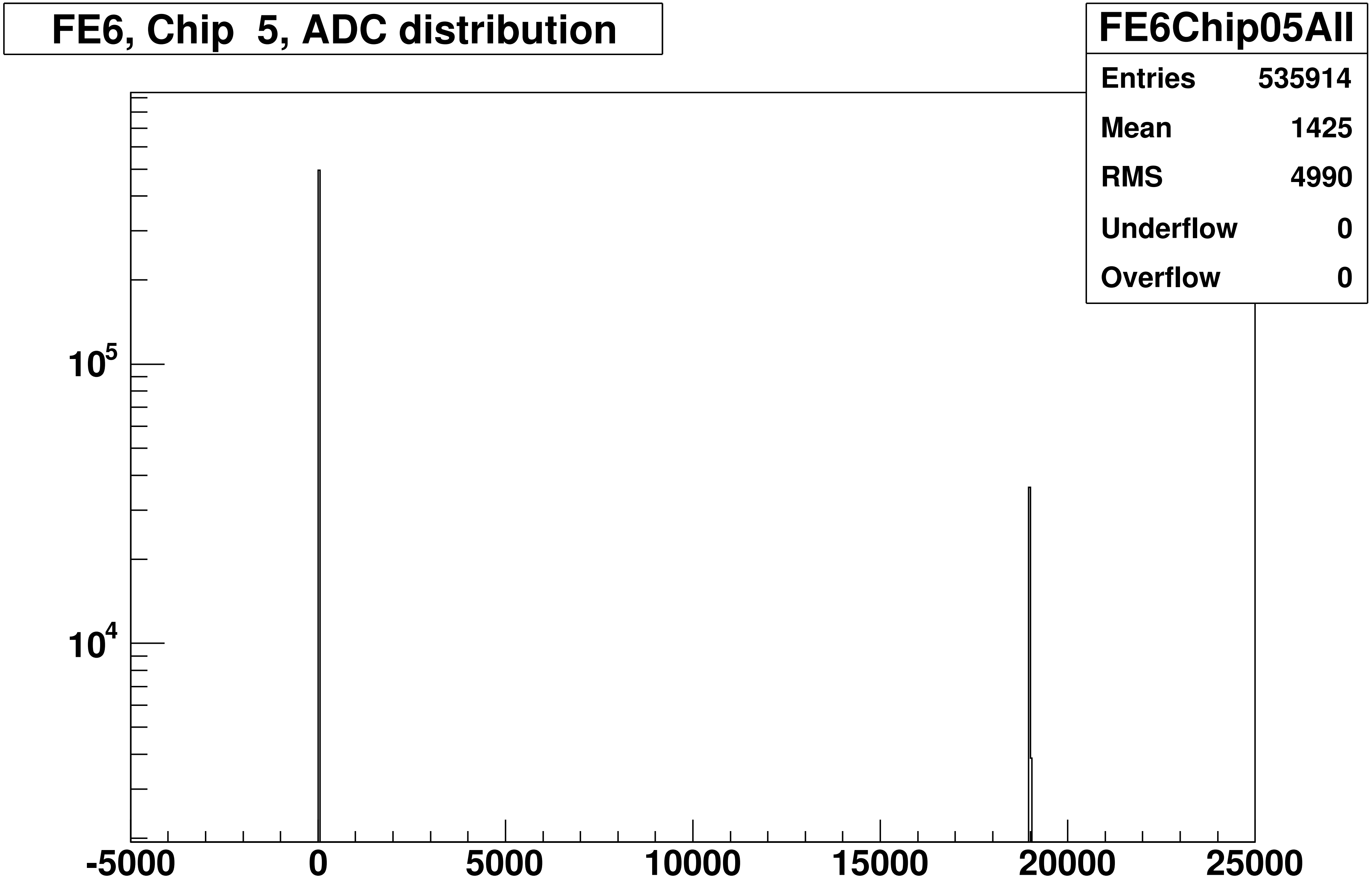

FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 535914
Mean 1.892e+04
RMS 6.668
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

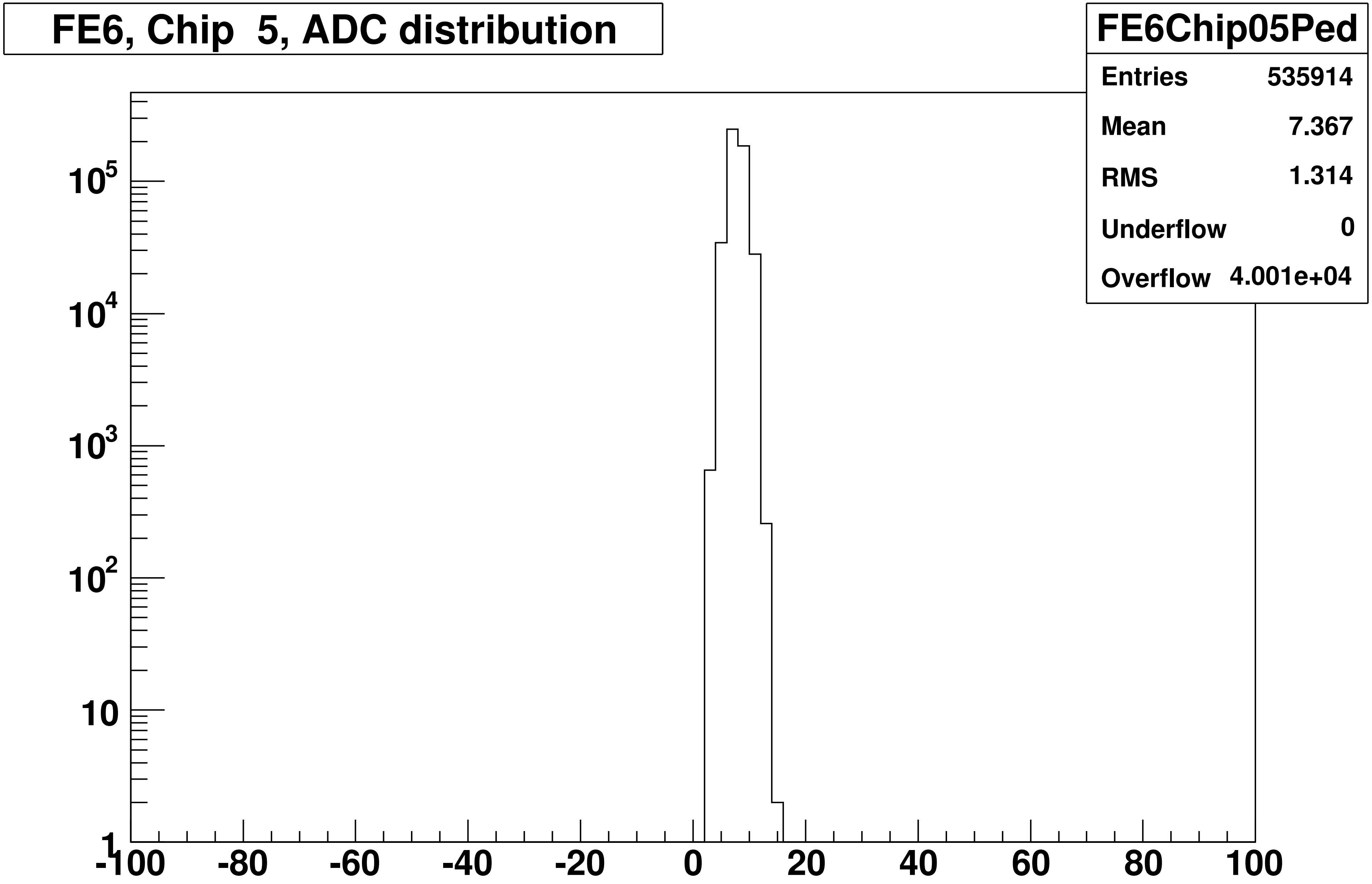

FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 535914
Mean 1418
RMS 4990
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



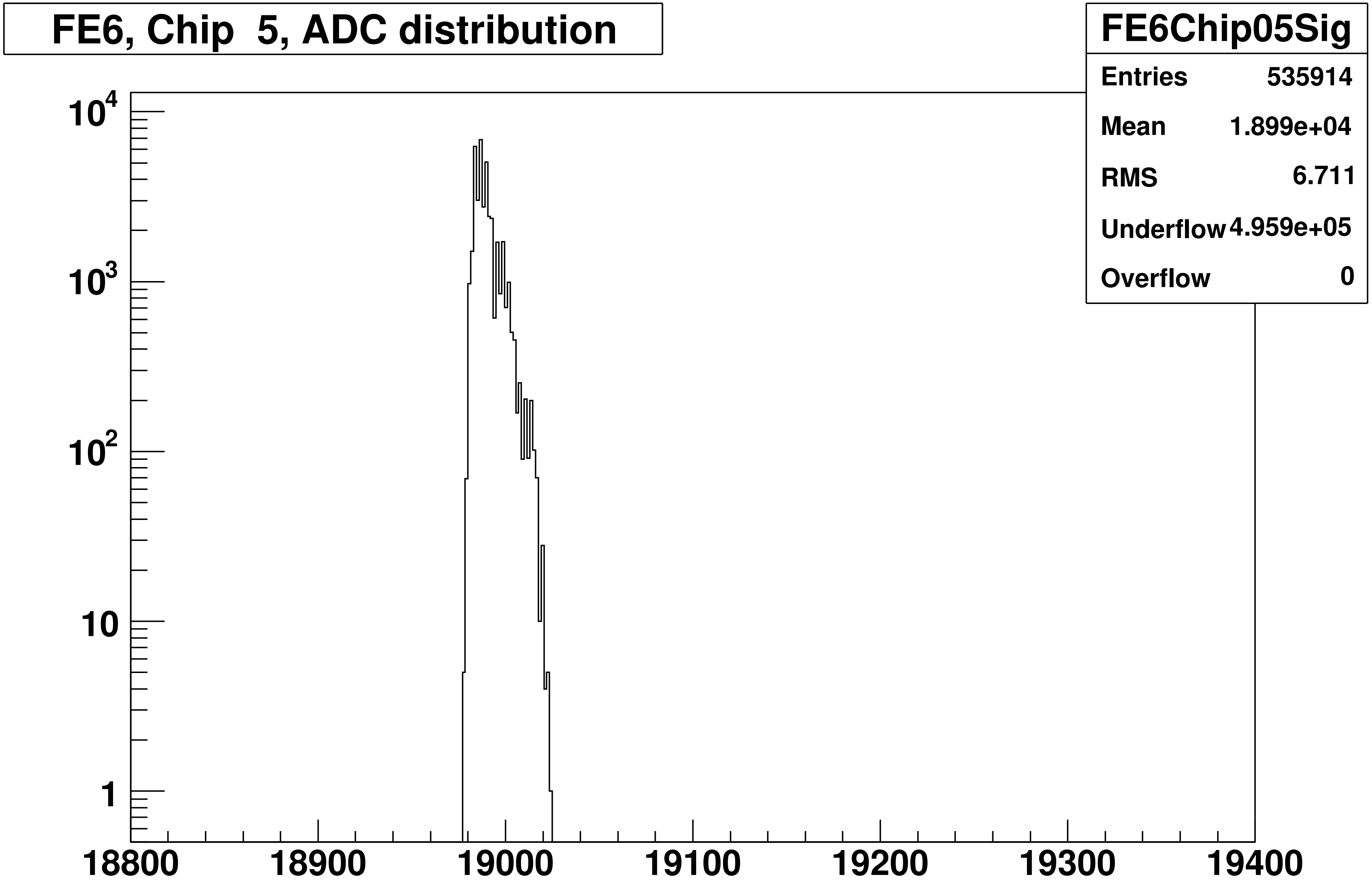
FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 535914
Mean 0.4567
RMS 1.254
Underflow 0
Overflow 4.003e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

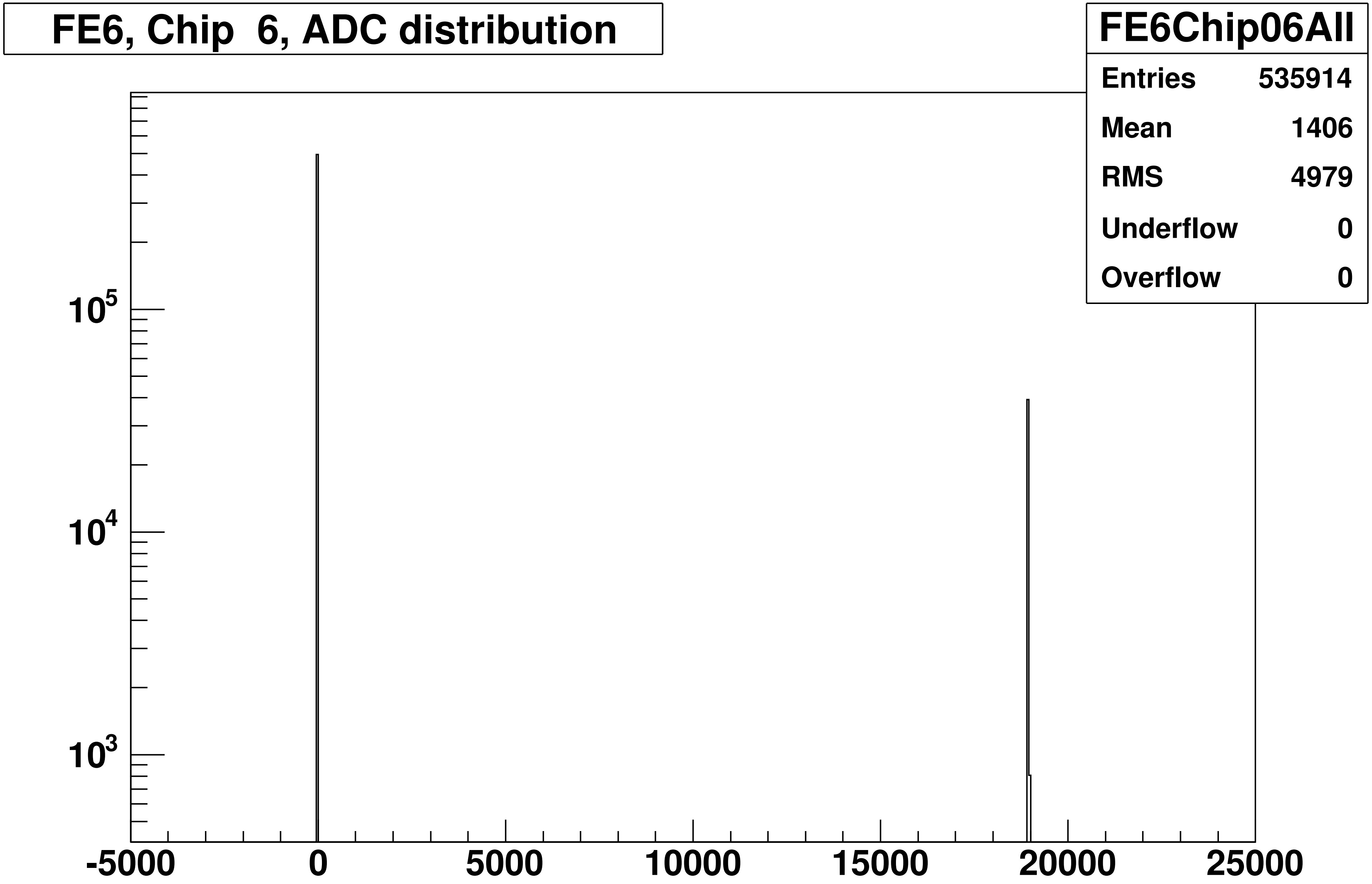

FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 535914
Mean 1.898e+04
RMS 6.758
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 535914
Mean 1425
RMS 4990
Underflow 0
Overflow 0
10^5
10^4
-5000
0
5000
10000
15000
20000
25000

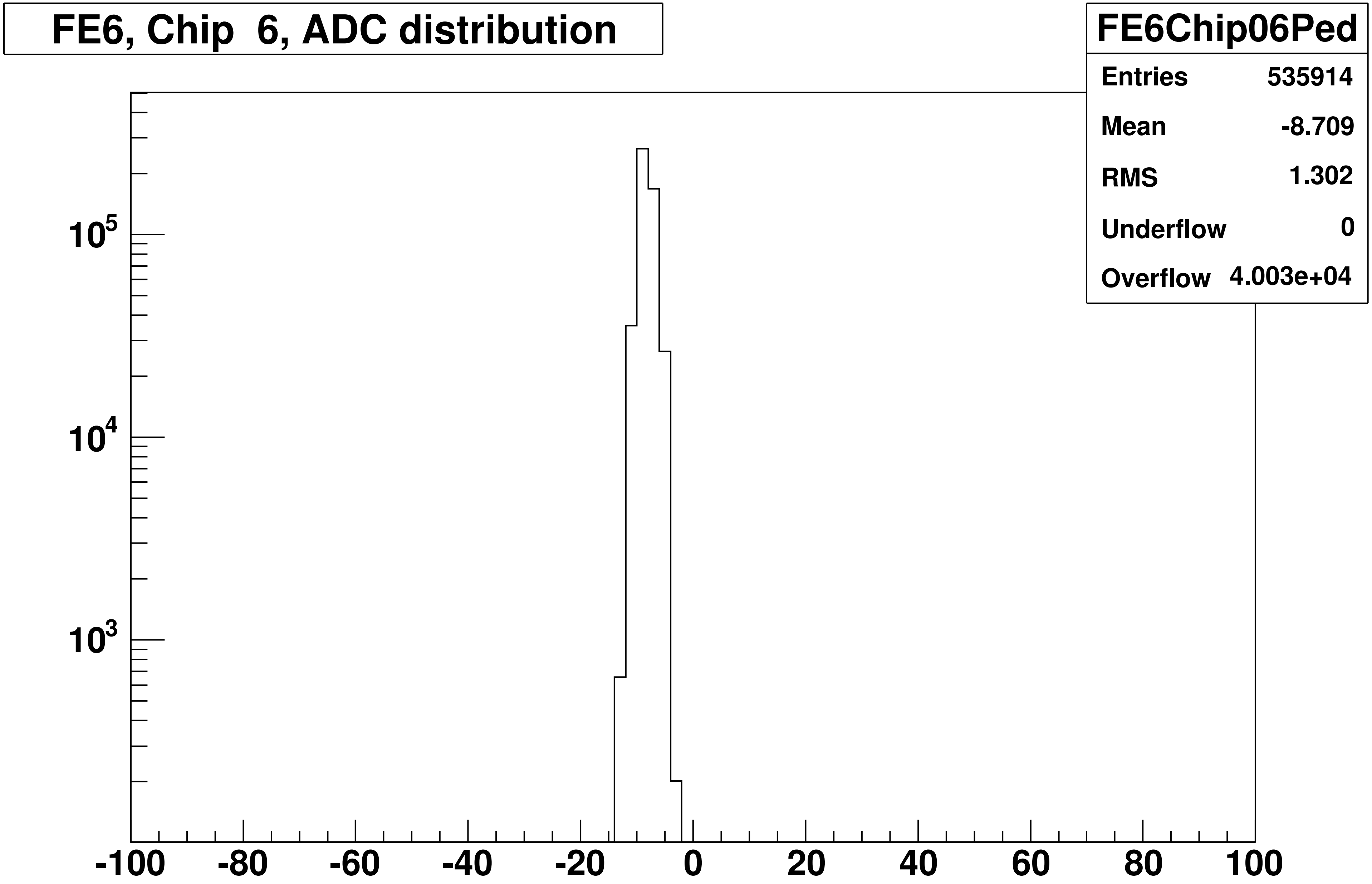


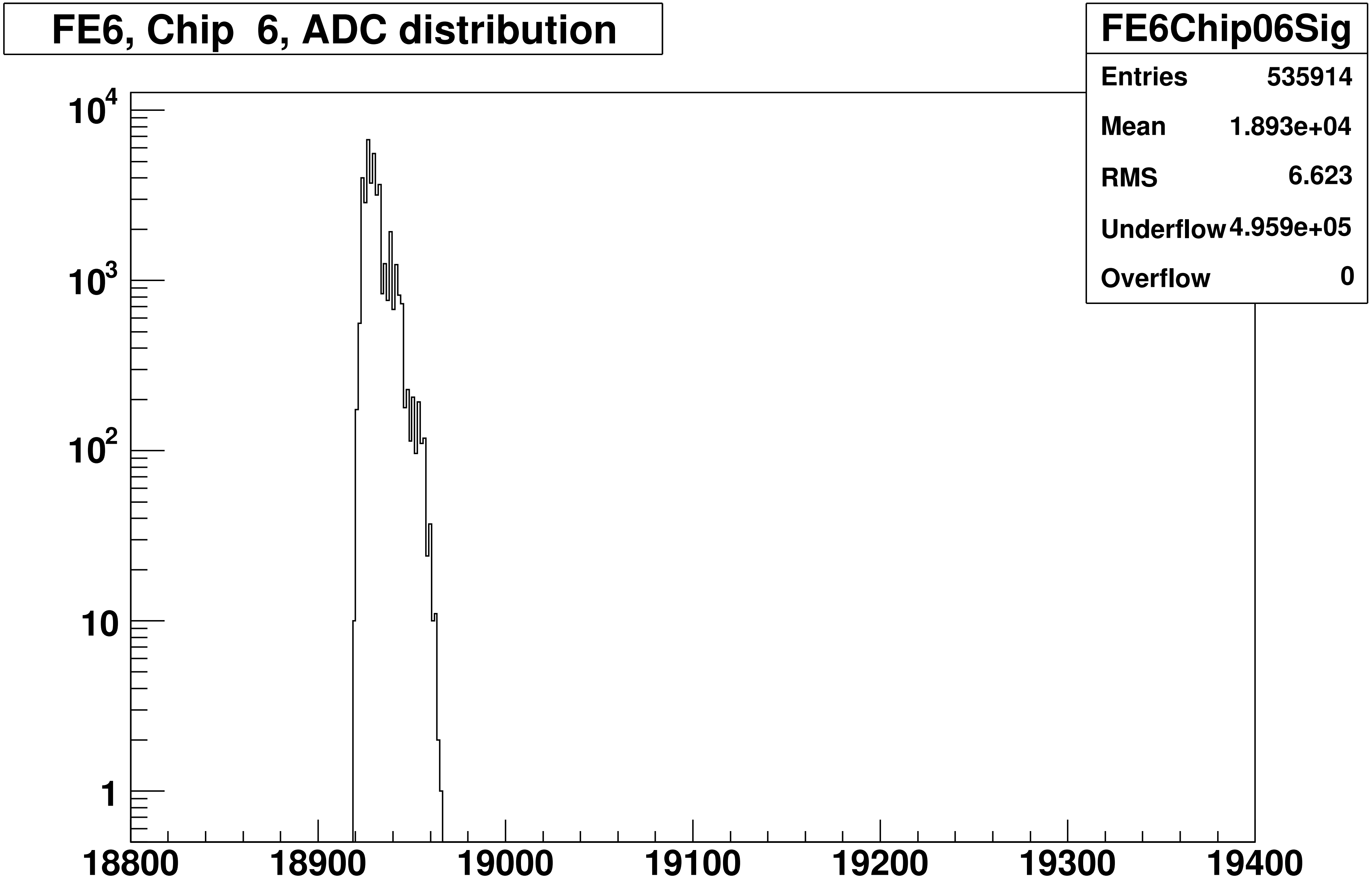
FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 535914
Mean 7.367
RMS 1.314
Underflow 0
Overflow 4.001e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 535914
Mean 1.899e+04
RMS 6.711
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

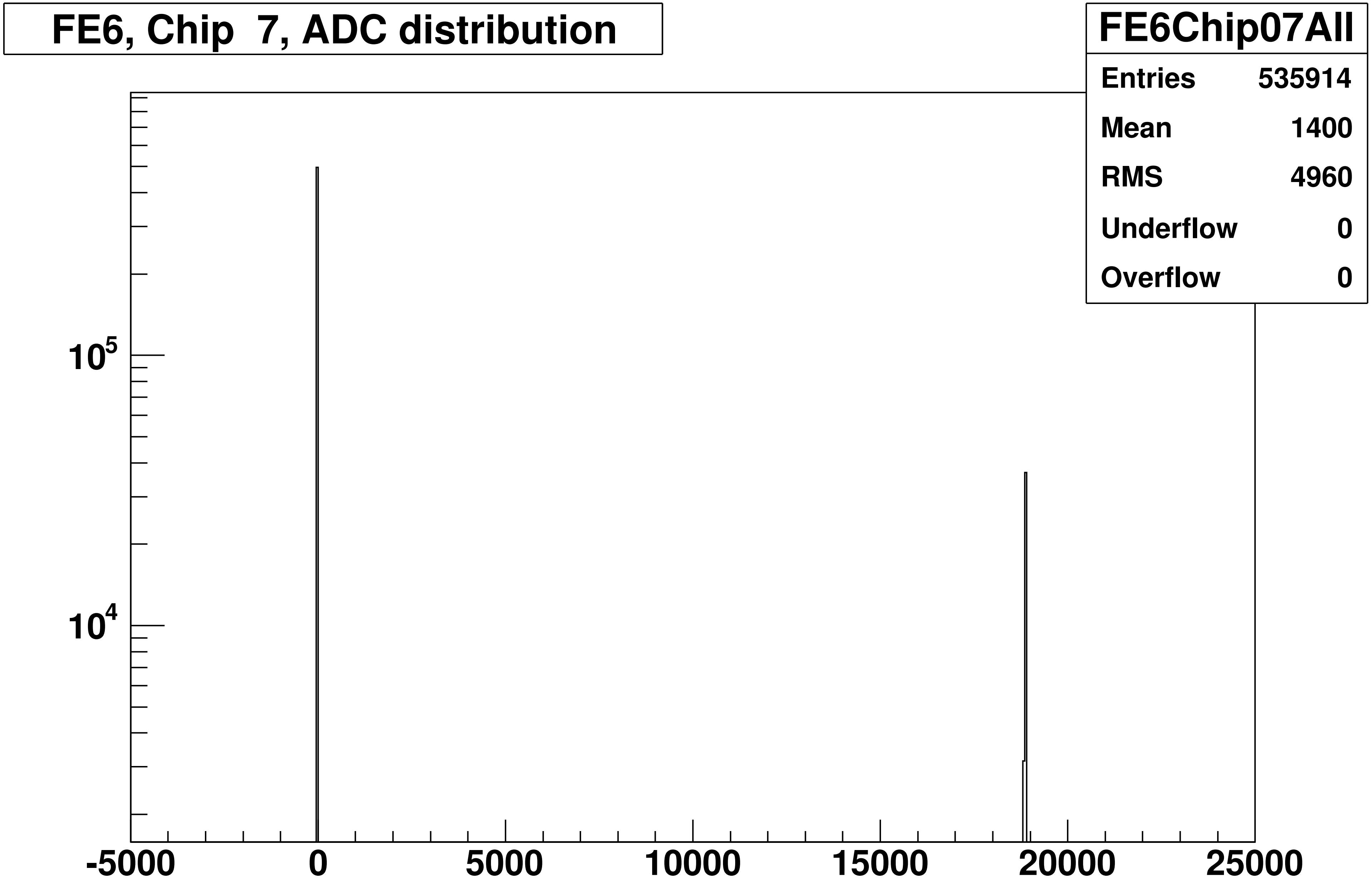


FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 535914
Mean 1406
RMS 4979
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

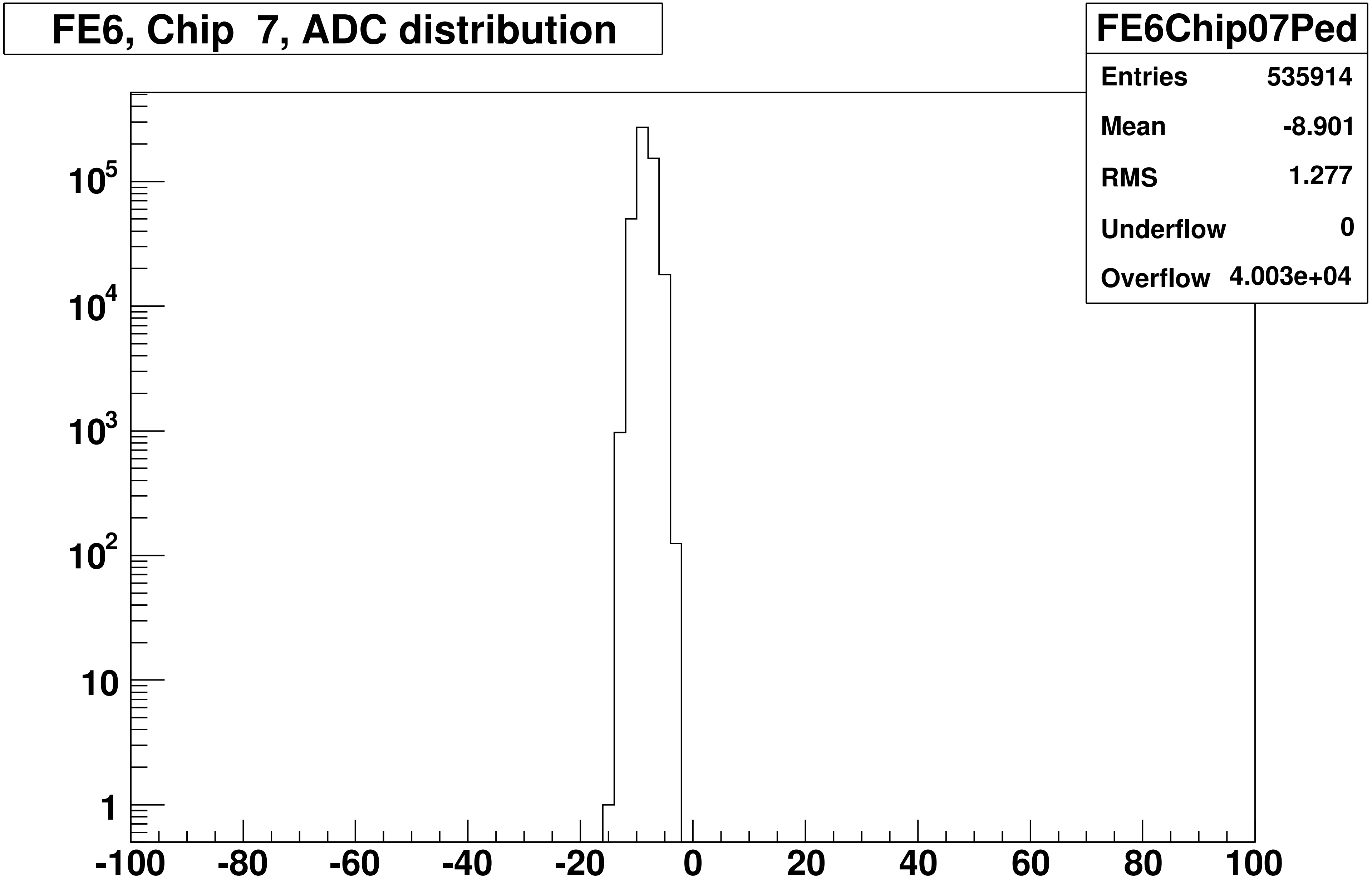

FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 535914
Mean -8.709
RMS 1.302
Underflow 0
Overflow 4.003e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100

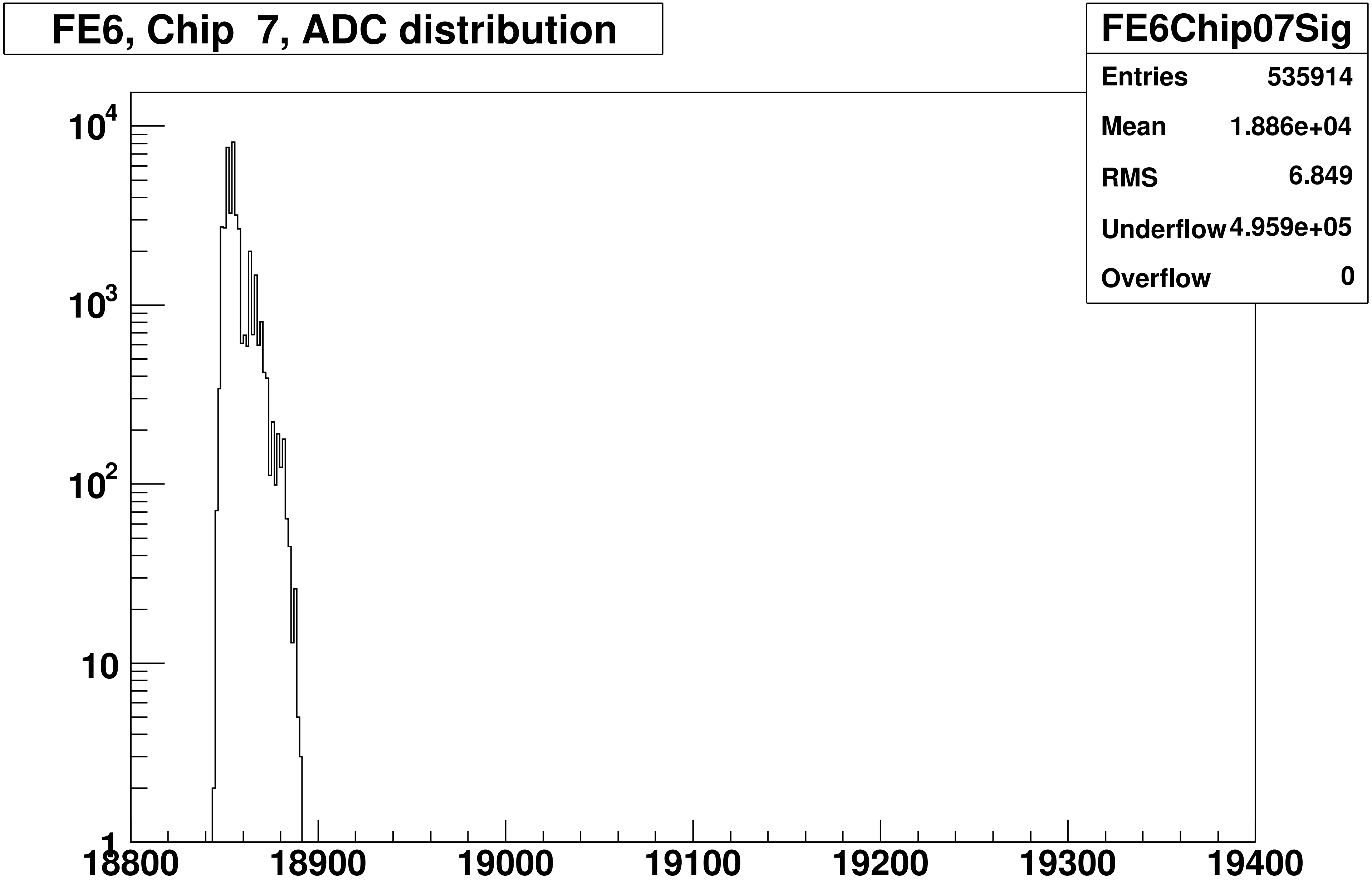


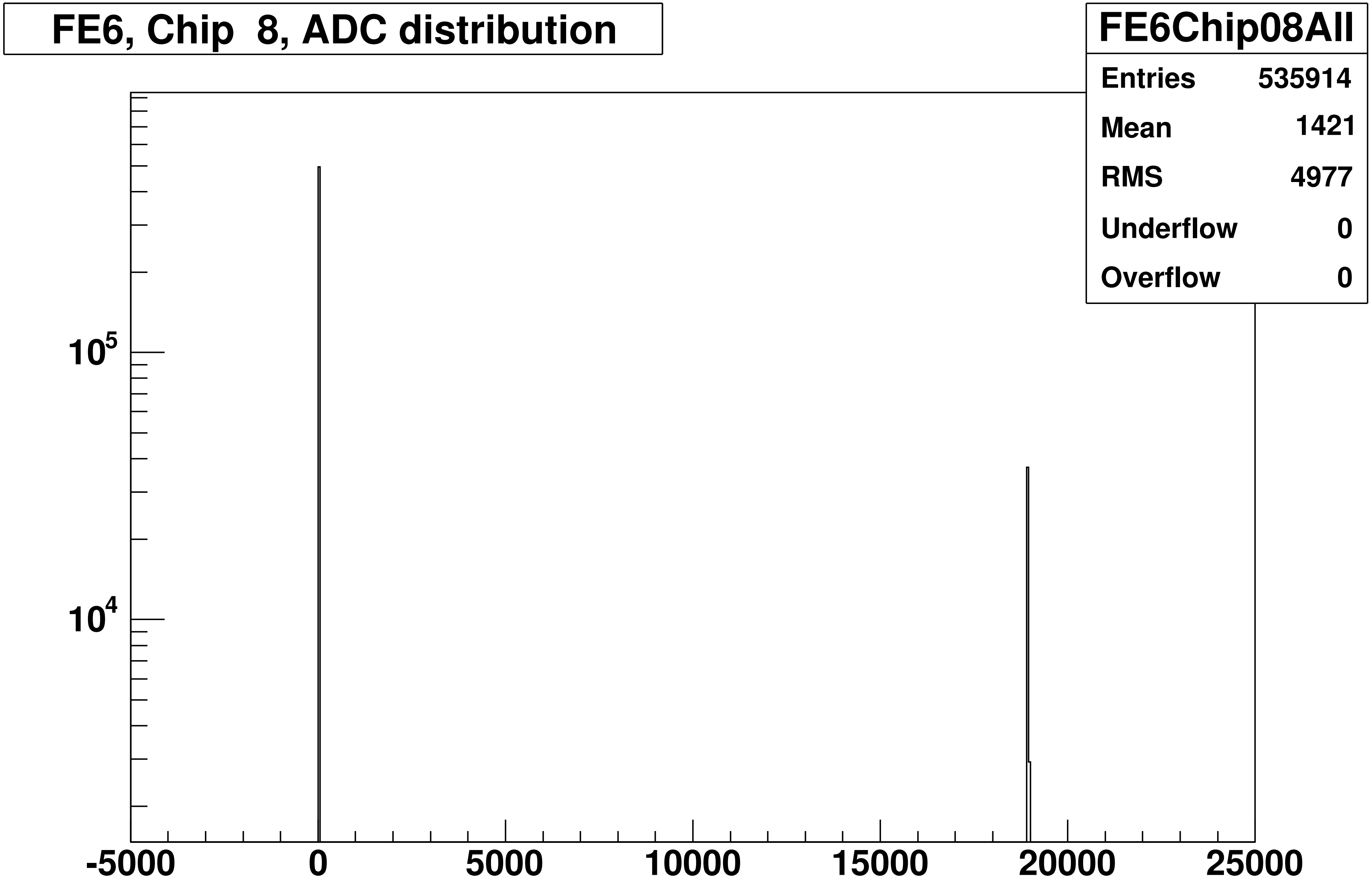
FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 535914
Mean 1.893e+04
RMS 6.623
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



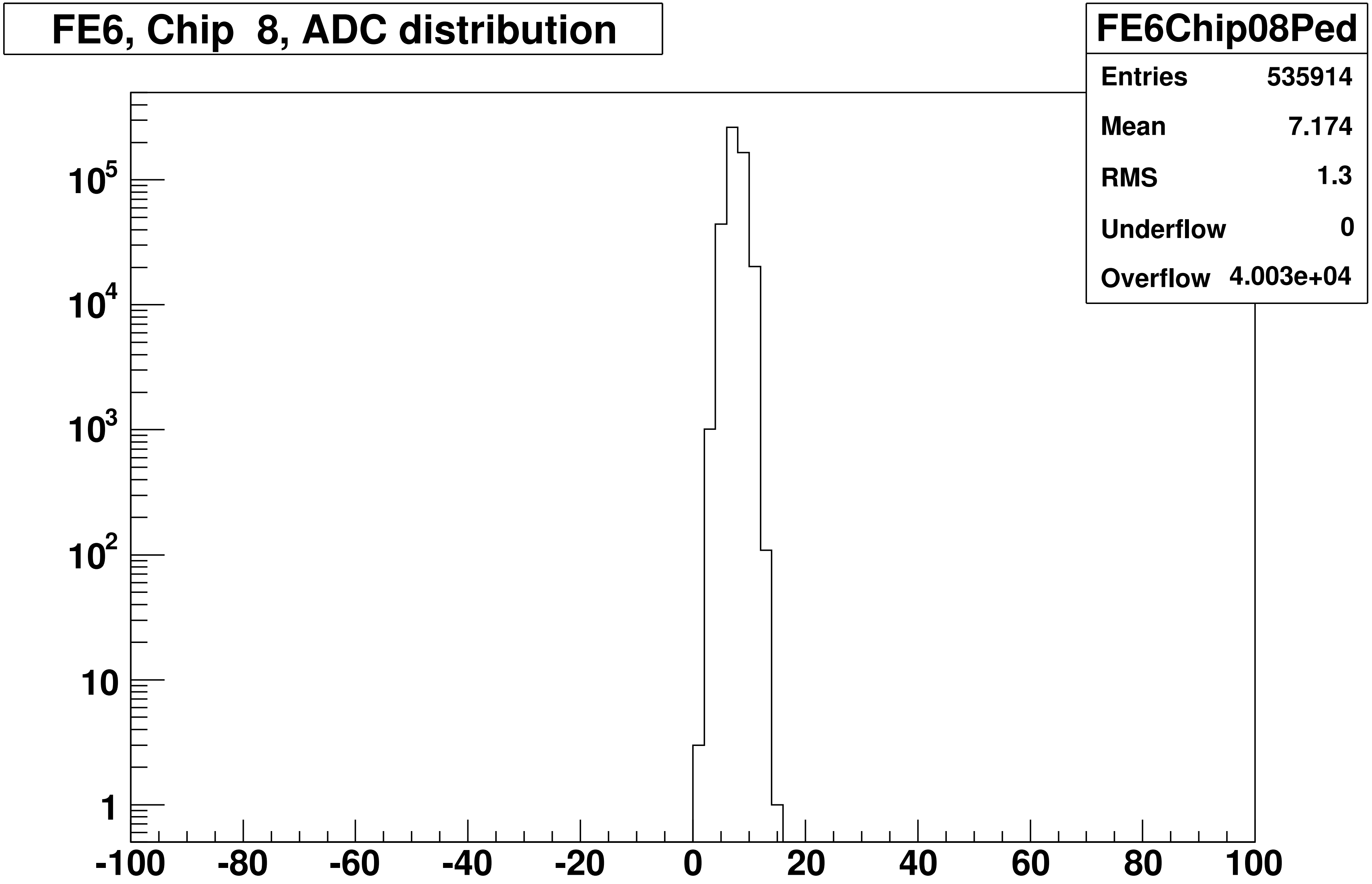
FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 535914
Mean 1400
RMS 4960
Underflow 0
Overflow 0
10^5
10^4
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 535914
Mean -8.901
RMS 1.277
Underflow 0
Overflow 4.003e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

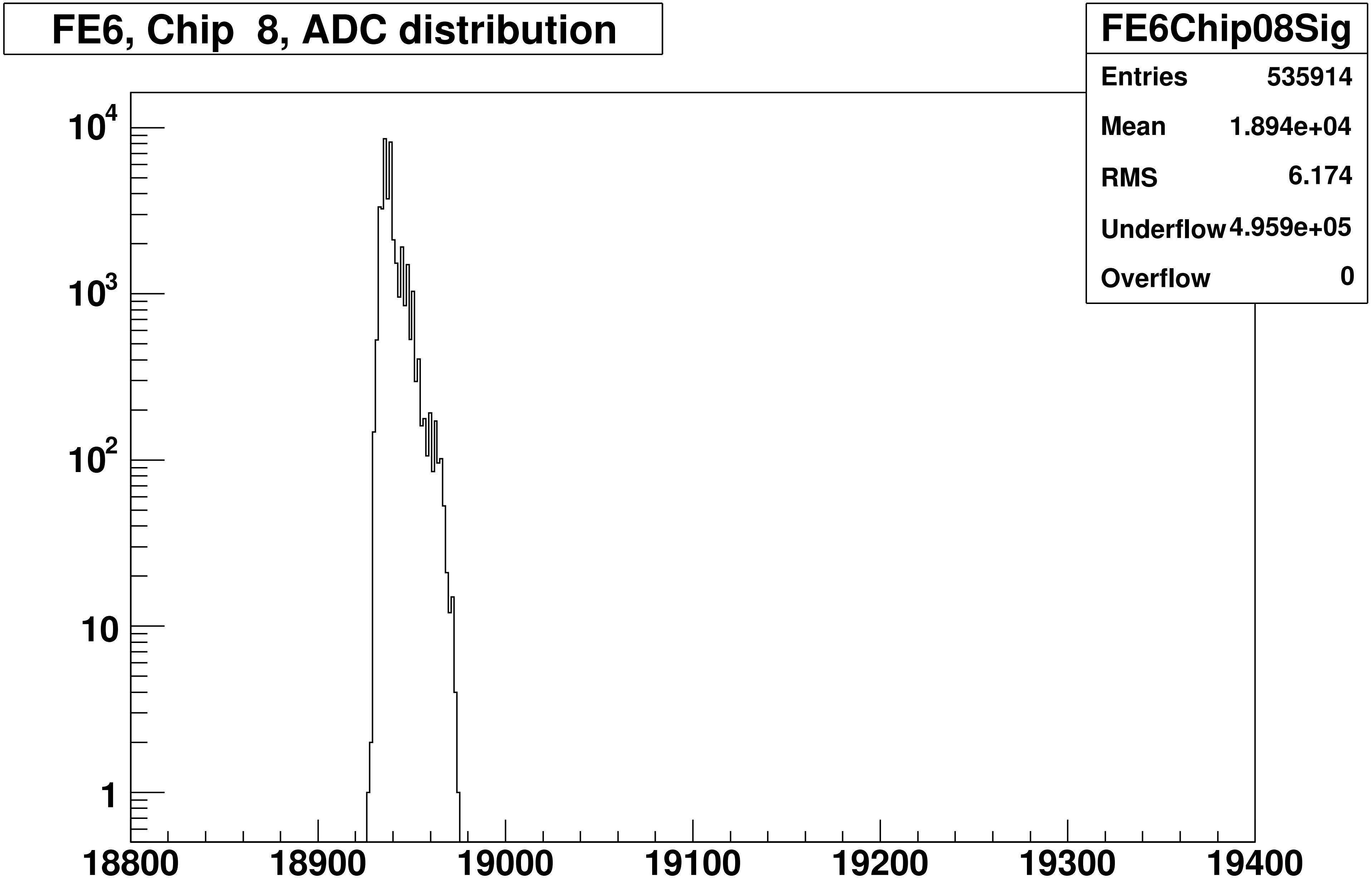

FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 535914
Mean 1.886e+04
RMS 6.849
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

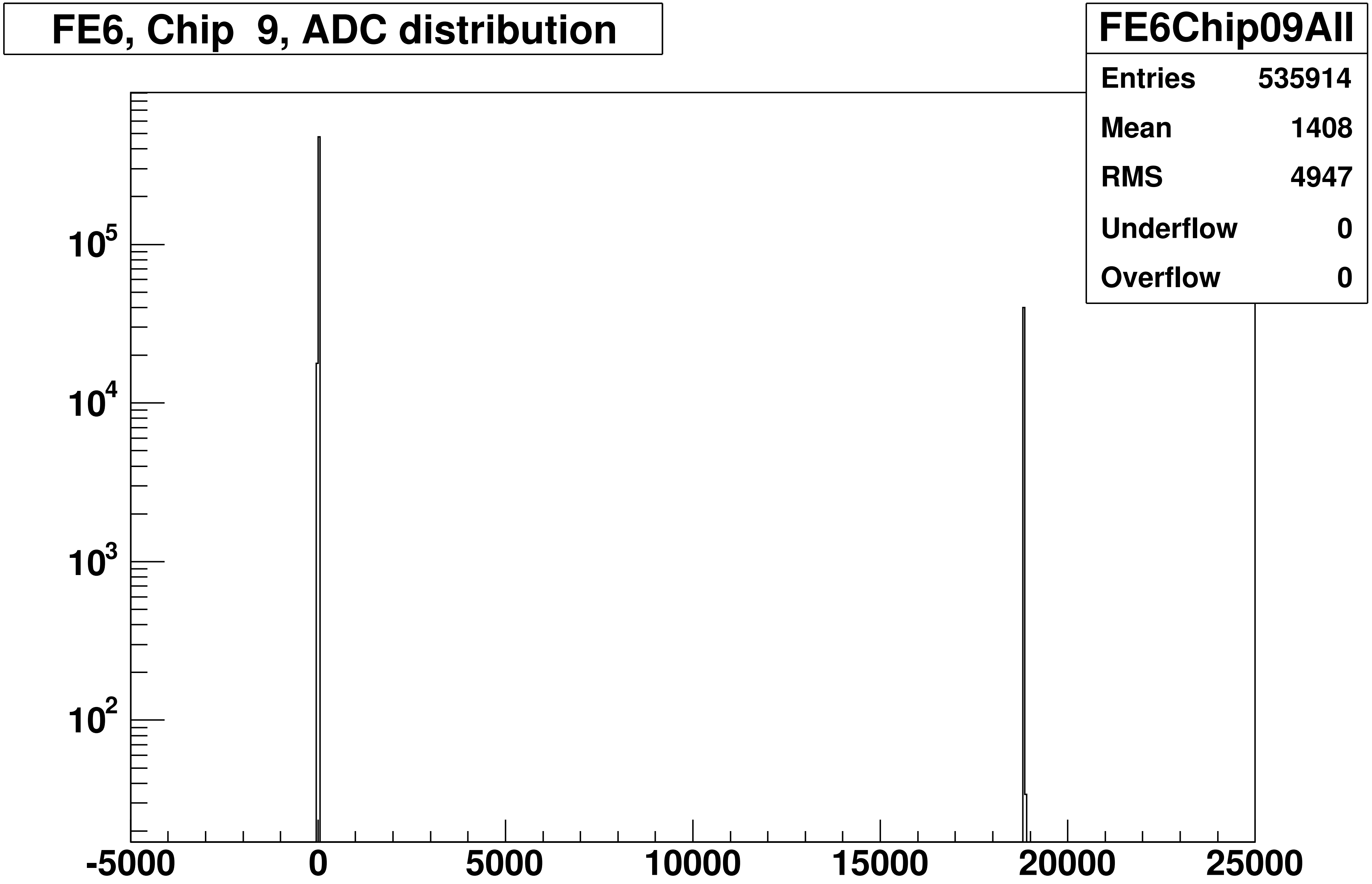


FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 535914
Mean 1421
RMS 4977
Underflow 0
Overflow 0
10^5
10^4
-5000
0
5000
10000
15000
20000
25000

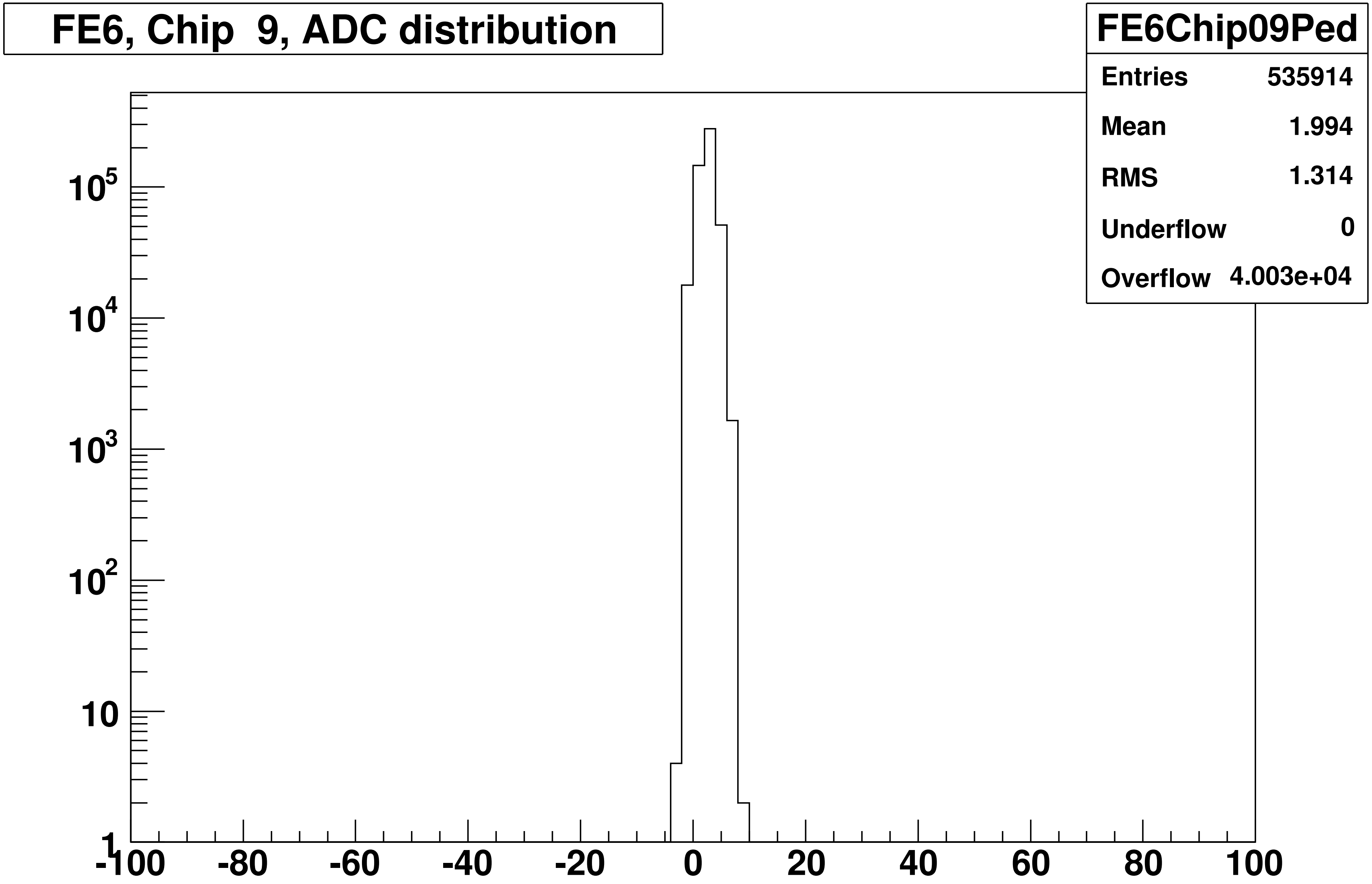


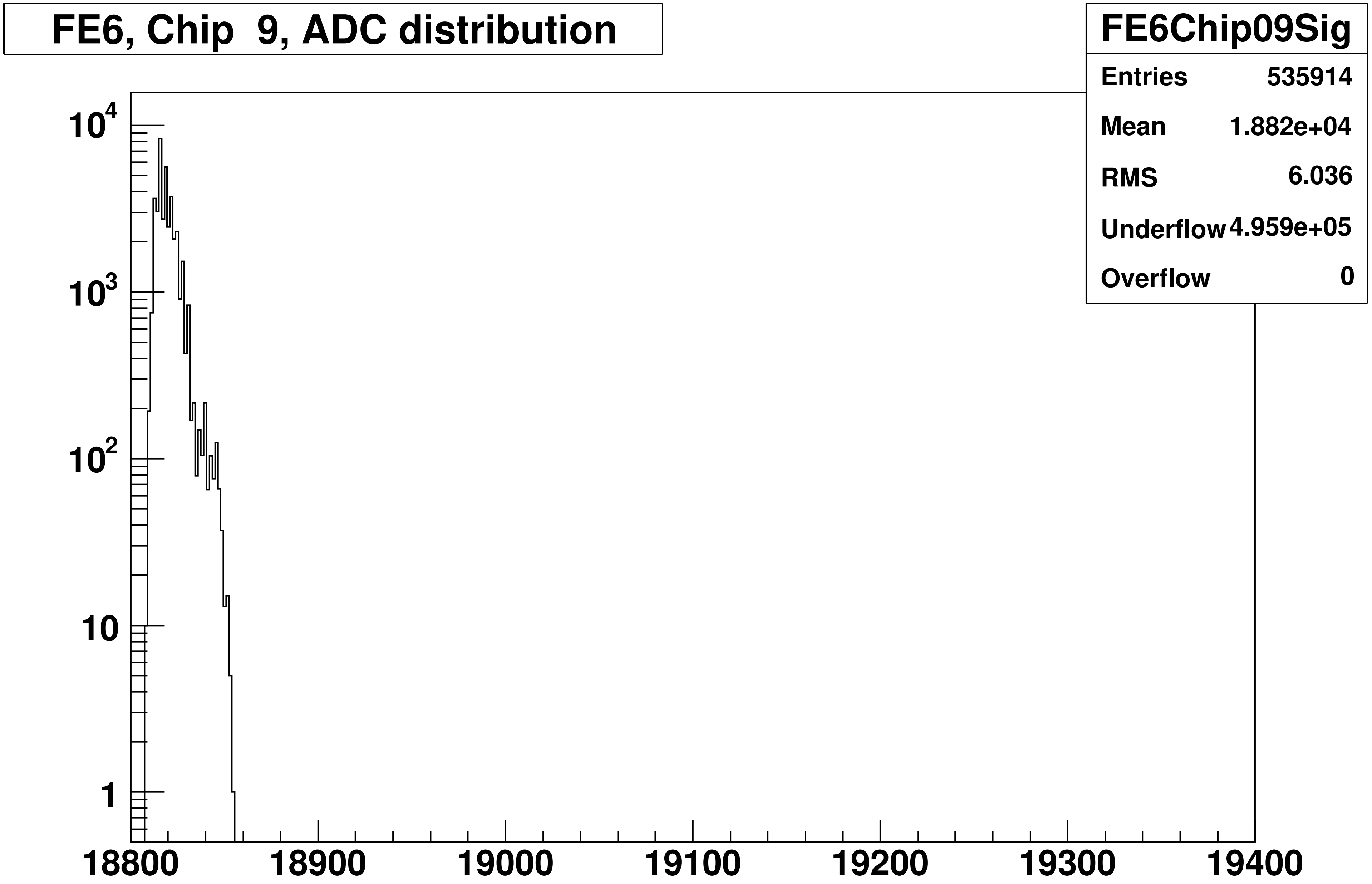
FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 535914
Mean 7.174
RMS 1.3
Underflow 0
Overflow 4.003e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 535914
Mean 1.894e+04
RMS 6.174
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

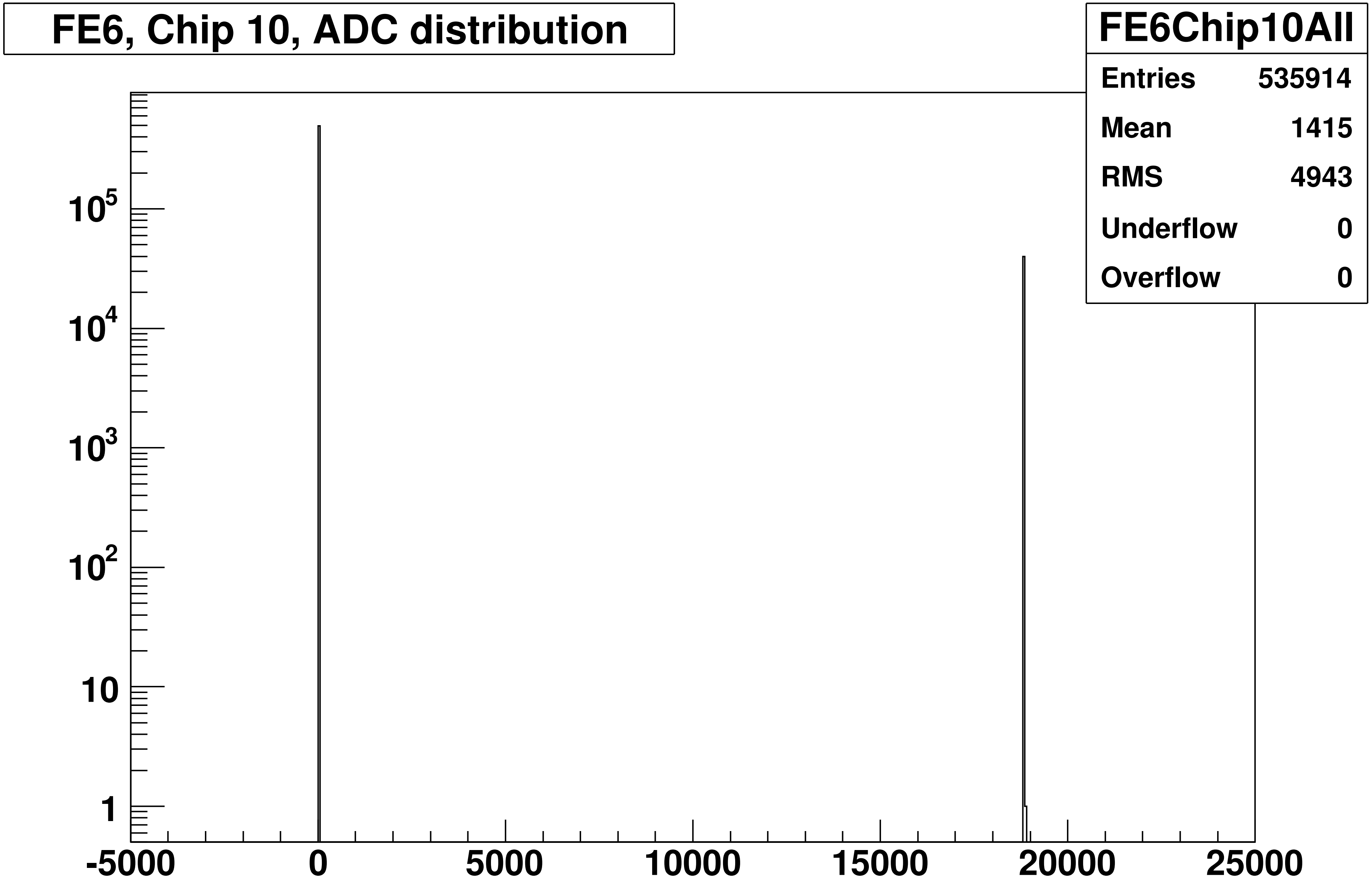


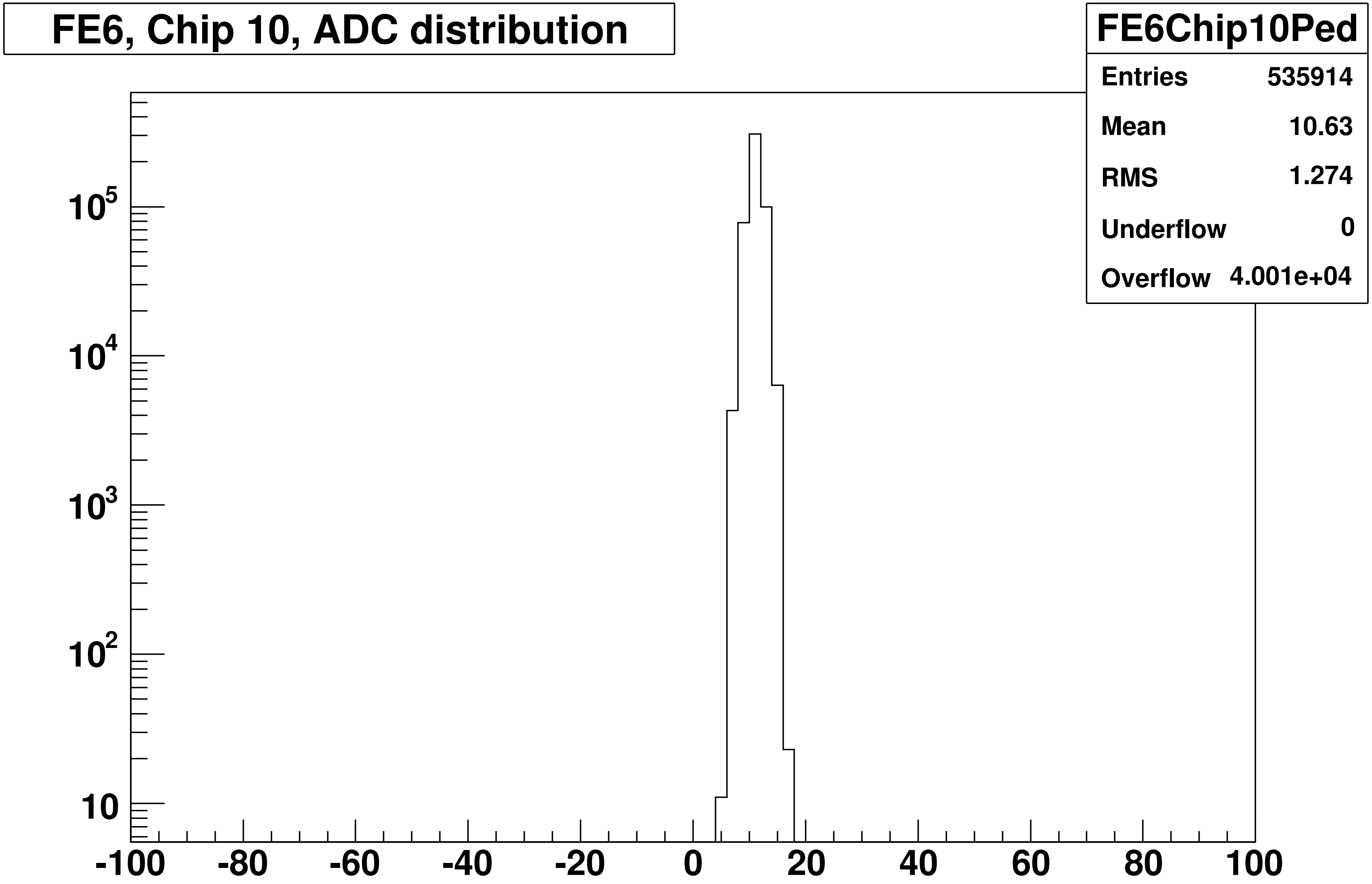
FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 535914
Mean 1408
RMS 4947
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

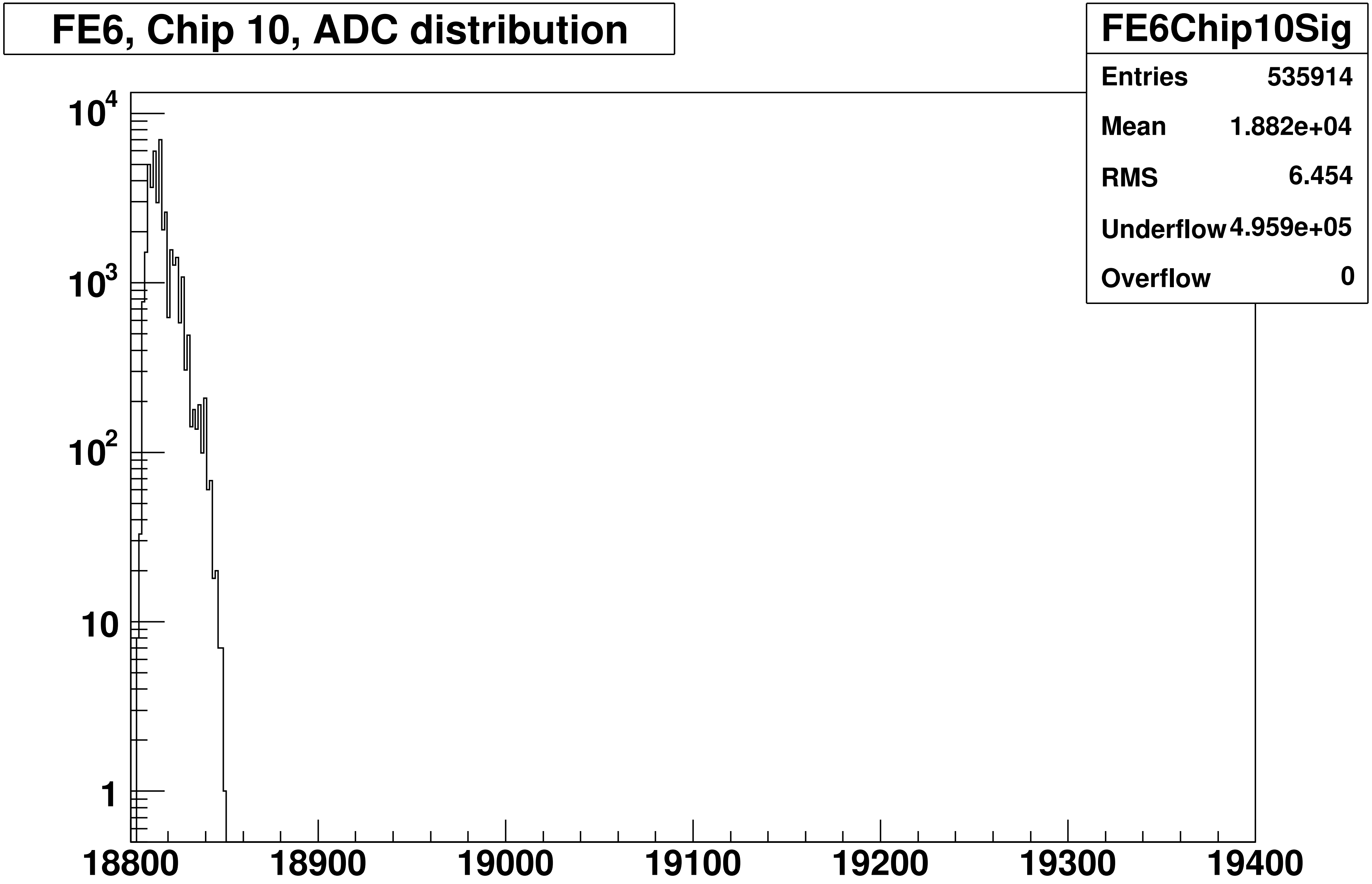

FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 535914
Mean 1.994
RMS 1.314
Underflow 0
Overflow 4.003e+04
1
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



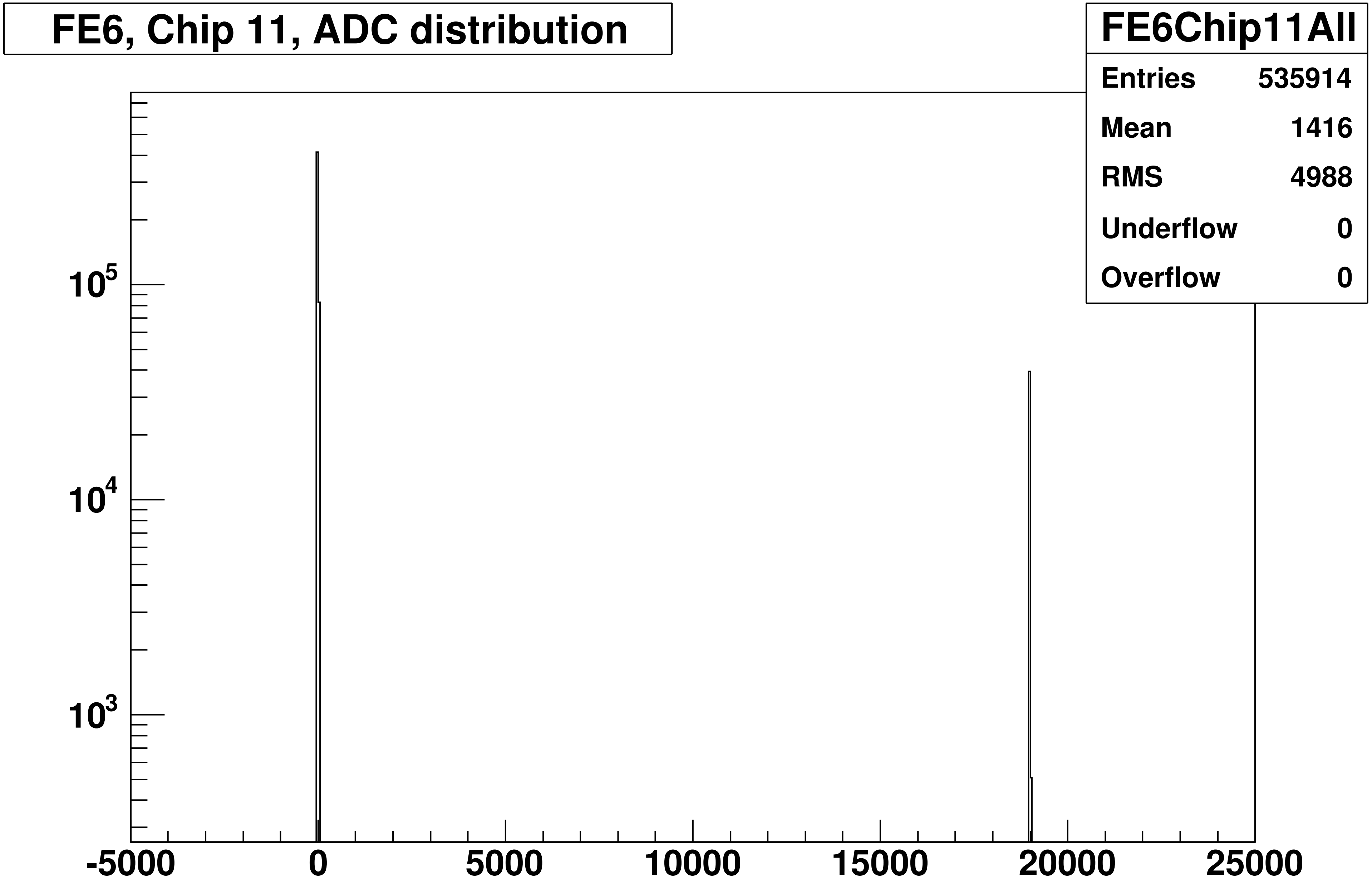
FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 535914
Mean 1.882e+04
RMS 6.036
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

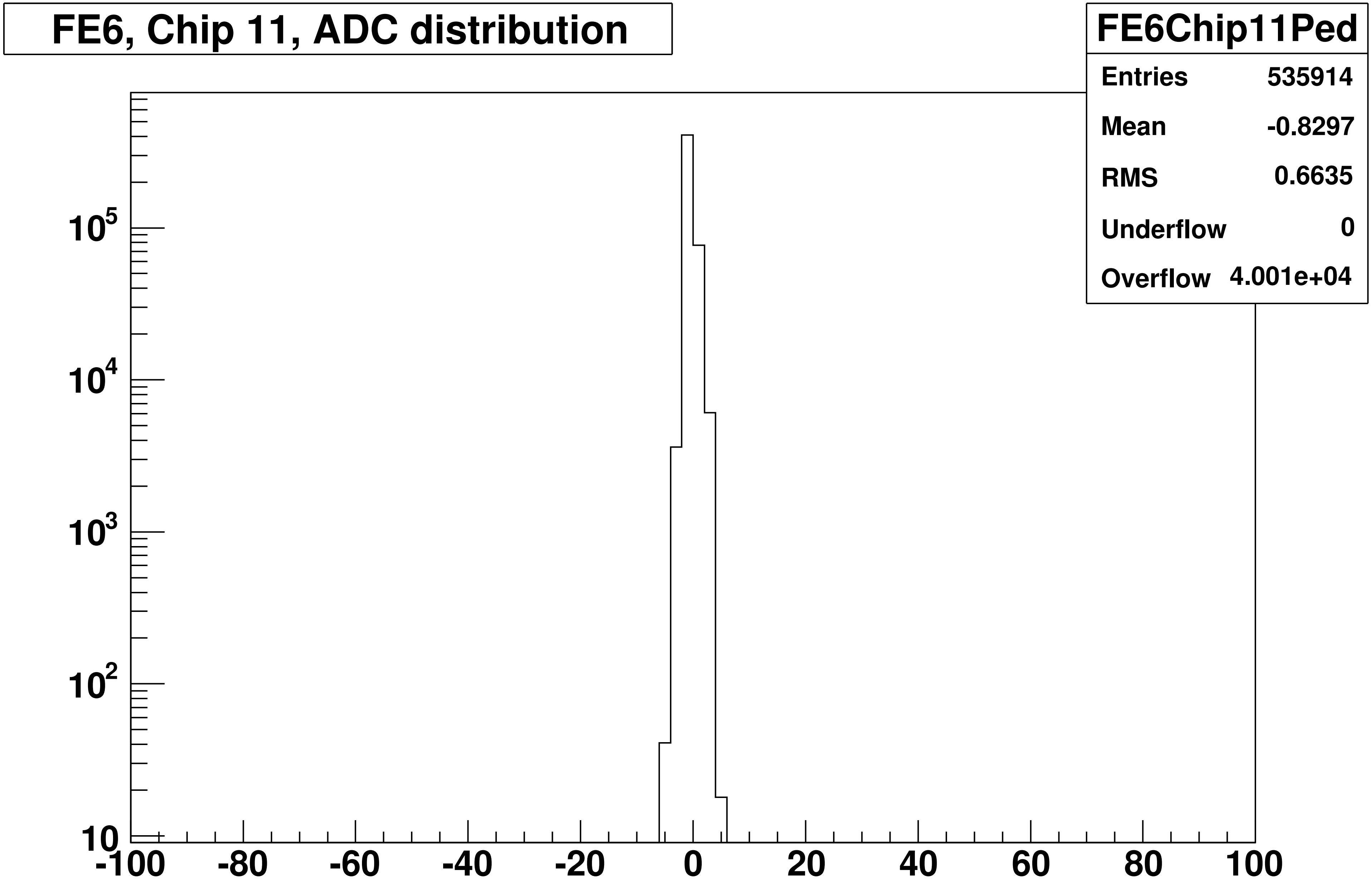

FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 535914
Mean 1415
RMS 4943
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

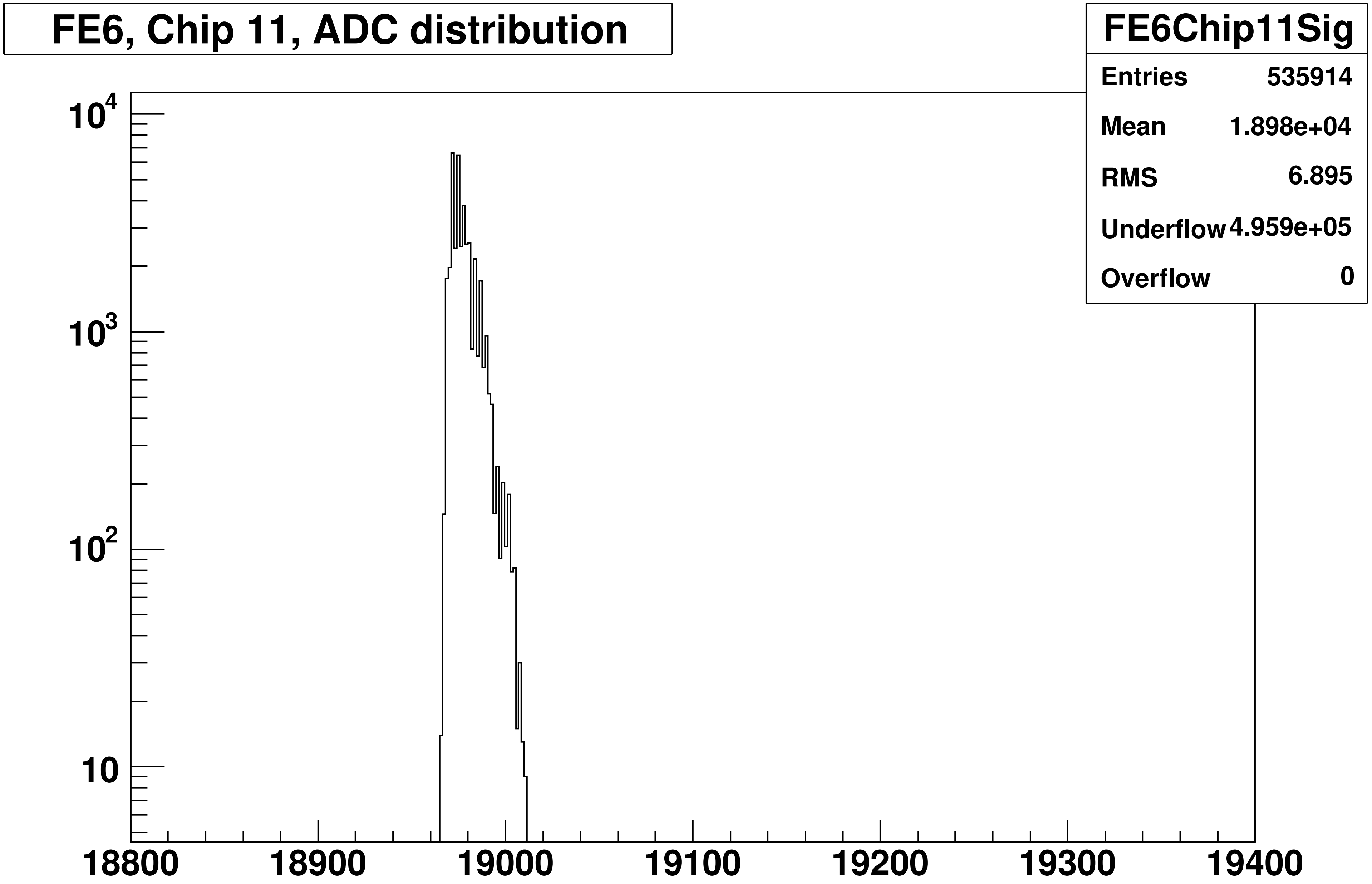

FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 535914
Mean 10.63
RMS 1.274
Underflow 0
Overflow 4.001e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

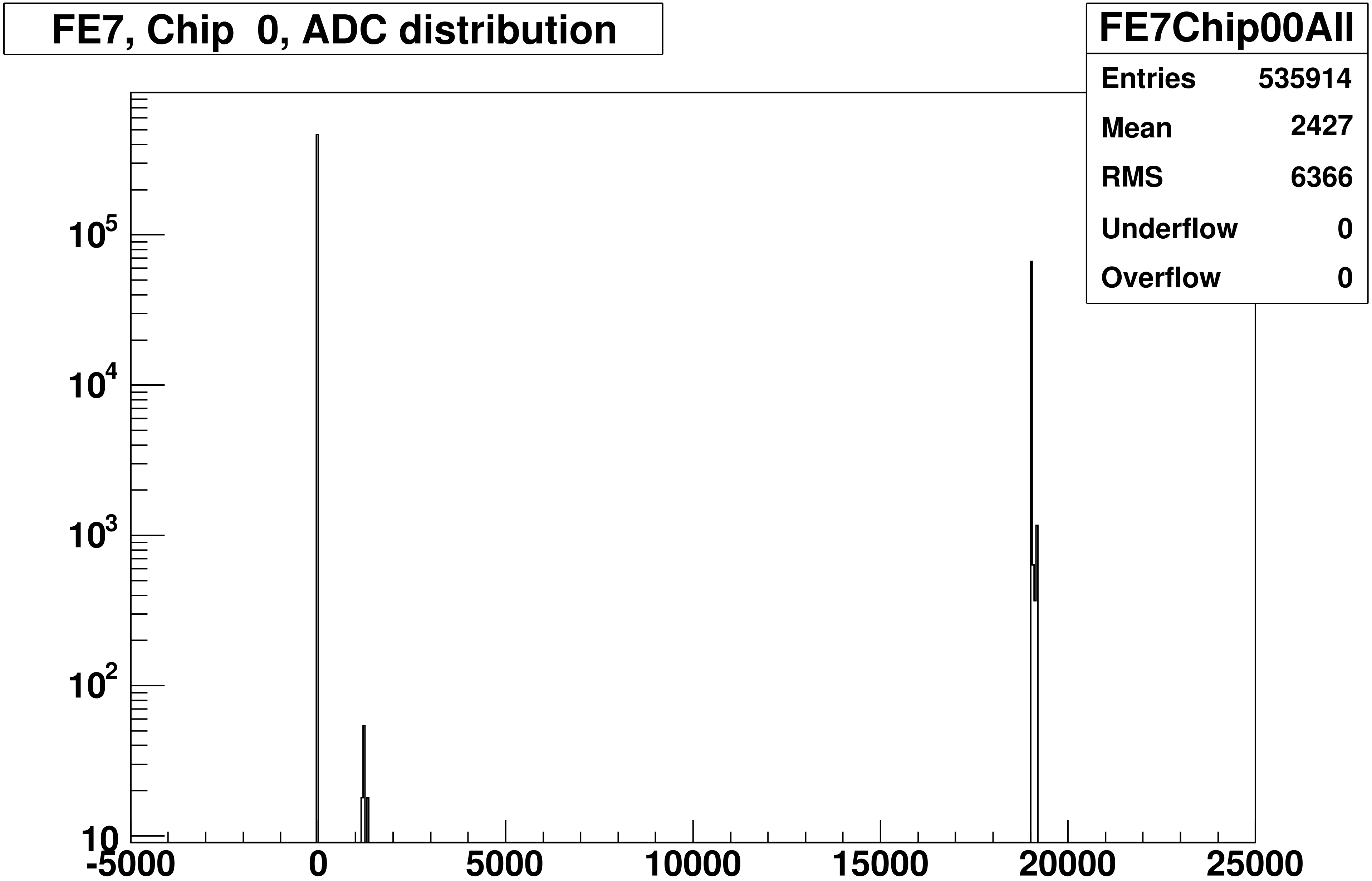


FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 535914
Mean 1.882e+04
RMS 6.454
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

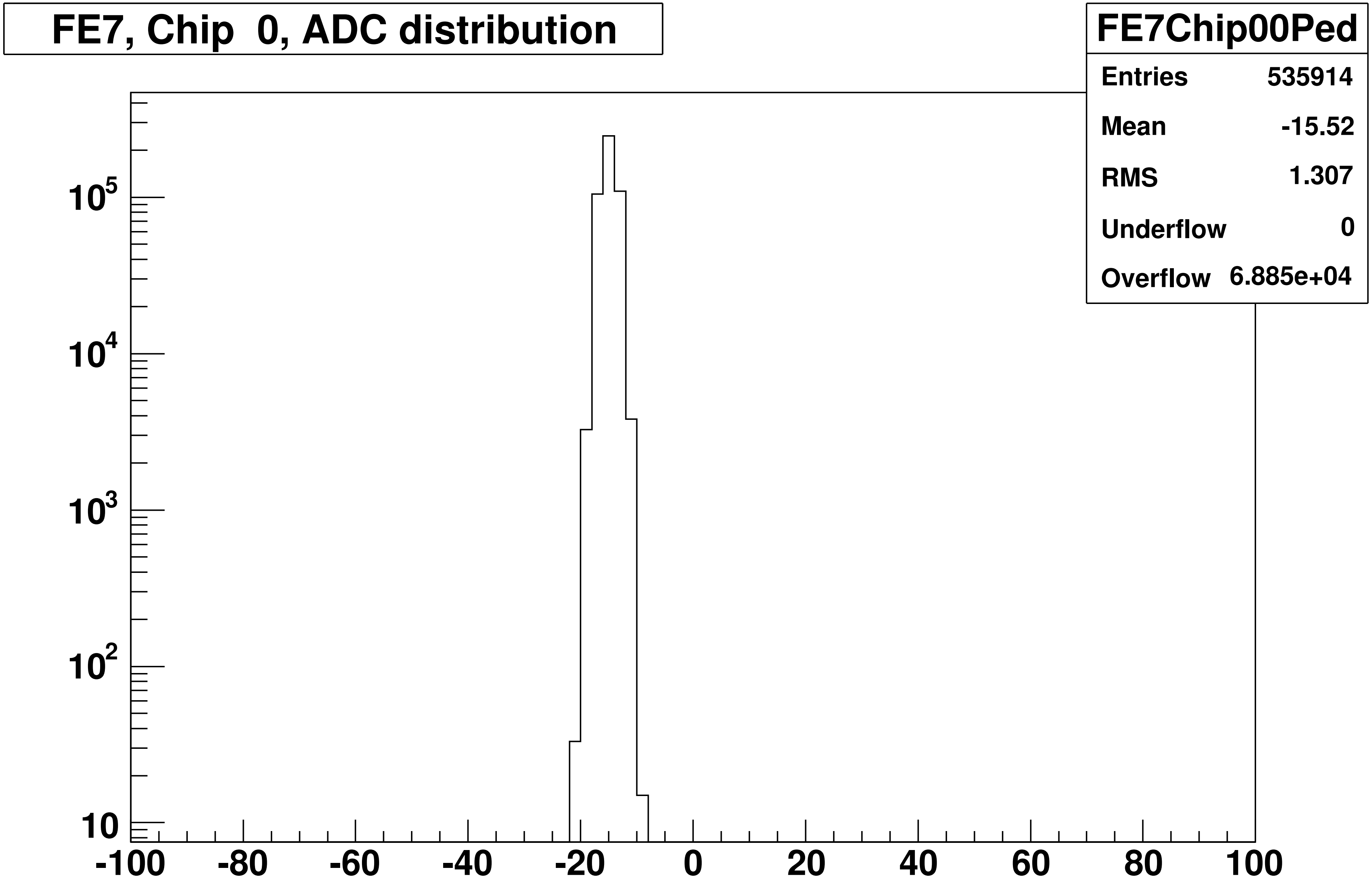

FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 535914
Mean 1416
RMS 4988
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 535914
Mean -0.8297
RMS 0.6635
Underflow 0
Overflow 4.001e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

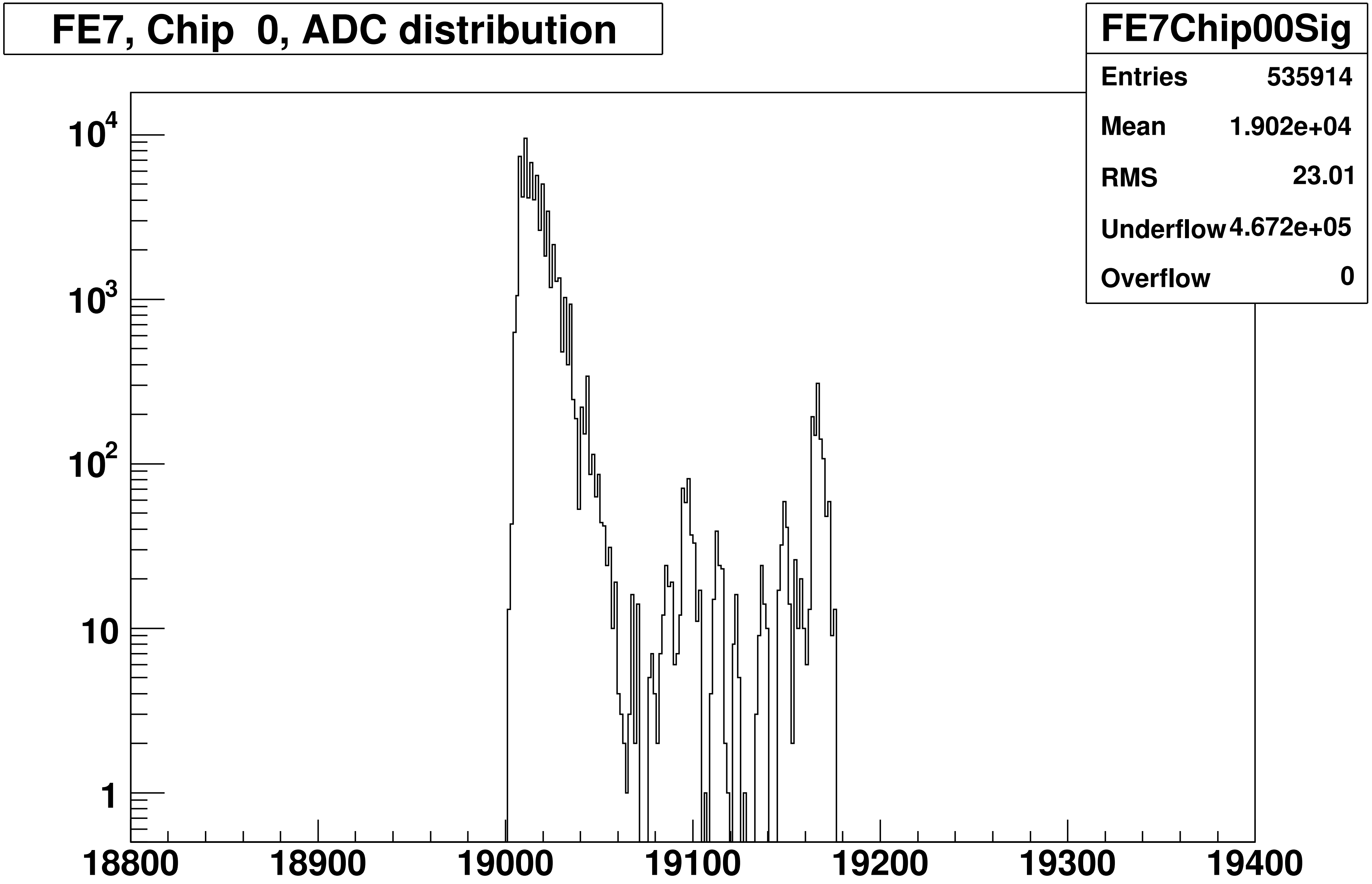

FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 535914
Mean 1.898e+04
RMS 6.895
Underflow 4.959e+05
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400



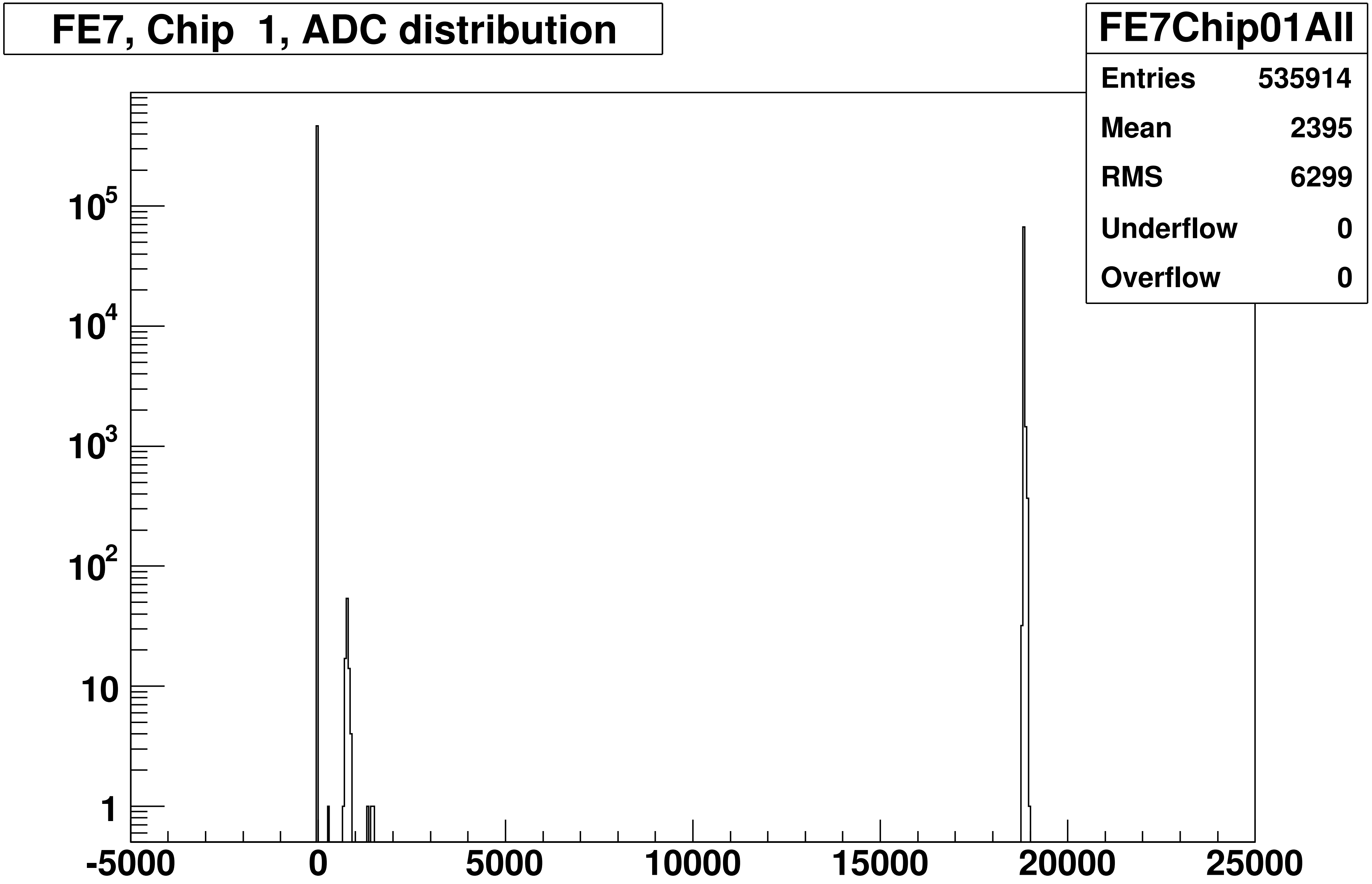
FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 535914
Mean 2427
RMS 6366
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



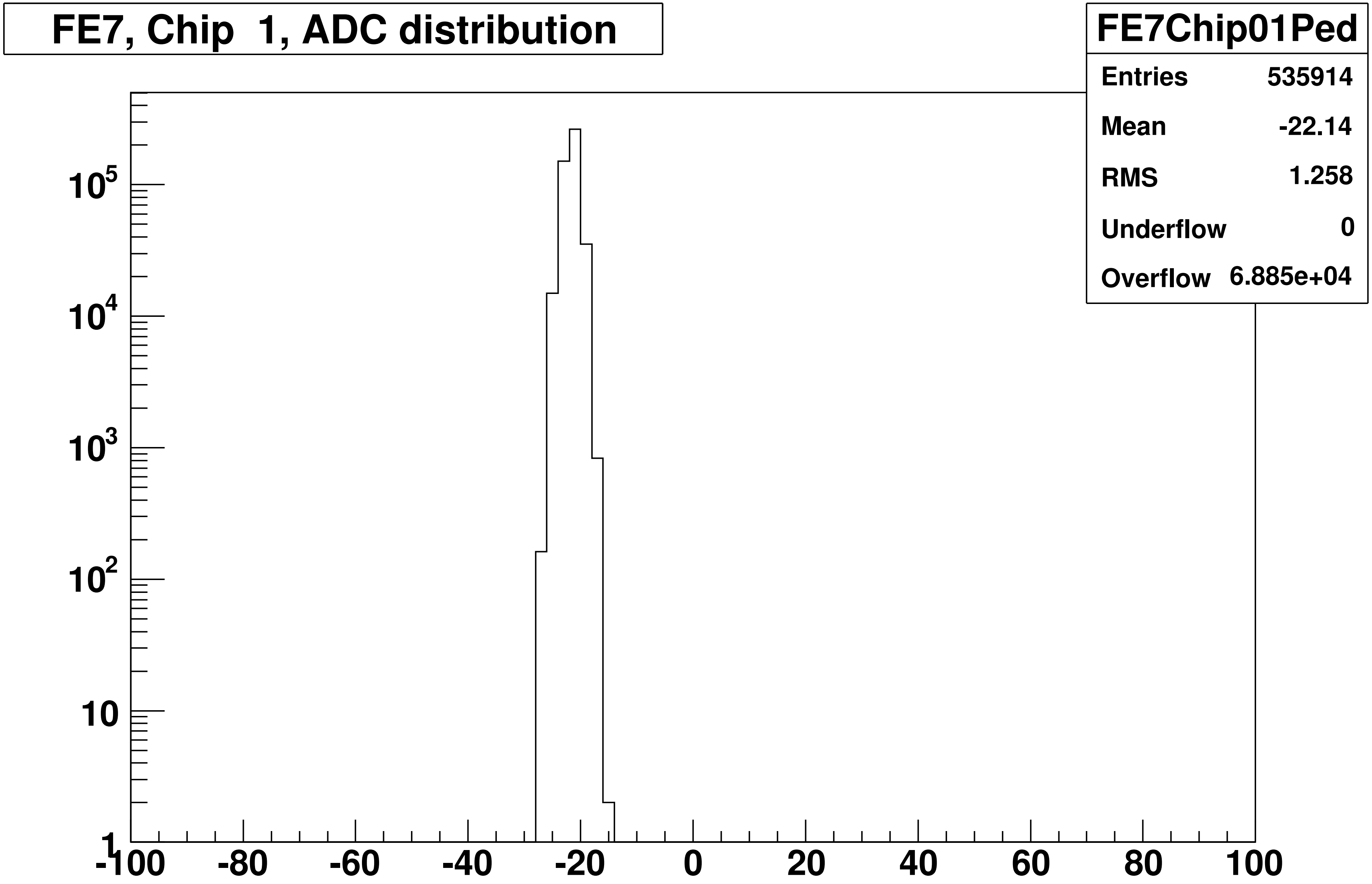
FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 535914
Mean -15.52
RMS 1.307
Underflow 0
Overflow 6.885e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

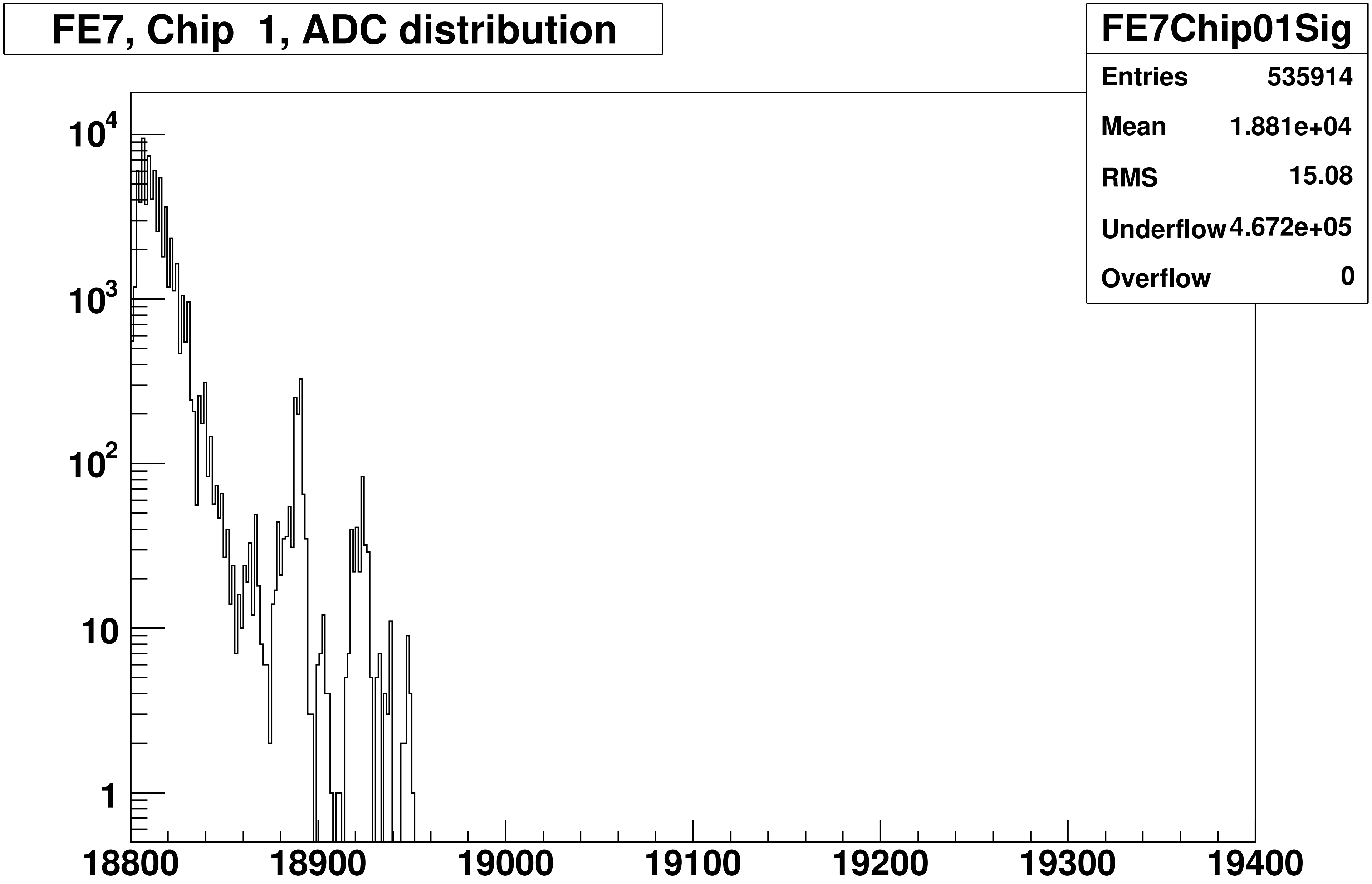


FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 535914
Mean 1.902e+04
RMS 23.01
Underflow 4.672e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

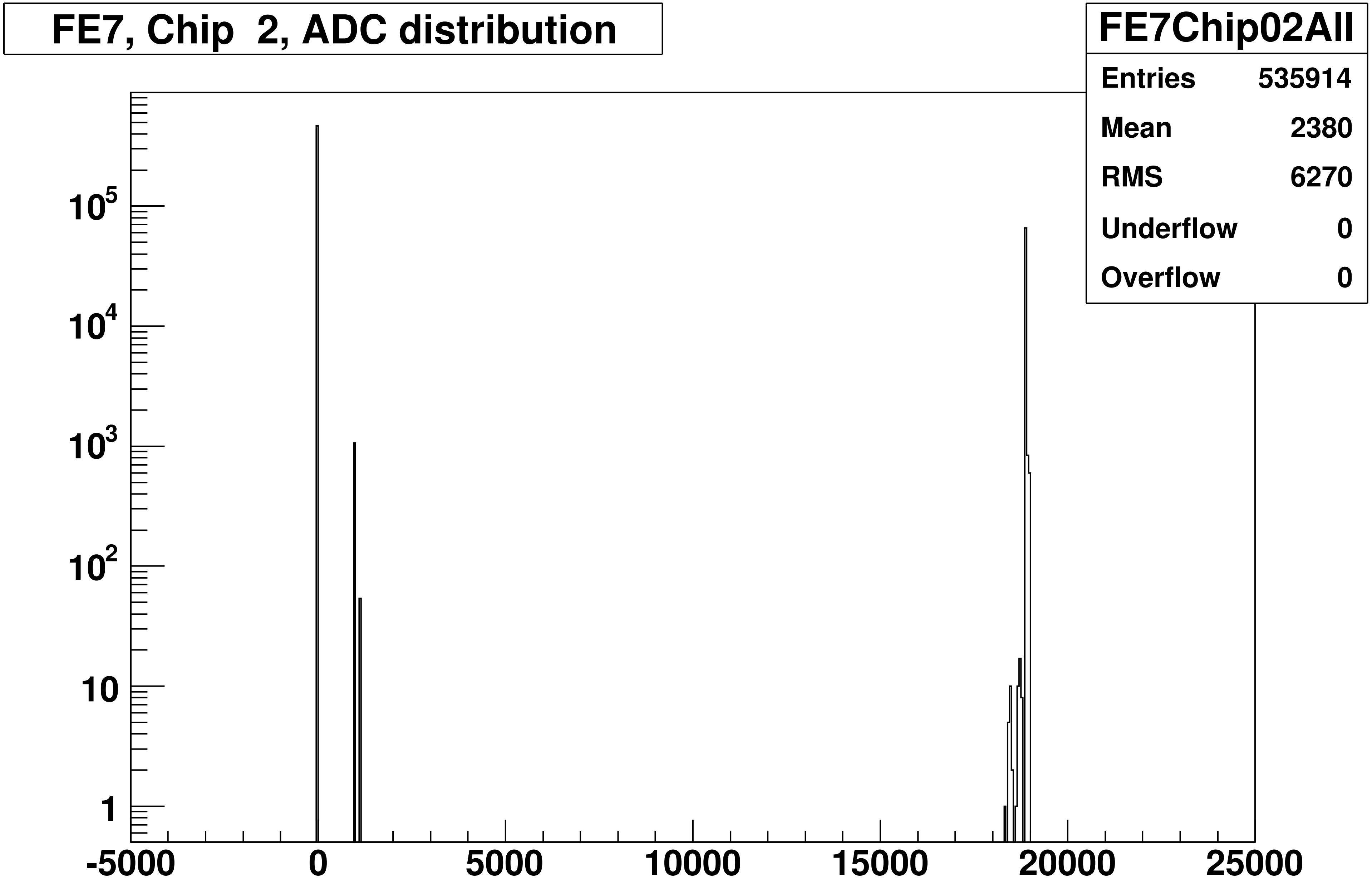


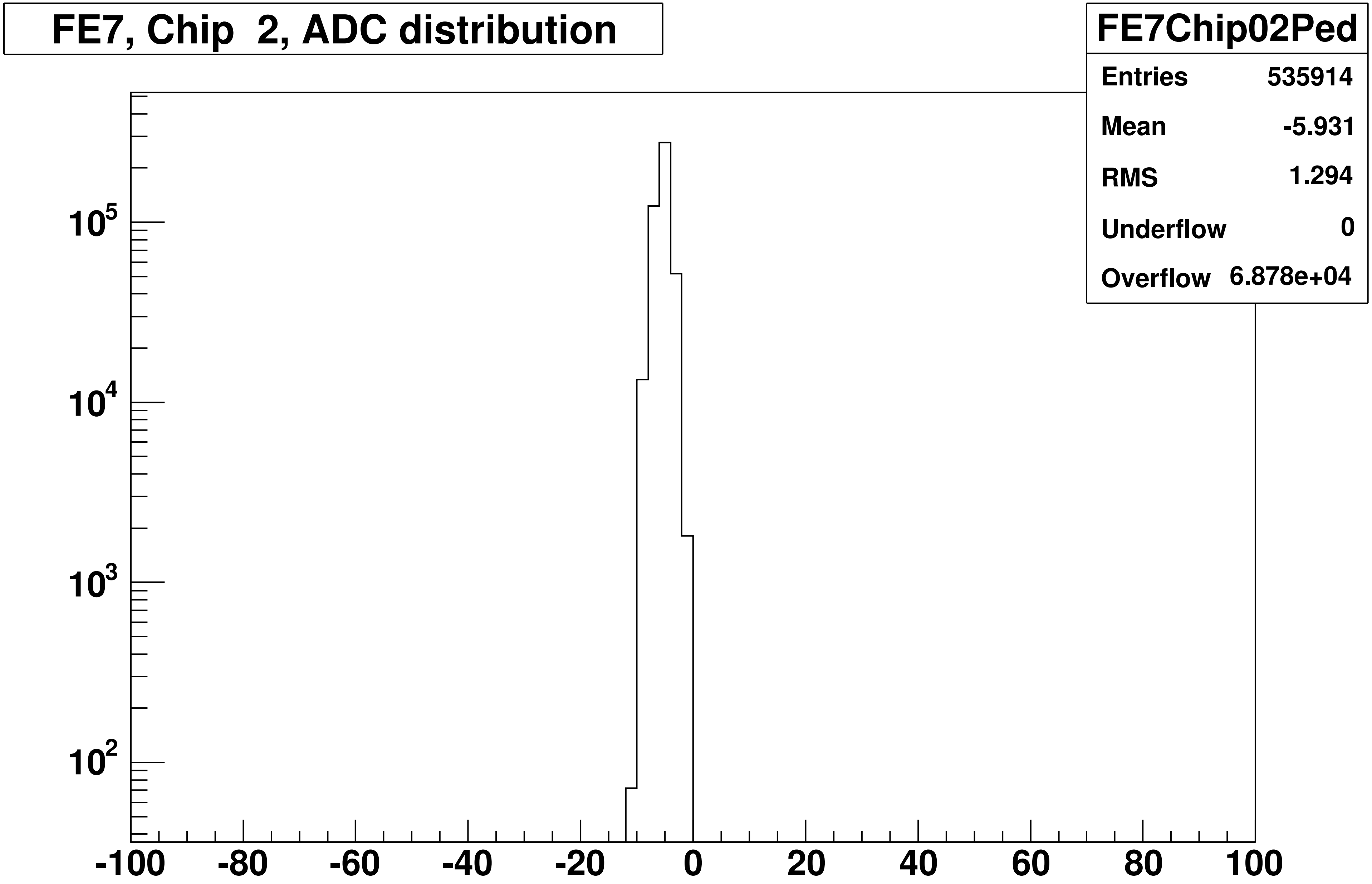
FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 535914
Mean 2395
RMS 6299
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

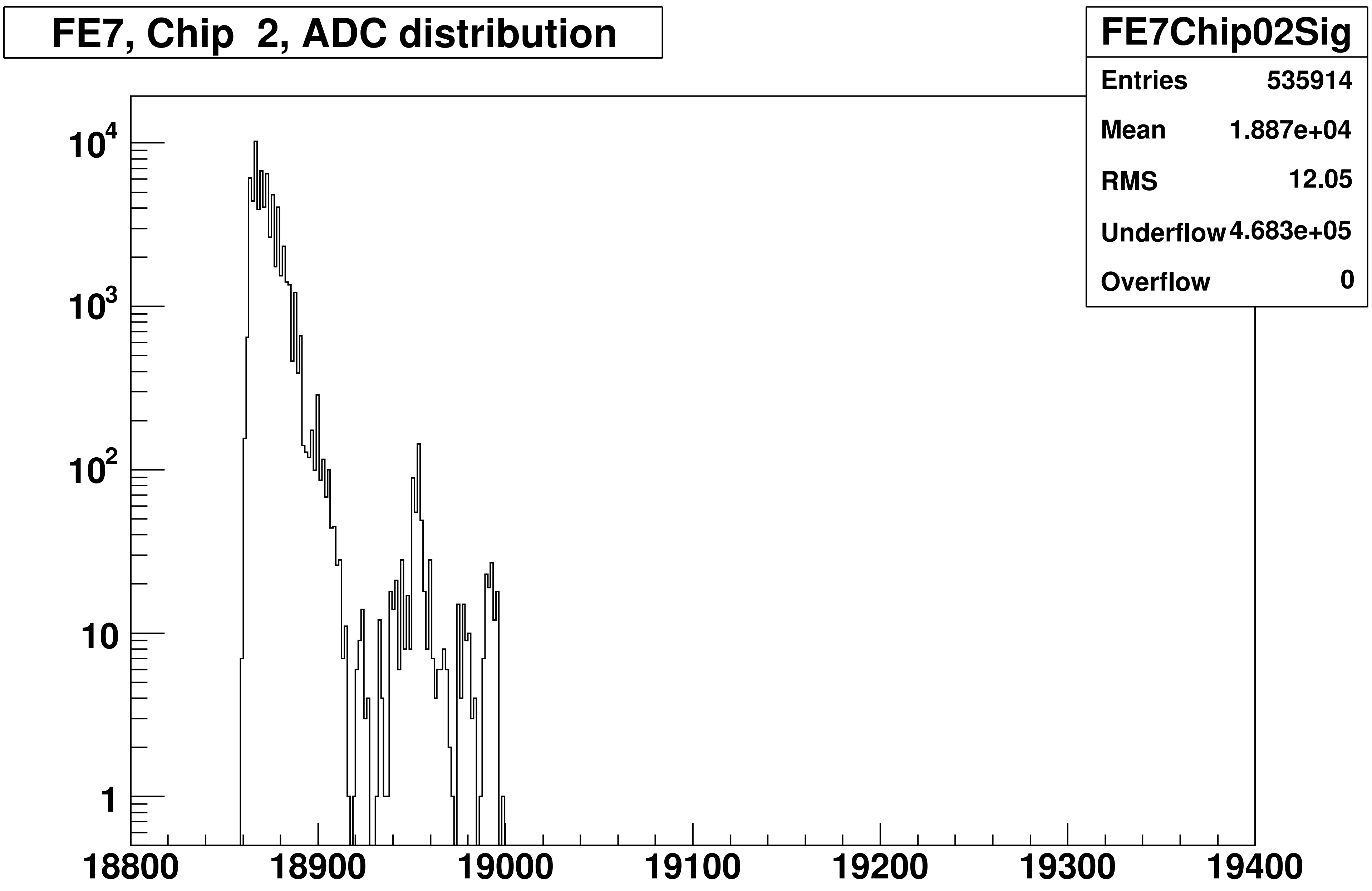

FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 535914
Mean -22.14
RMS 1.258
Underflow 0
Overflow 6.885e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



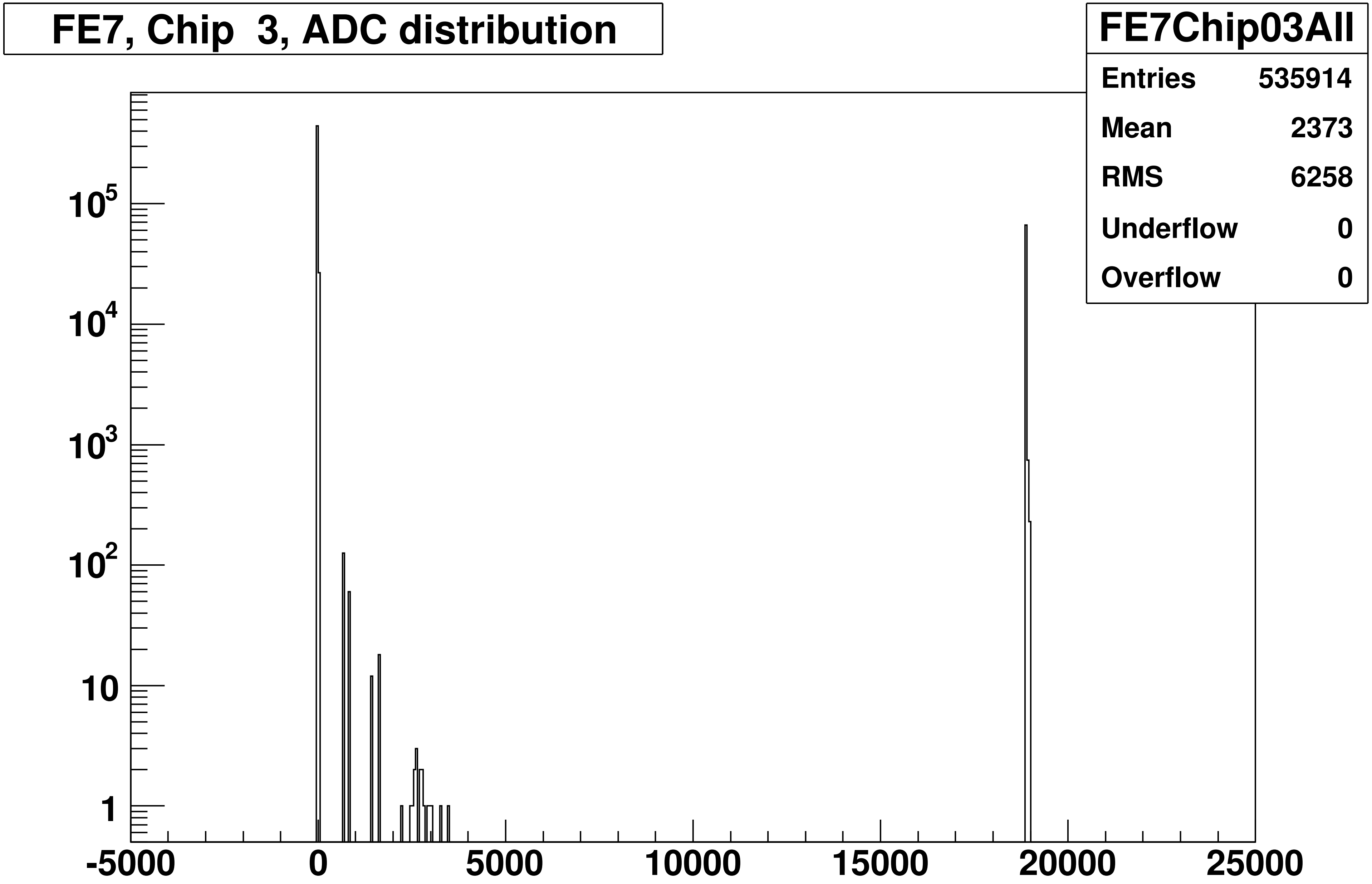
FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 535914
Mean 1.881e+04
RMS 15.08
Underflow 4.672e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

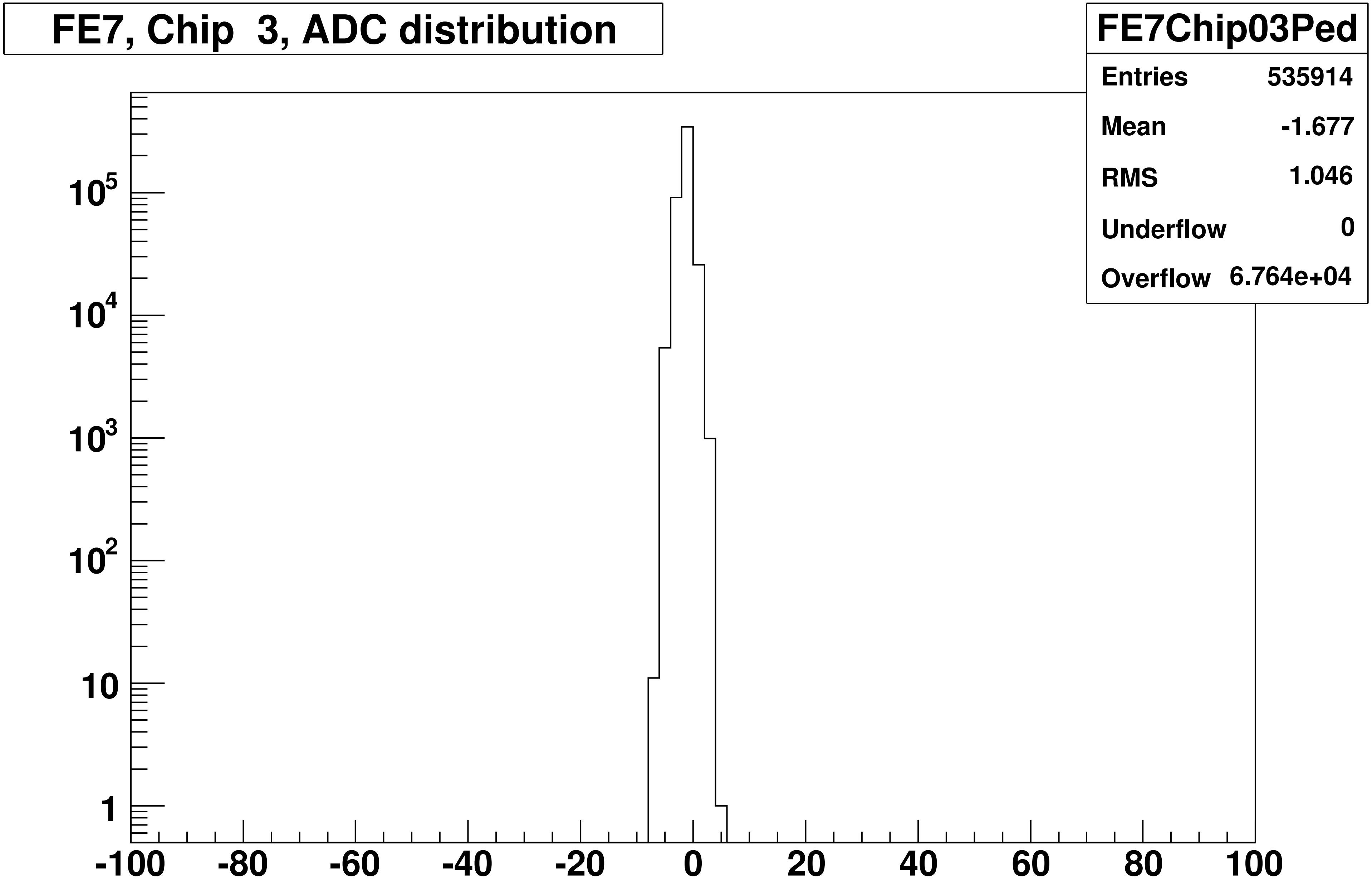

FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 535914
Mean 2380
RMS 6270
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 535914
Mean -5.931
RMS 1.294
Underflow 0
Overflow 6.878e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

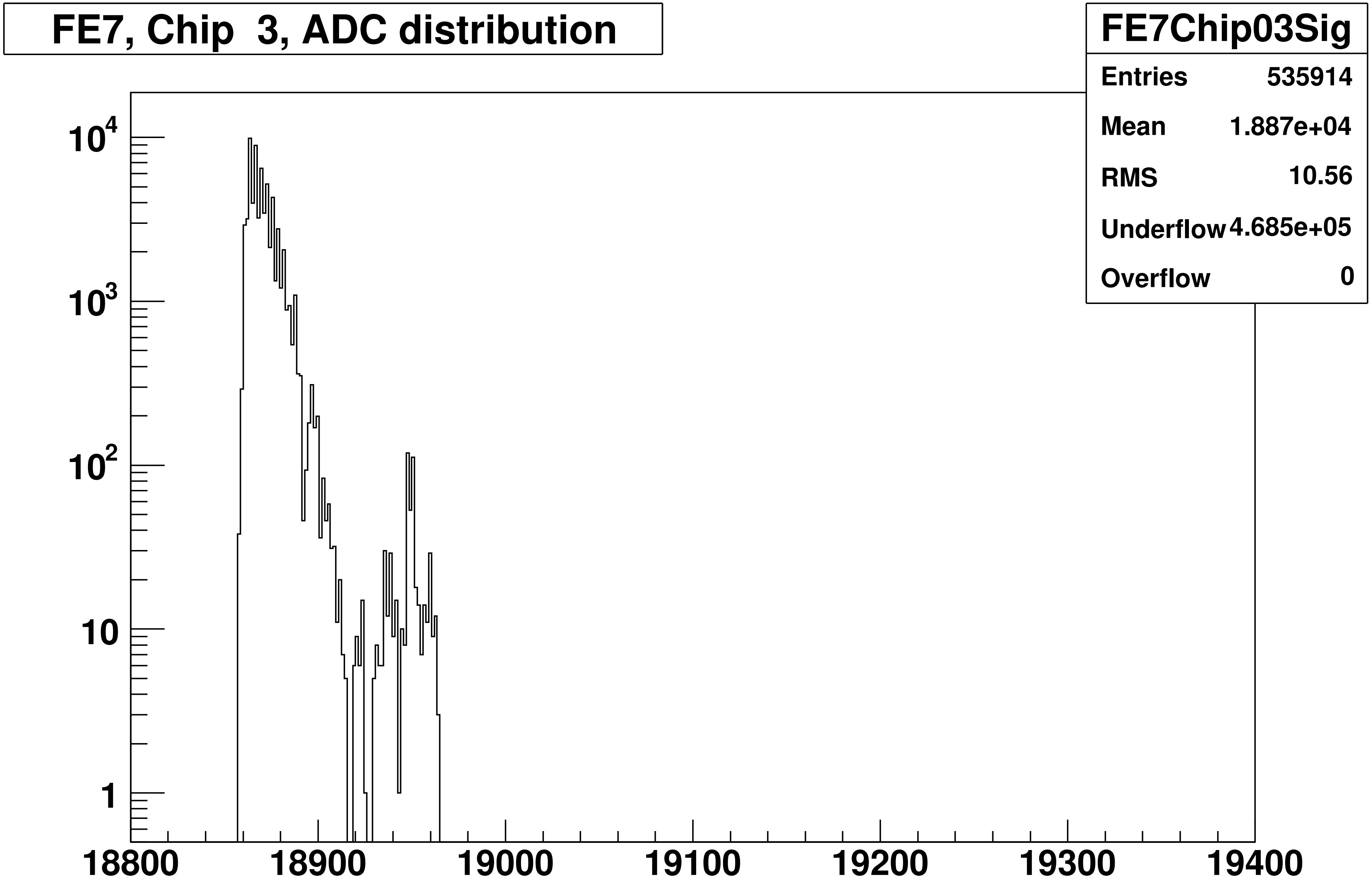


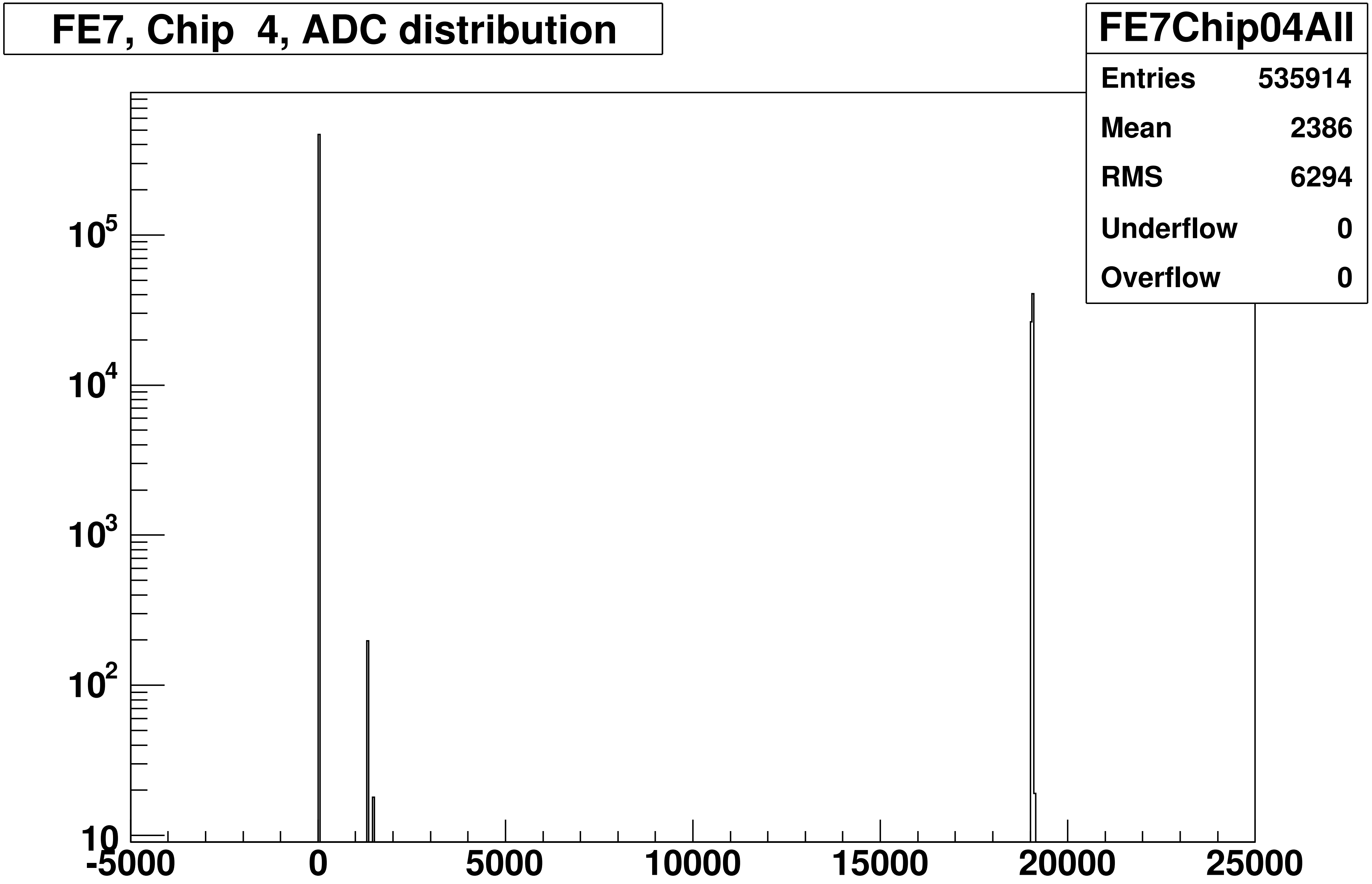
FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 535914
Mean 1.887e+04
RMS 12.05
Underflow 4.683e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

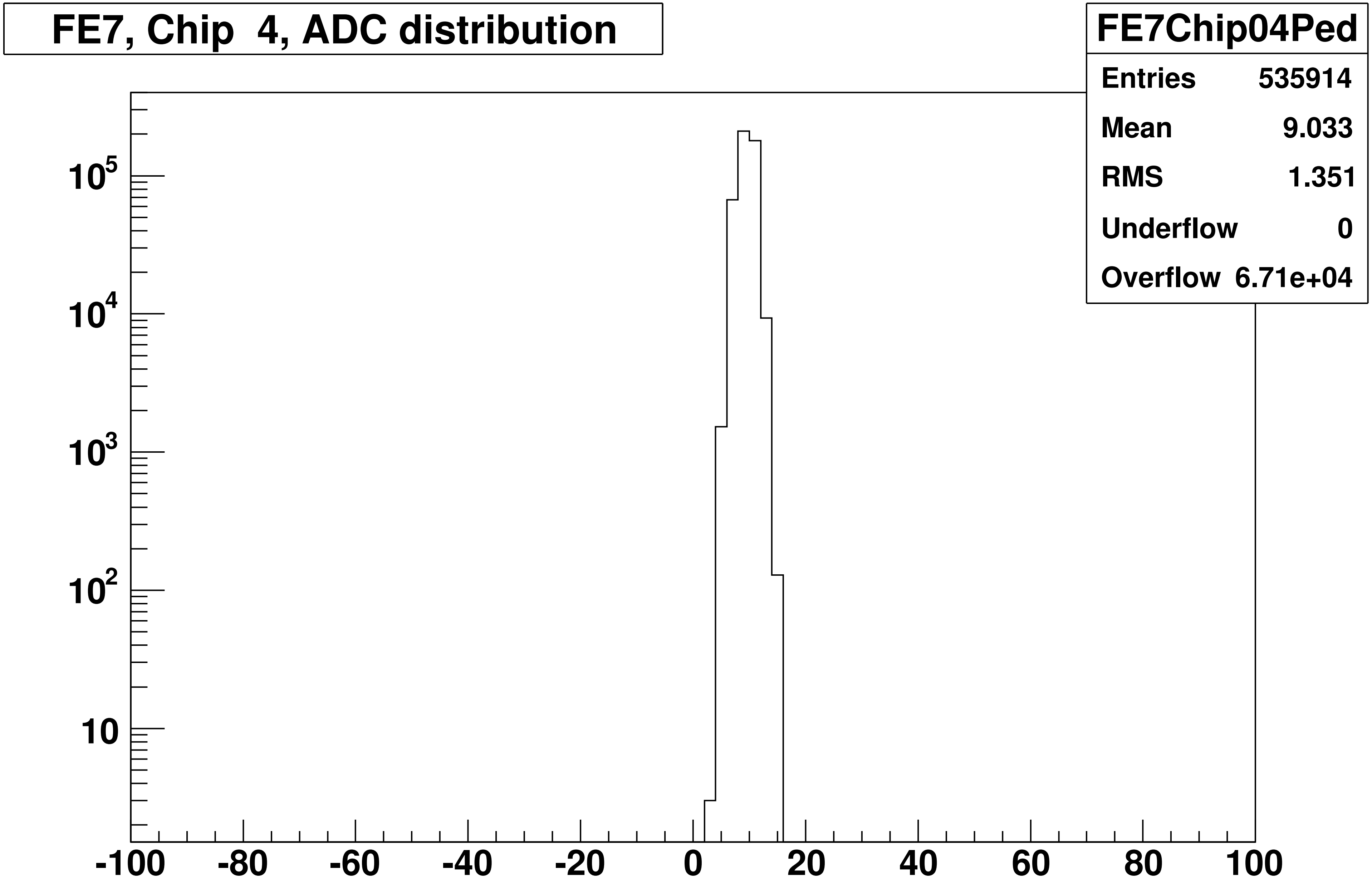

FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 535914
Mean 2373
RMS 6258
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

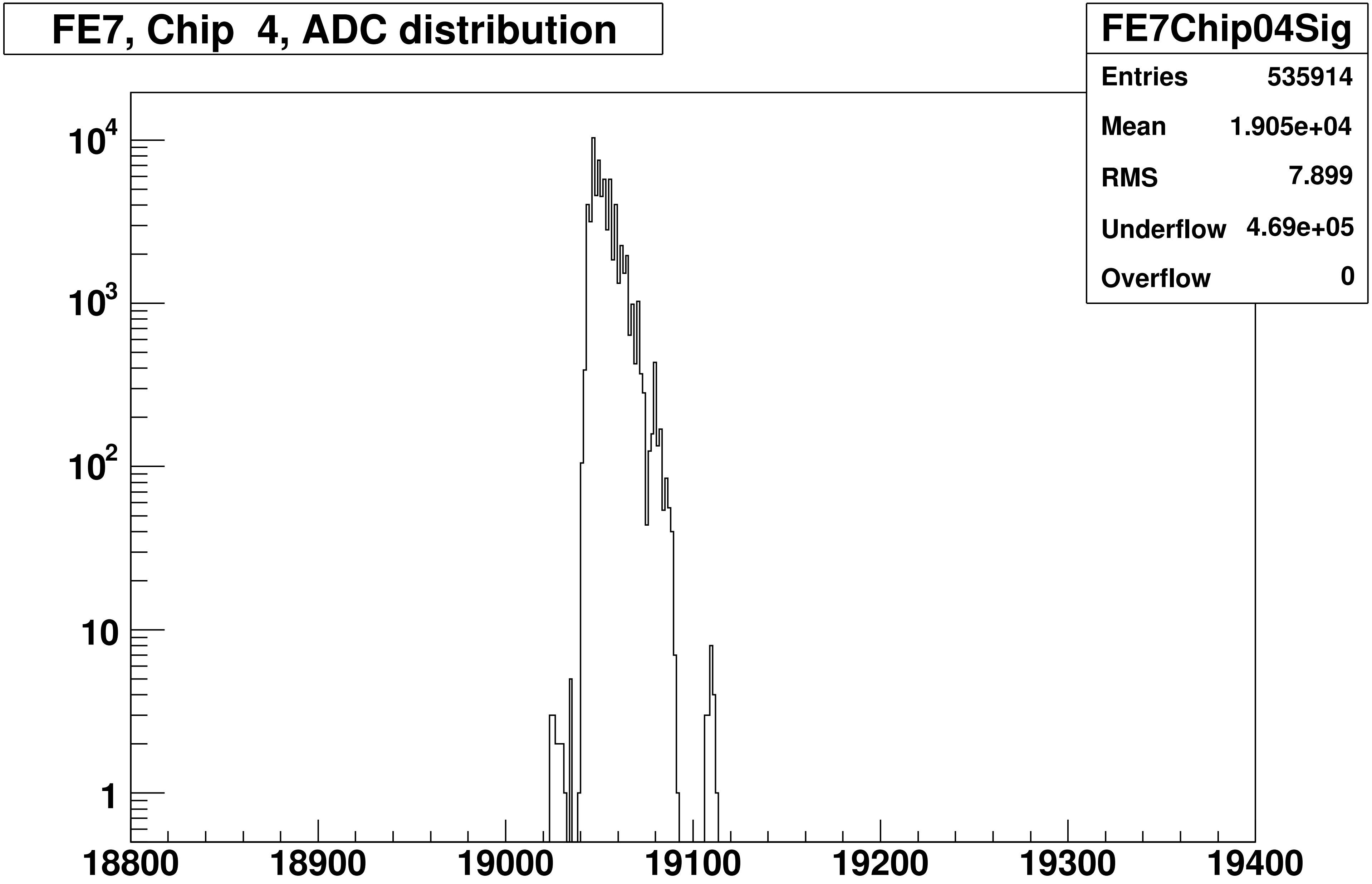


FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 535914
Mean -1.677
RMS 1.046
Underflow 0
Overflow 6.764e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

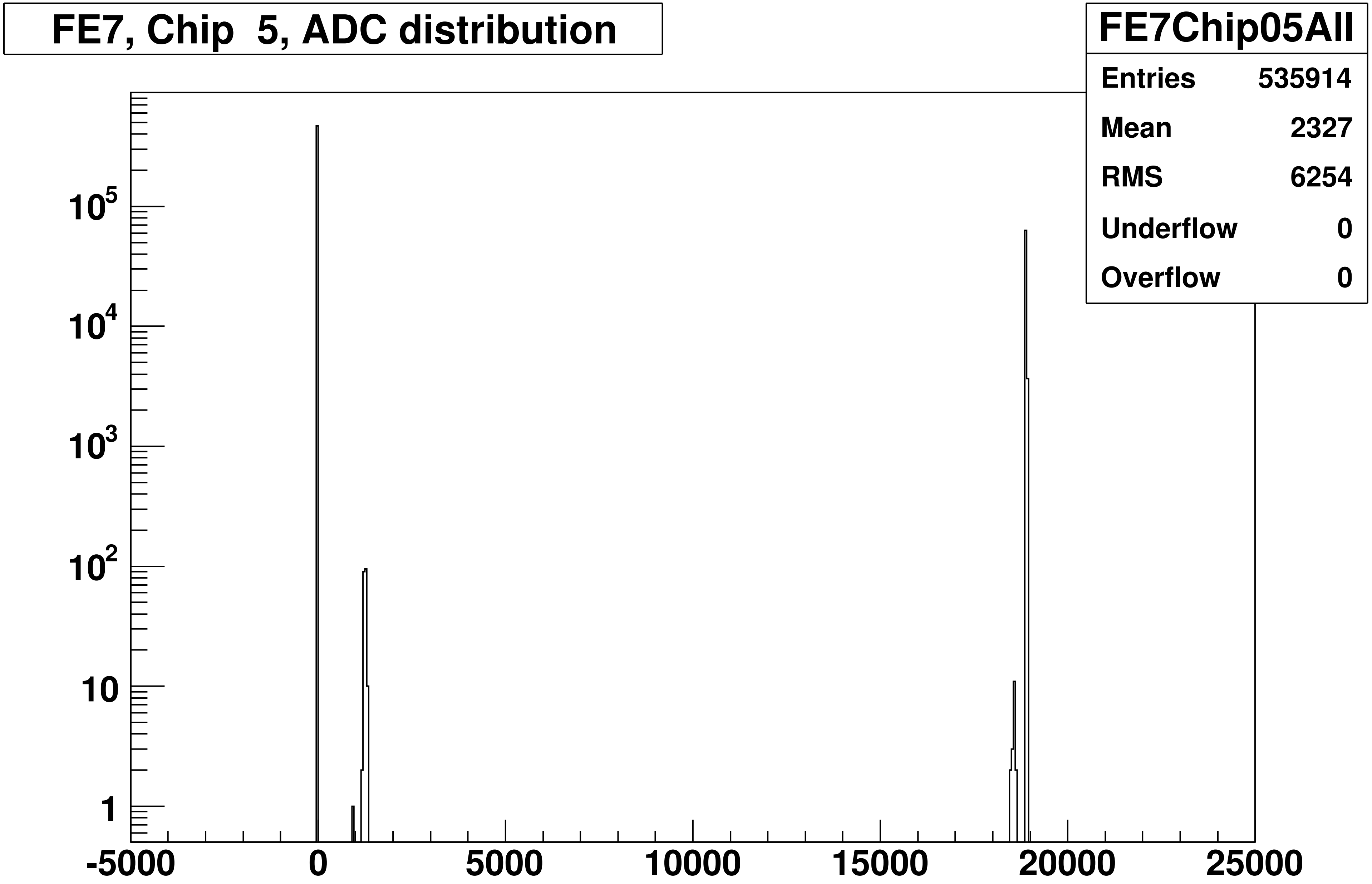

FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 535914
Mean 1.887e+04
RMS 10.56
Underflow 4.685e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

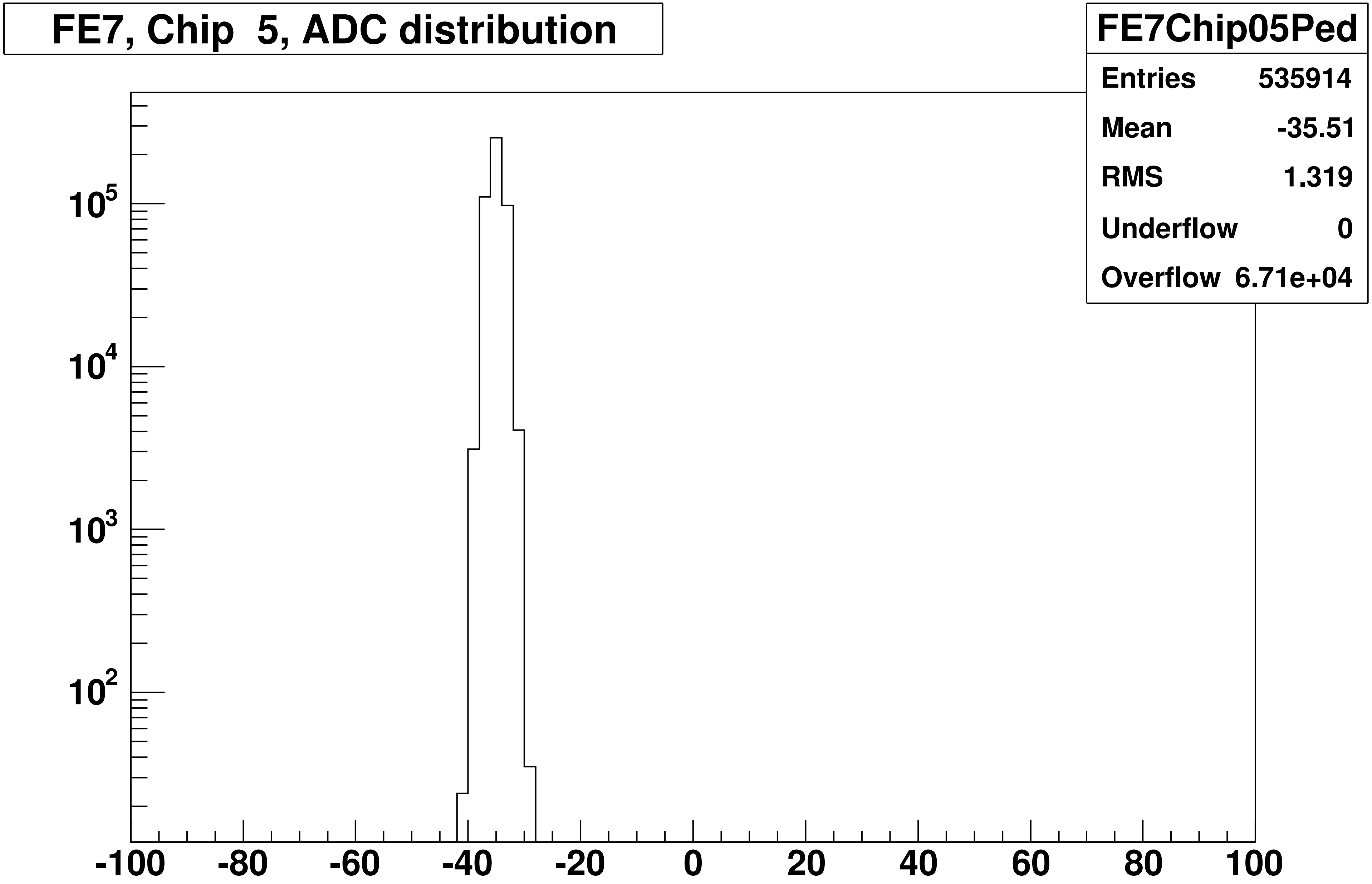

FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 535914
Mean 2386
RMS 6294
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

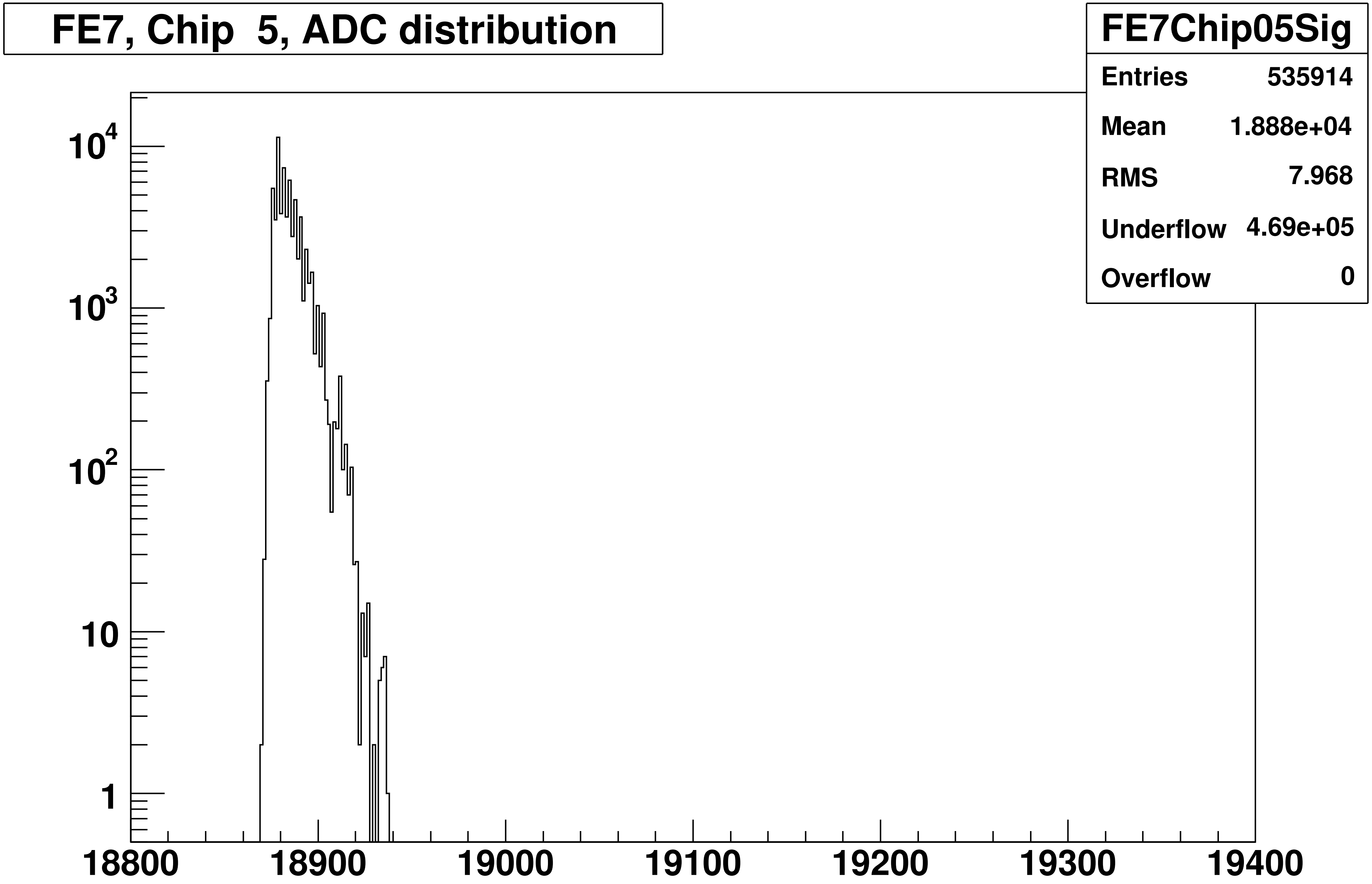

FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 535914
Mean 9.033
RMS 1.351
Underflow 0
Overflow 6.71e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



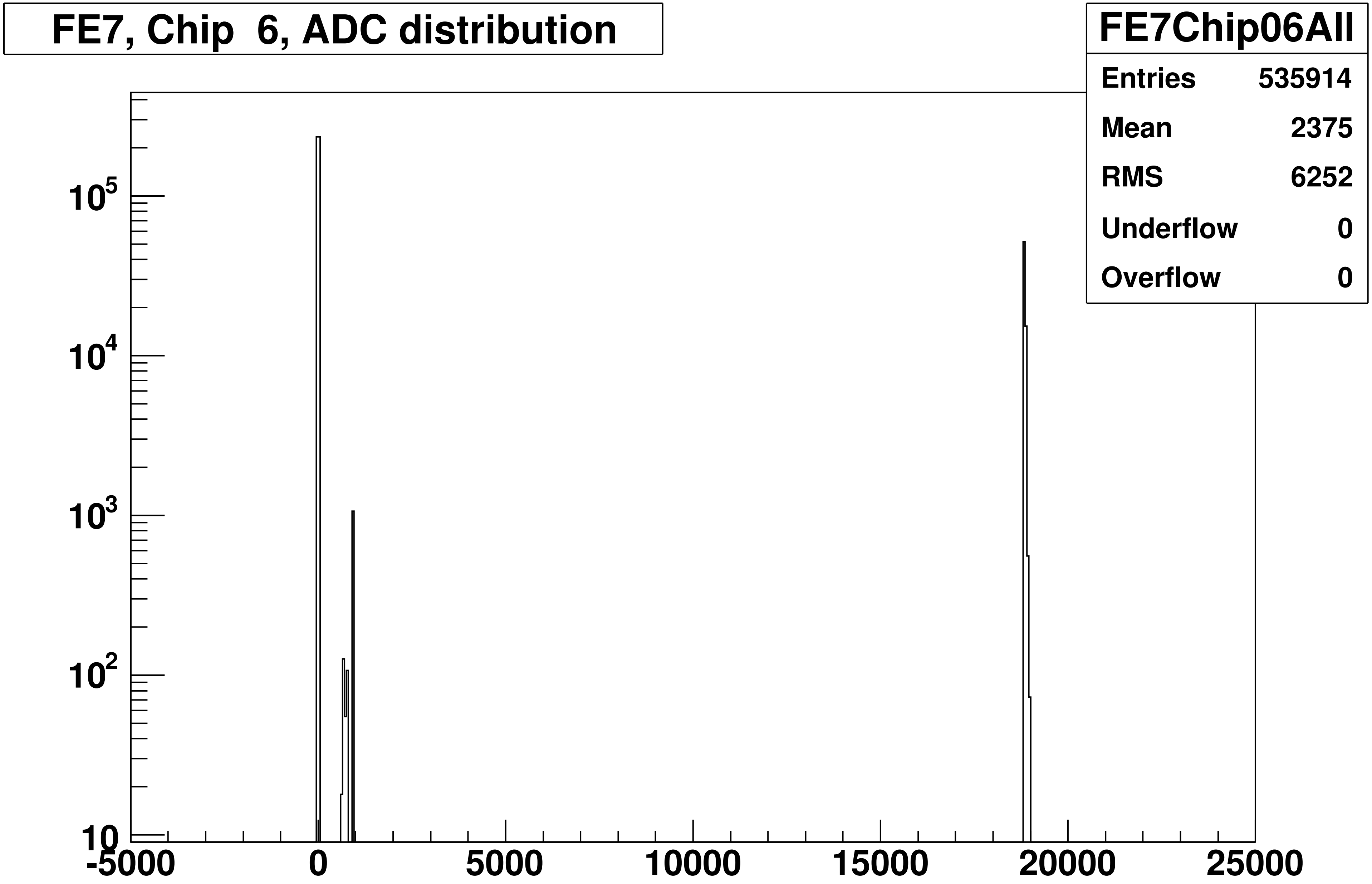
FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 535914
Mean 1.905e+04
RMS 7.899
Underflow 4.69e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

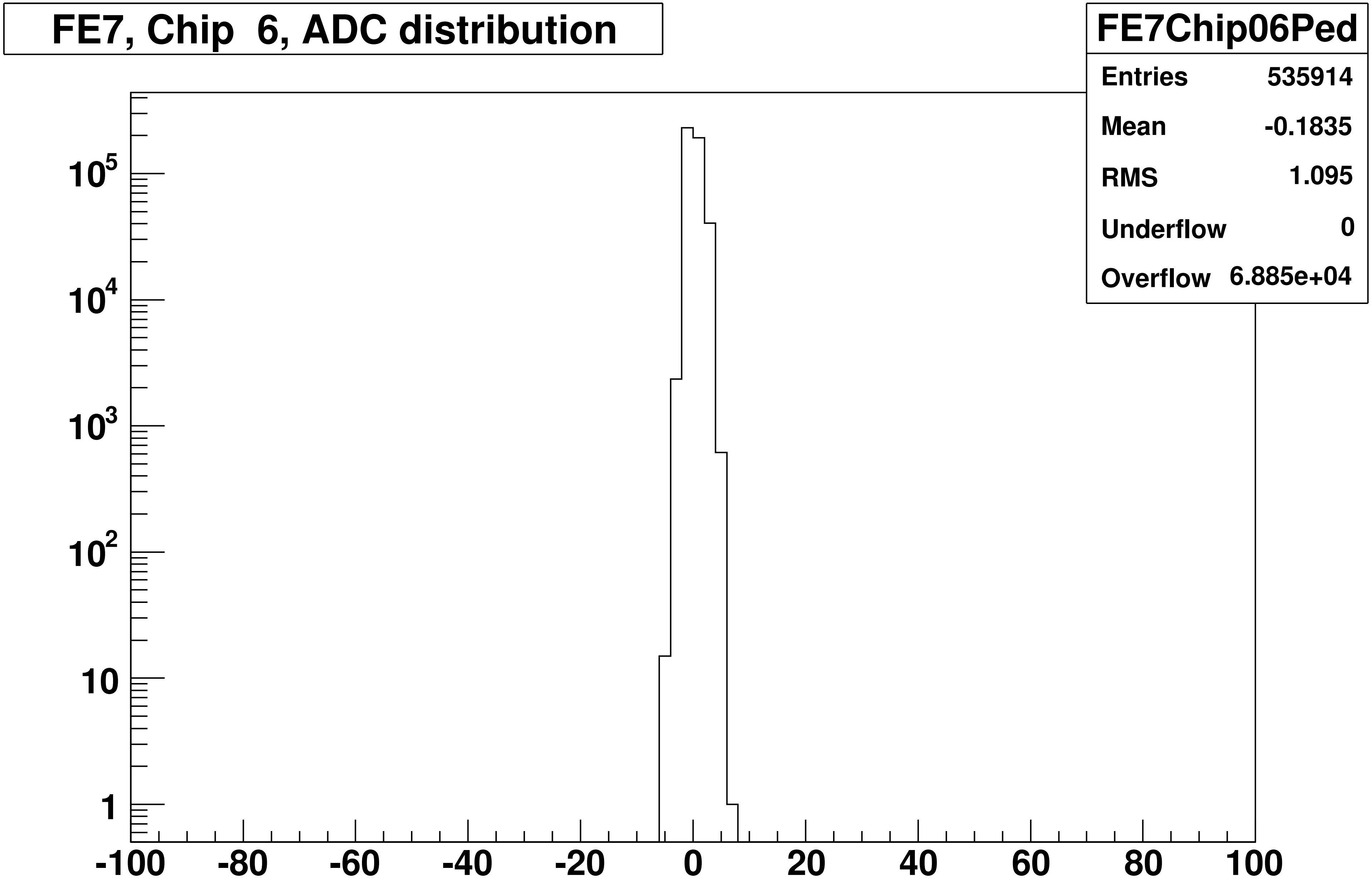

FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 535914
Mean 2327
RMS 6254
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

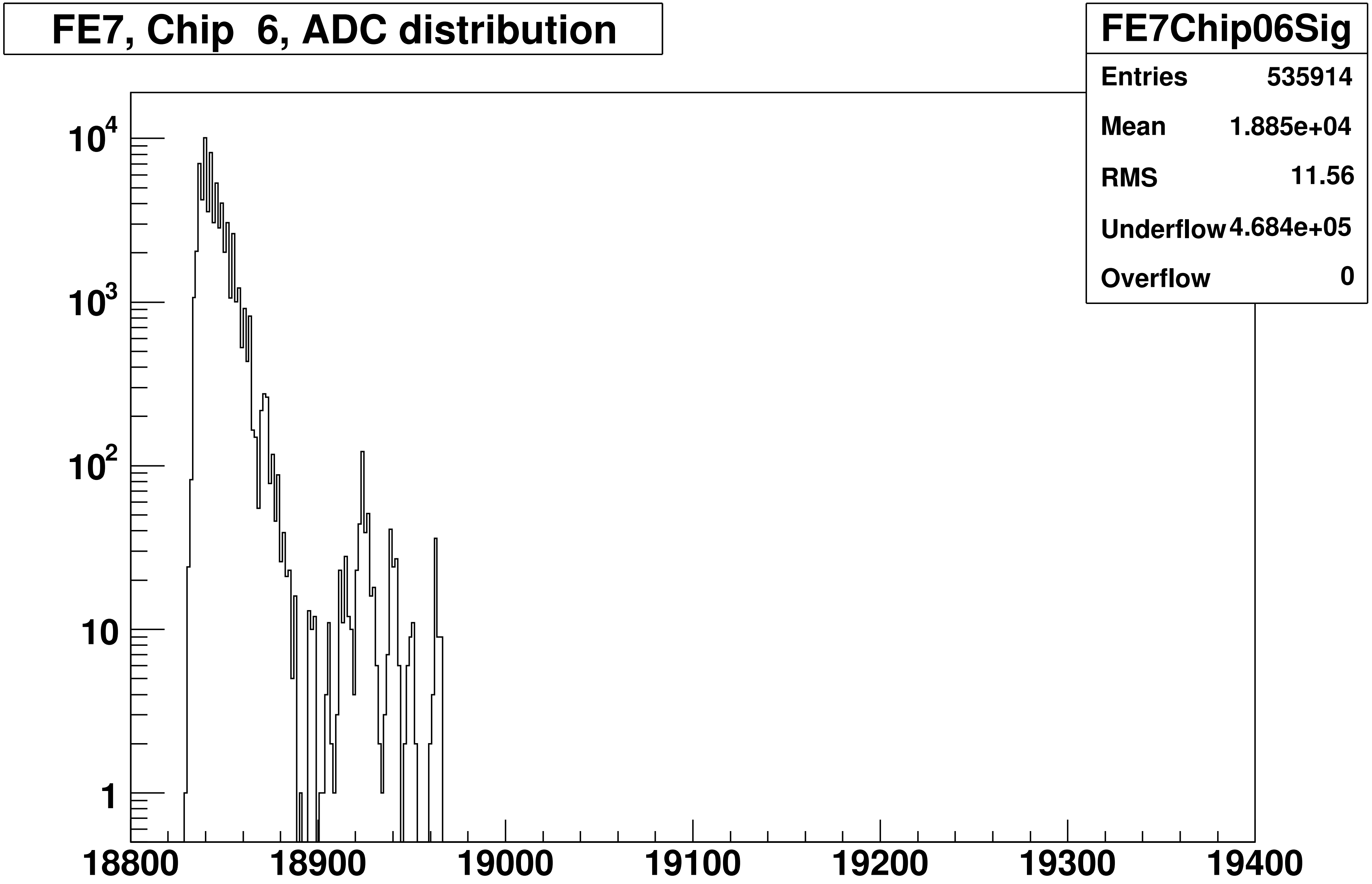

FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 535914
Mean -35.51
RMS 1.319
Underflow 0
Overflow 6.71e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100



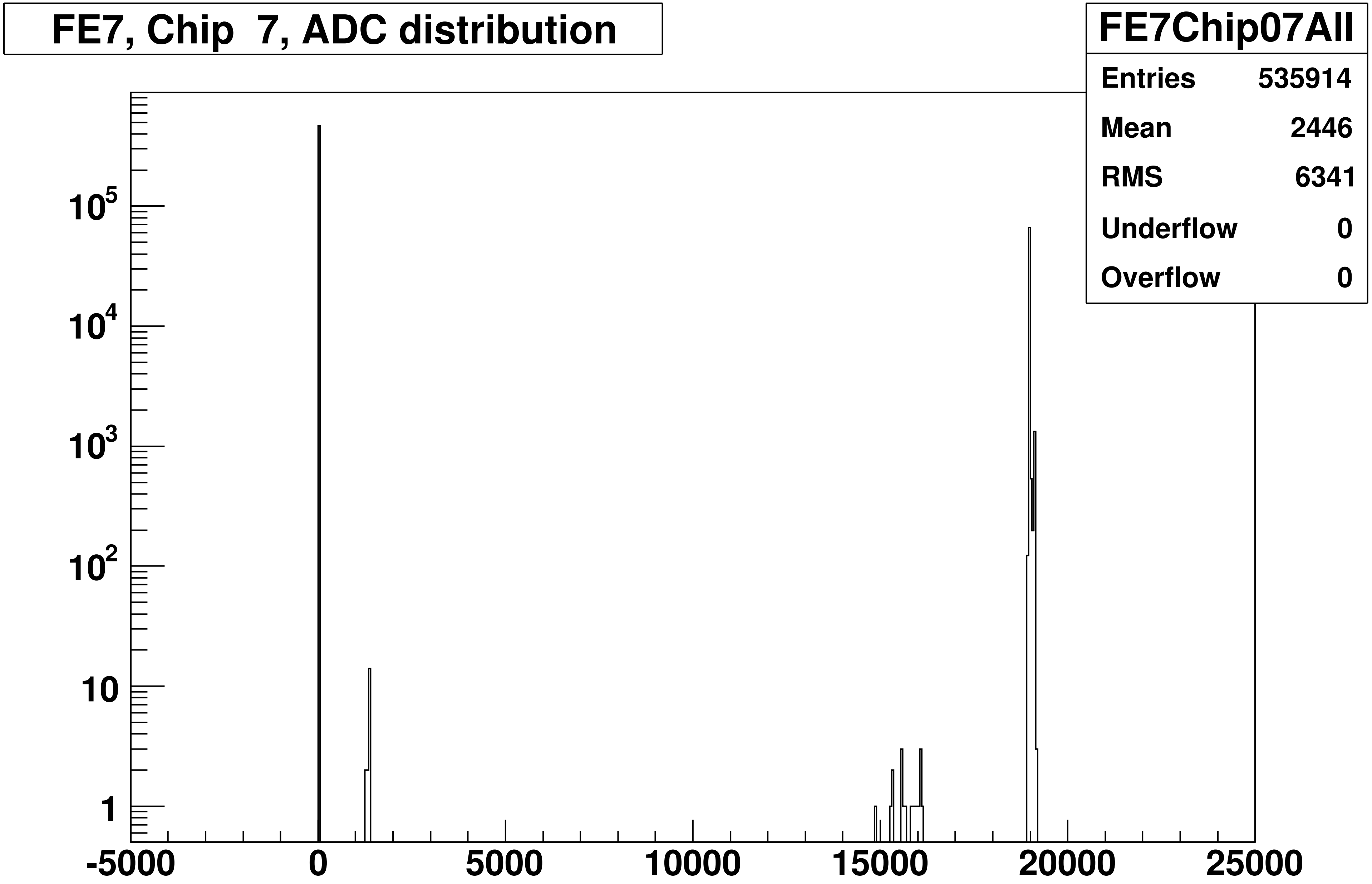
FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 535914
Mean 1.888e+04
RMS 7.968
Underflow 4.69e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

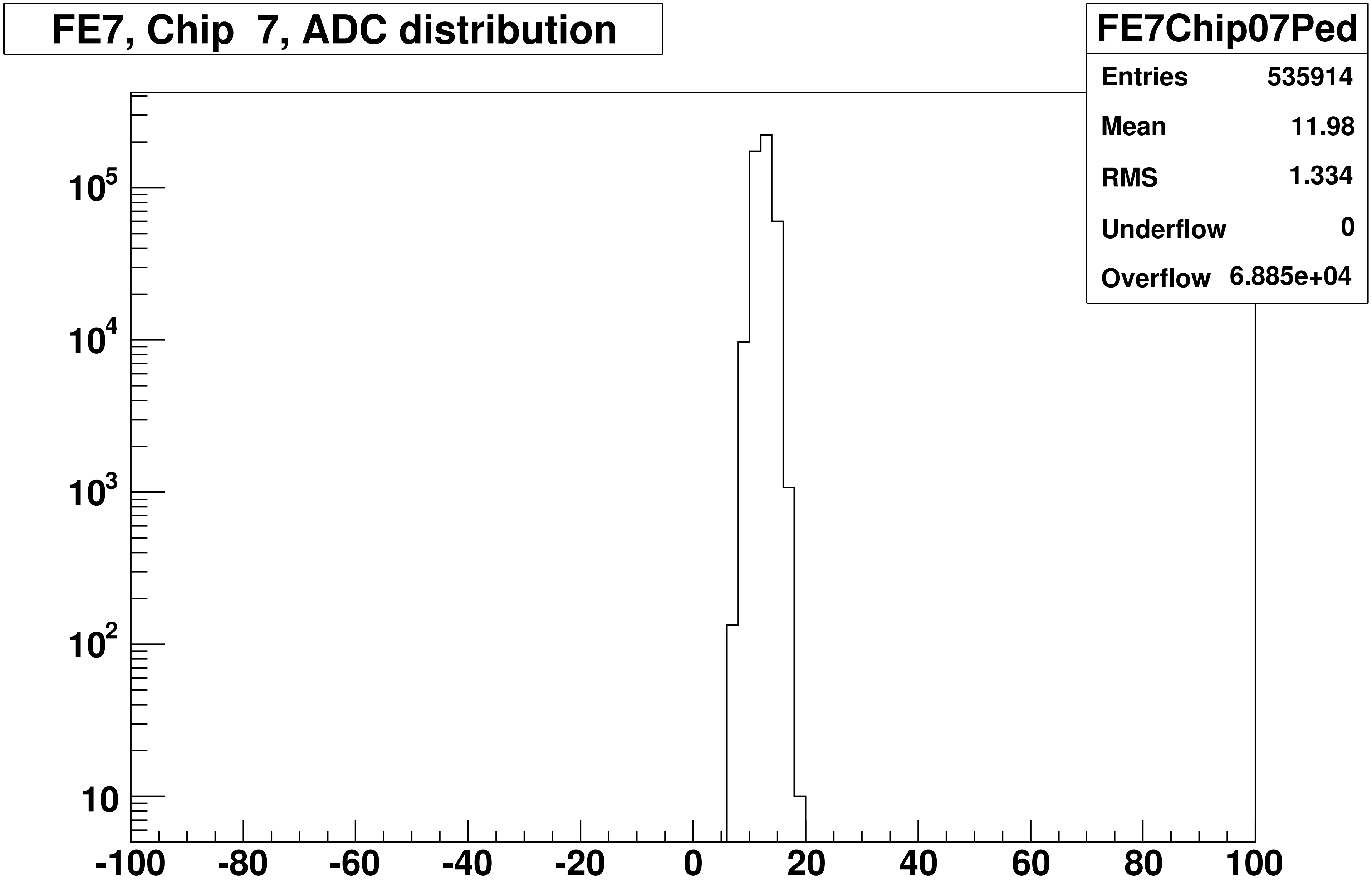

FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 535914
Mean 2375
RMS 6252
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 535914
Mean -0.1835
RMS 1.095
Underflow 0
Overflow 6.885e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

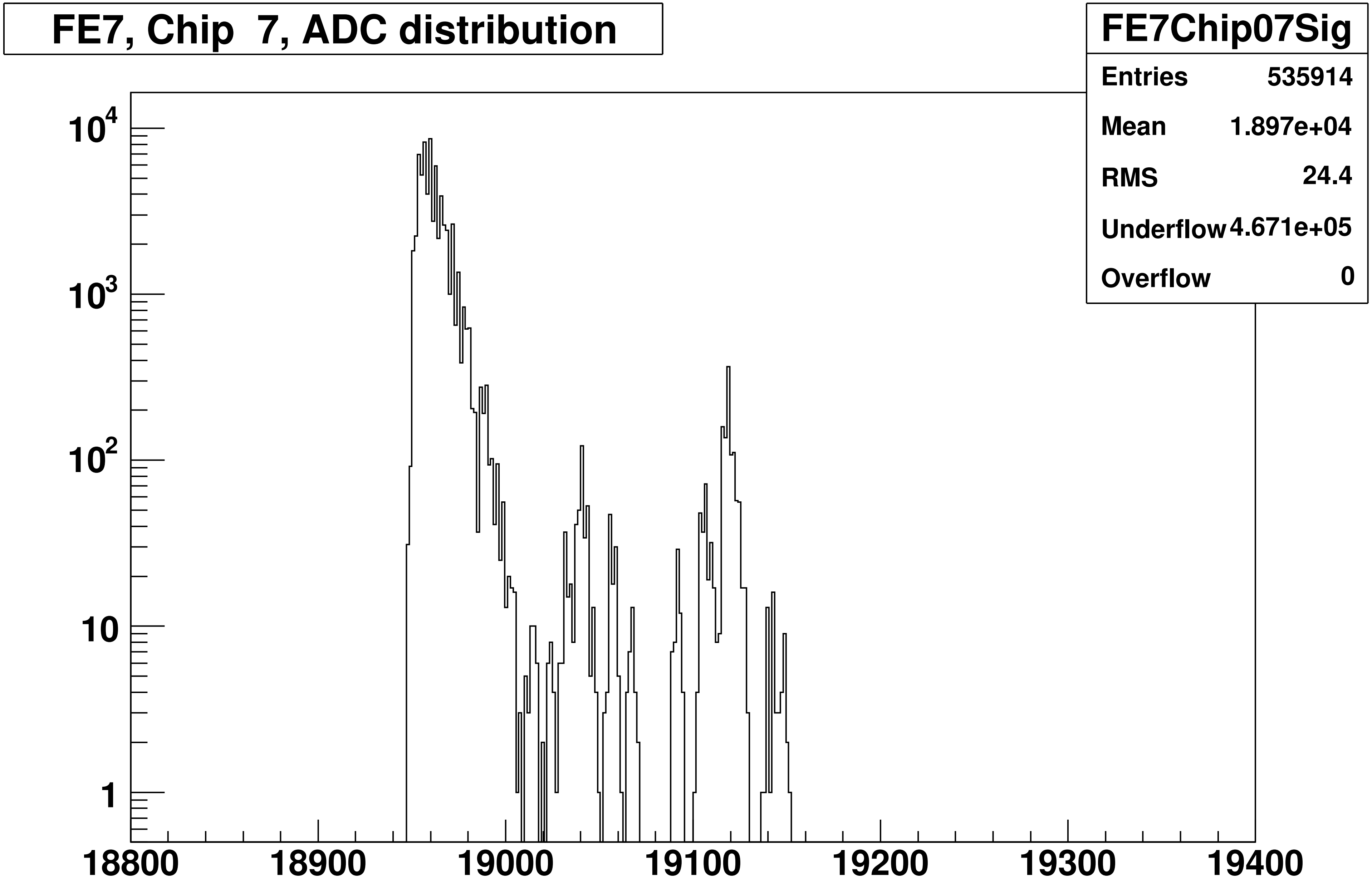


FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 535914
Mean 1.885e+04
RMS 11.56
Underflow 4.684e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

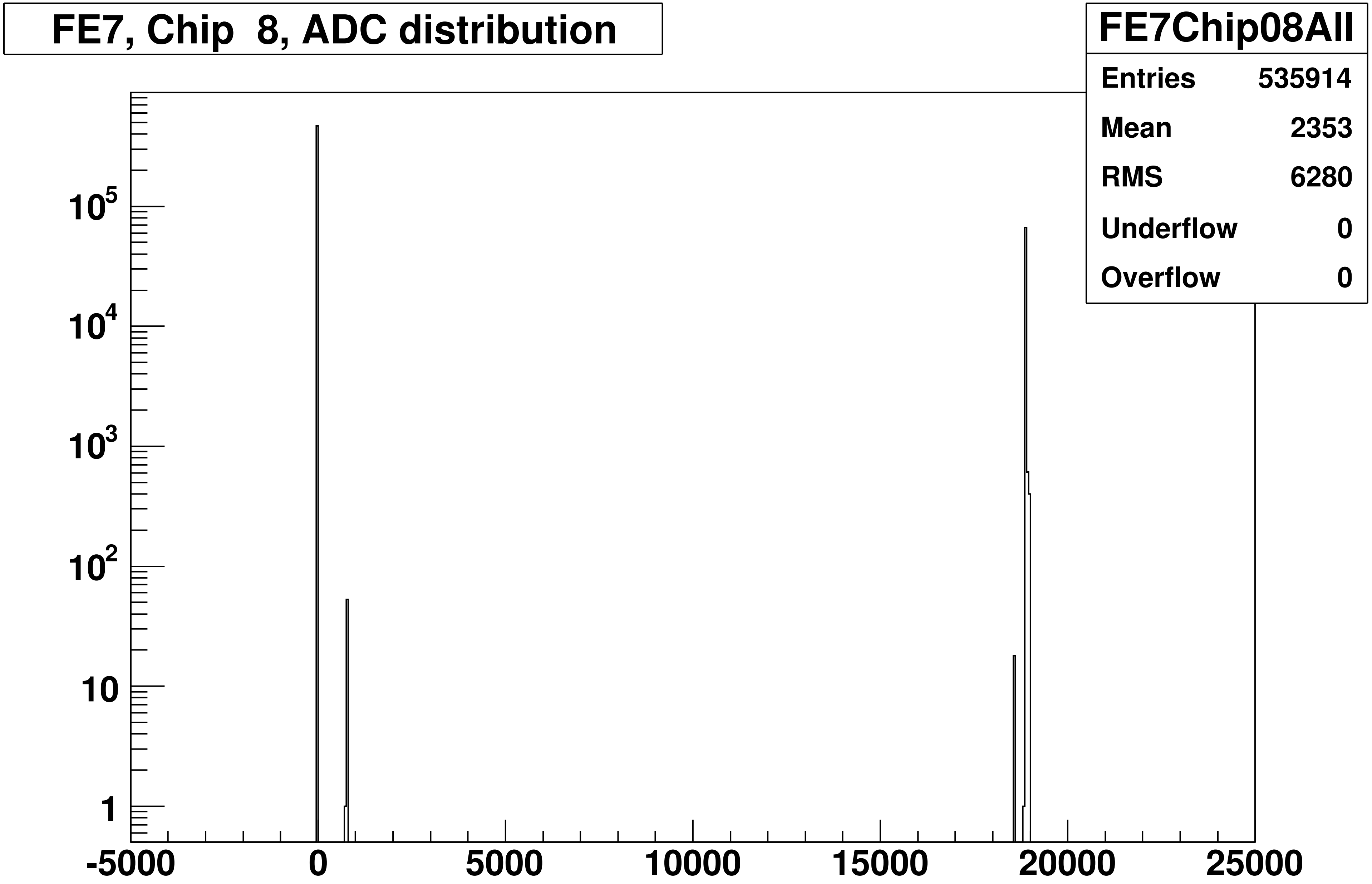

FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 535914
Mean 2446
RMS 6341
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

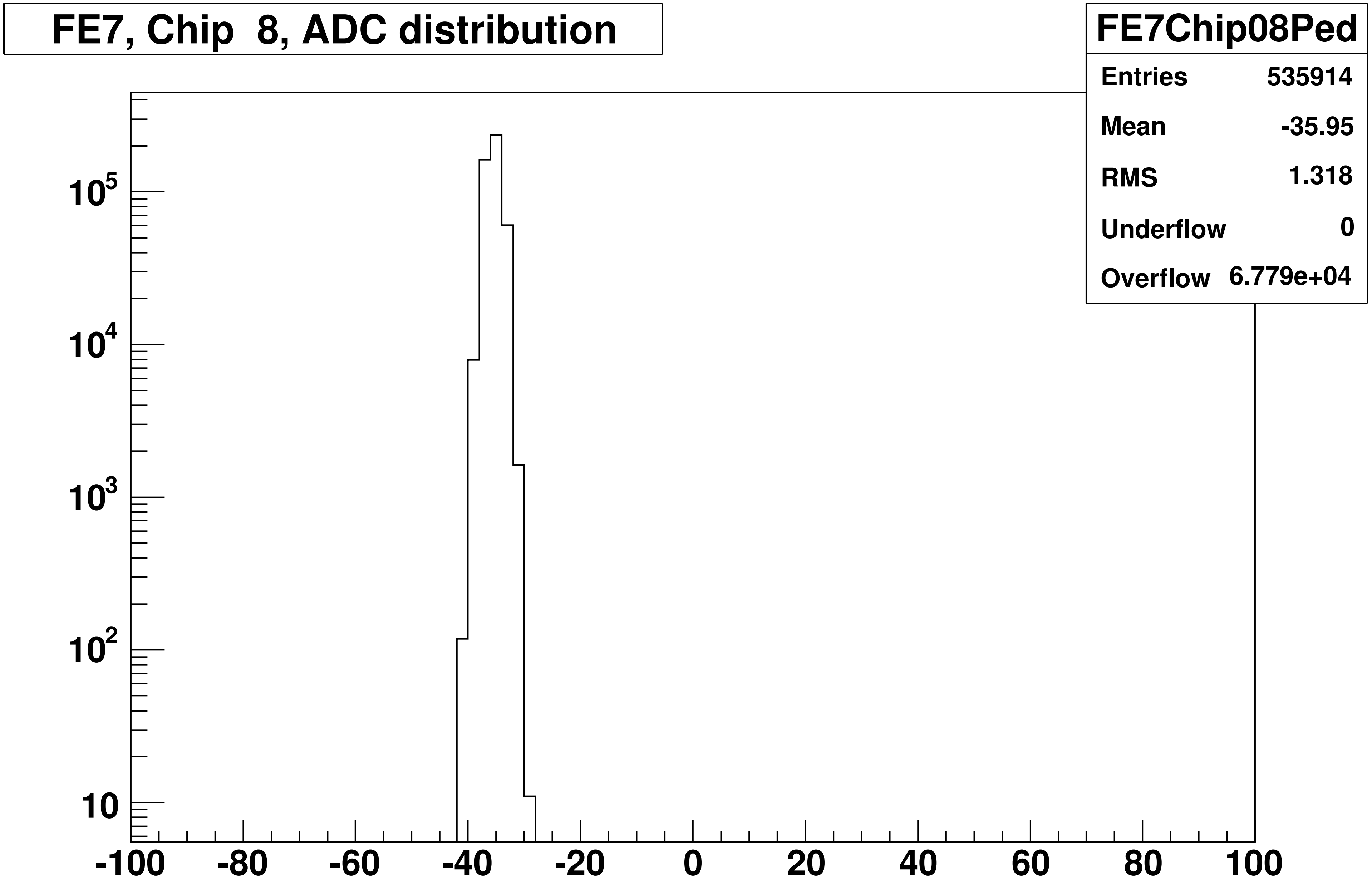


FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 535914
Mean 11.98
RMS 1.334
Underflow 0
Overflow 6.885e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

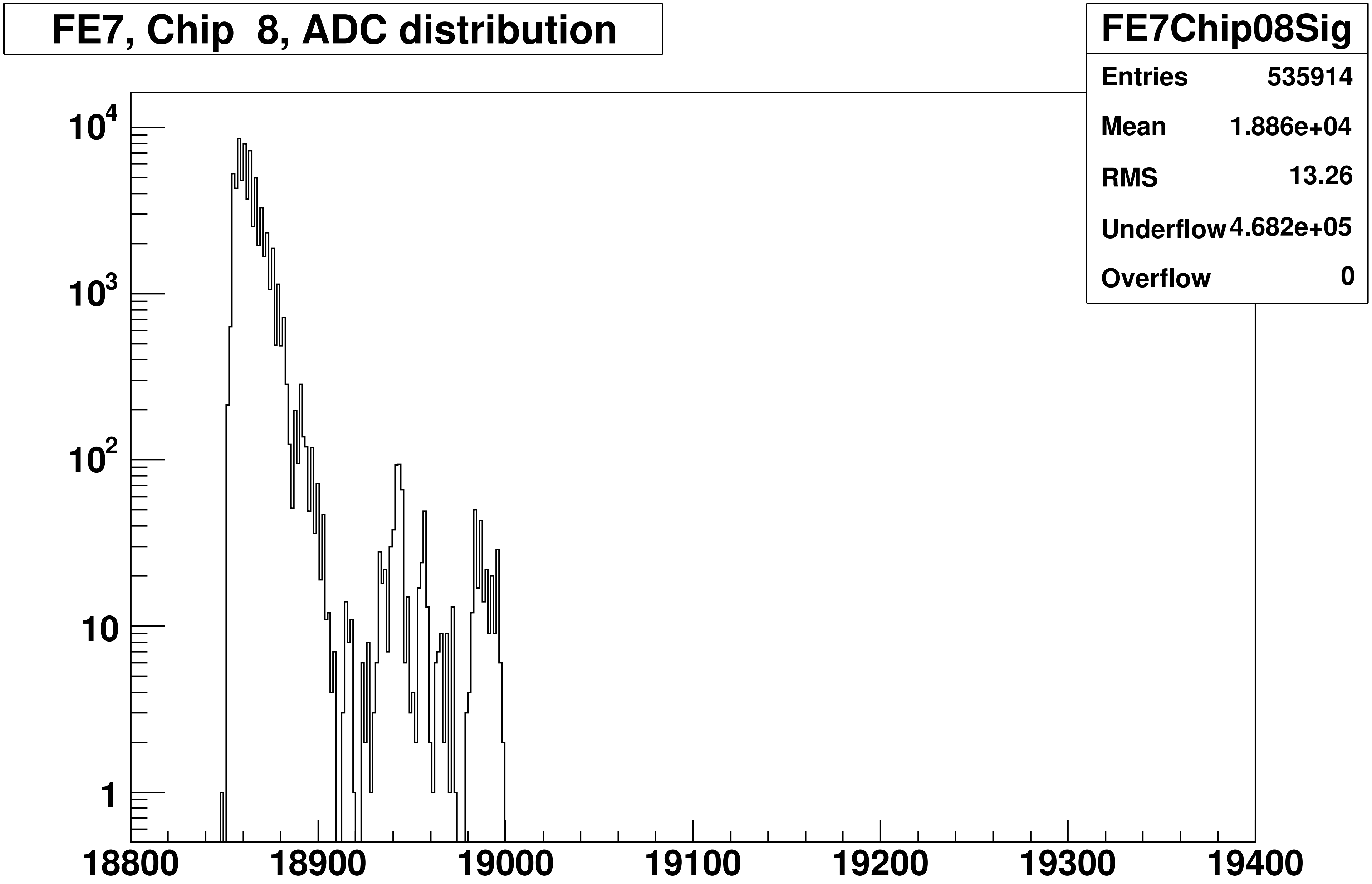


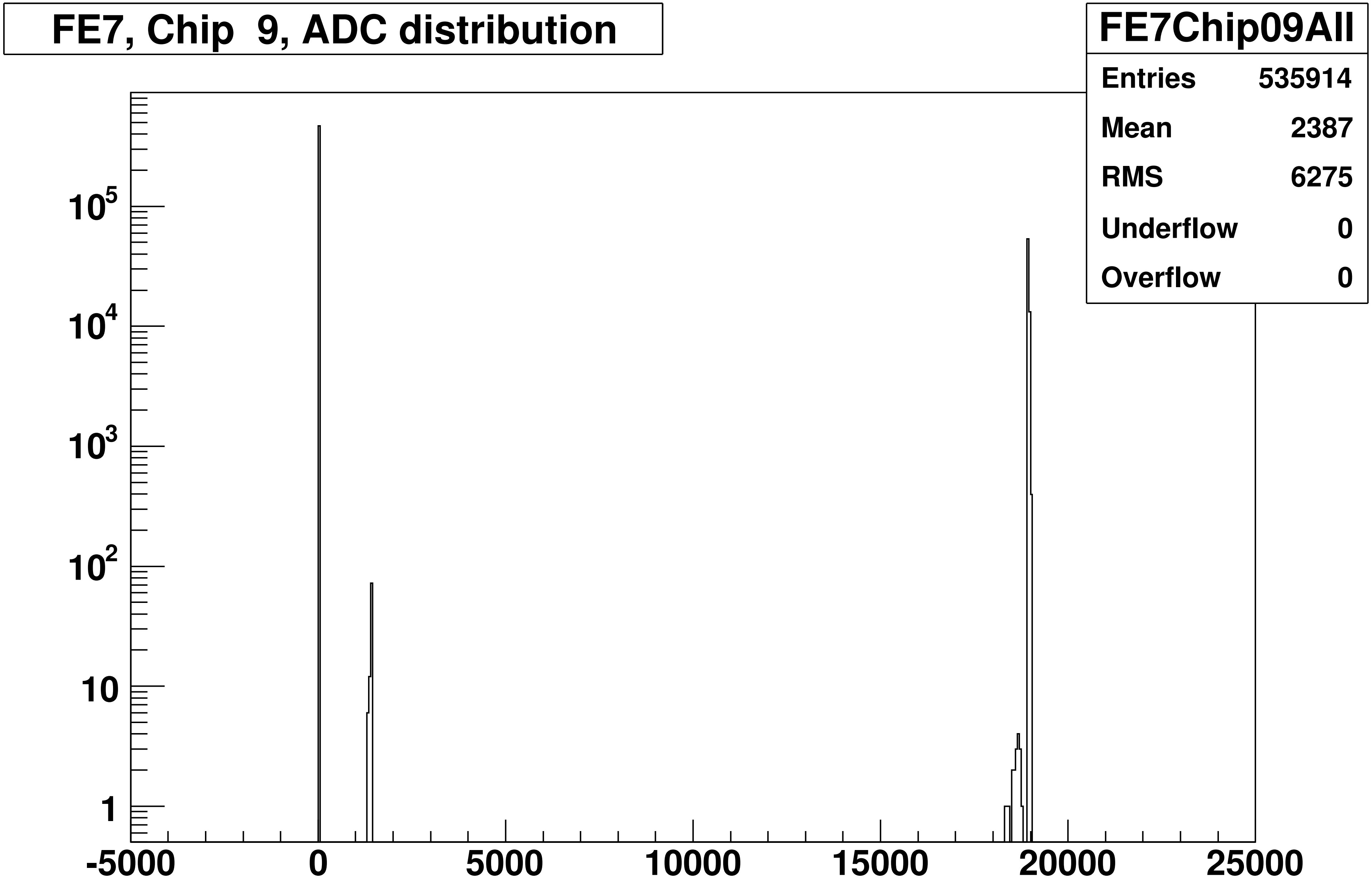
FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 535914
Mean 1.897e+04
RMS 24.4
Underflow 4.671e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

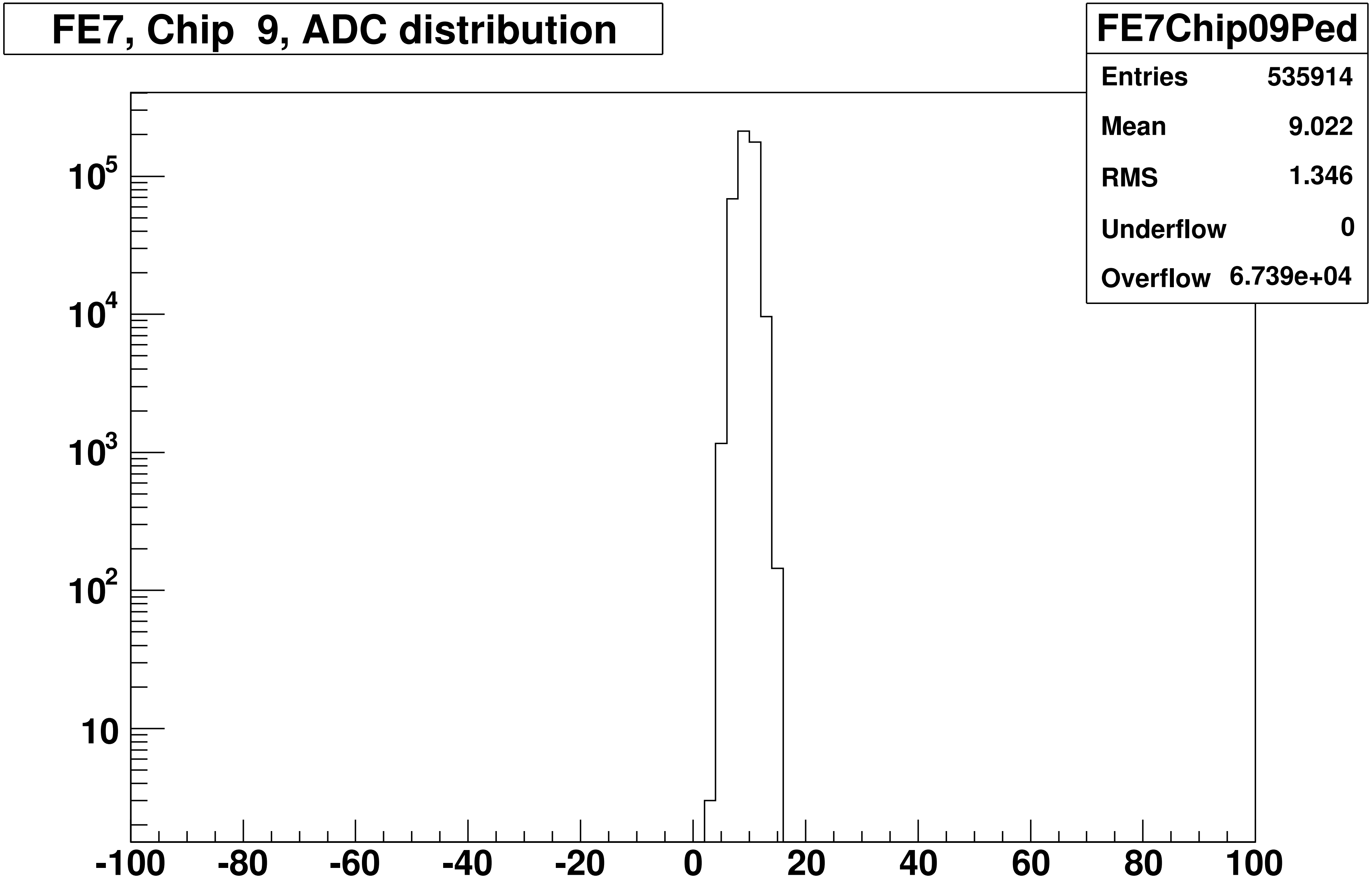

FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 535914
Mean 2353
RMS 6280
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

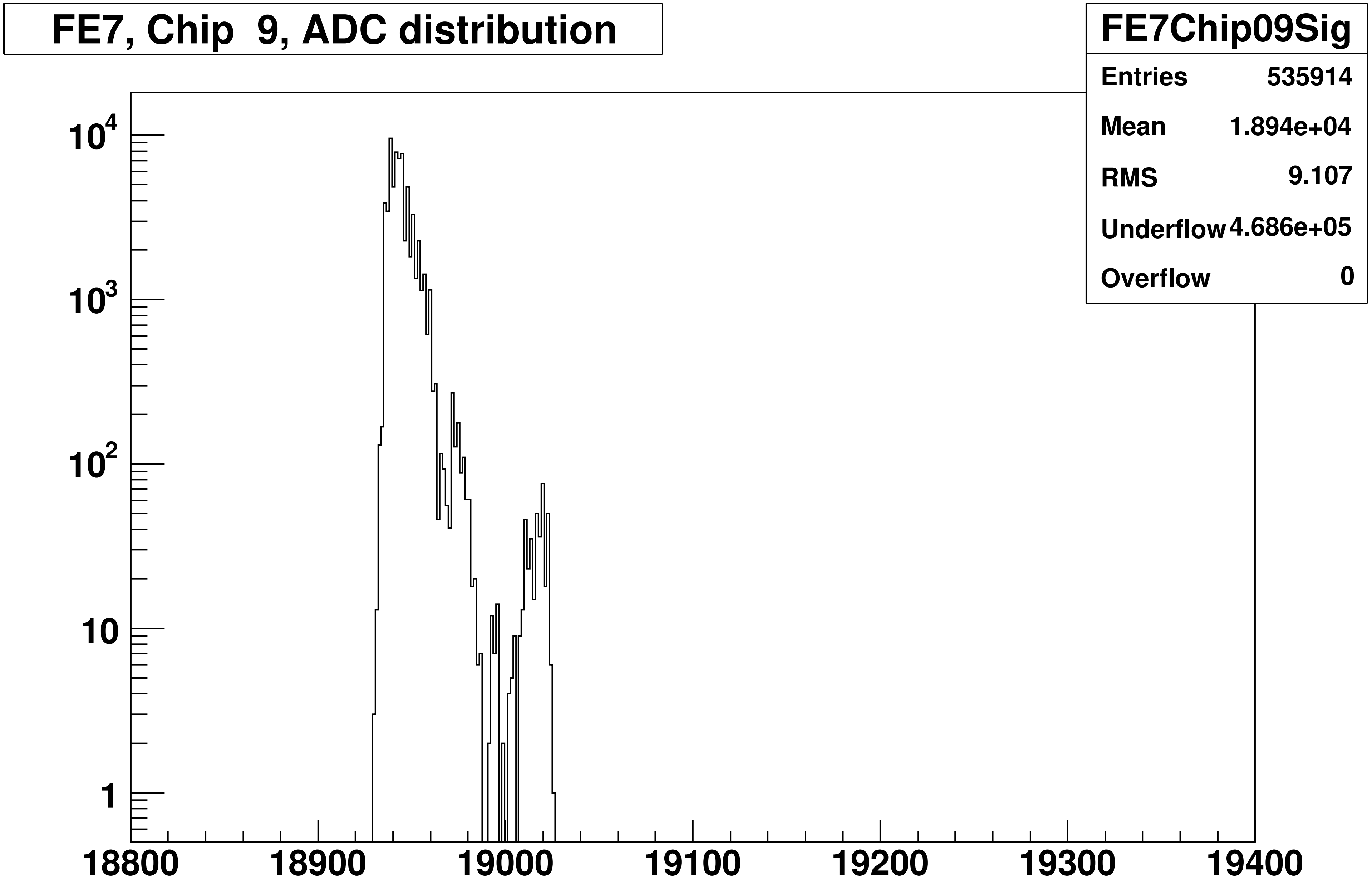


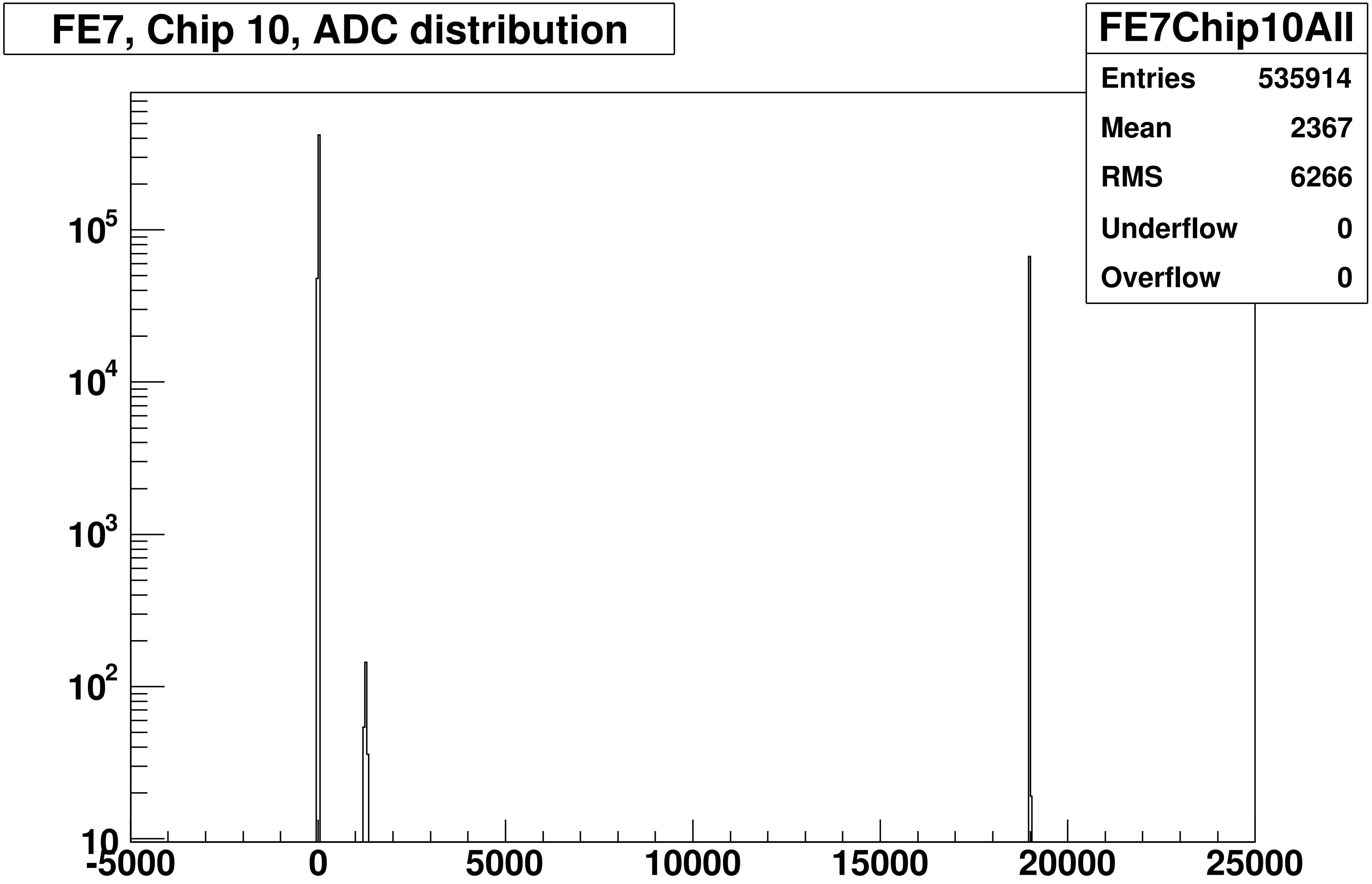
FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 535914
Mean -35.95
RMS 1.318
Underflow 0
Overflow 6.779e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 535914
Mean 1.886e+04
RMS 13.26
Underflow 4.682e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

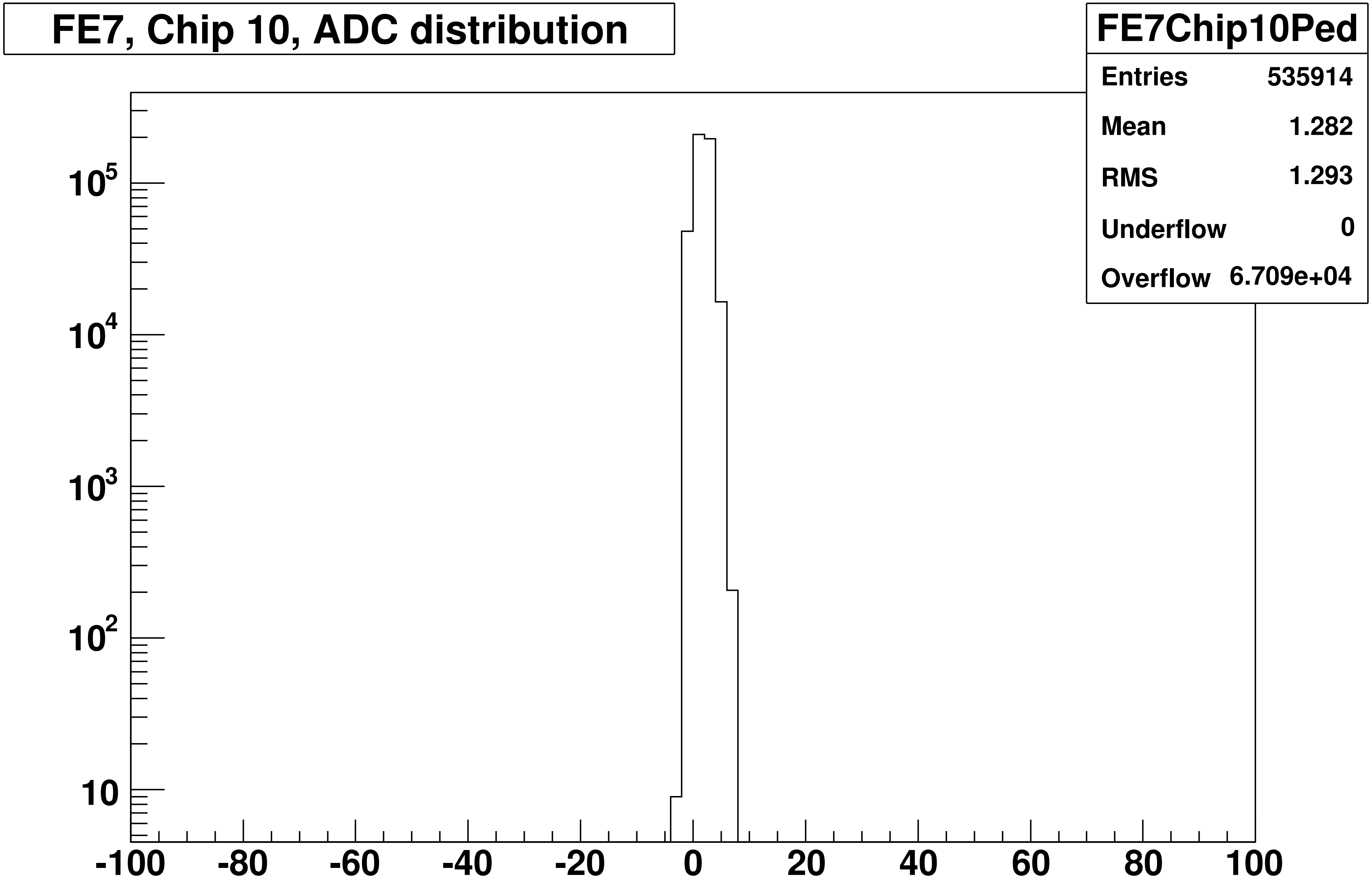

FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 535914
Mean 2387
RMS 6275
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

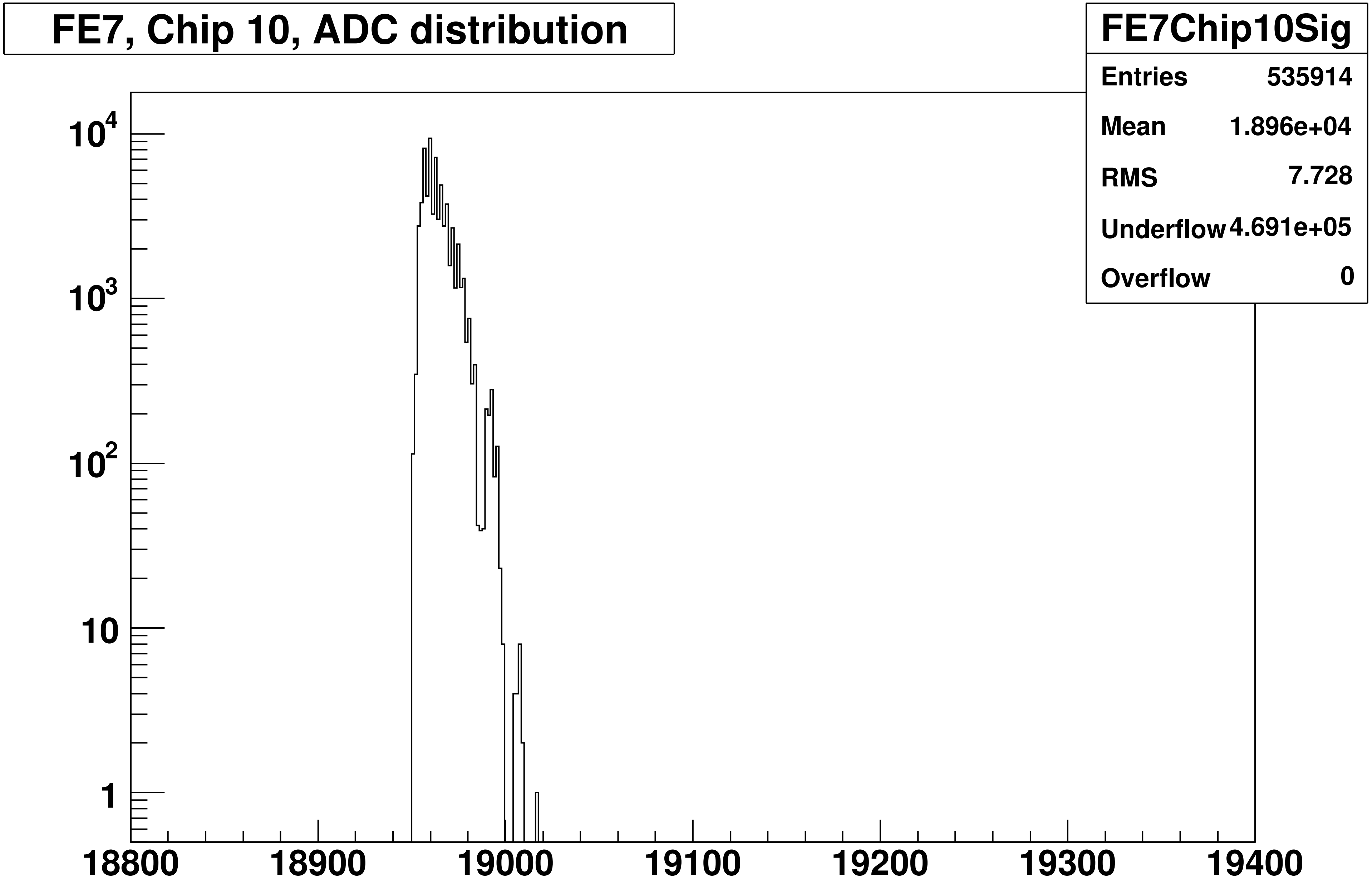

FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 535914
Mean 9.022
RMS 1.346
Underflow 0
Overflow 6.739e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 535914
Mean 1.894e+04
RMS 9.107
Underflow 4.686e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

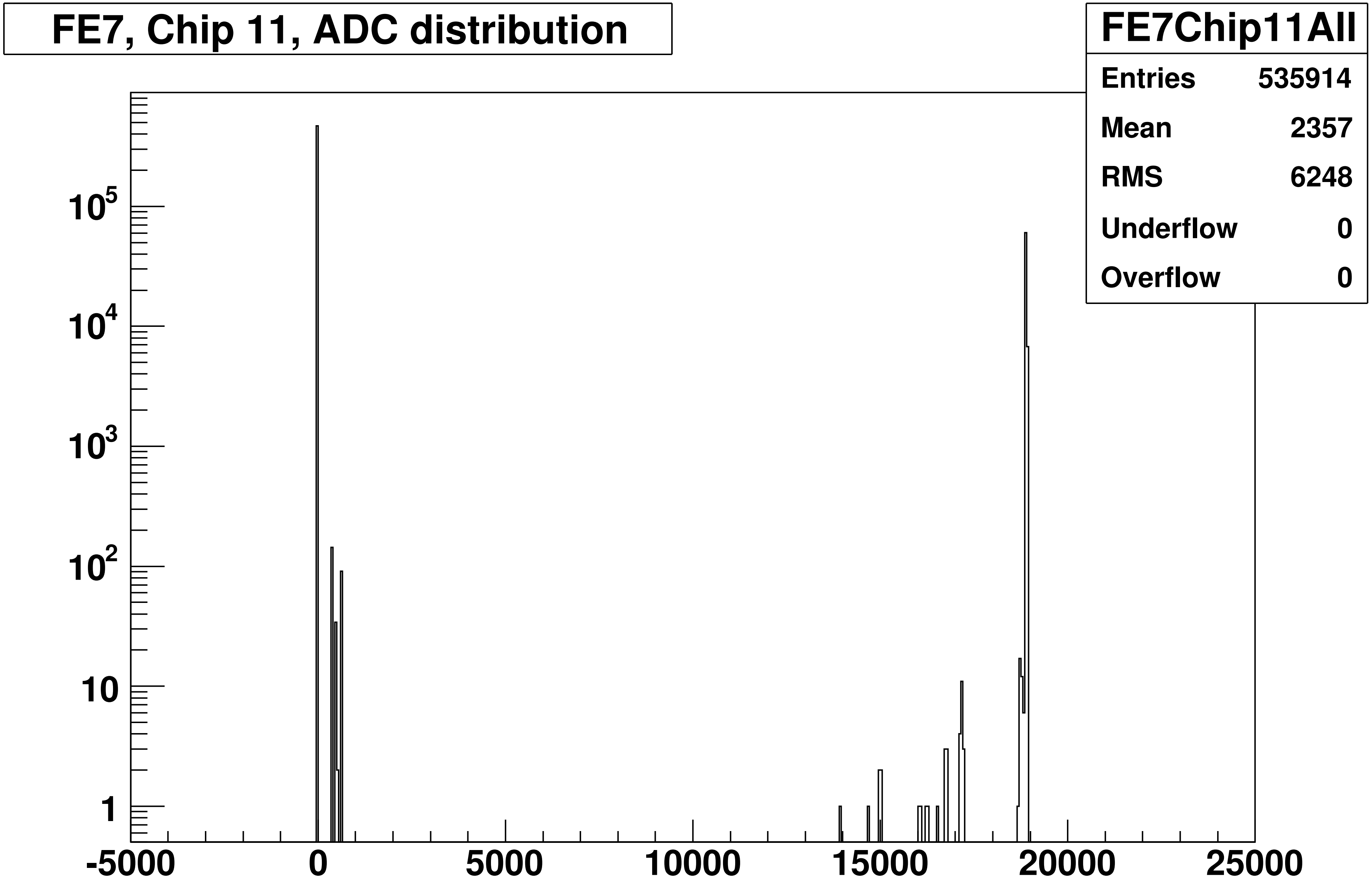


FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 535914
Mean 2367
RMS 6266
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

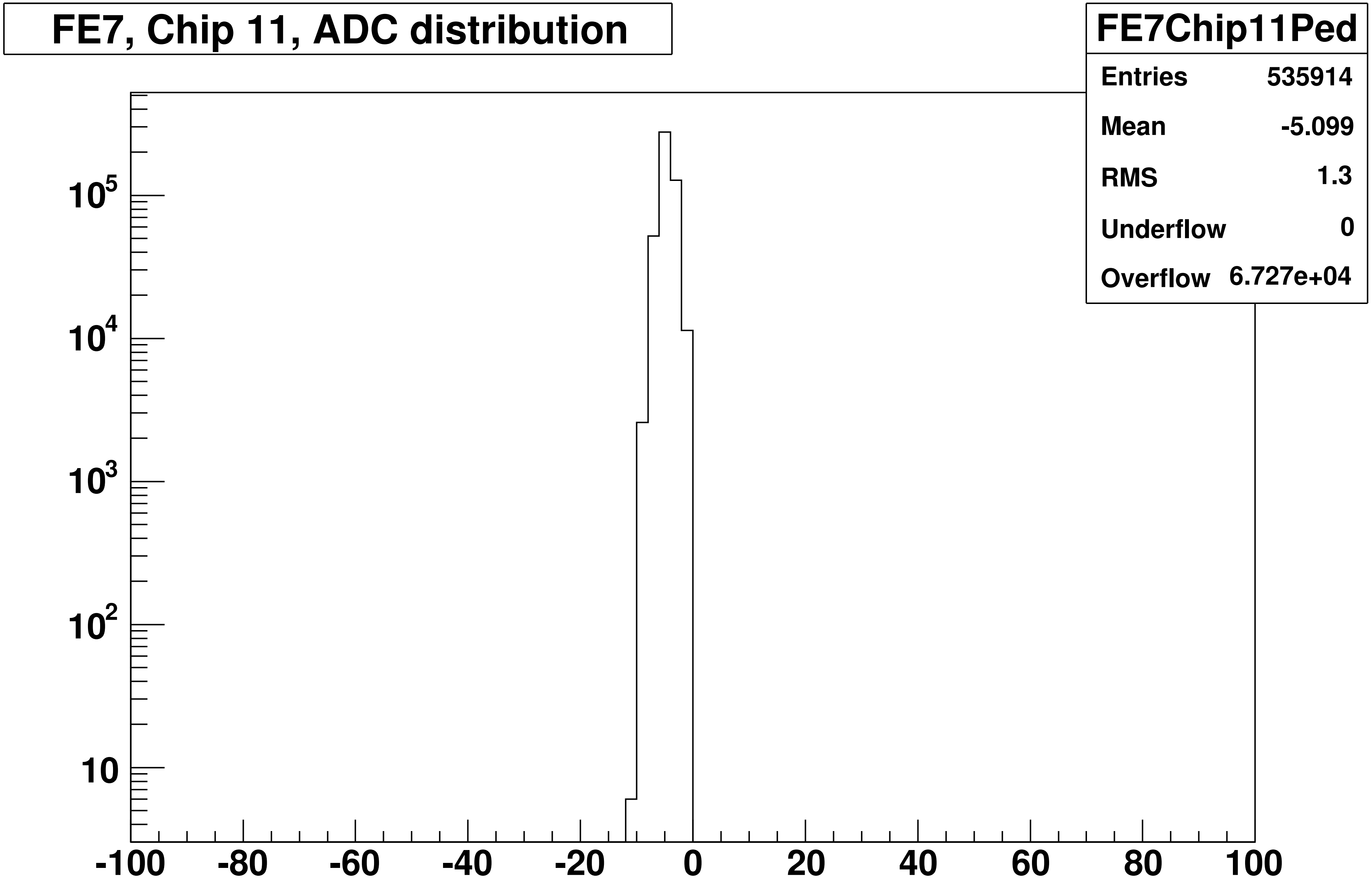

FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 535914
Mean 1.282
RMS 1.293
Underflow 0
Overflow 6.709e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



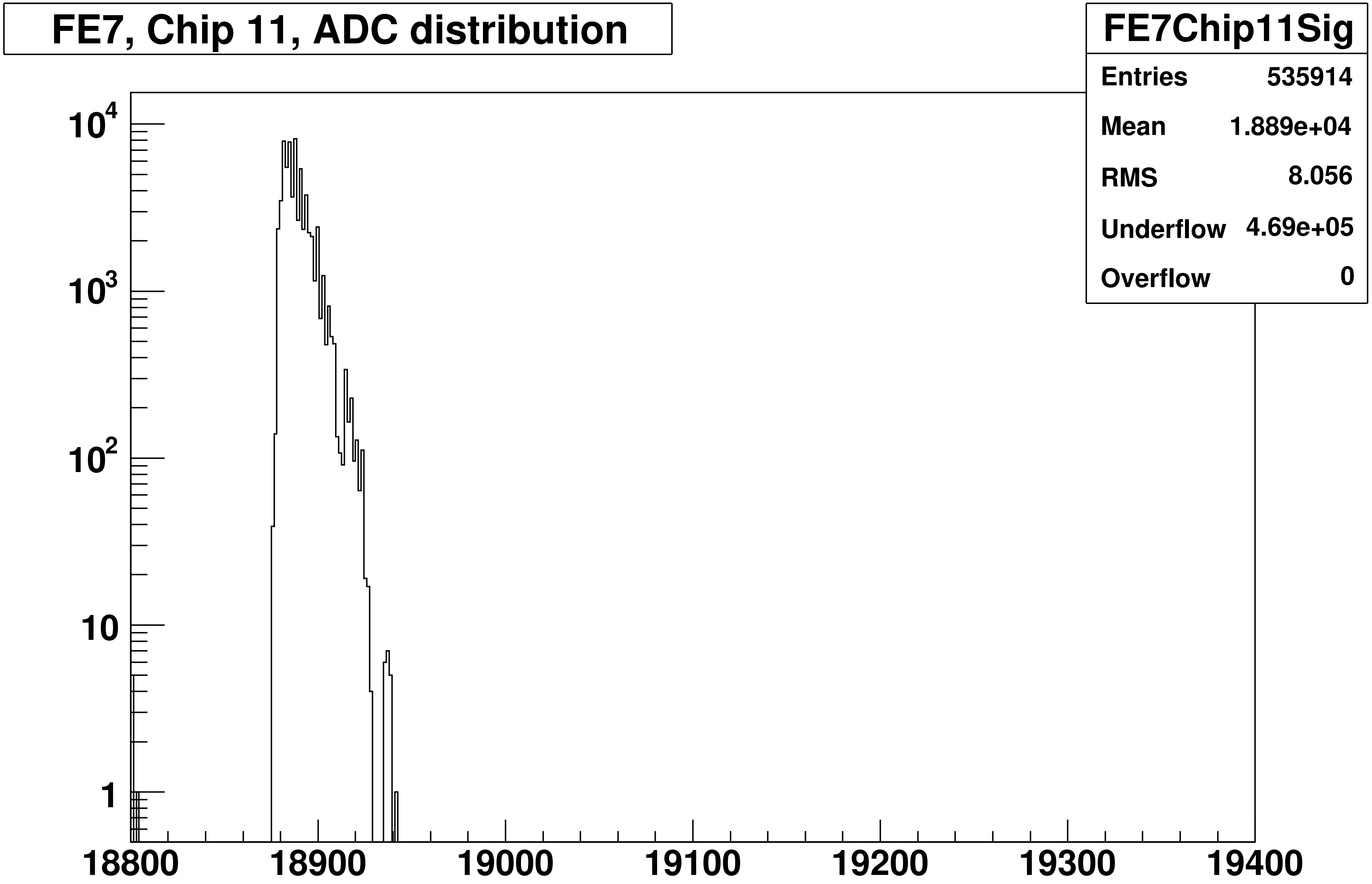
FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 535914
Mean 1.896e+04
RMS 7.728
Underflow 4.691e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



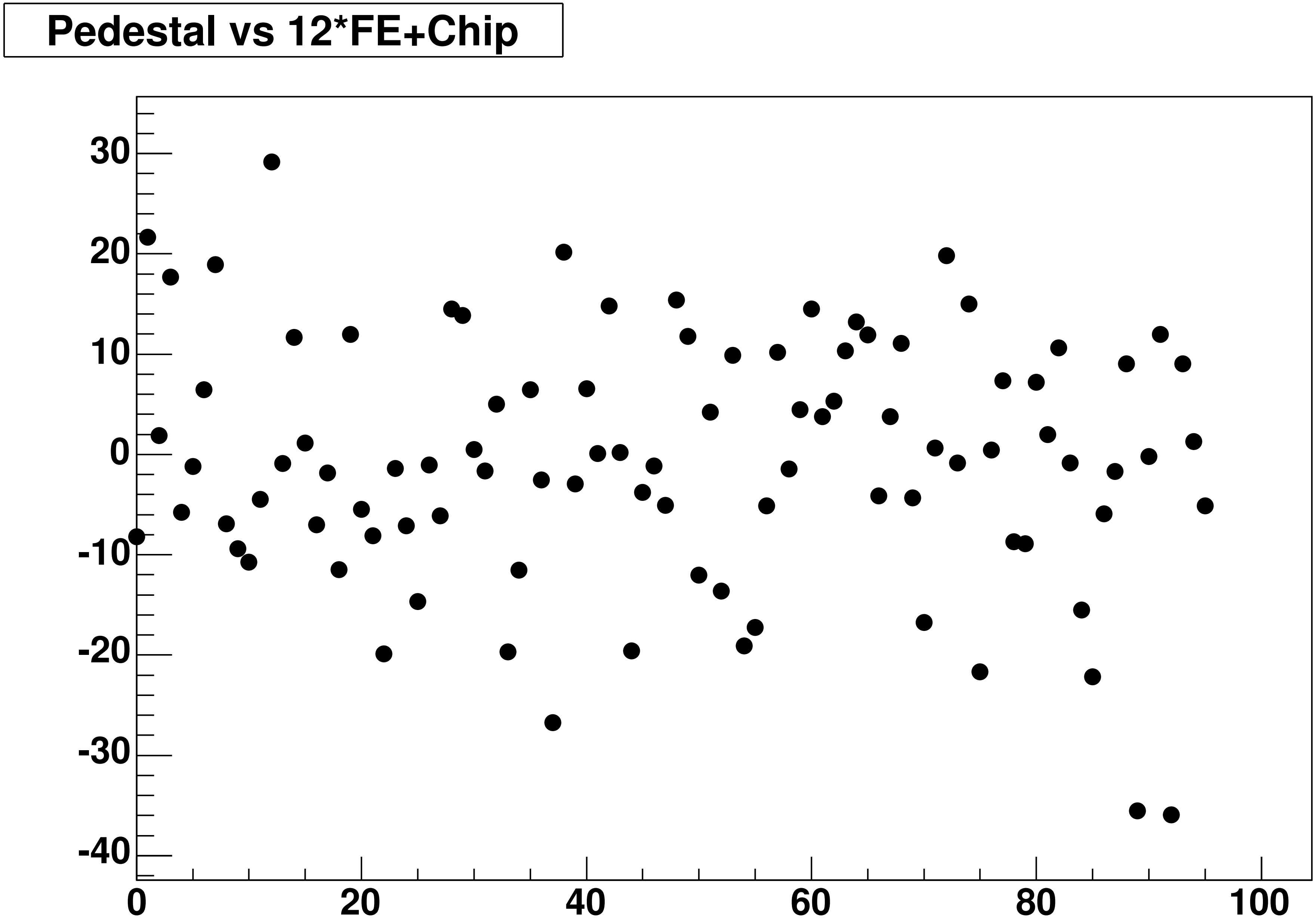
FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 535914
Mean 2357
RMS 6248
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

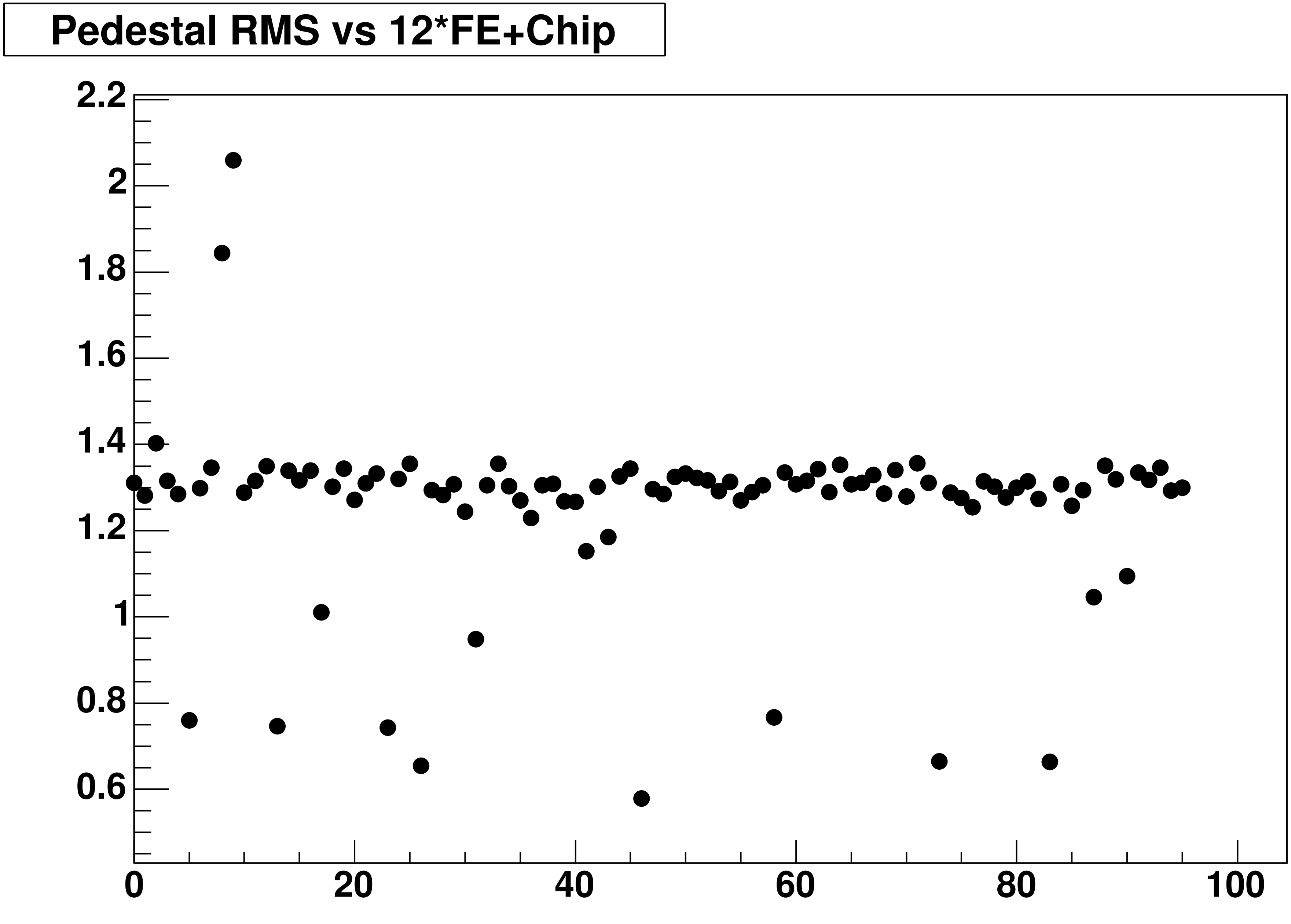


FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 535914
Mean -5.099
RMS 1.3
Underflow 0
Overflow 6.727e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

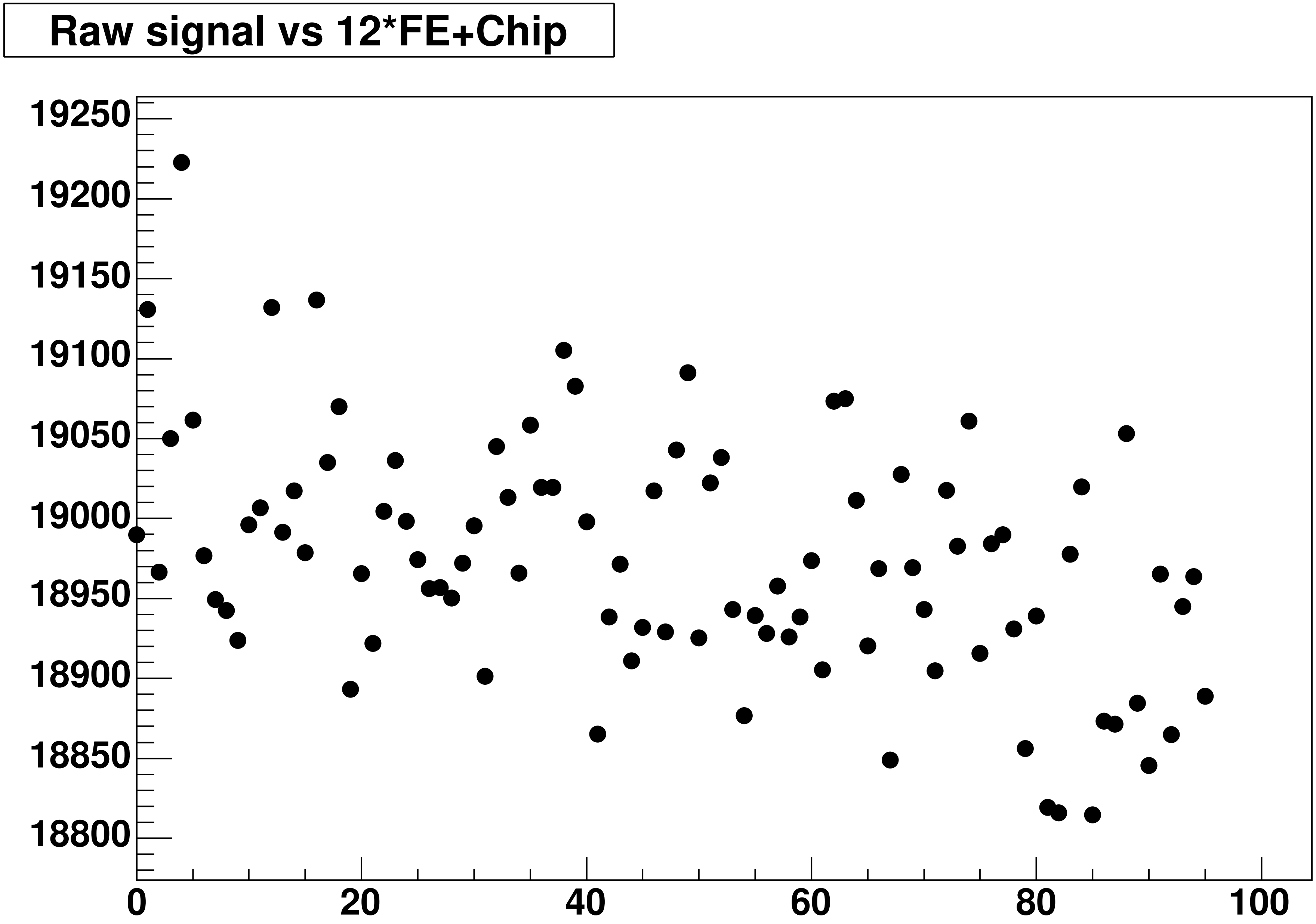


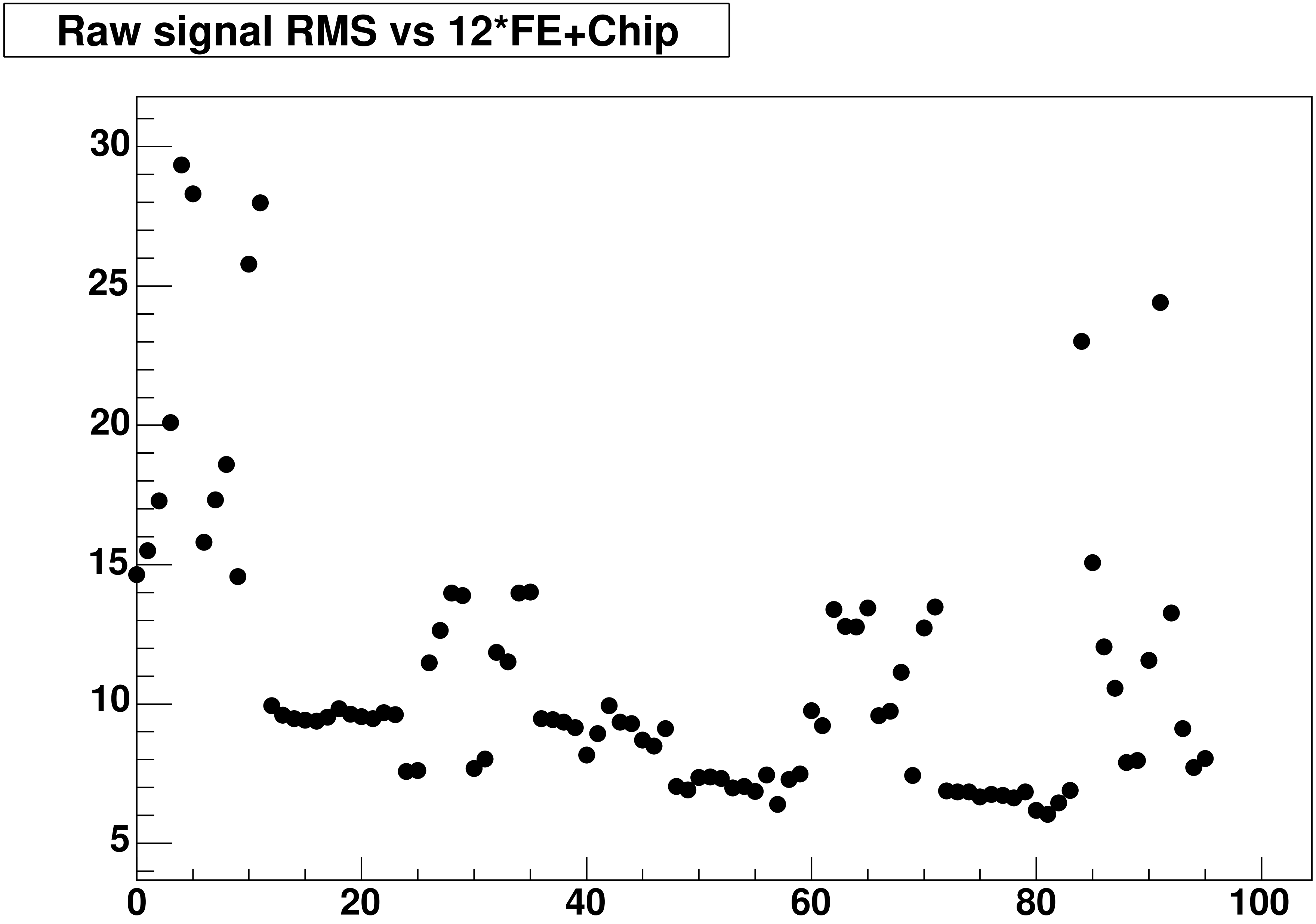
FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 535914
Mean 1.889e+04
RMS 8.056
Underflow 4.69e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

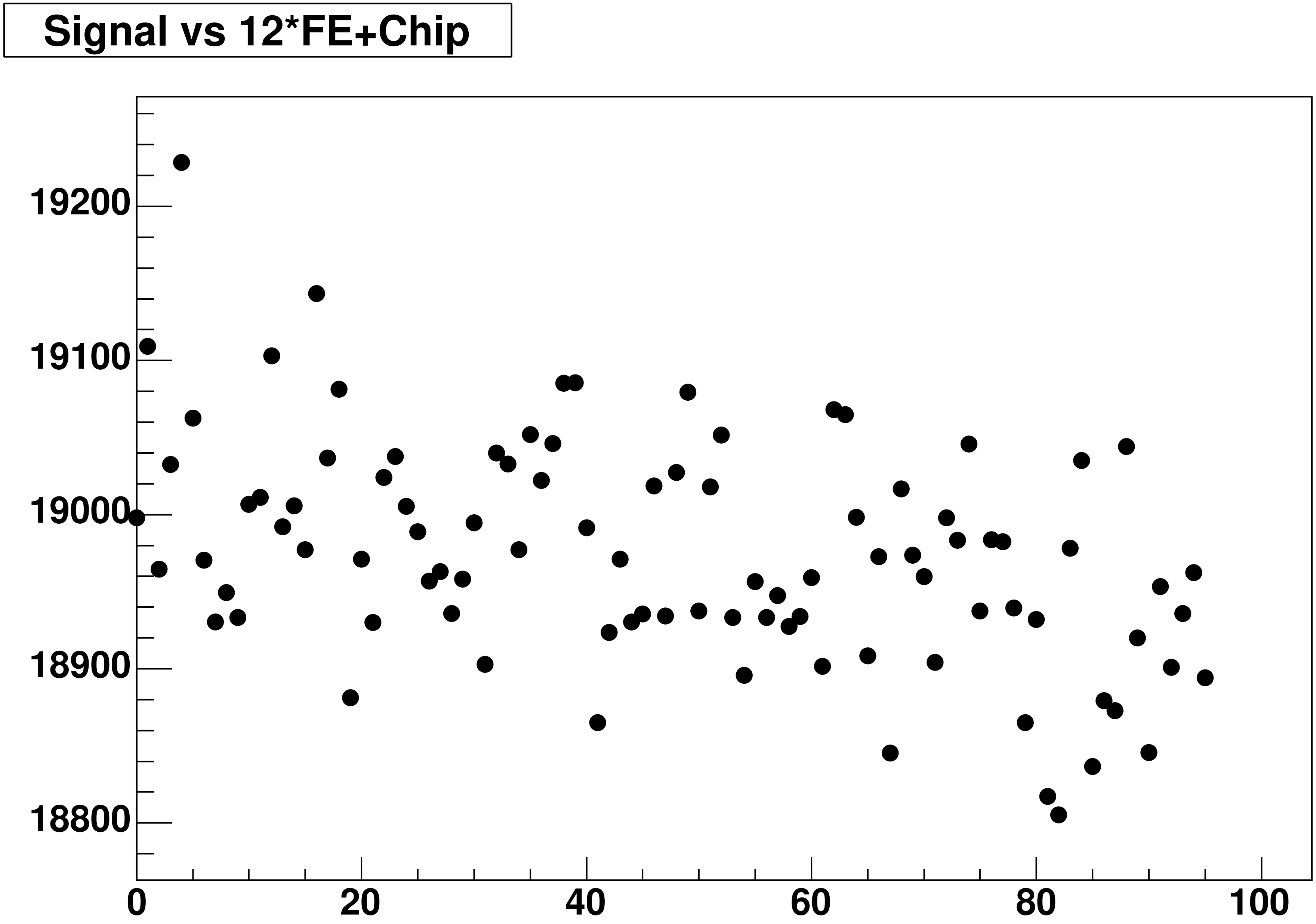

Pedestal vs 12*FE+Chip
30
20
10
0
-10
-20
-30
-40
0
20
40
60
80
100



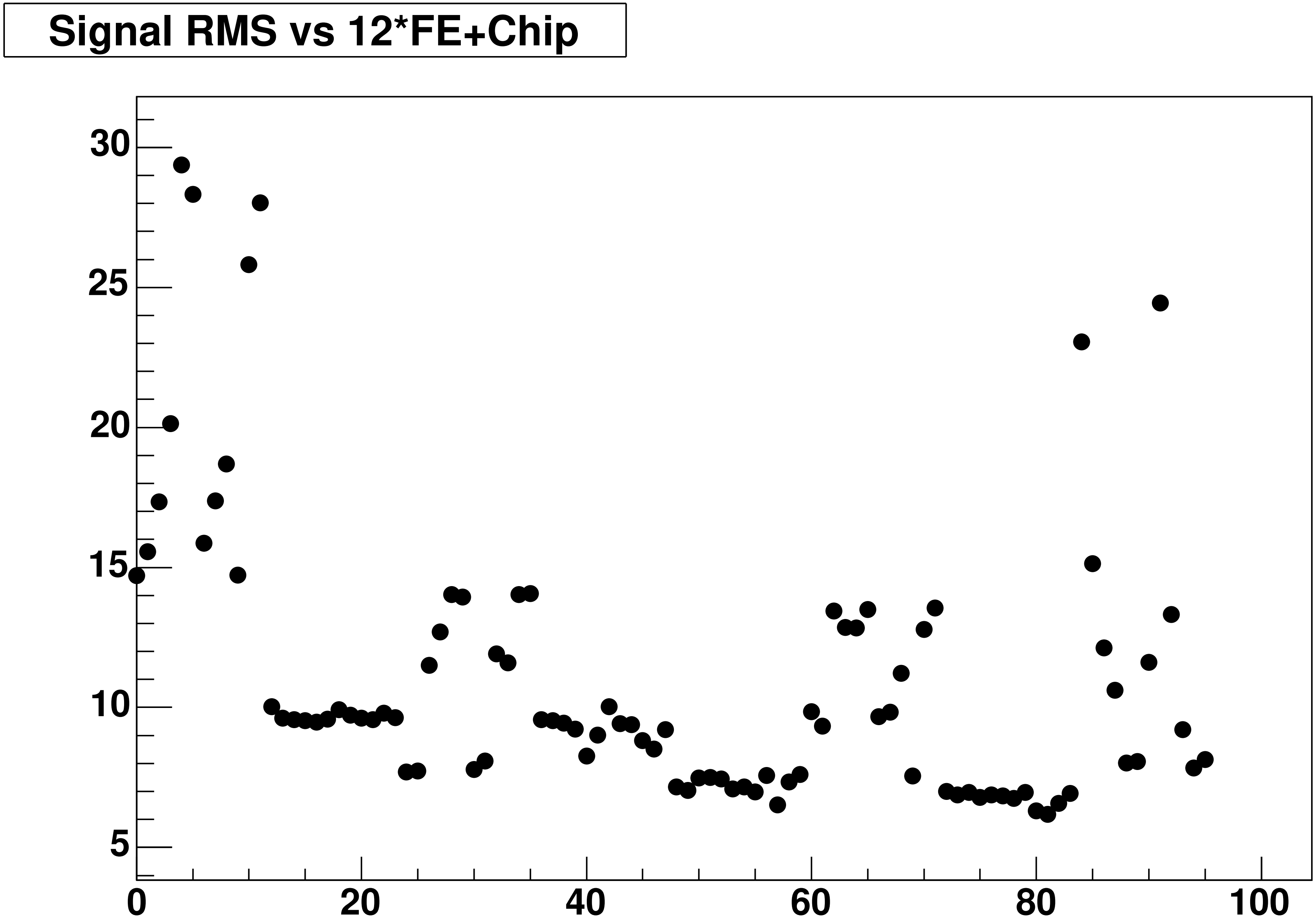
Pedestal RMS vs 12*FE+Chip
2.2
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0
20
40
60
80
100


Raw signal vs 12*FE+Chip
19250
19200
19150
19100
19050
19000
18950
18900
18850
18800
0
20
40
60
80
100


Raw signal RMS vs 12*FE+Chip
30
25
20
15
10
5
0
20
40
60
80
100



Signal vs 12*FE+Chip
19200
19100
19000
18900
18800
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
30
25
20
15
10
5
0
20
40
60
80
100