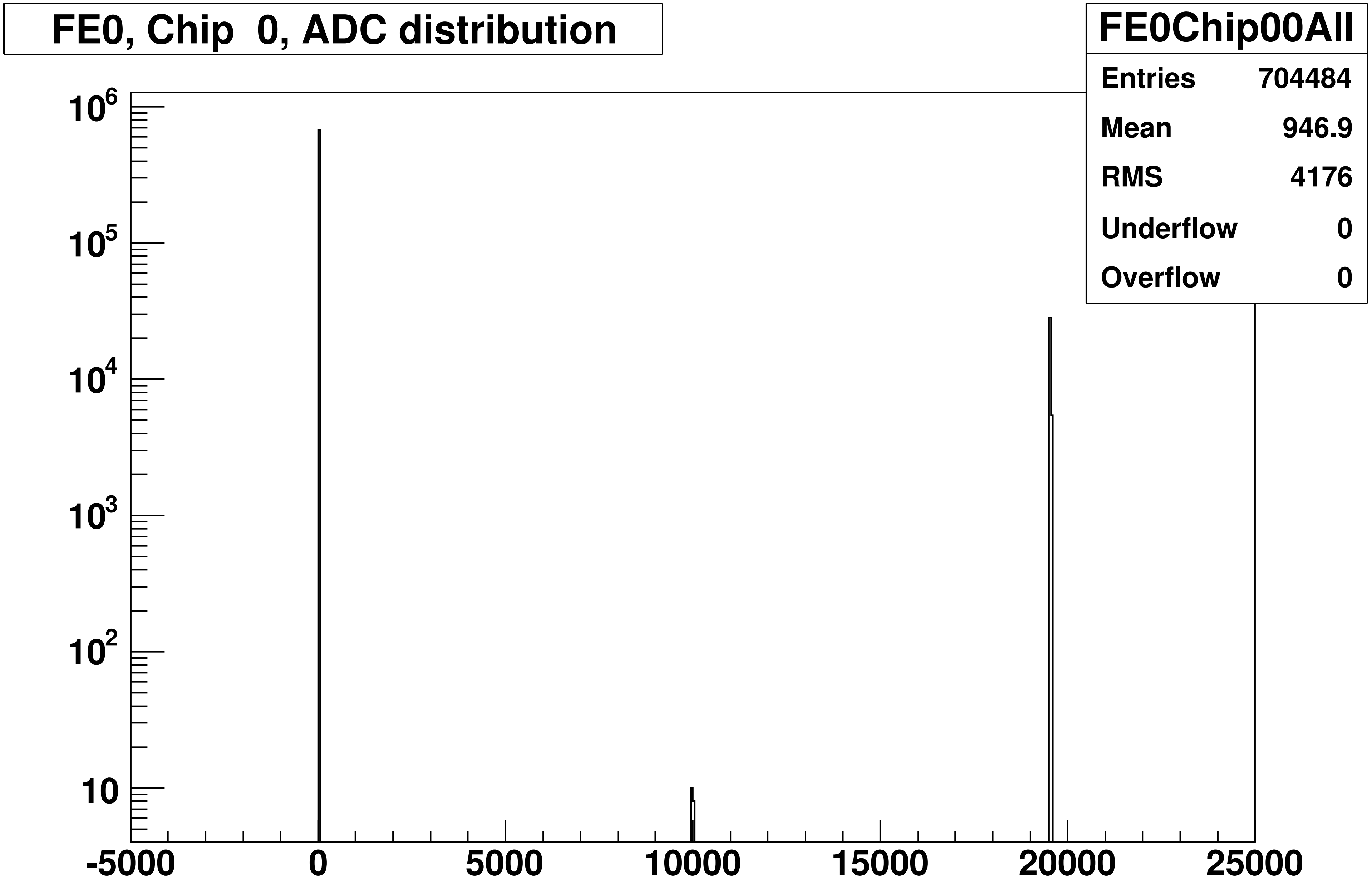
FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 704484
Mean 946.9
RMS 4176
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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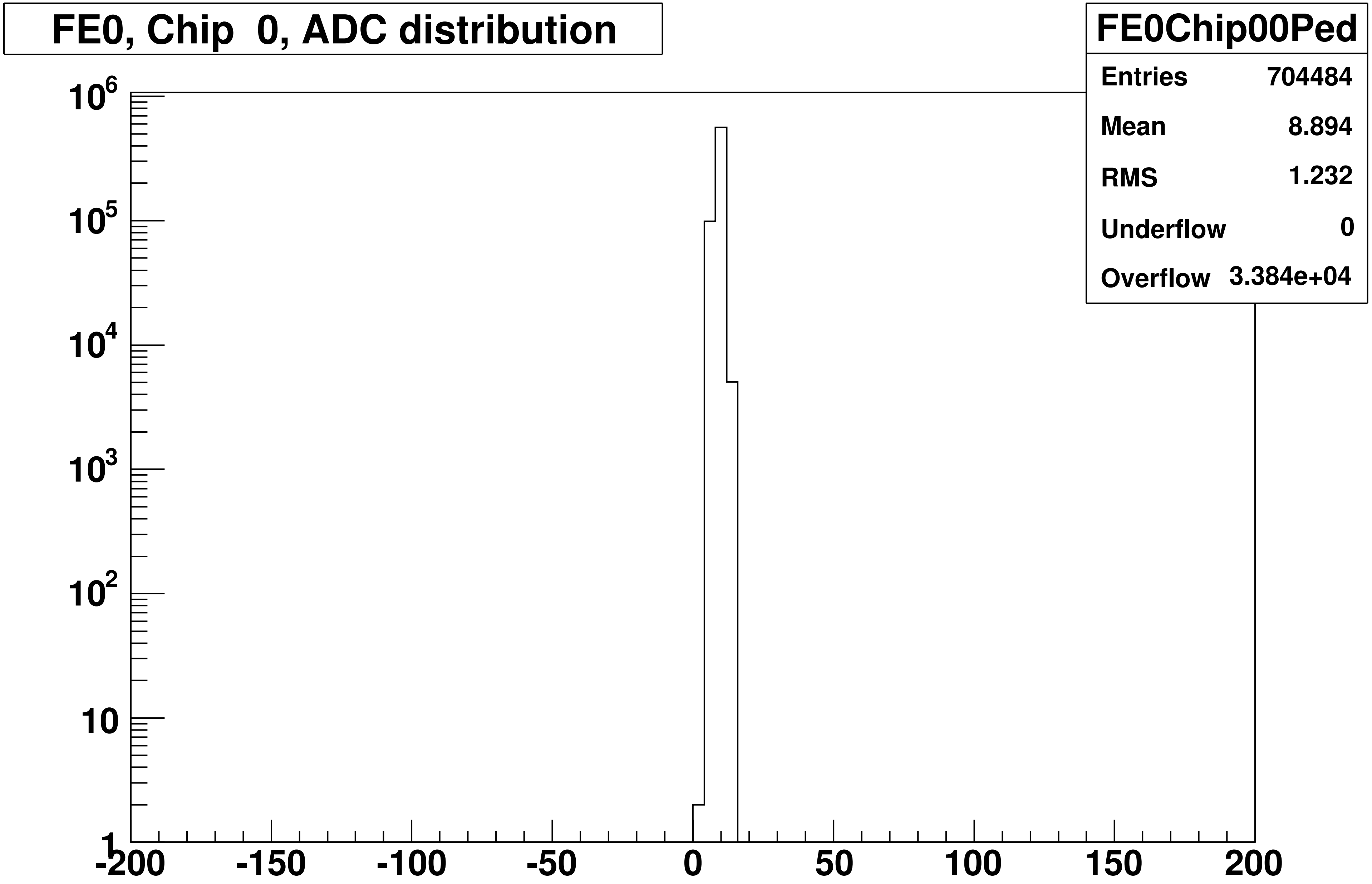
25000


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 704484
Mean 8.894
RMS 1.232
Underflow 0
Overflow 3.384e+04
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10^5
10^4
10^3
10^2
10
1
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-100
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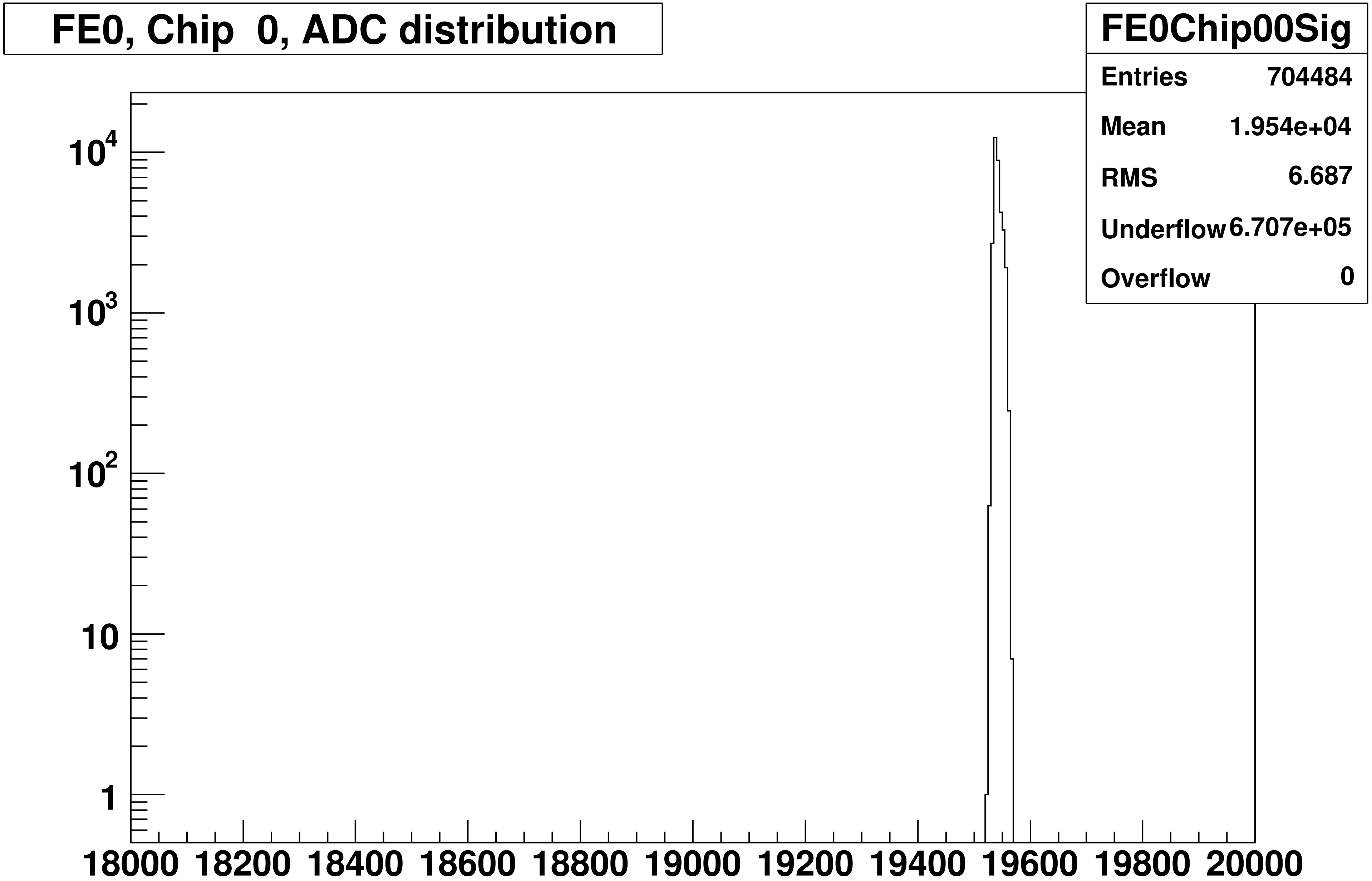
100
150
200


FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 704484
Mean 1.954e+04
RMS 6.687
Underflow 6.707e+05
Overflow 0
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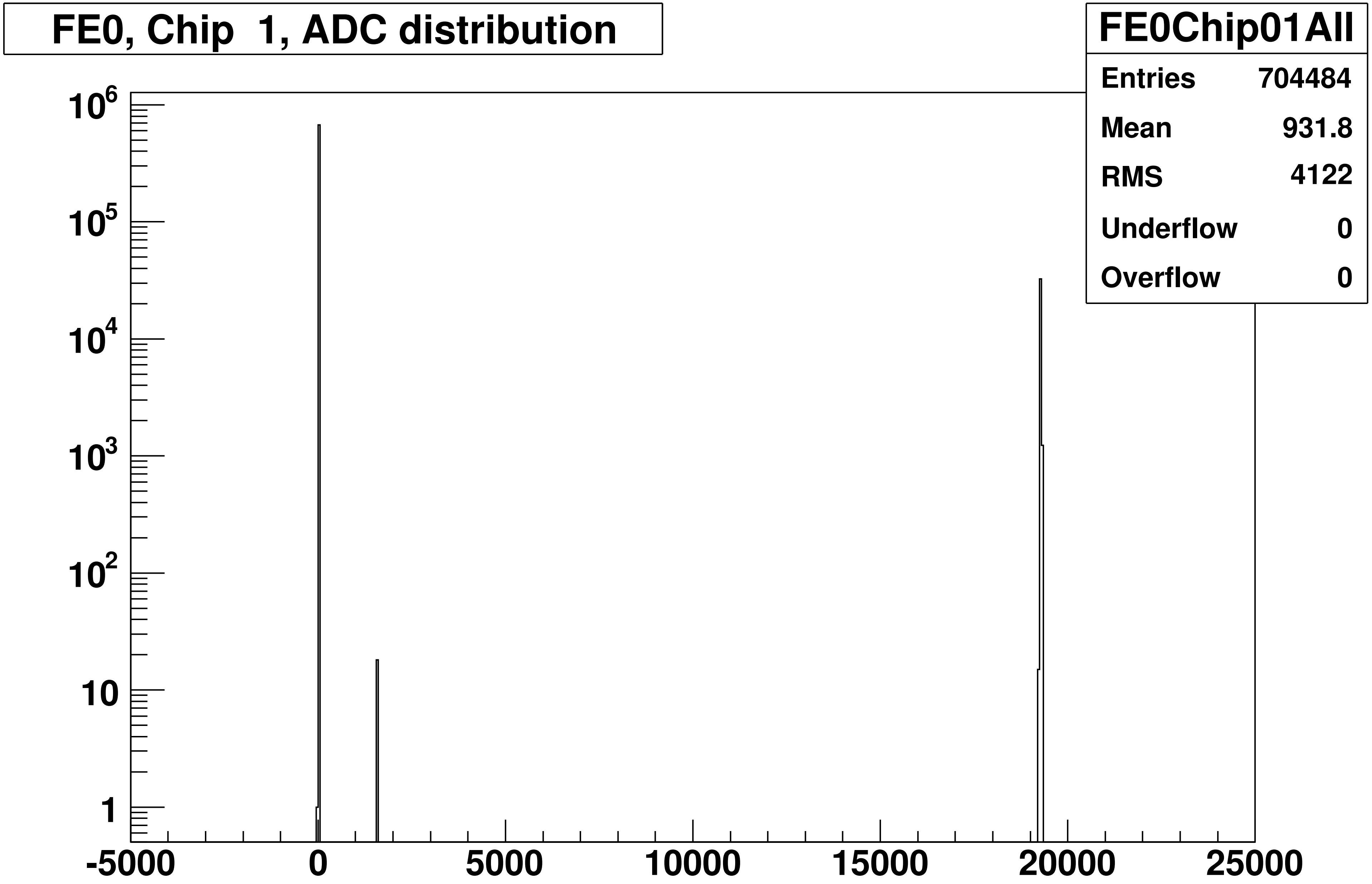
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 704484
Mean 931.8
RMS 4122
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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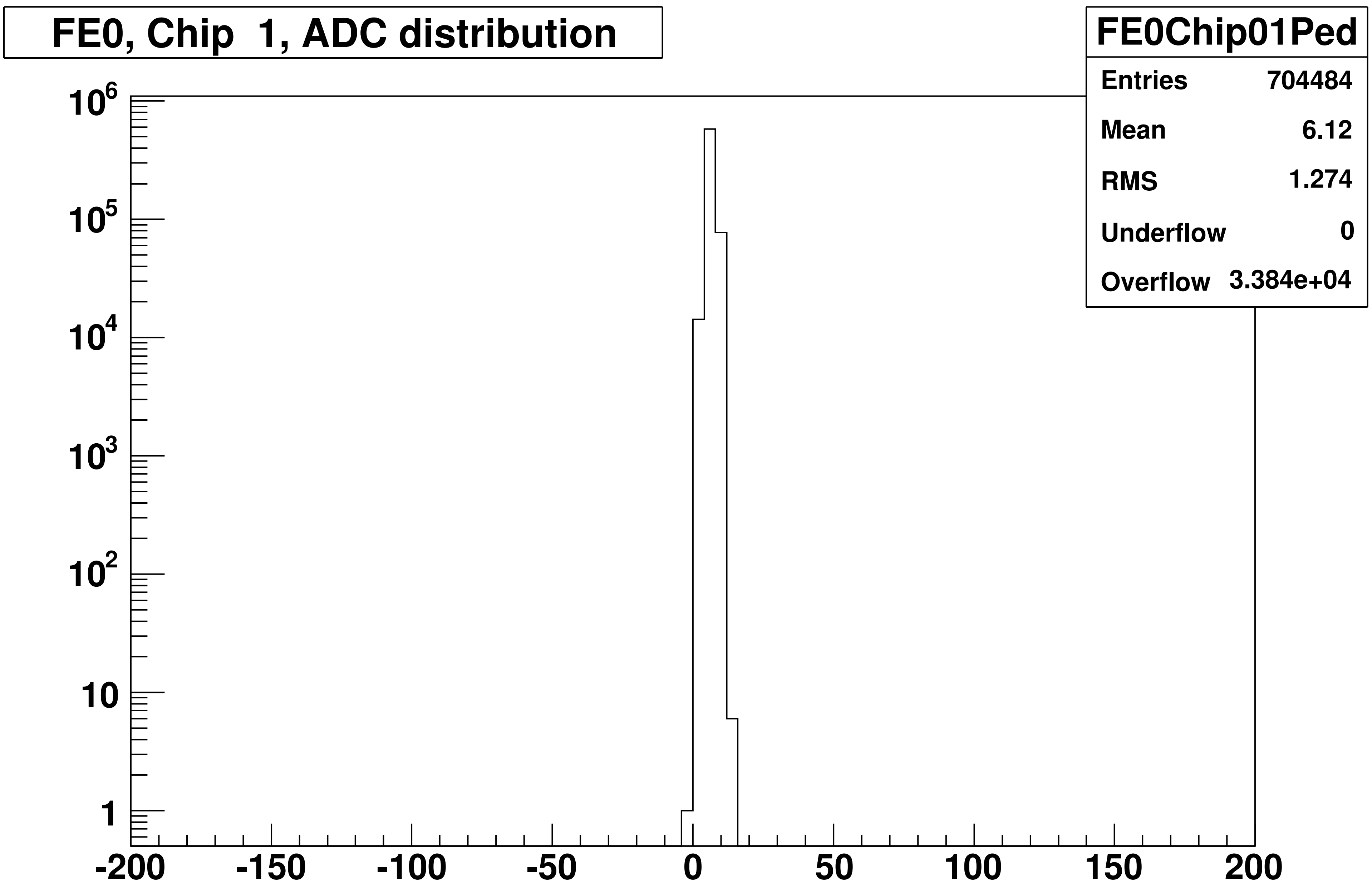
FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 704484
Mean 6.12
RMS 1.274
Underflow 0
Overflow 3.384e+04
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10^3
10^2
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150
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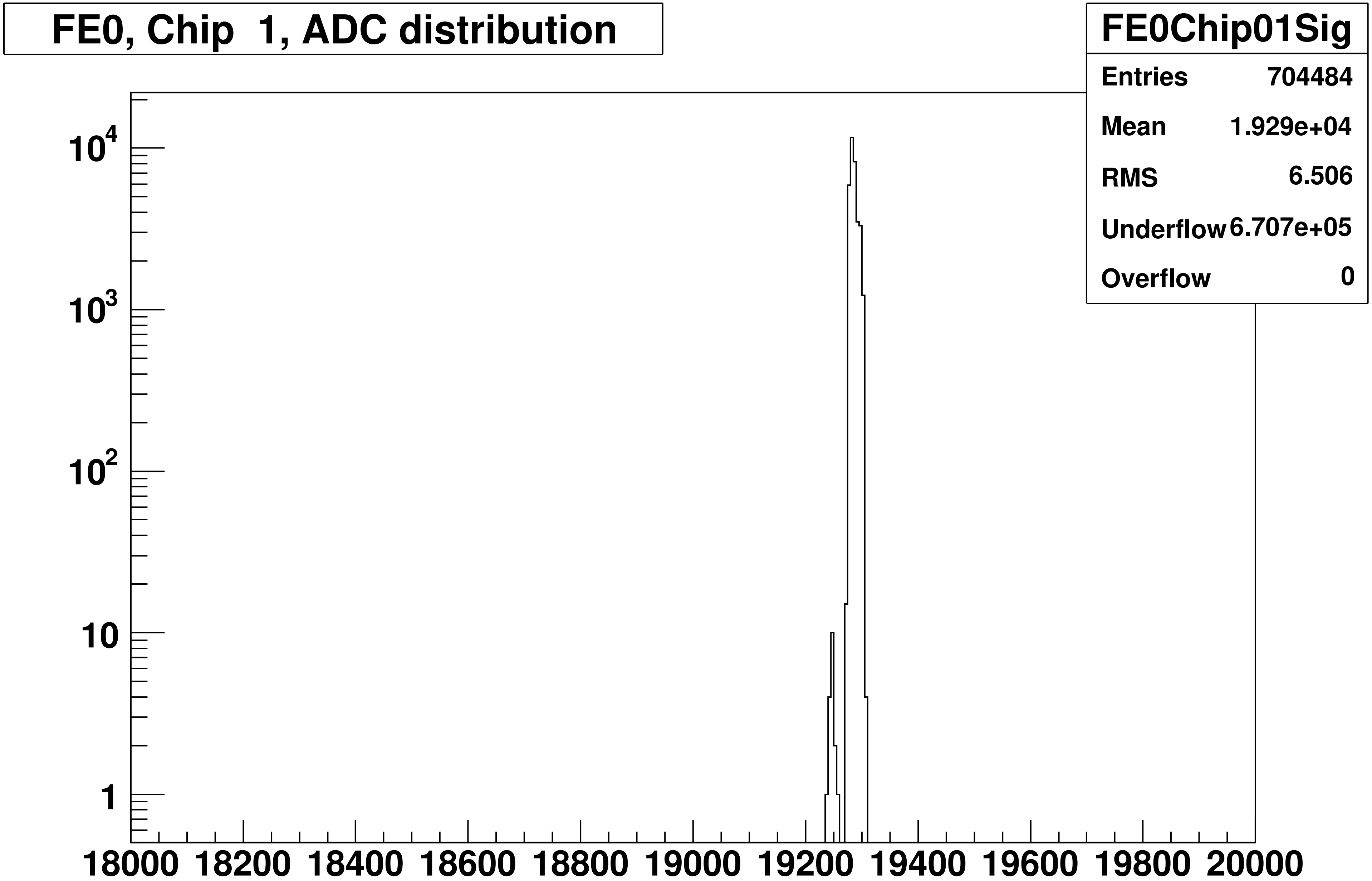
FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 704484
Mean 1.929e+04
RMS 6.506
Underflow 6.707e+05
Overflow 0
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10^3
10^2
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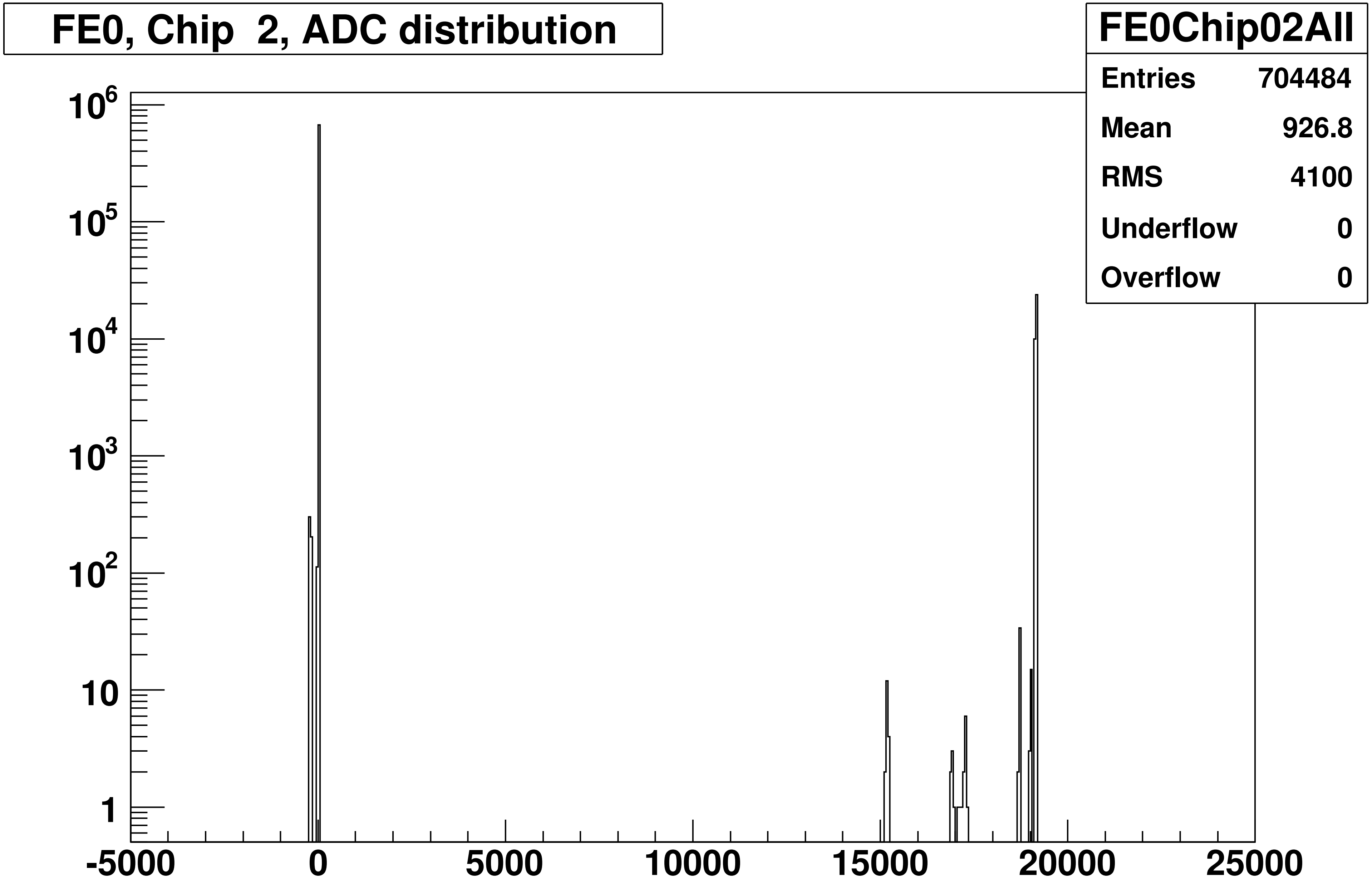
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 704484
Mean 926.8
RMS 4100
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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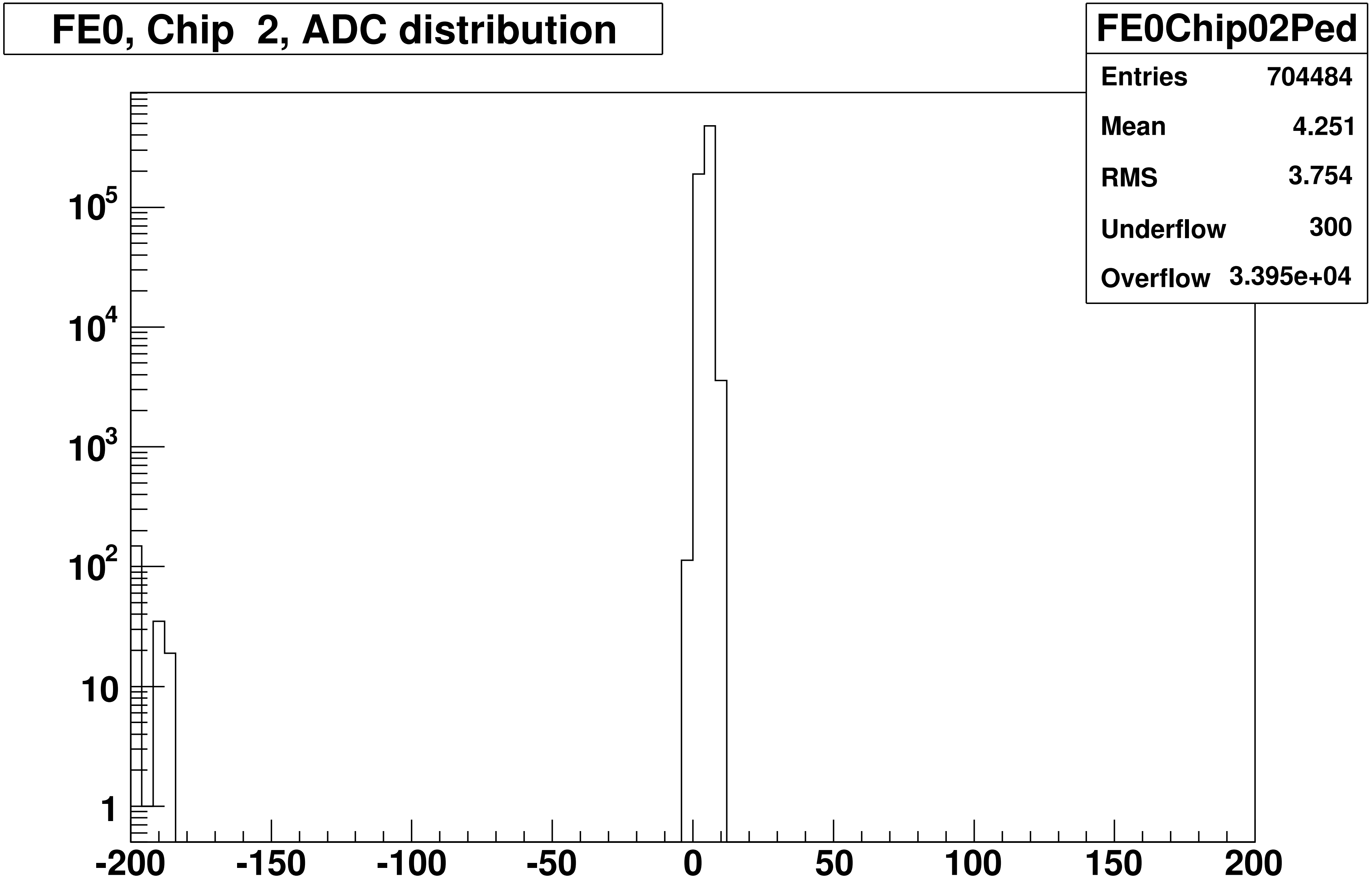
20000
25000



FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 704484
Mean 4.251
RMS 3.754
Underflow 300
Overflow 3.395e+04
10^5
10^4
10^3
10^2
10
1
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-100
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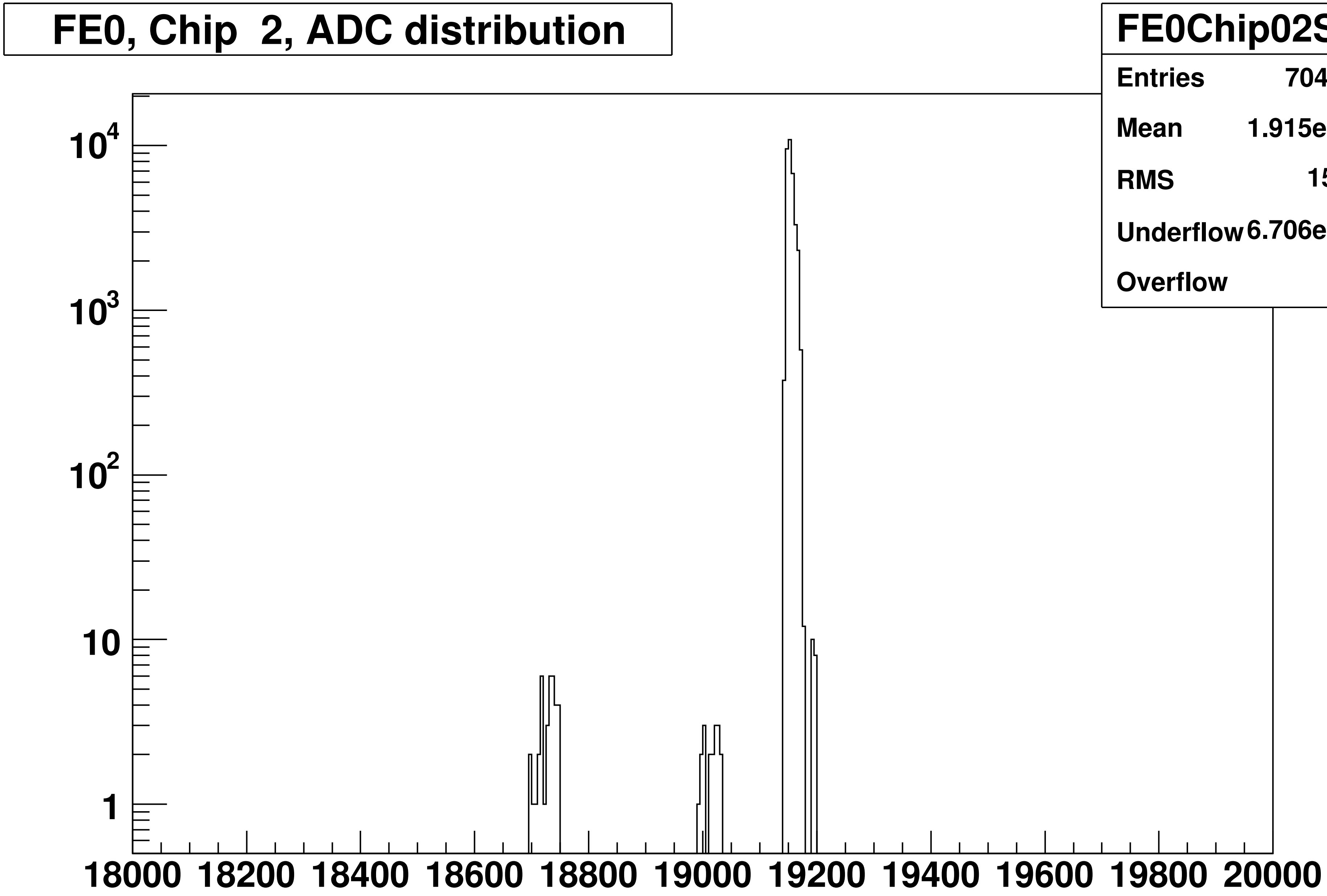
100
150
200


FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 704484
Mean 1.915e+04
RMS 15.64
Underflow 6.706e+05
Overflow 0
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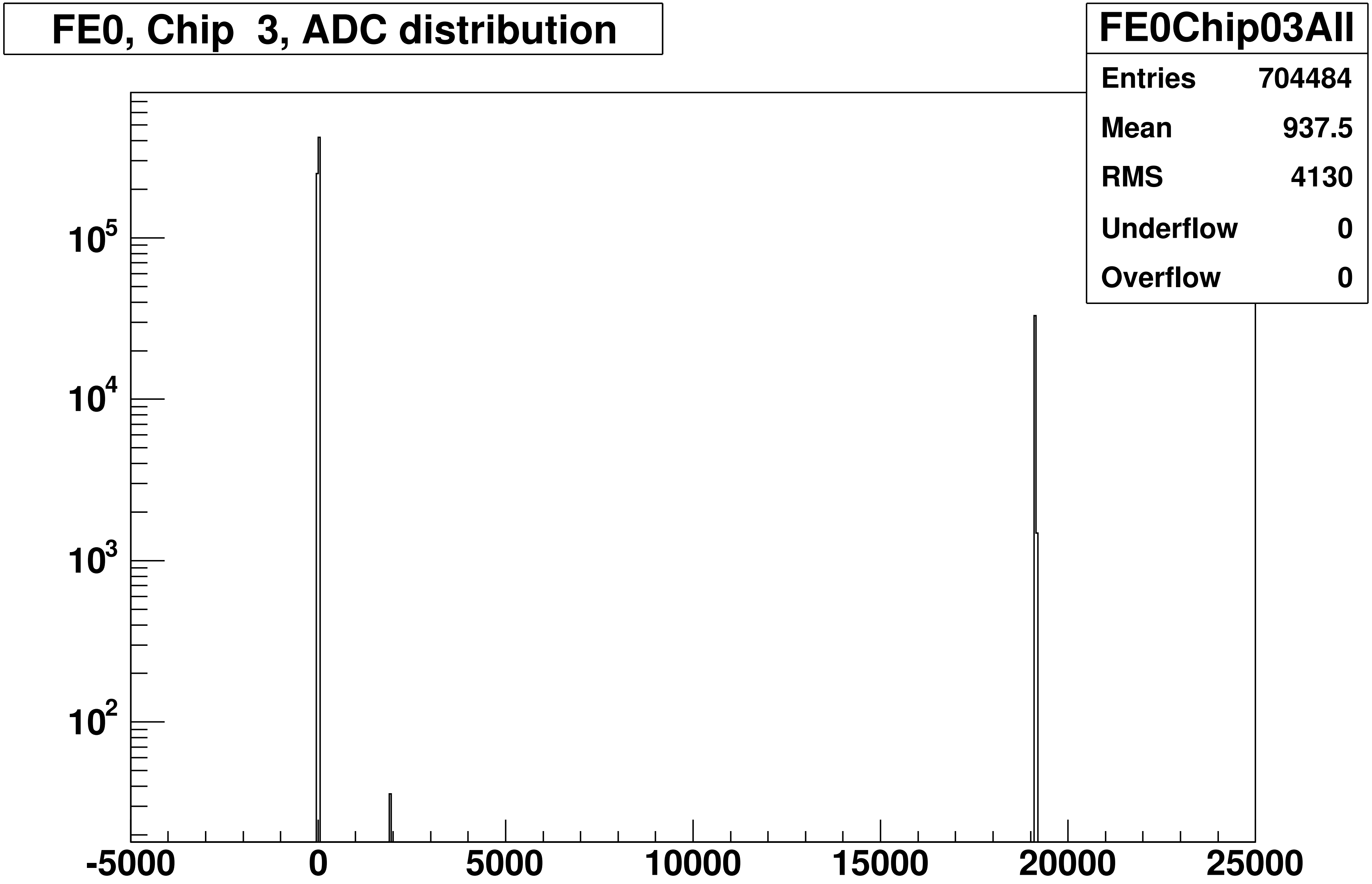
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 704484
Mean 937.5
RMS 4130
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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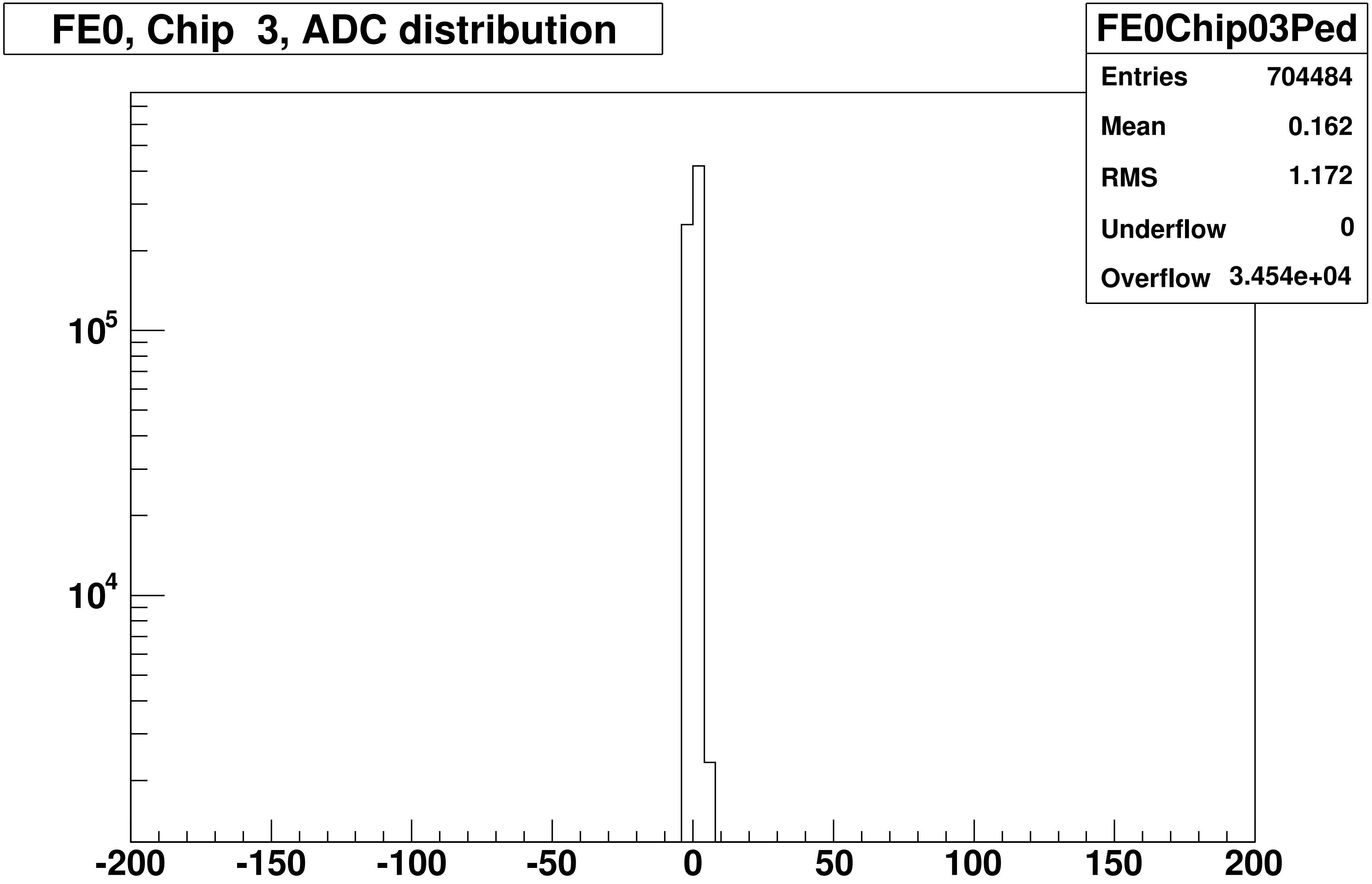
15000
20000
25000


FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 704484
Mean 0.162
RMS 1.172
Underflow 0
Overflow 3.454e+04
10^5
10^4
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-100
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0
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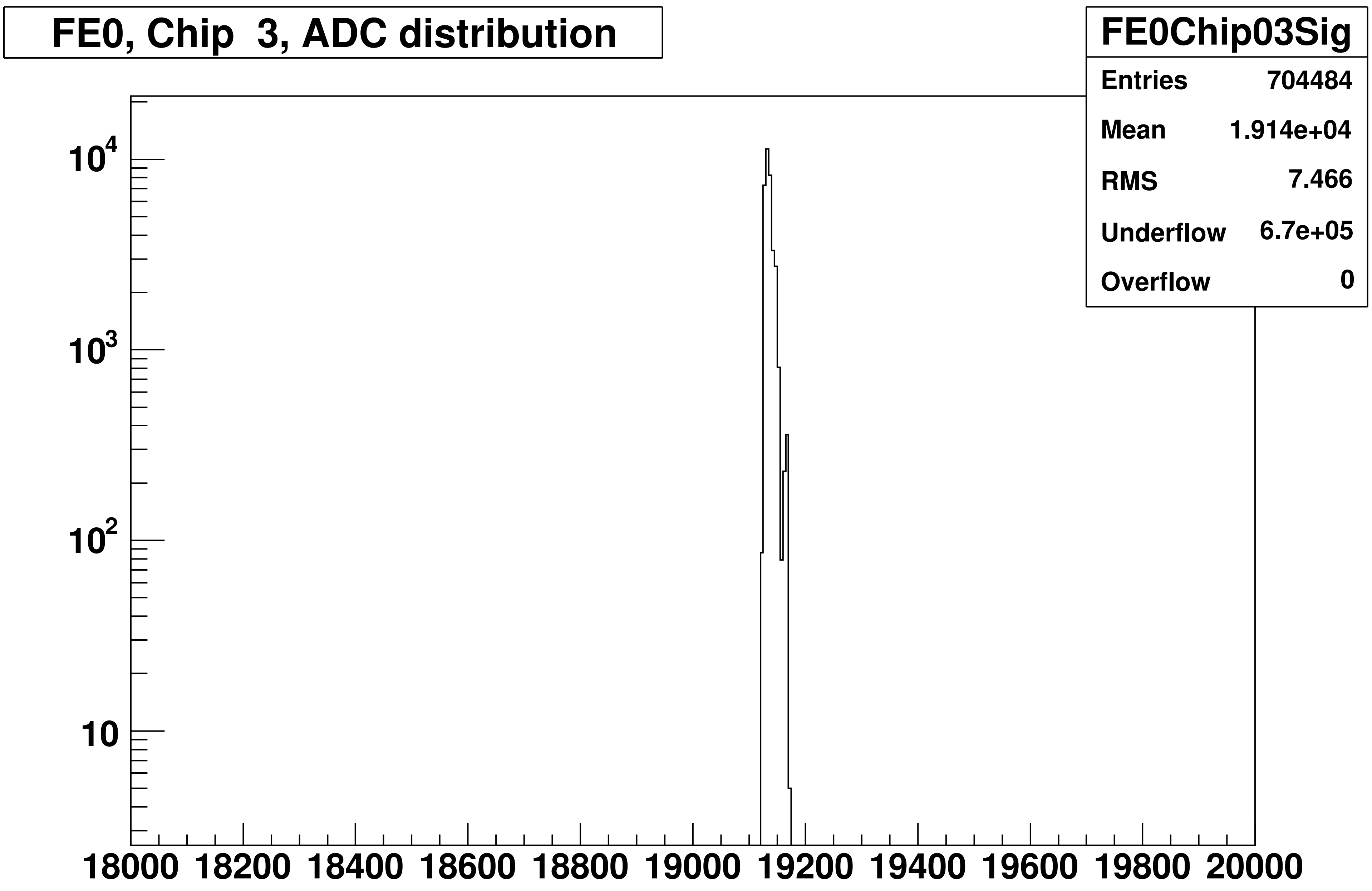
100
150
200


FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 704484
Mean 1.914e+04
RMS 7.466
Underflow 6.7e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
18800
19000
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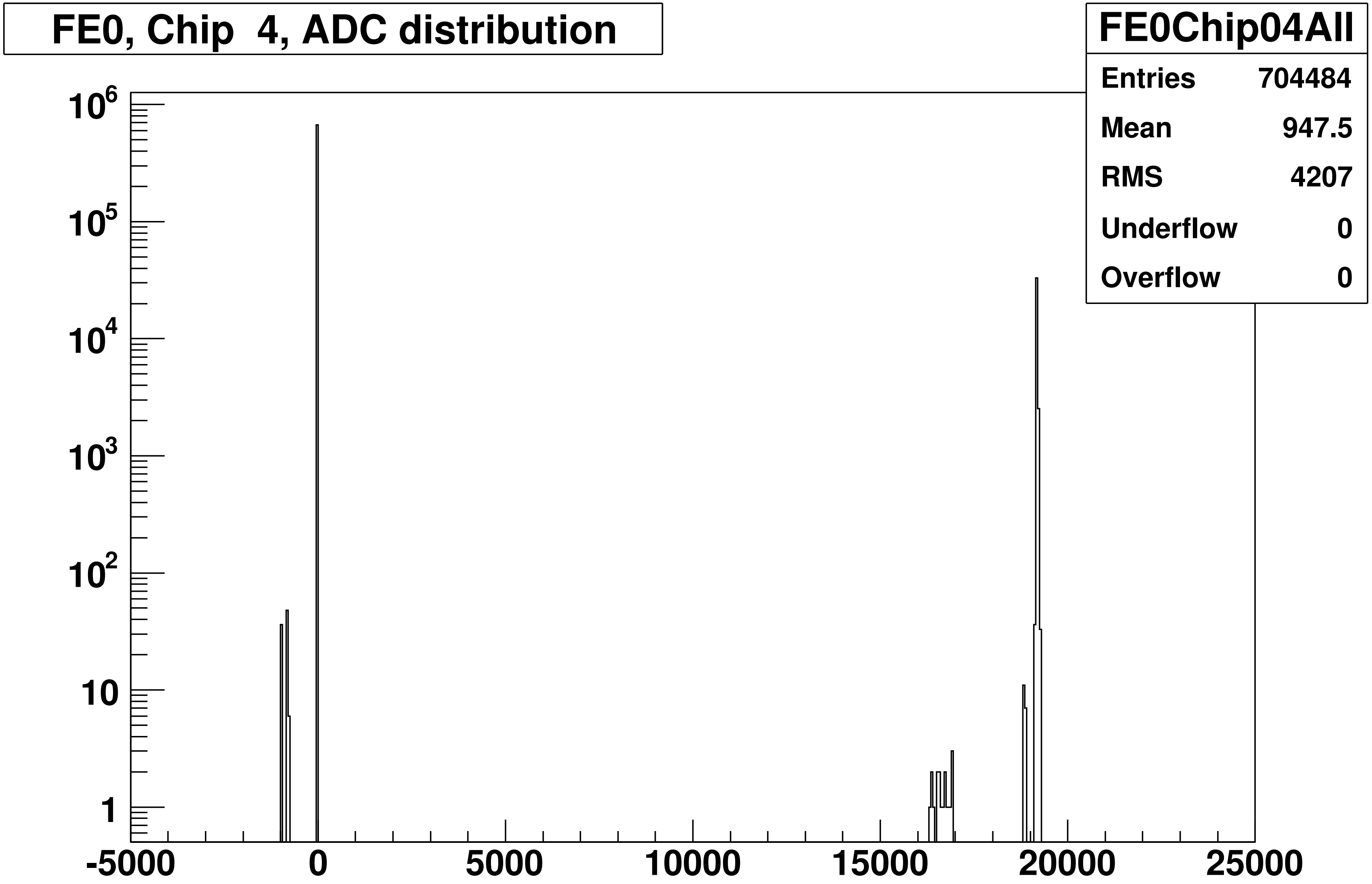
19400
19600
19800
20000


FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 704484
Mean 947.5
RMS 4207
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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20000
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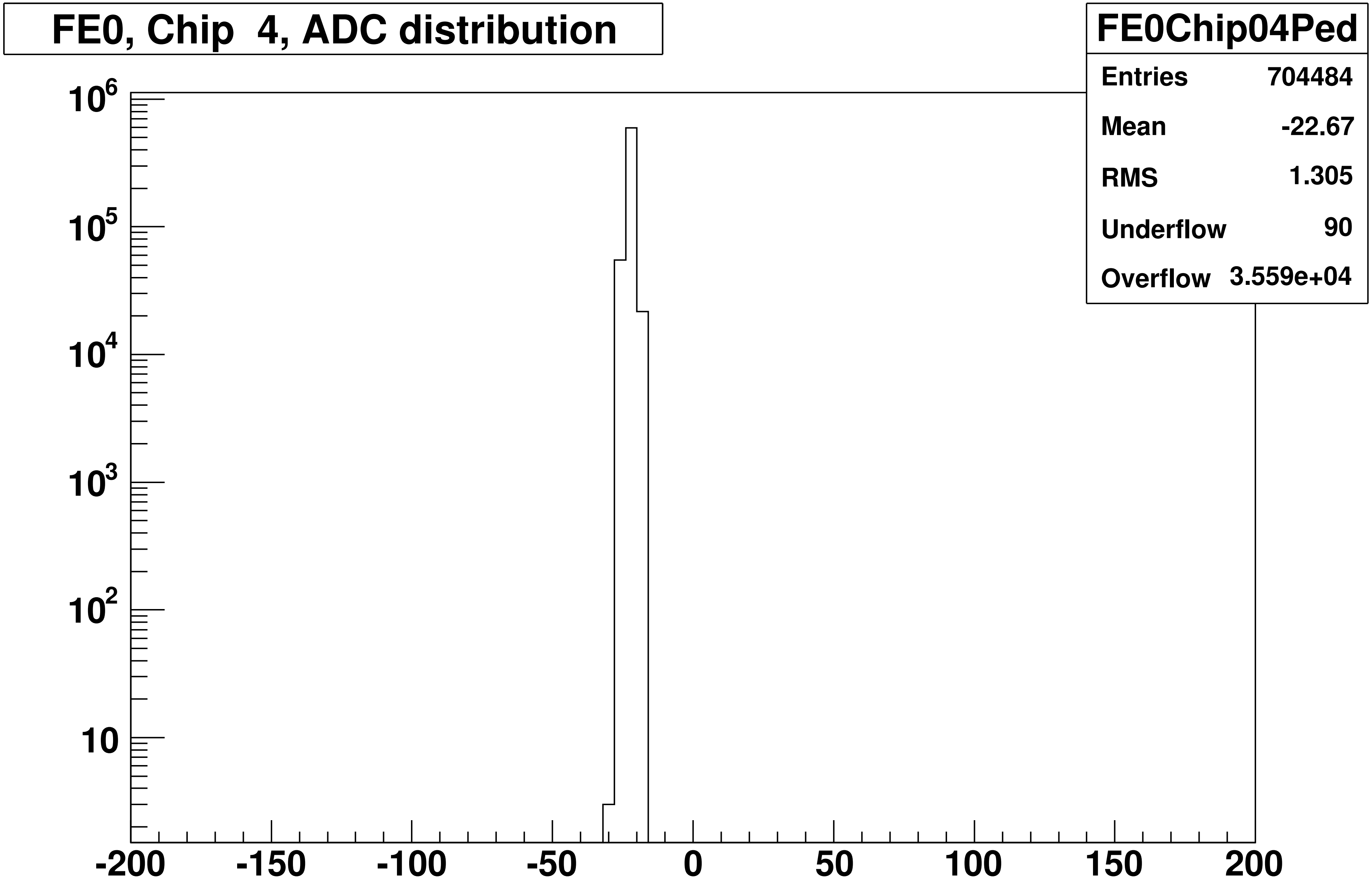
FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 704484
Mean -22.67
RMS 1.305
Underflow 90
Overflow 3.559e+04
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10^5
10^4
10^3
10^2
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-100
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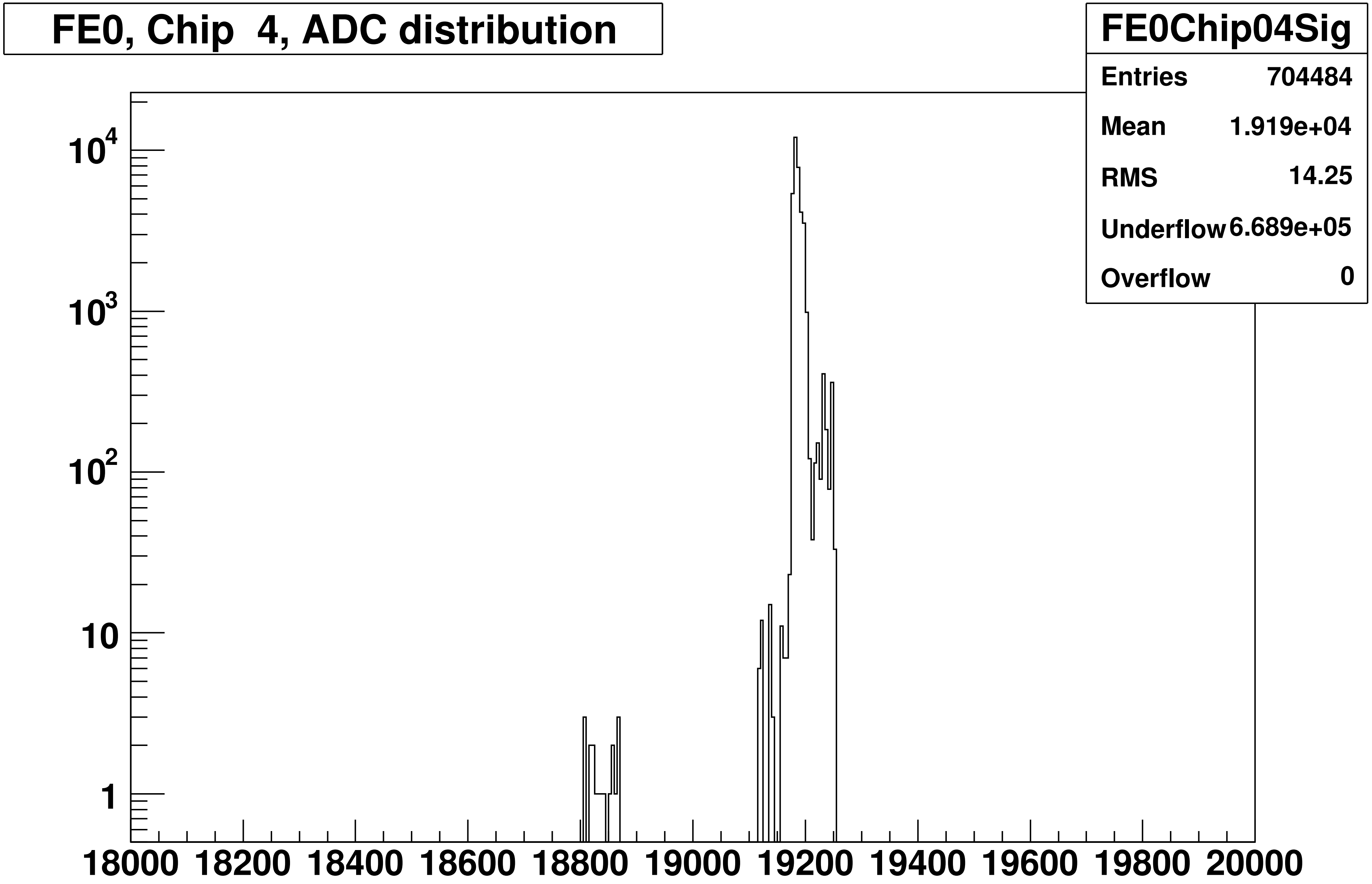
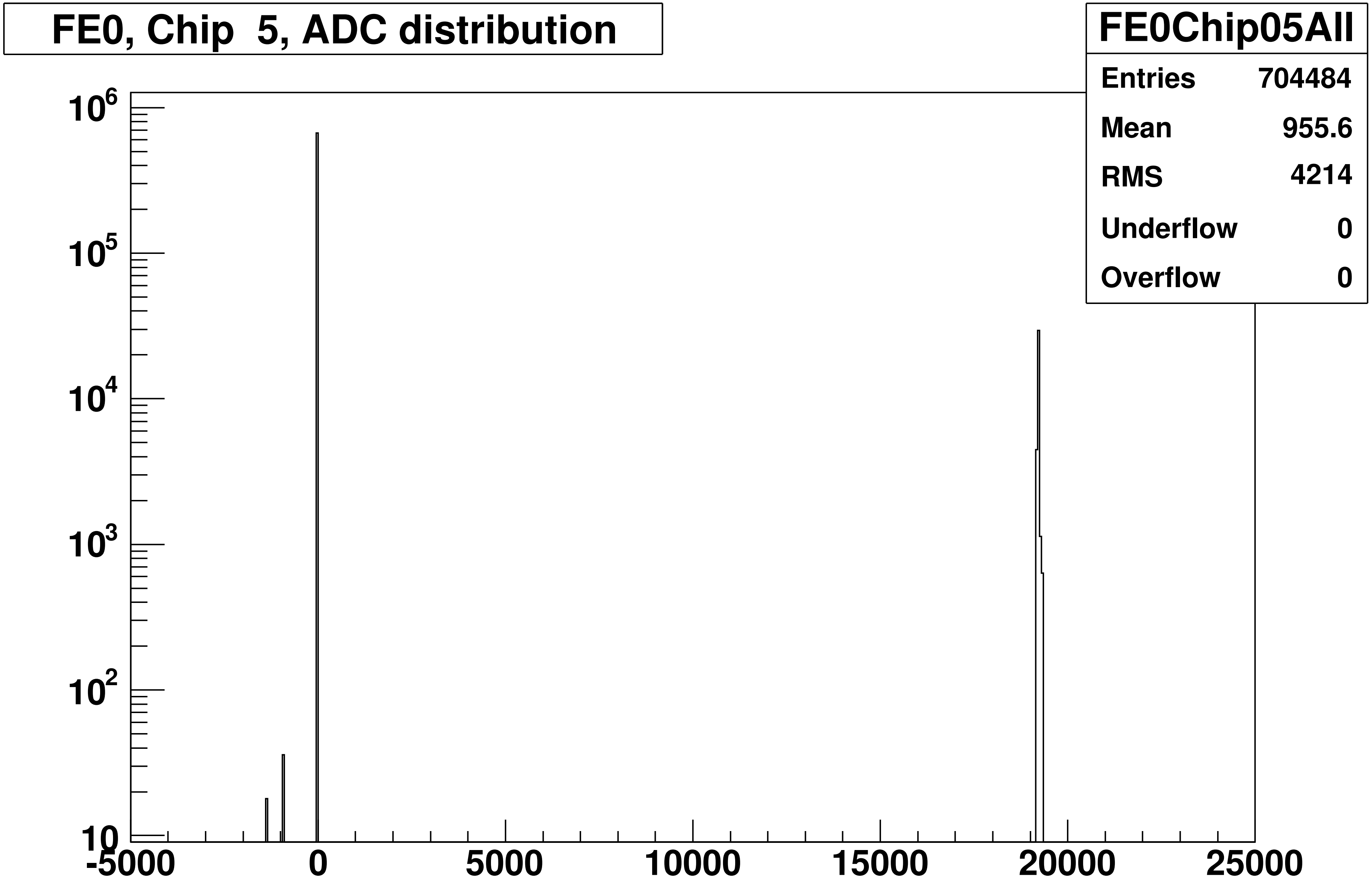
100
150
200



FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 704484
Mean 1.919e+04
RMS 14.25
Underflow 6.689e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 704484
Mean 955.6
RMS 4214
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
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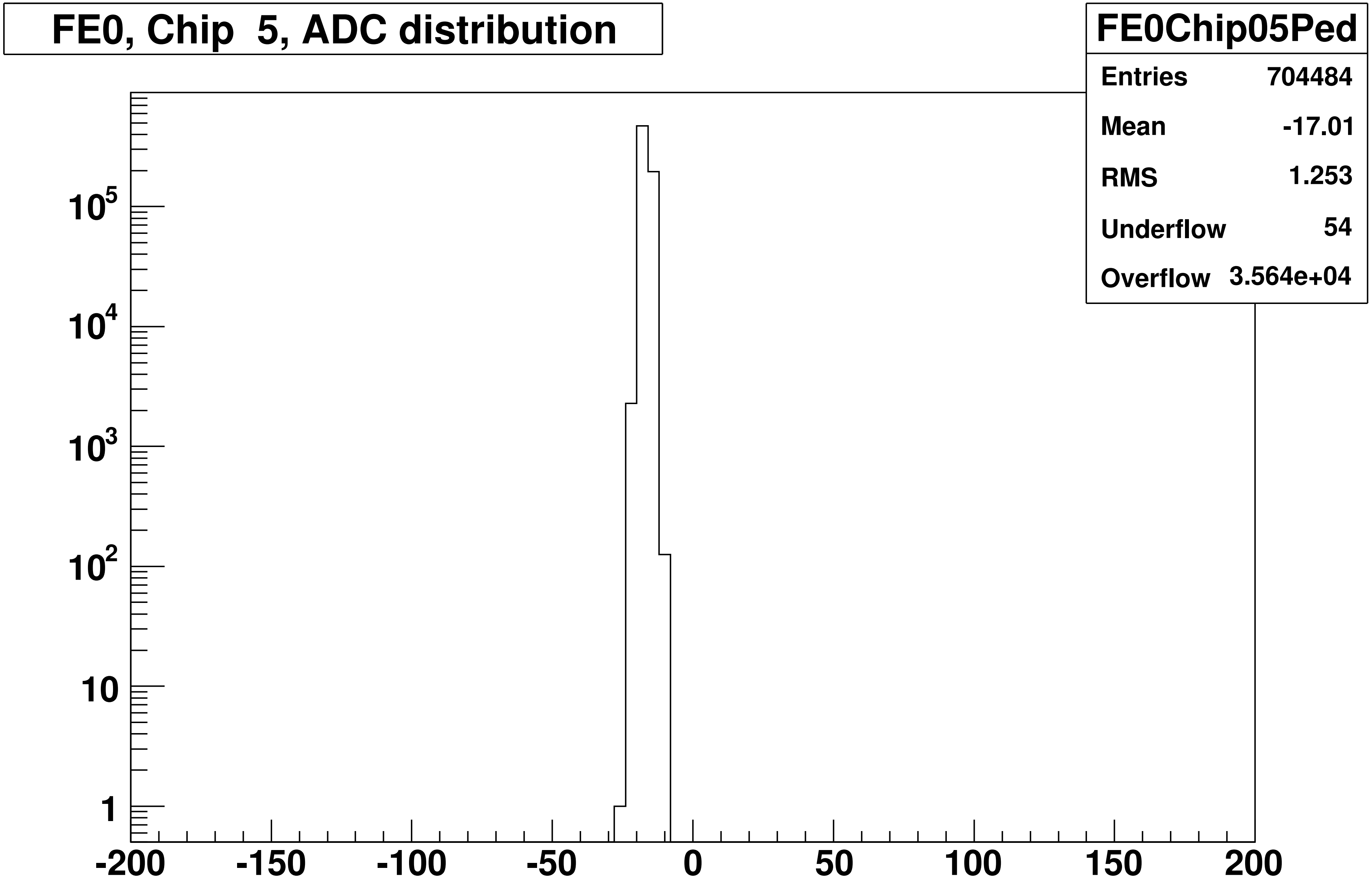
10000
15000
20000
25000


FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 704484
Mean -17.01
RMS 1.253
Underflow 54
Overflow 3.564e+04
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10^4
10^3
10^2
10
1
-200
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-100
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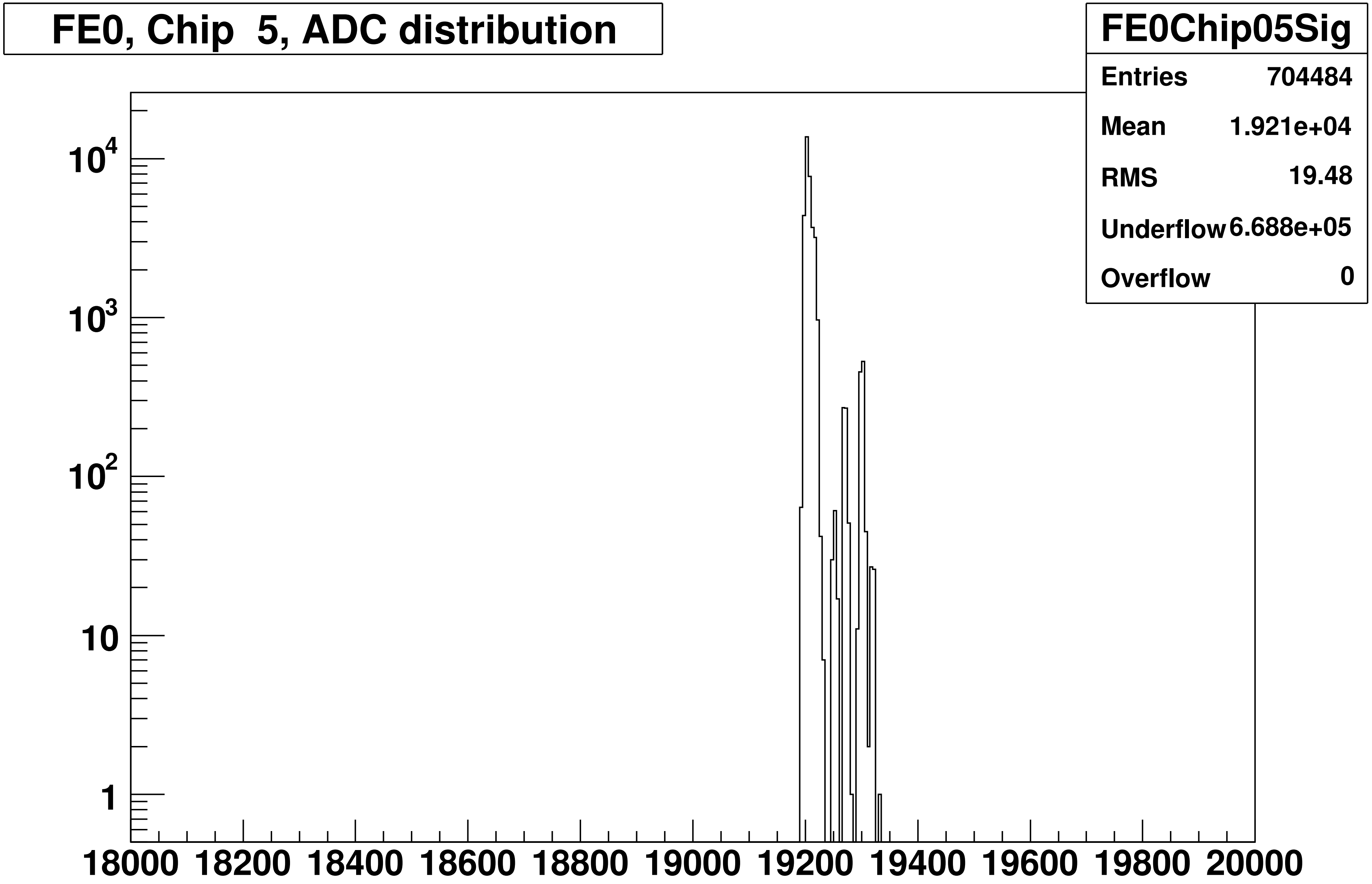
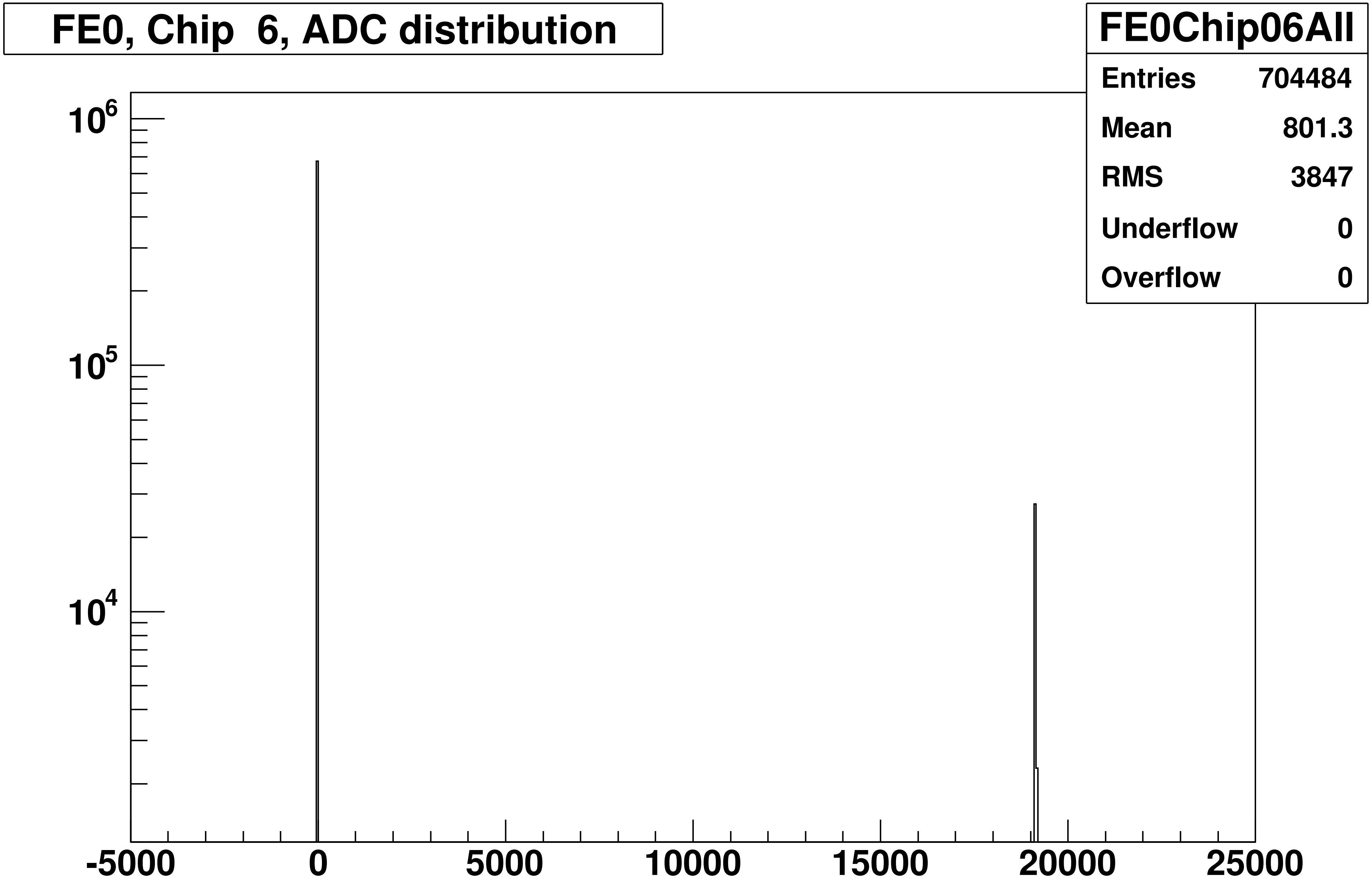
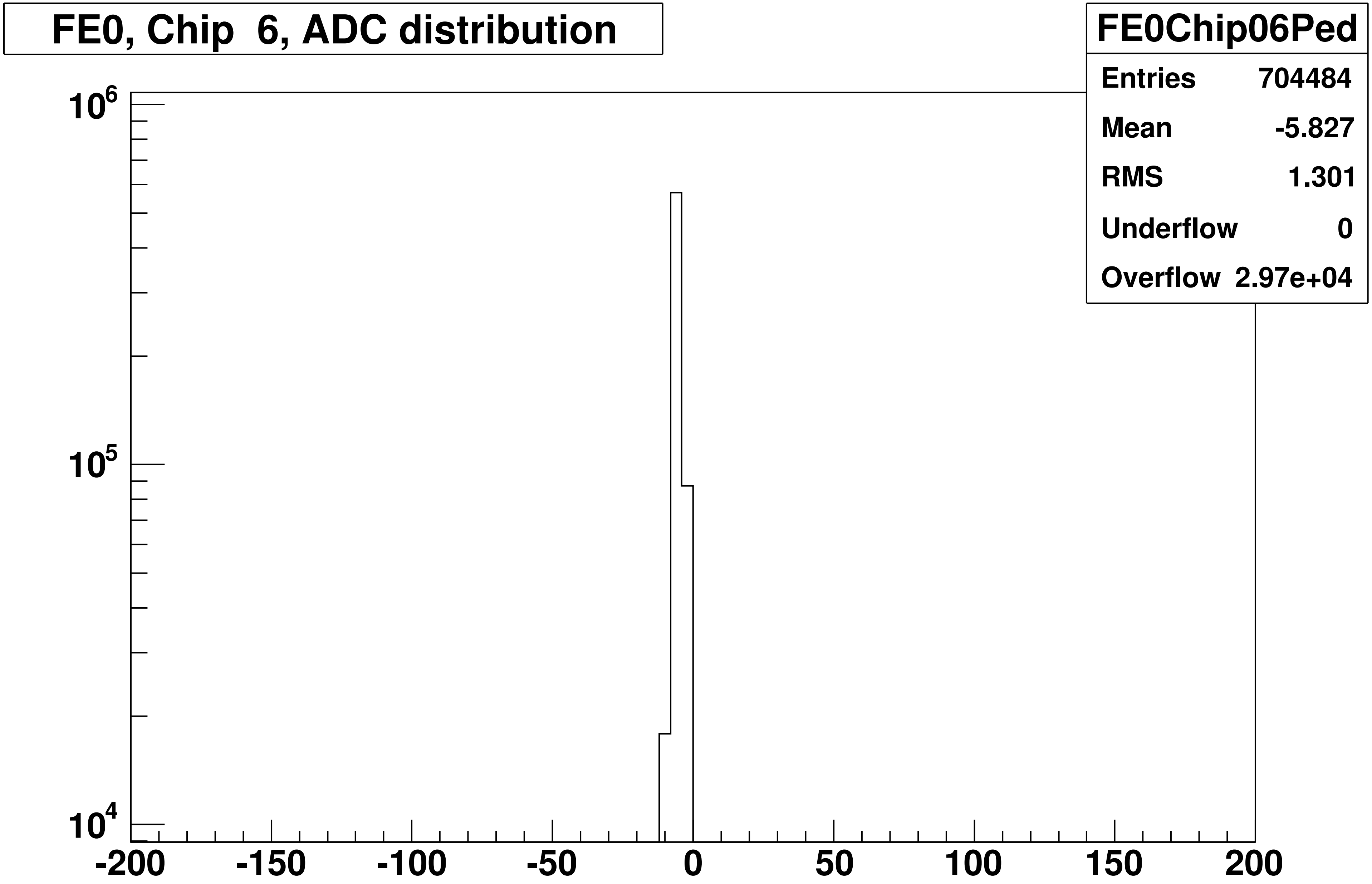
0
50
100
150
200


FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 704484
Mean 1.921e+04
RMS 19.48
Underflow 6.688e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 704484
Mean 801.3
RMS 3847
Underflow 0
Overflow 0
10^6
10^5
10^4
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 704484
Mean -5.827
RMS 1.301
Underflow 0
Overflow 2.97e+04
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10^5
10^4
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-150
-100
-50
0
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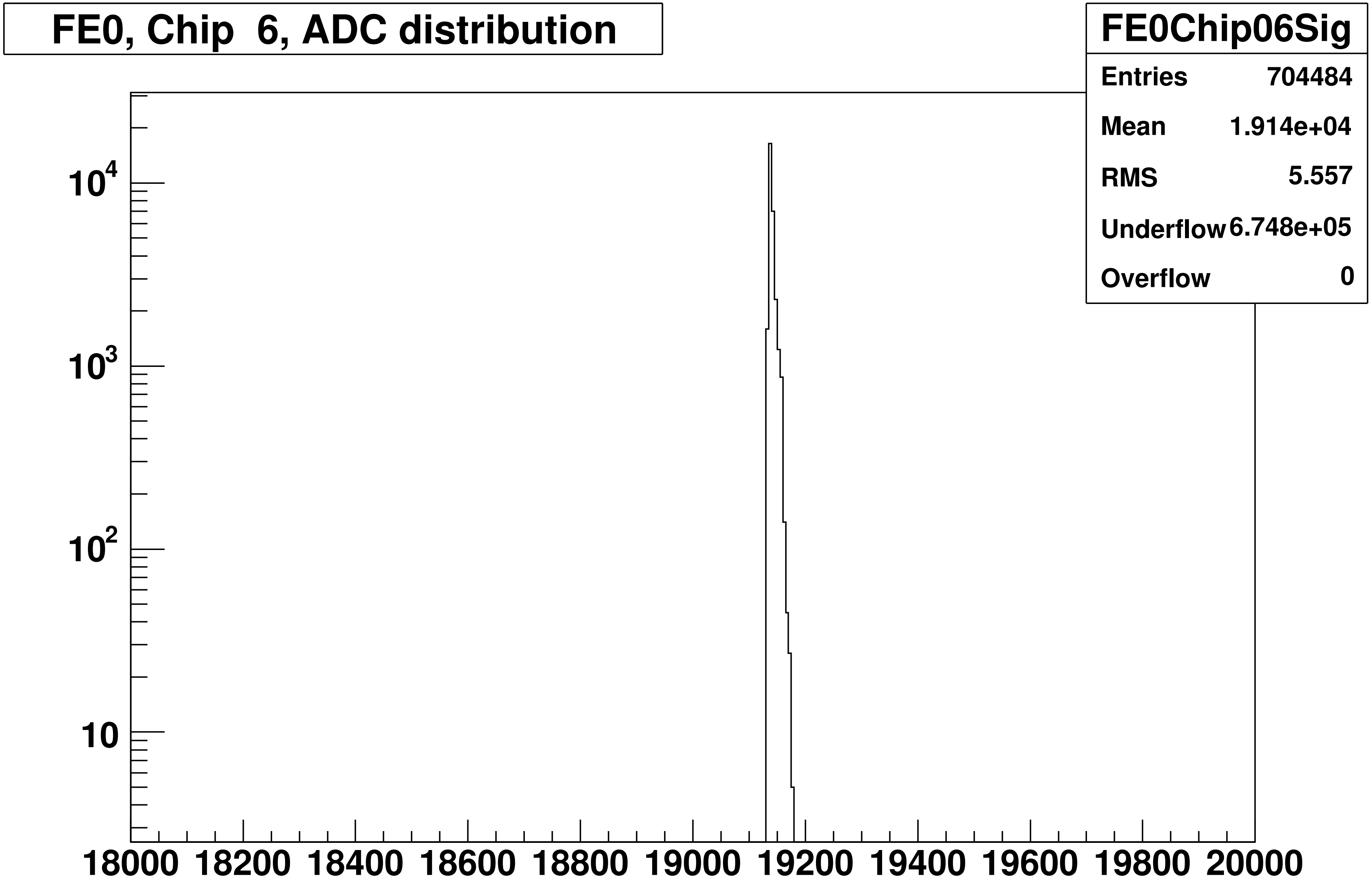
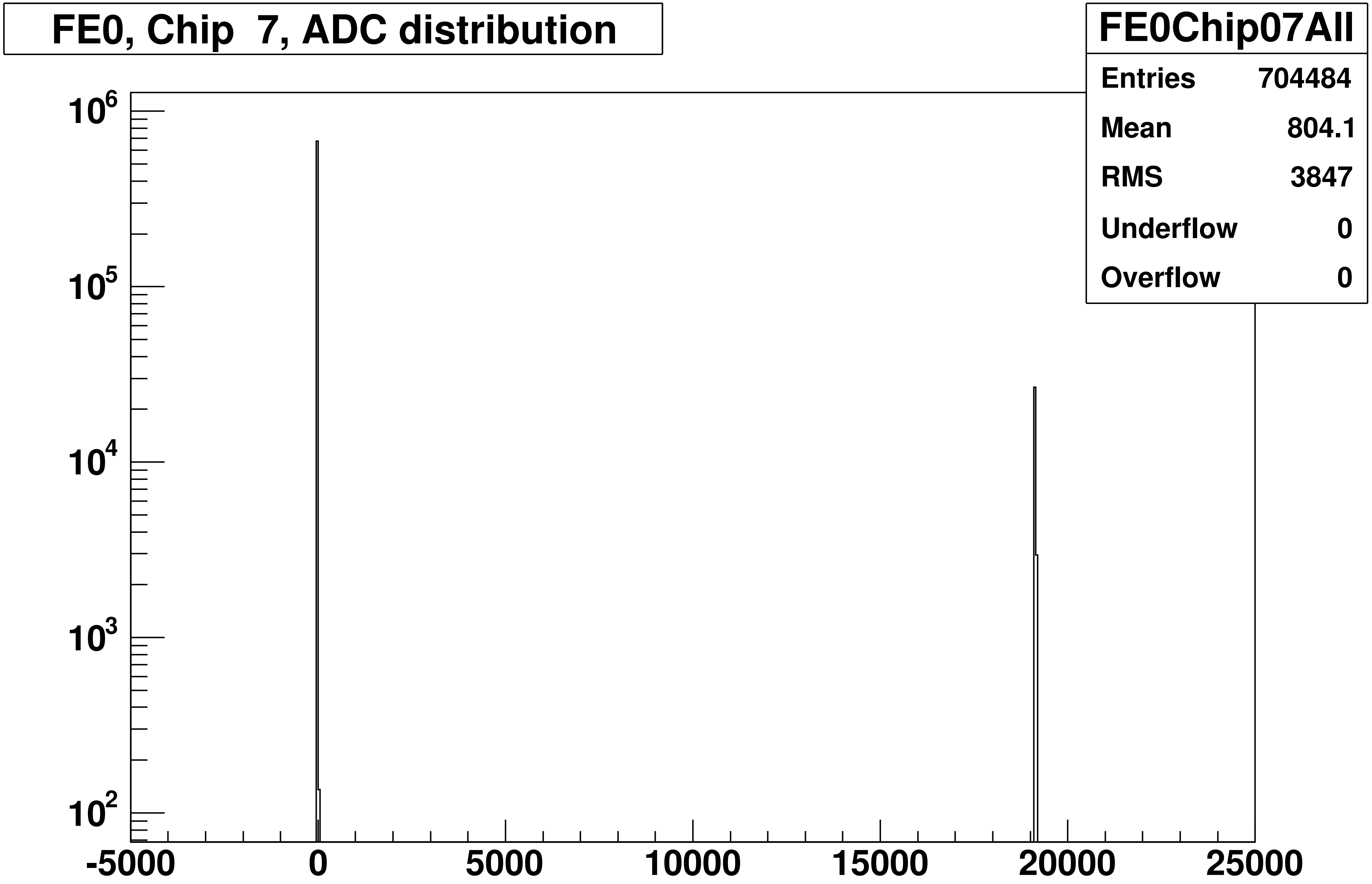
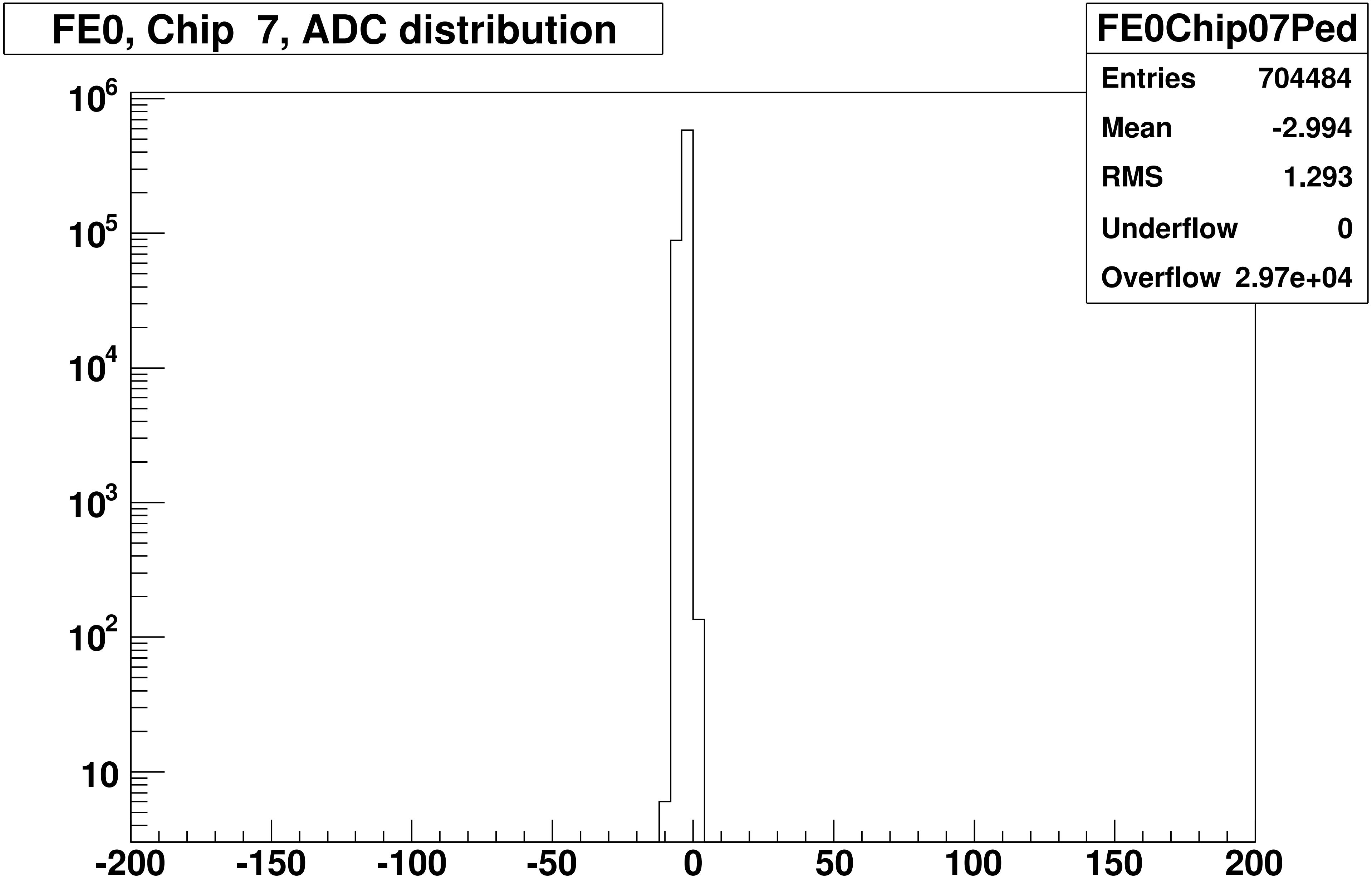
100
150
200


FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 704484
Mean 1.914e+04
RMS 5.557
Underflow 6.748e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 704484
Mean 804.1
RMS 3847
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 704484
Mean -2.994
RMS 1.293
Underflow 0
Overflow 2.97e+04
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10^5
10^4
10^3
10^2
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150
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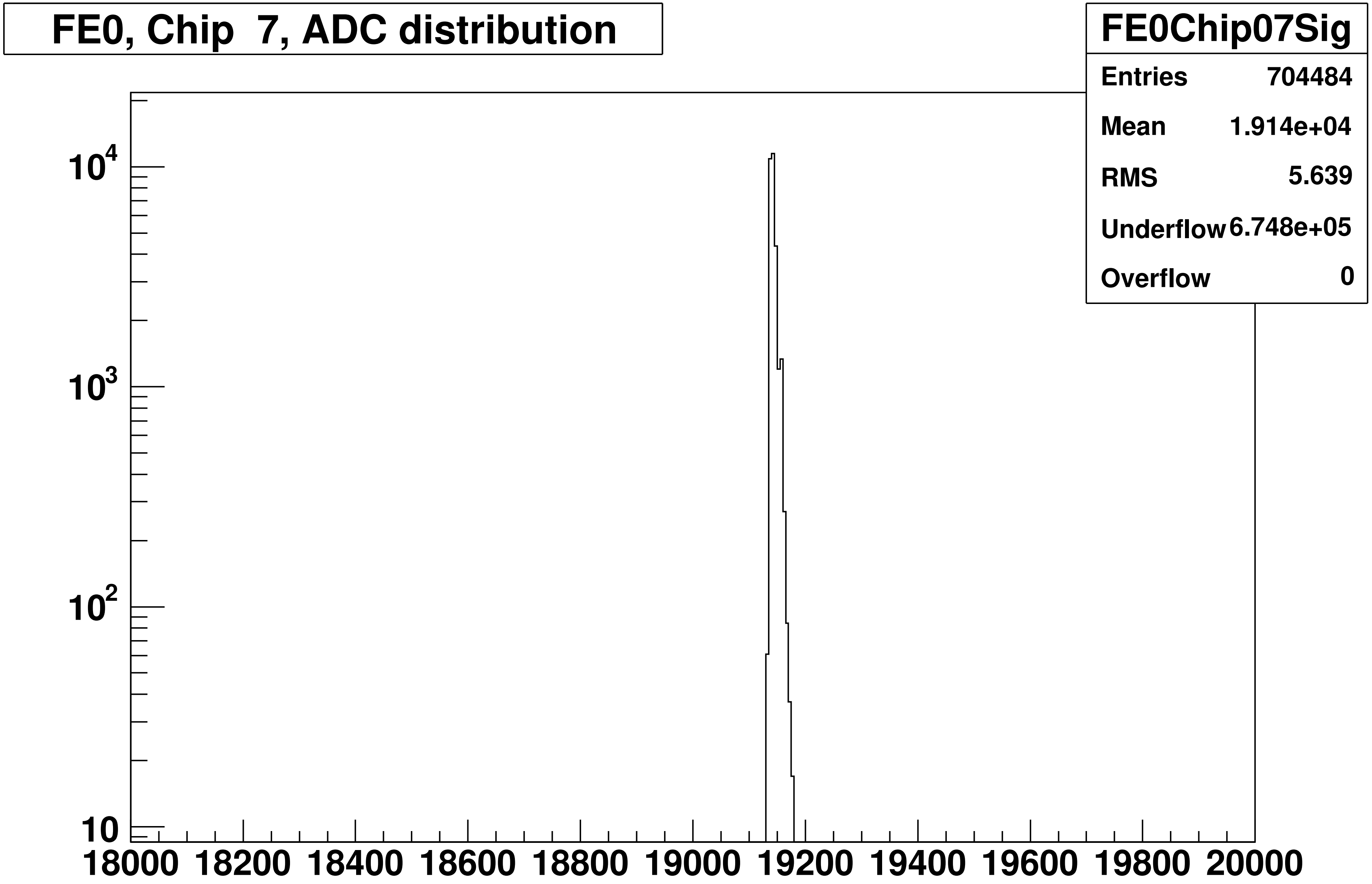

FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 704484
Mean 1.914e+04
RMS 5.639
Underflow 6.748e+05
Overflow 0
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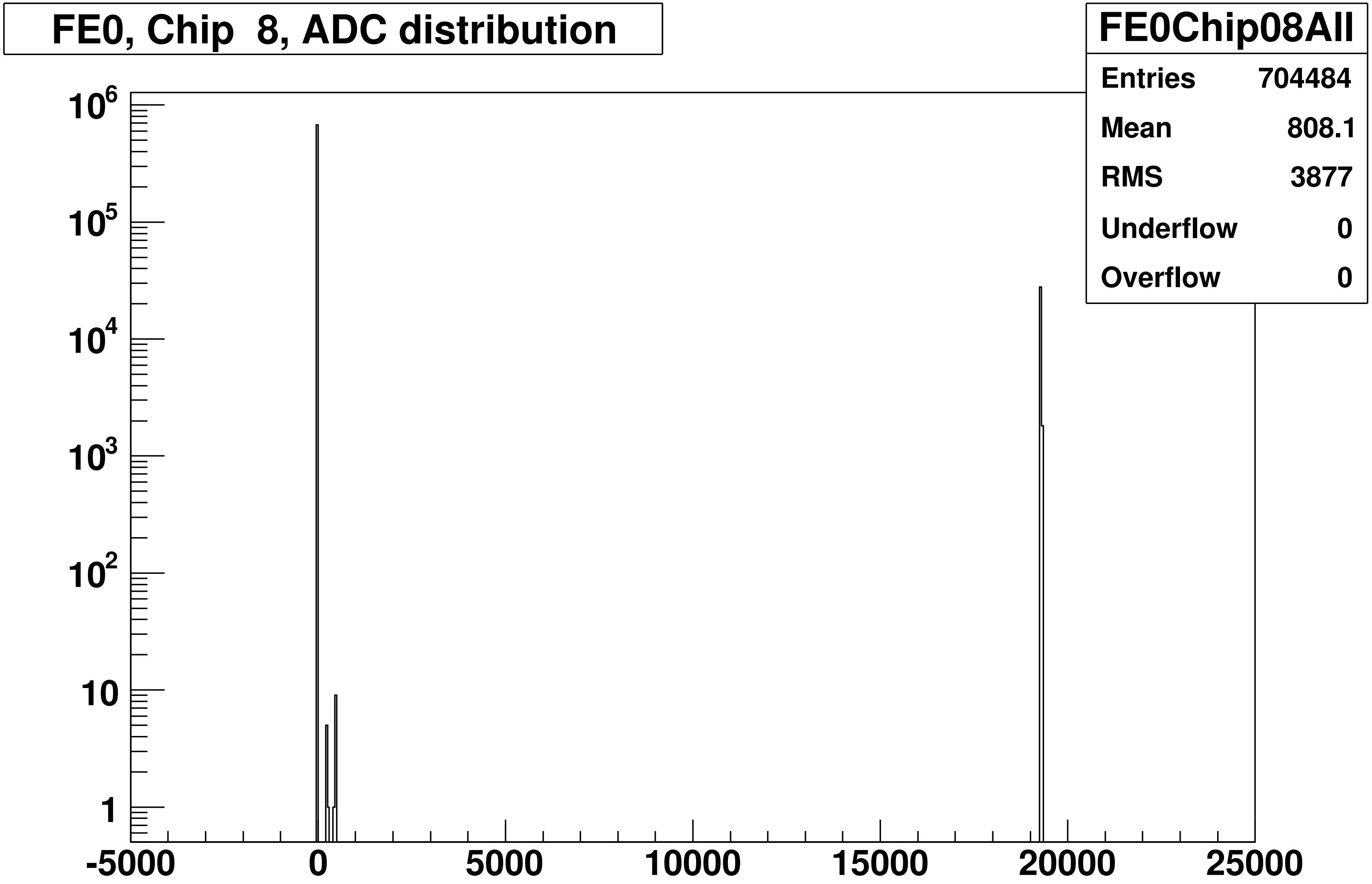
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 704484
Mean 808.1
RMS 3877
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
15000
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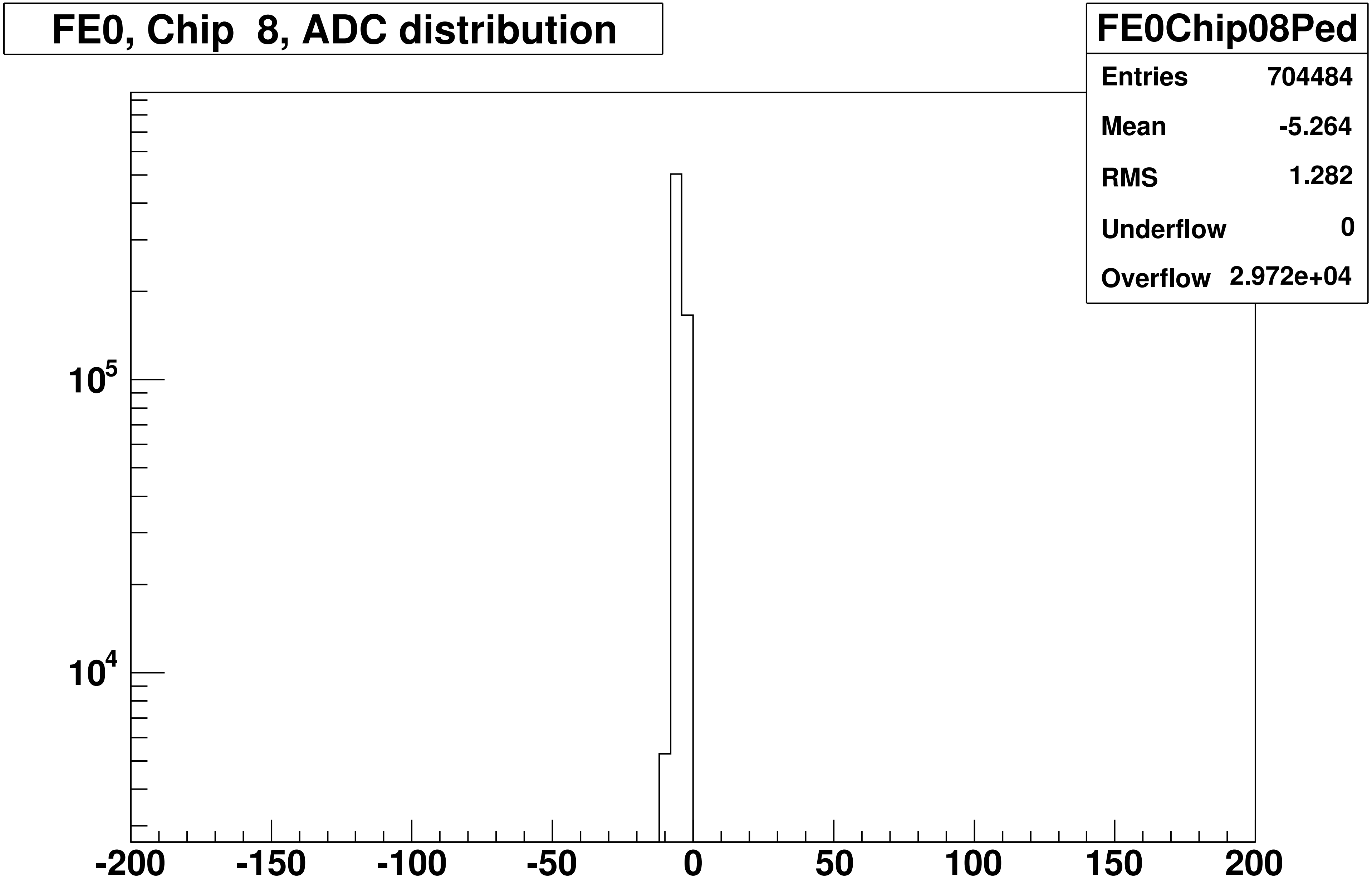
25000


FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 704484
Mean -5.264
RMS 1.282
Underflow 0
Overflow 2.972e+04
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10^4
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-150
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0
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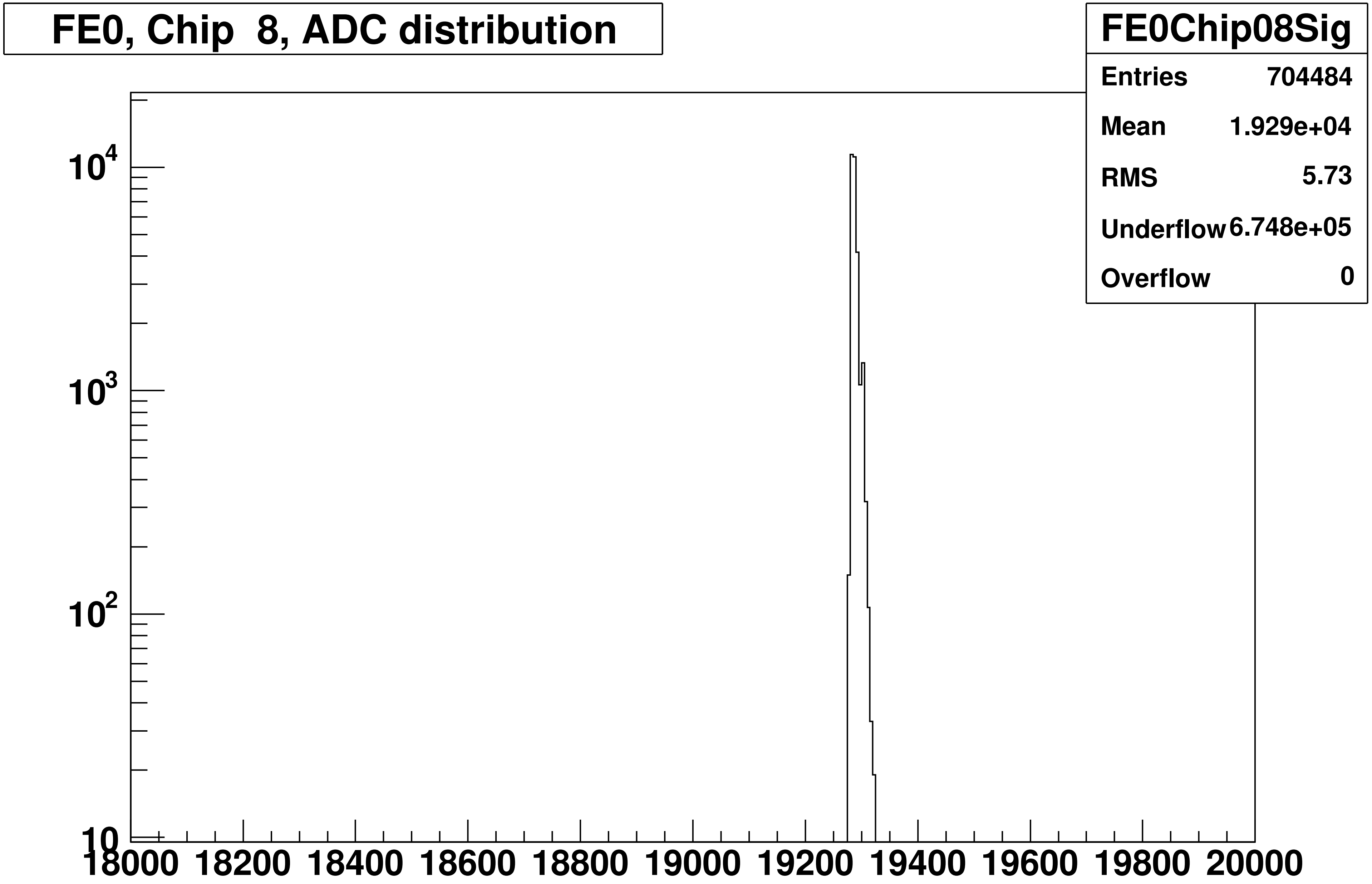
100
150
200


FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 704484
Mean 1.929e+04
RMS 5.73
Underflow 6.748e+05
Overflow 0
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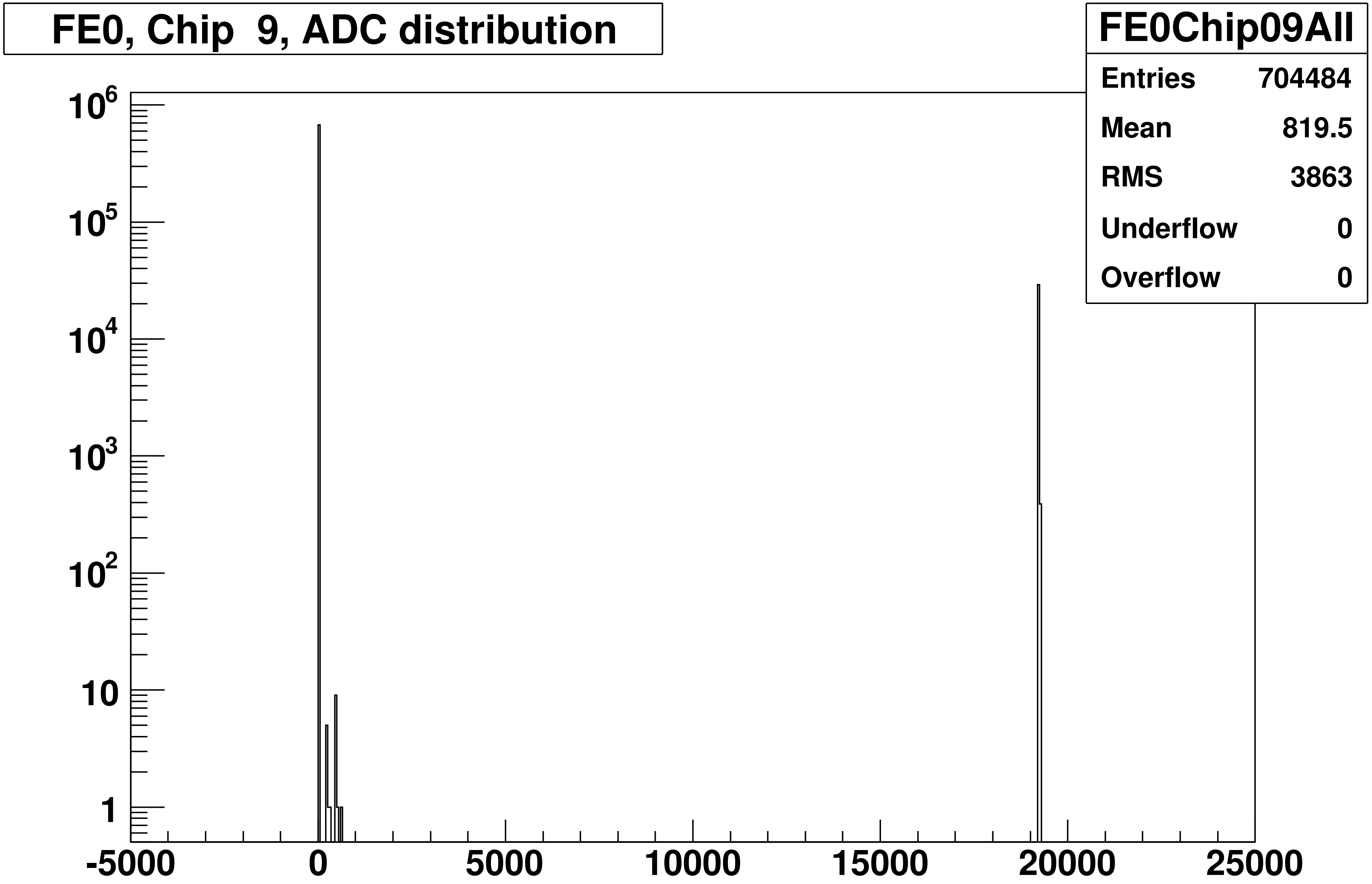
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 704484
Mean 819.5
RMS 3863
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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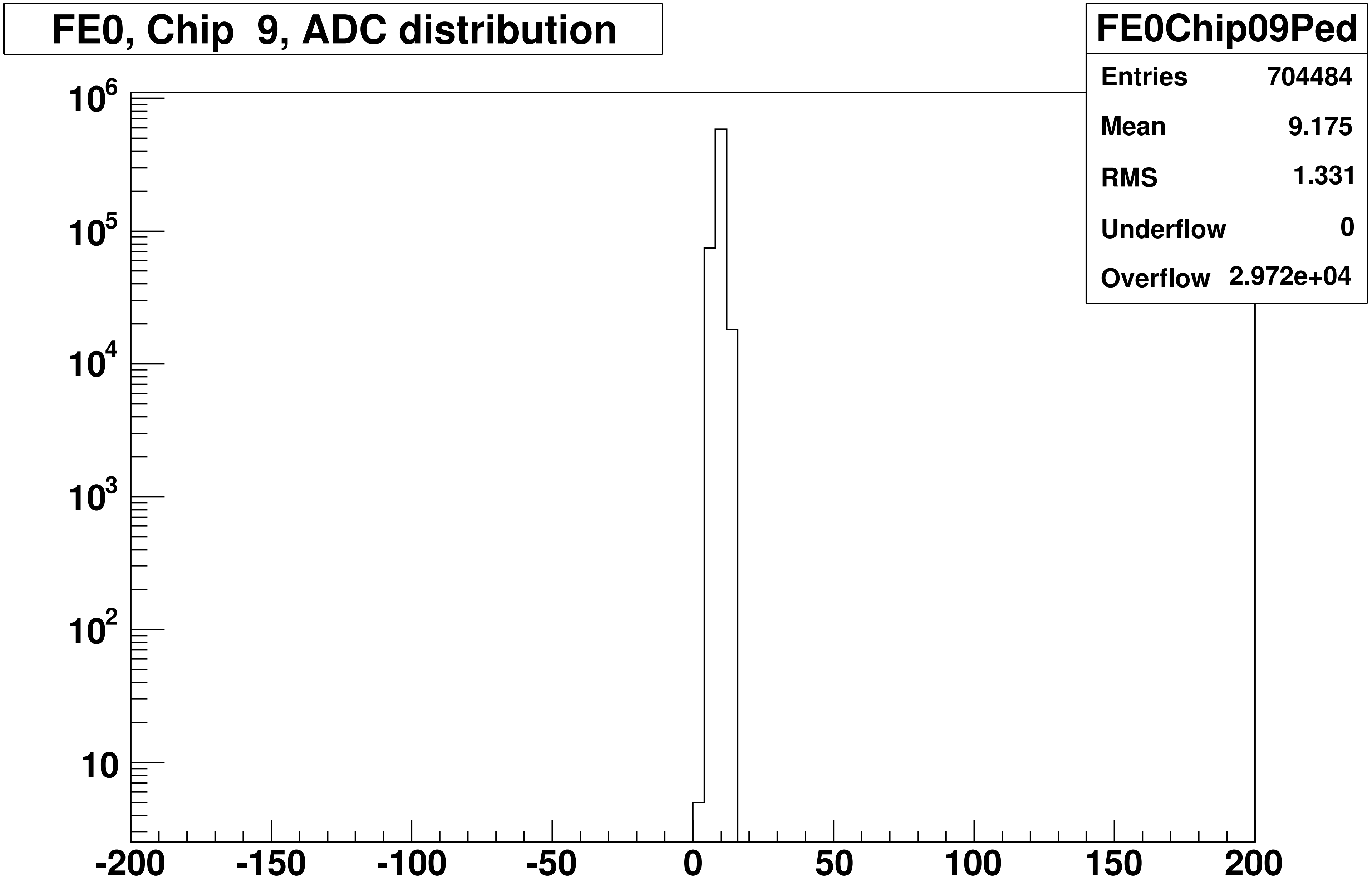
10000
15000
20000
25000



FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 704484
Mean 9.175
RMS 1.331
Underflow 0
Overflow 2.972e+04
10^6
10^5
10^4
10^3
10^2
10
-200
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-100
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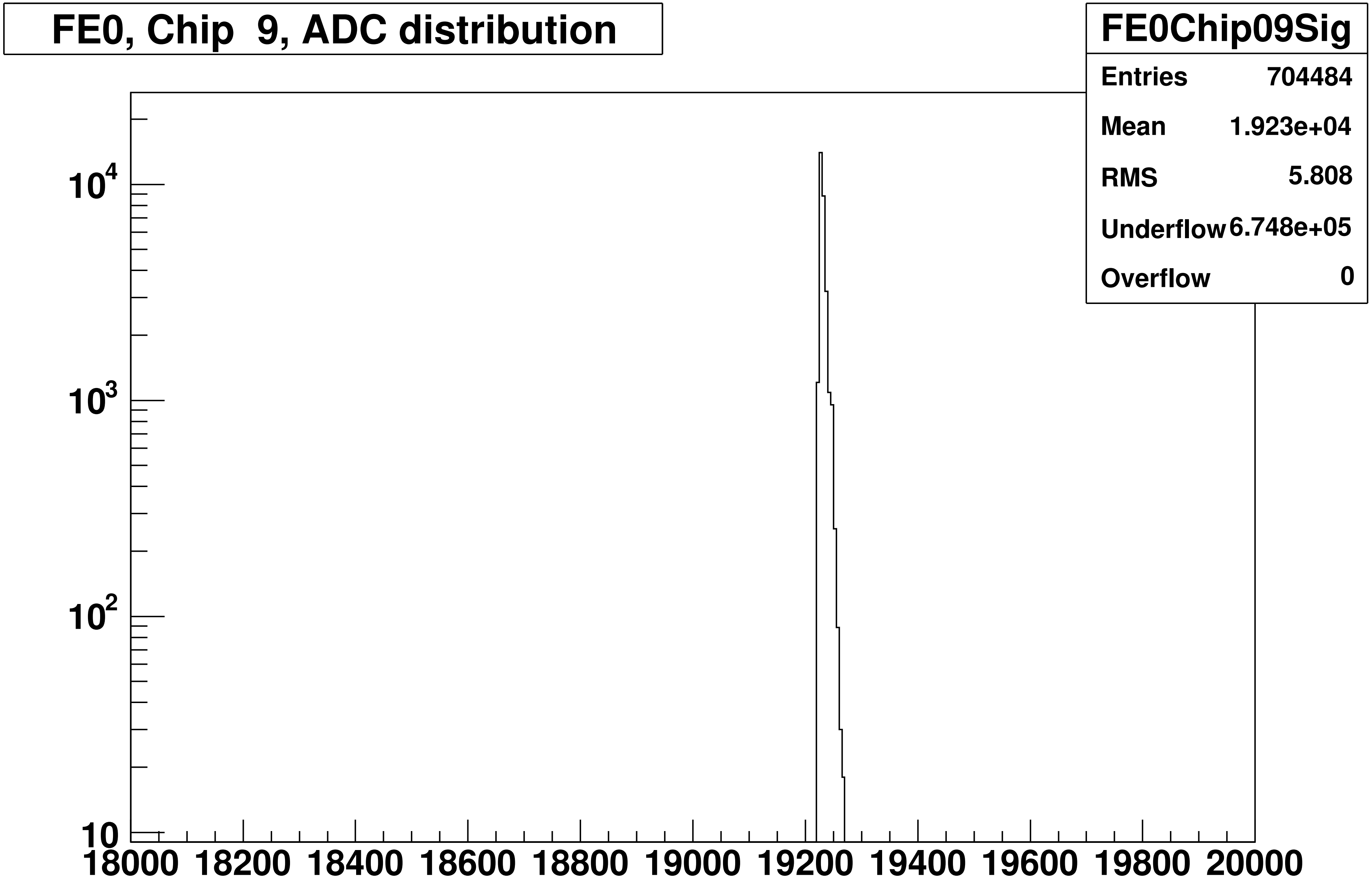
0
50
100
150
200


FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 704484
Mean 1.923e+04
RMS 5.808
Underflow 6.748e+05
Overflow 0
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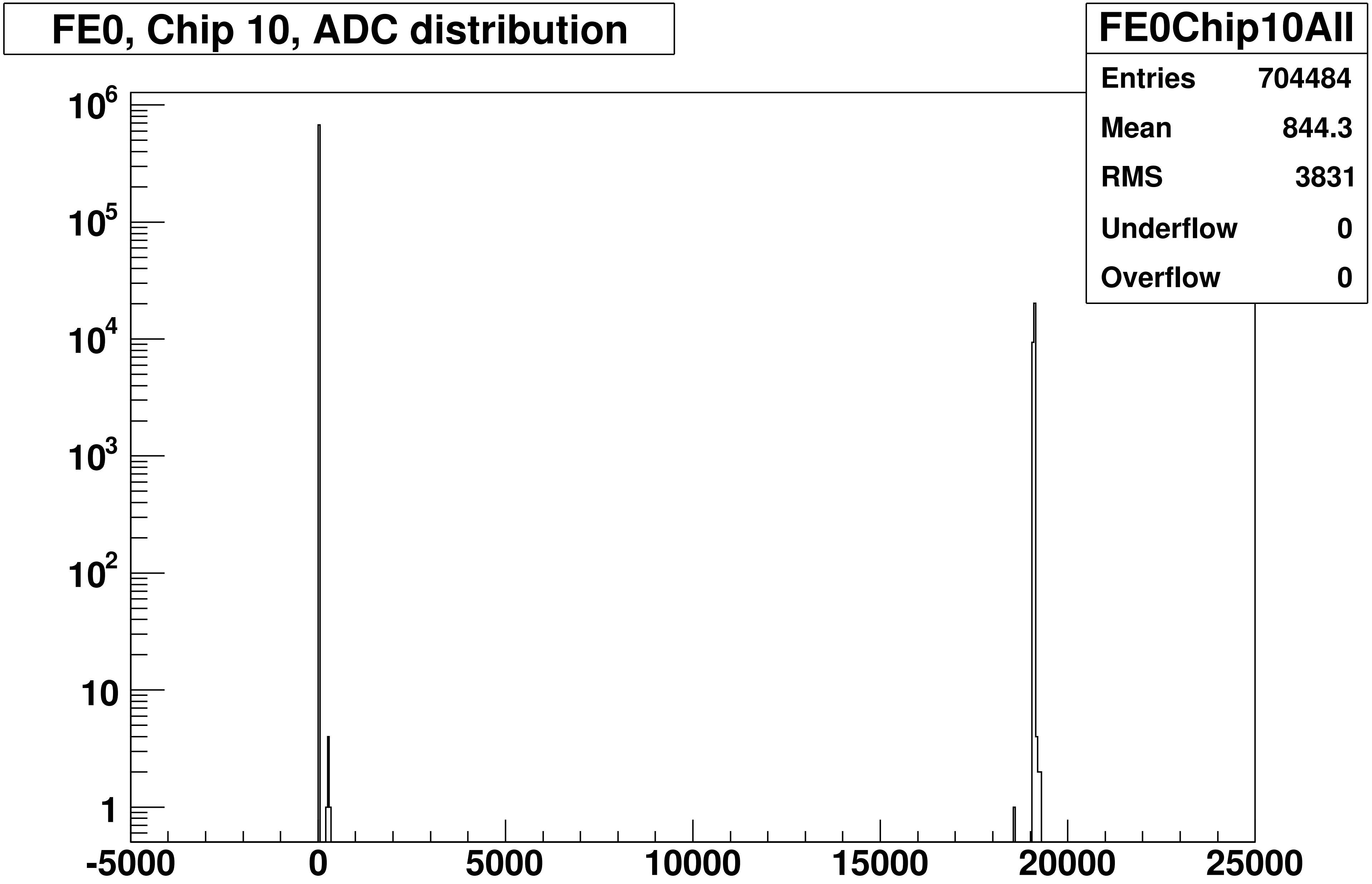
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 704484
Mean 844.3
RMS 3831
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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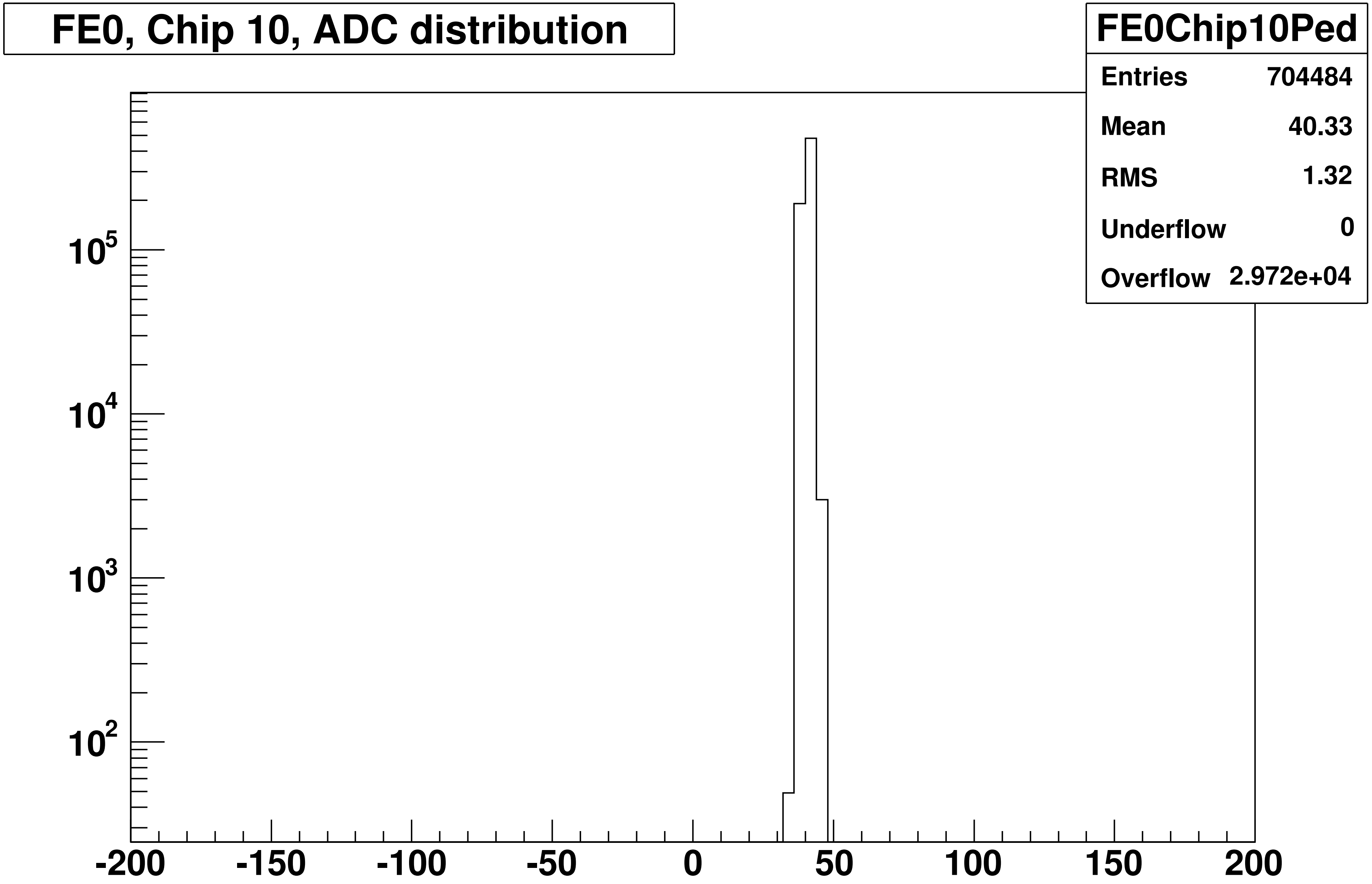
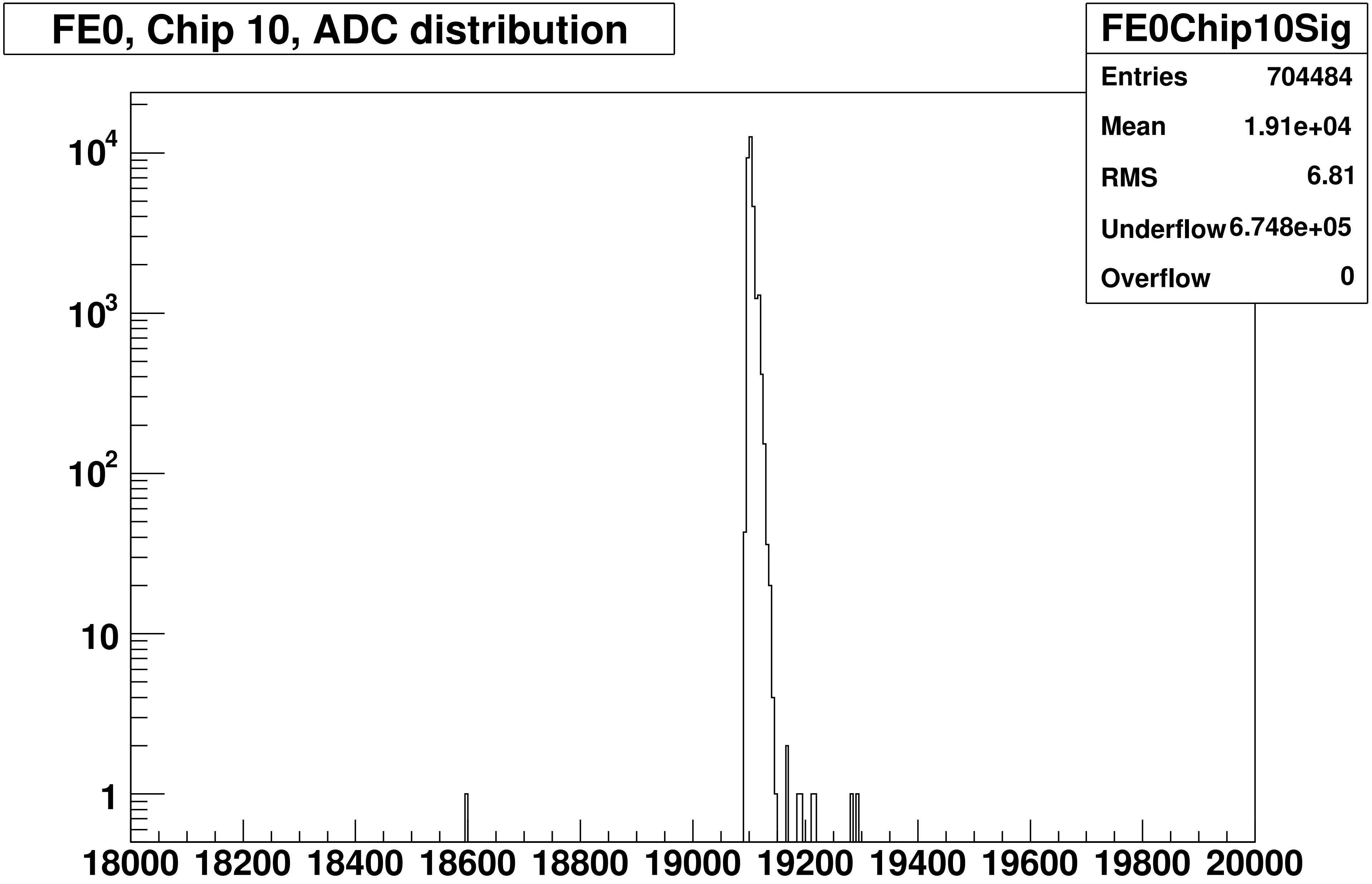
10000
15000
20000
25000


FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 704484
Mean 40.33
RMS 1.32
Underflow 0
Overflow 2.972e+04
10^5
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200



FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 704484
Mean 1.91e+04
RMS 6.81
Underflow 6.748e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
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18400
18600
18800
19000
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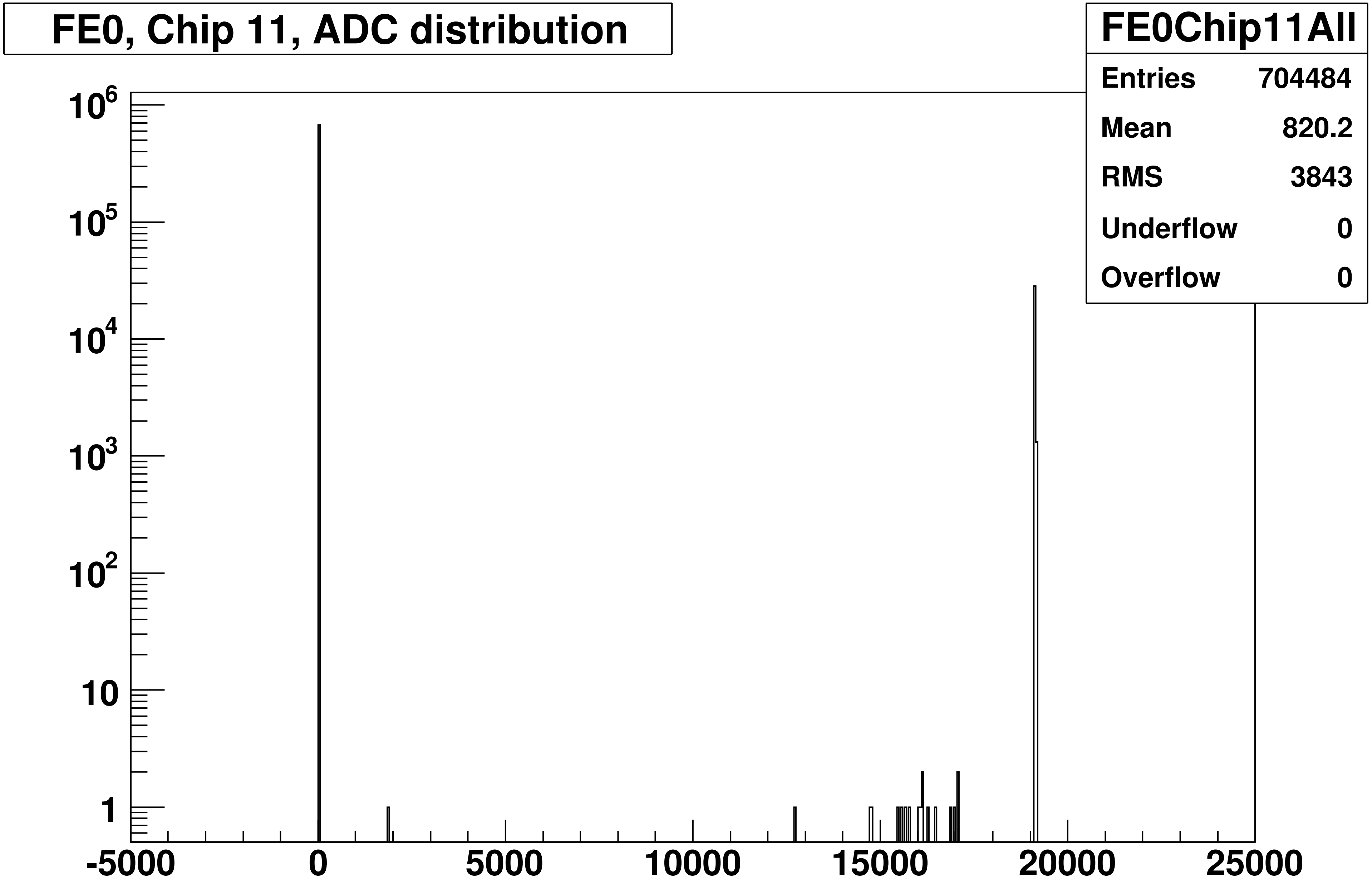
19400
19600
19800
20000



FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 704484
Mean 820.2
RMS 3843
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
5000
10000
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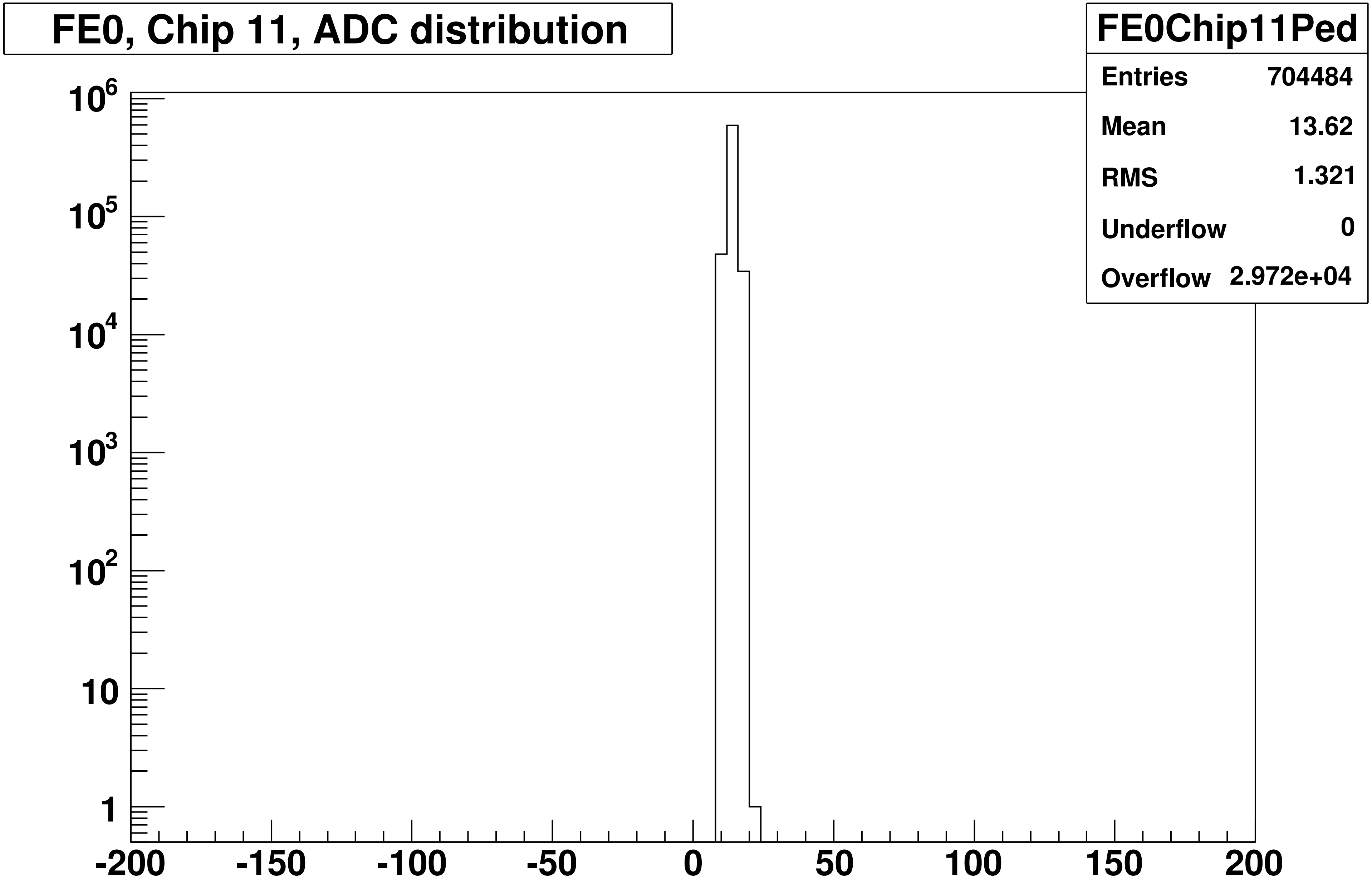
20000
25000



FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 704484
Mean 13.62
RMS 1.321
Underflow 0
Overflow 2.972e+04
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10^4
10^3
10^2
10
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150
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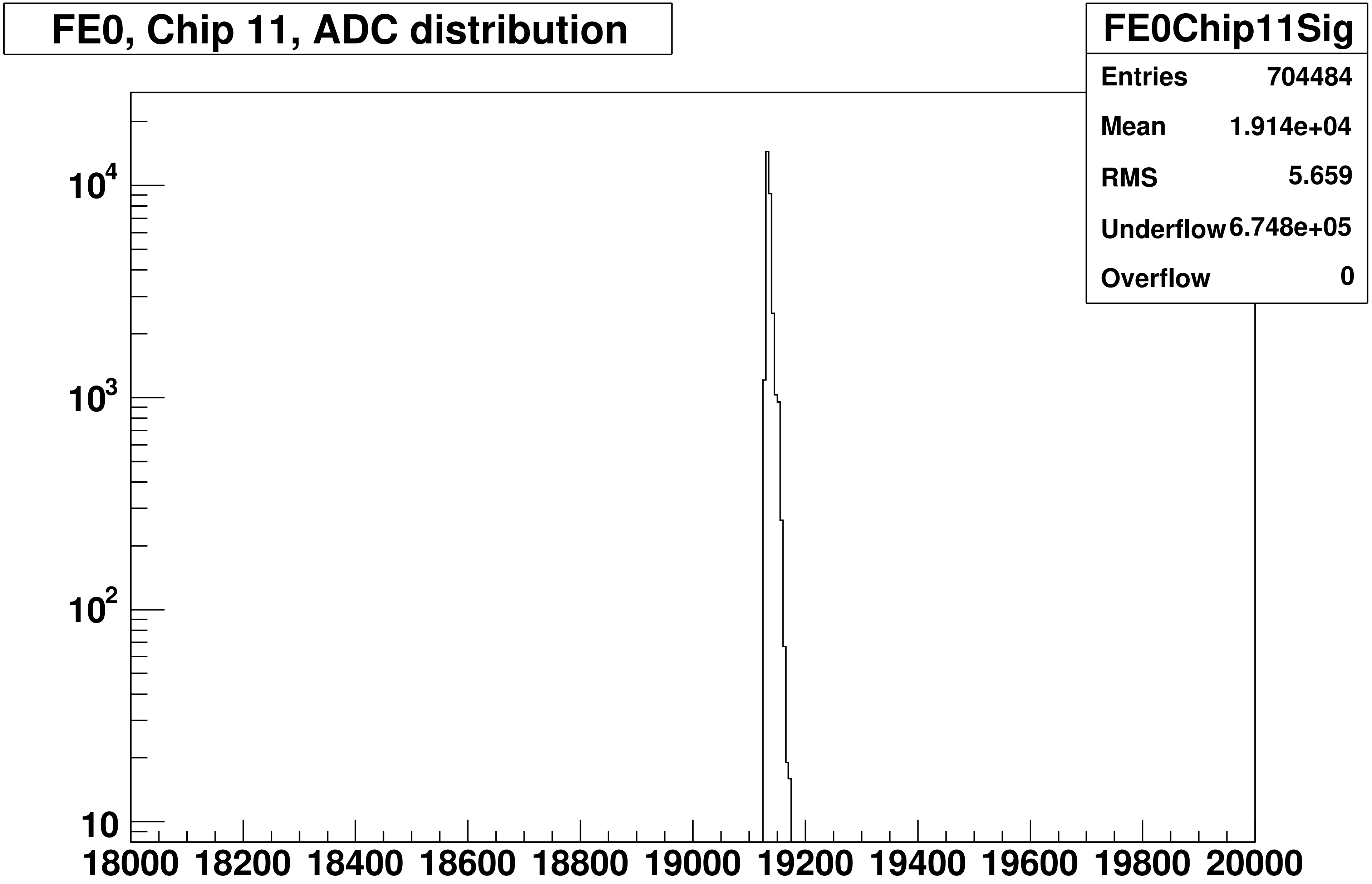
FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 704484
Mean 1.914e+04
RMS 5.659
Underflow 6.748e+05
Overflow 0
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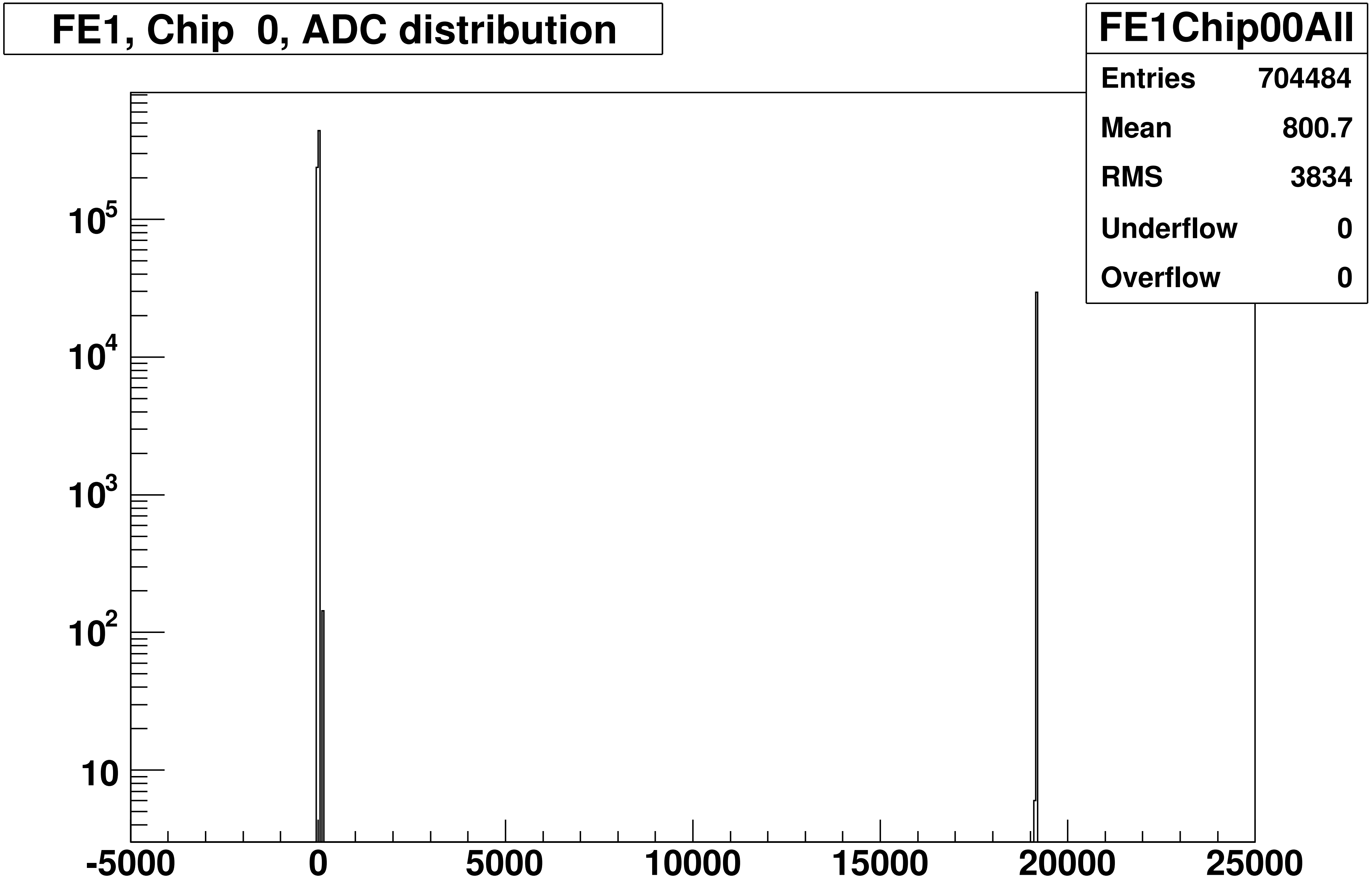
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 704484
Mean 800.7
RMS 3834
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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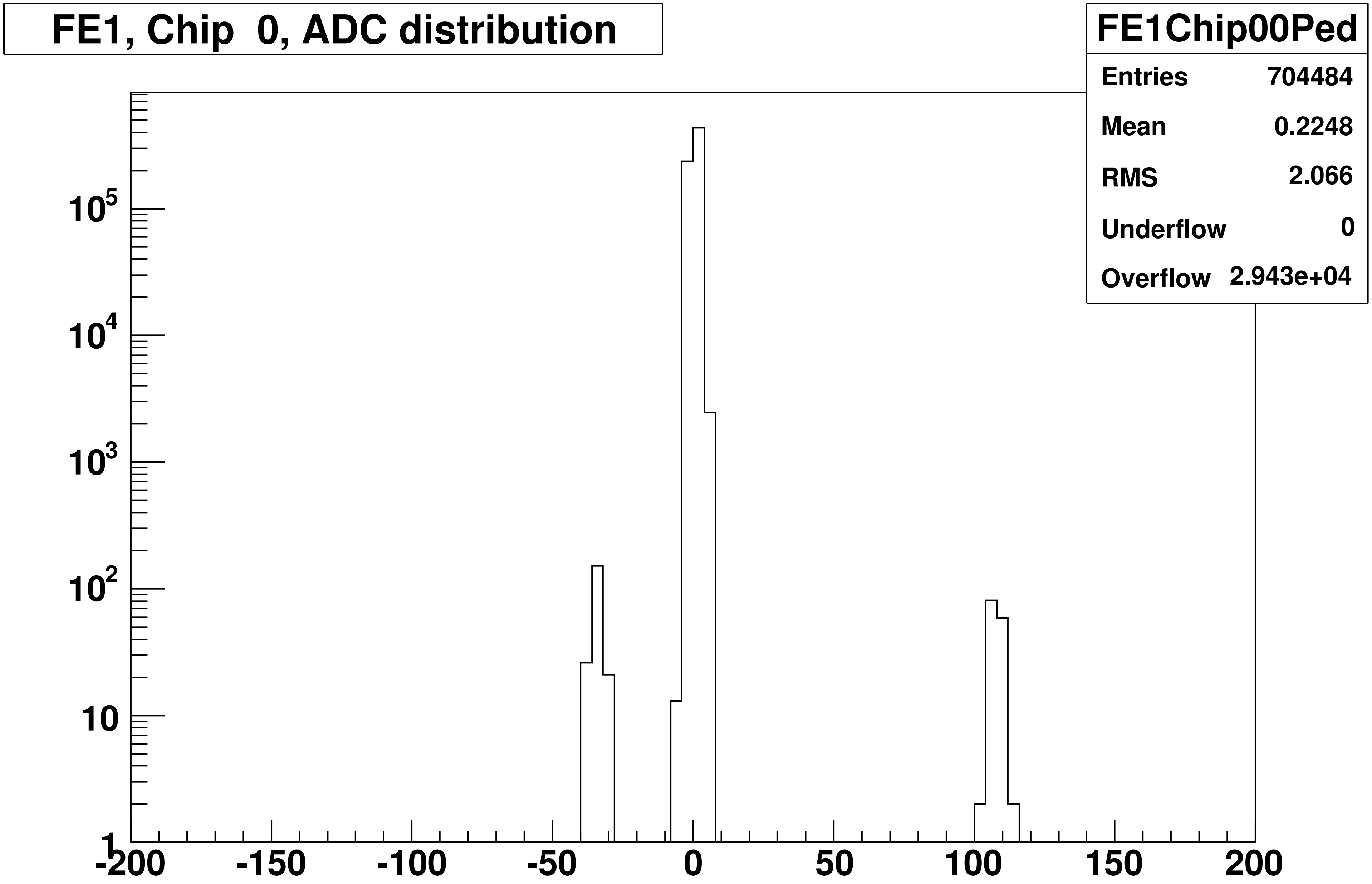
0
5000
10000
15000
20000
25000


FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 704484
Mean 0.2248
RMS 2.066
Underflow 0
Overflow 2.943e+04
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10^3
10^2
10
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-100
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50
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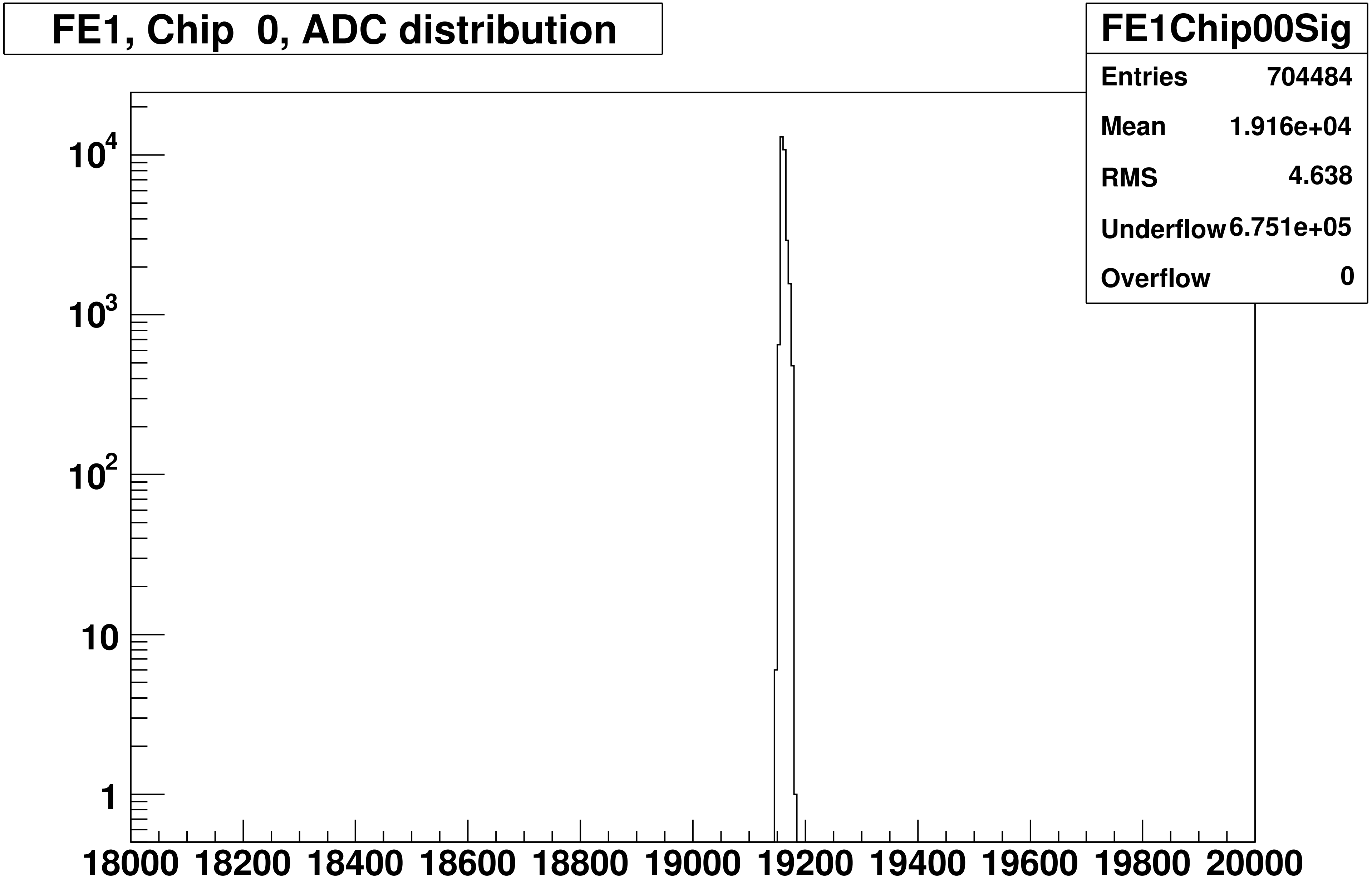
150
200


FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 704484
Mean 1.916e+04
RMS 4.638
Underflow 6.751e+05
Overflow 0
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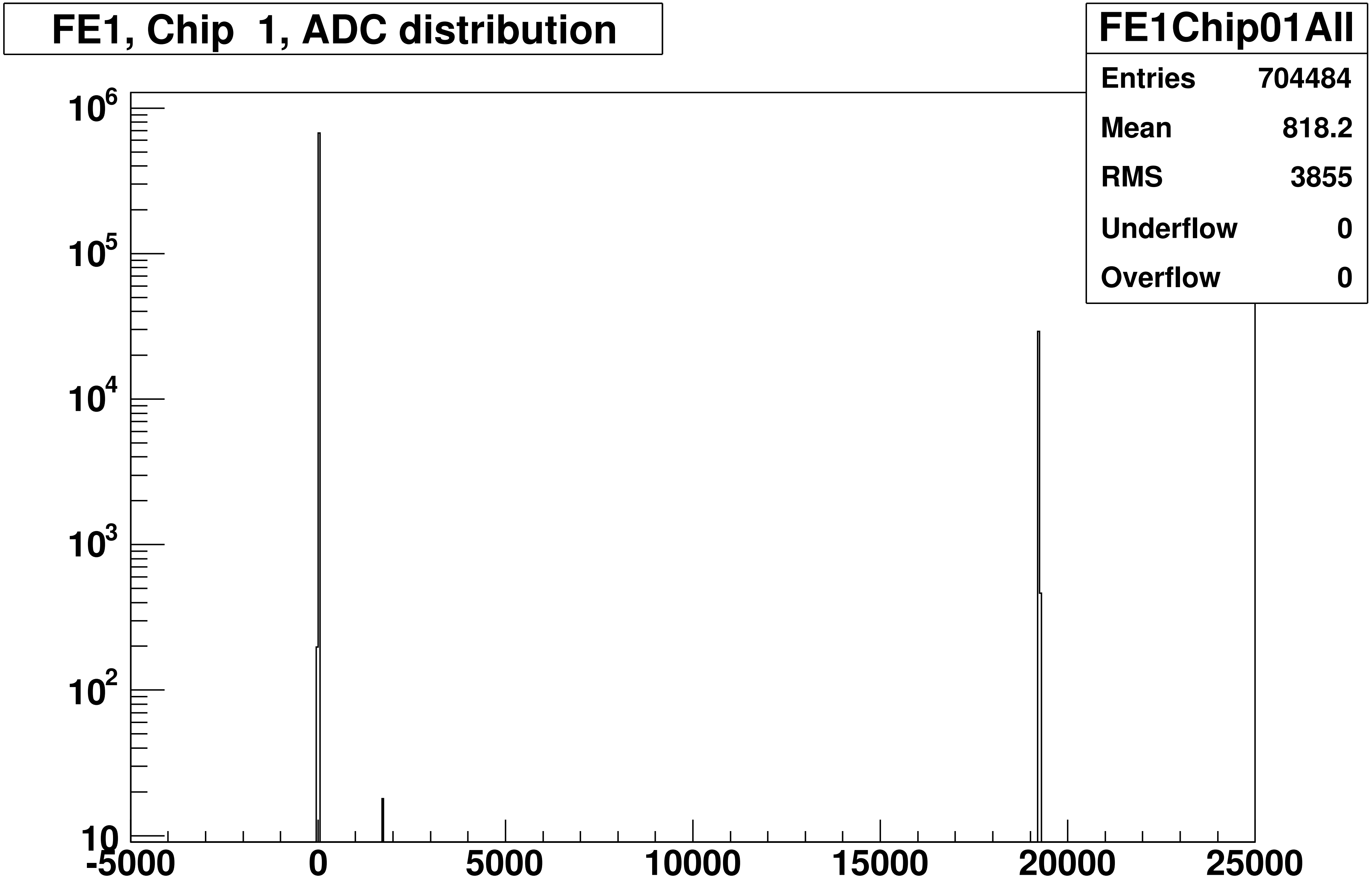
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 704484
Mean 818.2
RMS 3855
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
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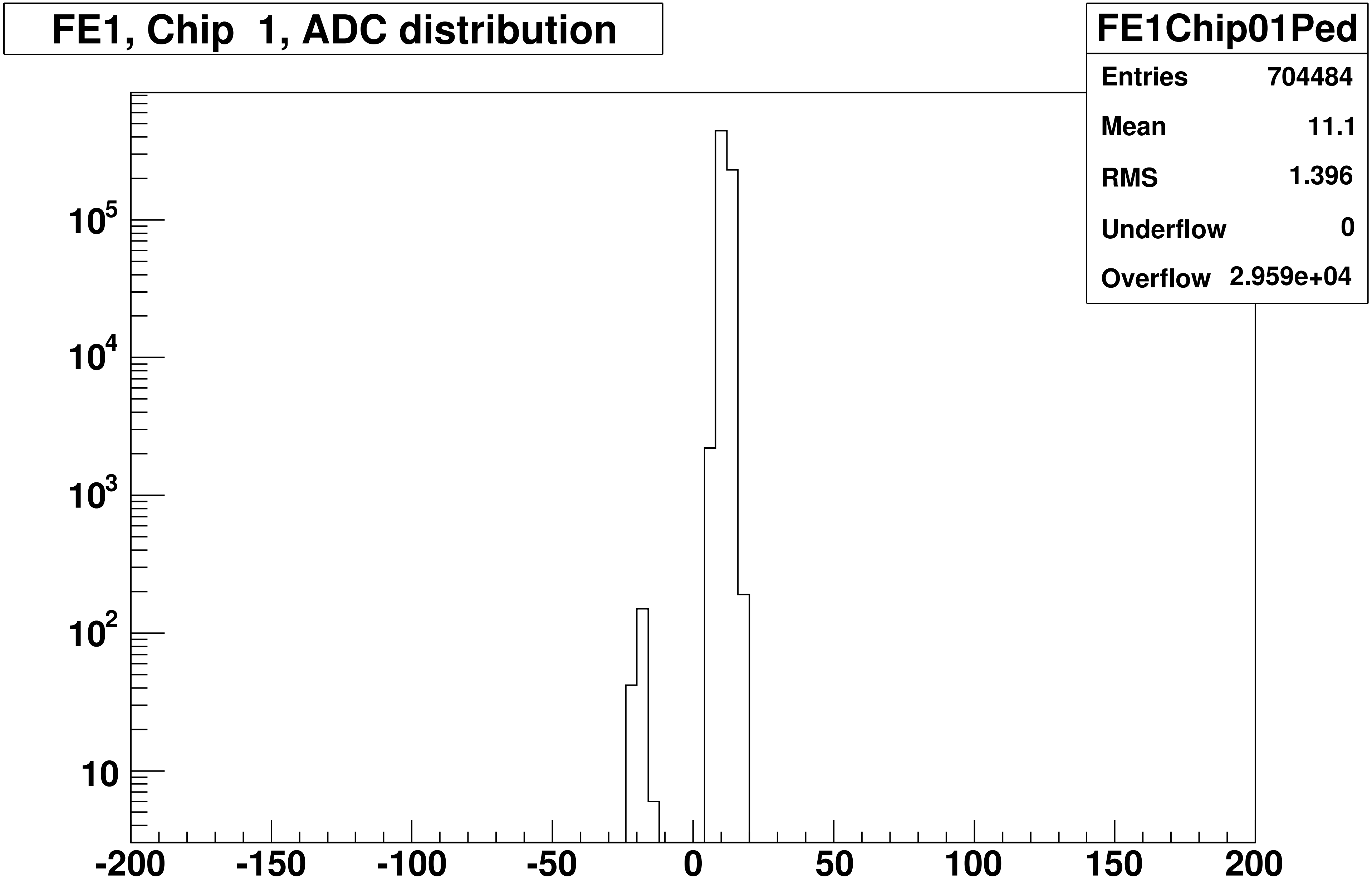
25000


FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 704484
Mean 11.1
RMS 1.396
Underflow 0
Overflow 2.959e+04
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10^2
10^3
10^4
10^5
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-100
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150
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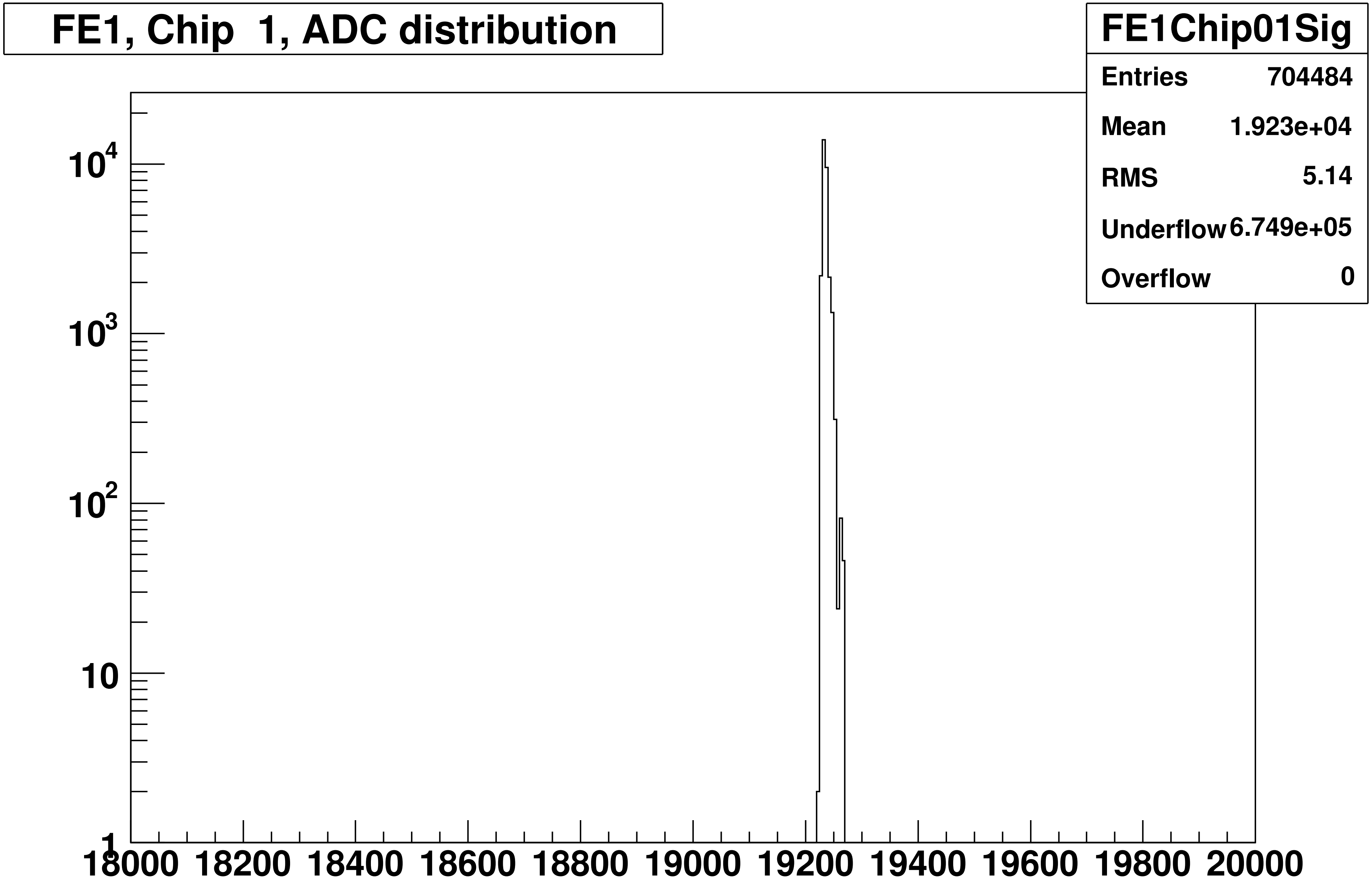
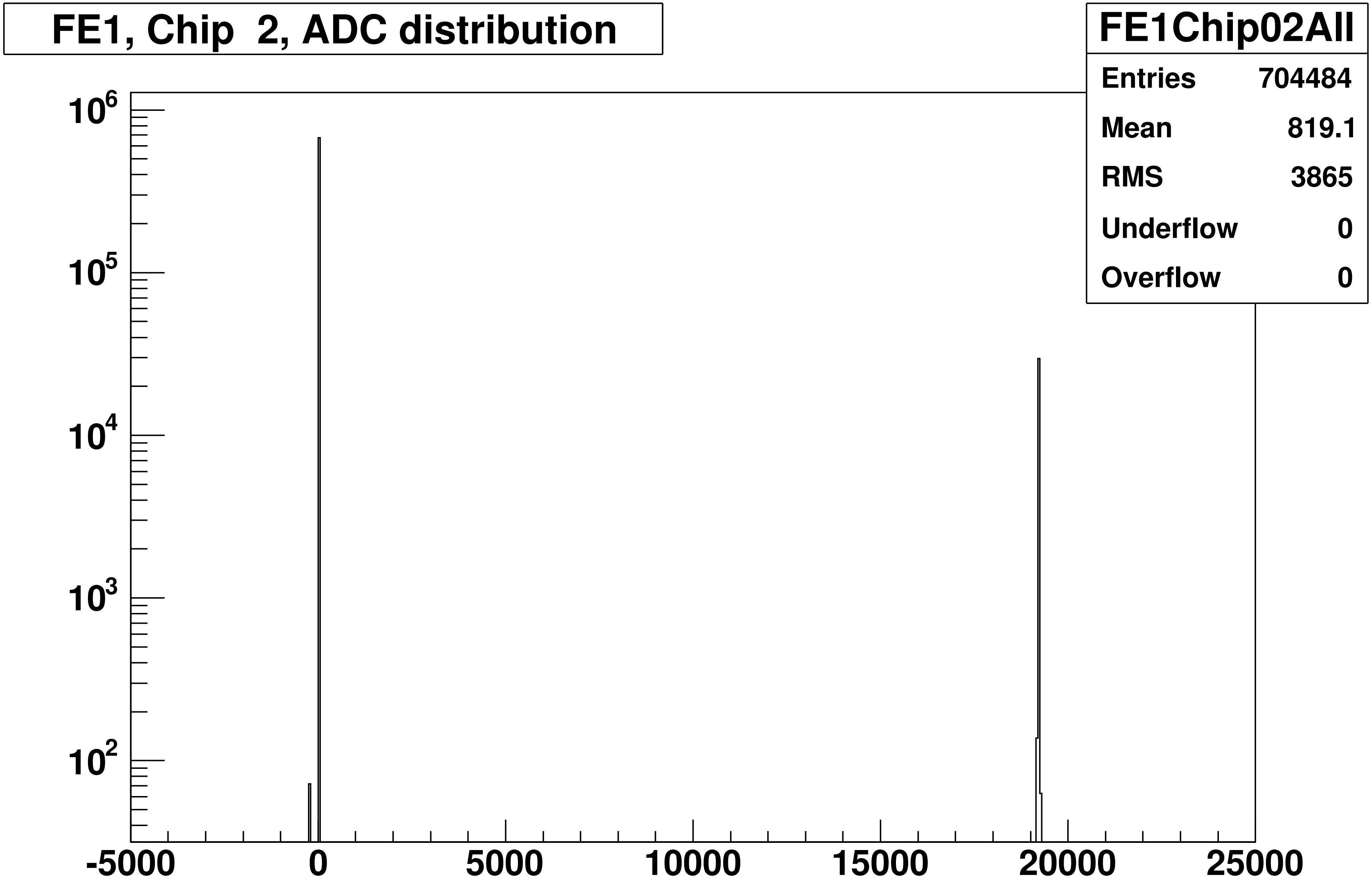

FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 704484
Mean 1.923e+04
RMS 5.14
Underflow 6.749e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 704484
Mean 819.1
RMS 3865
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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20000
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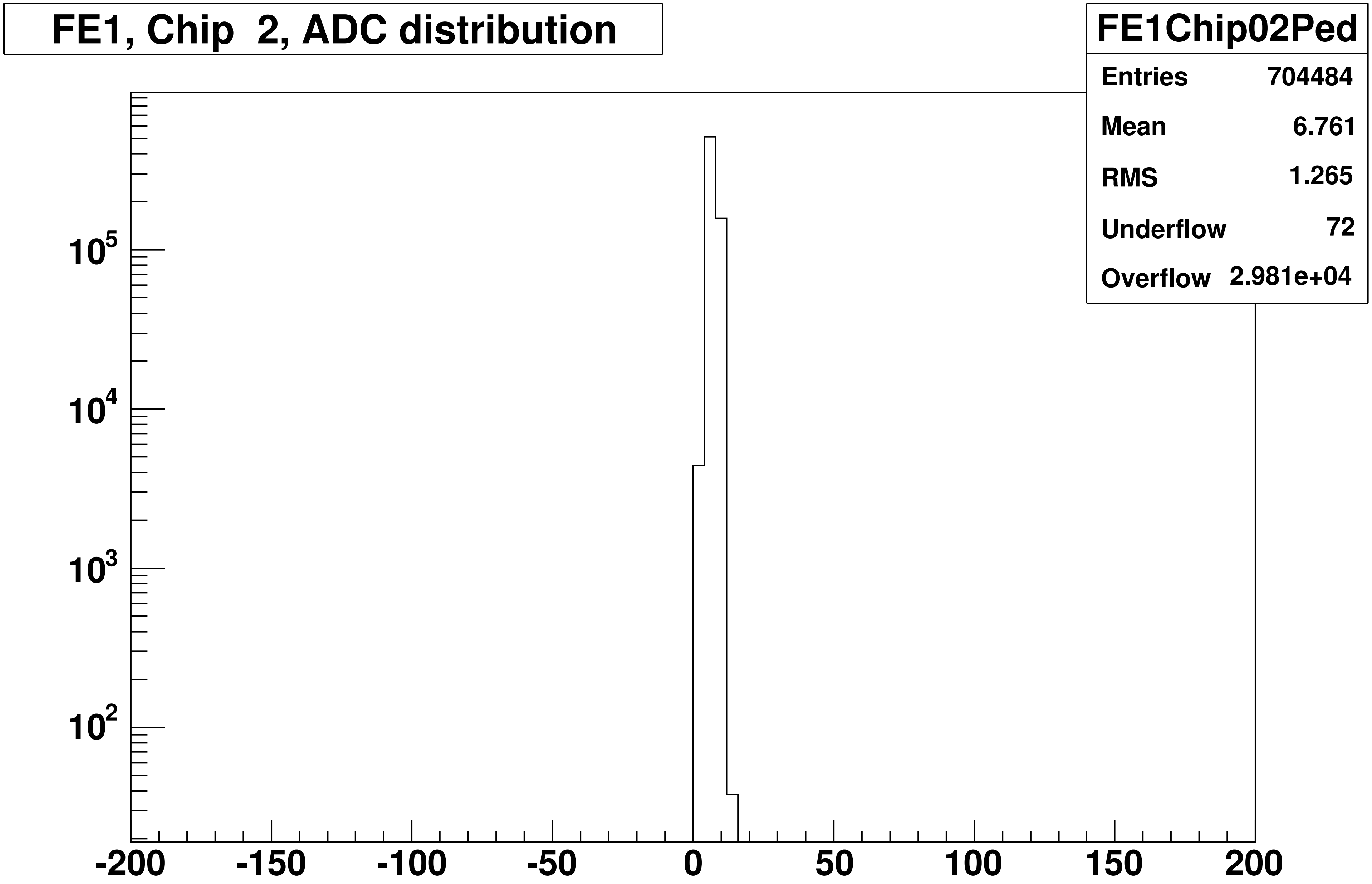
FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 704484
Mean 6.761
RMS 1.265
Underflow 72
Overflow 2.981e+04
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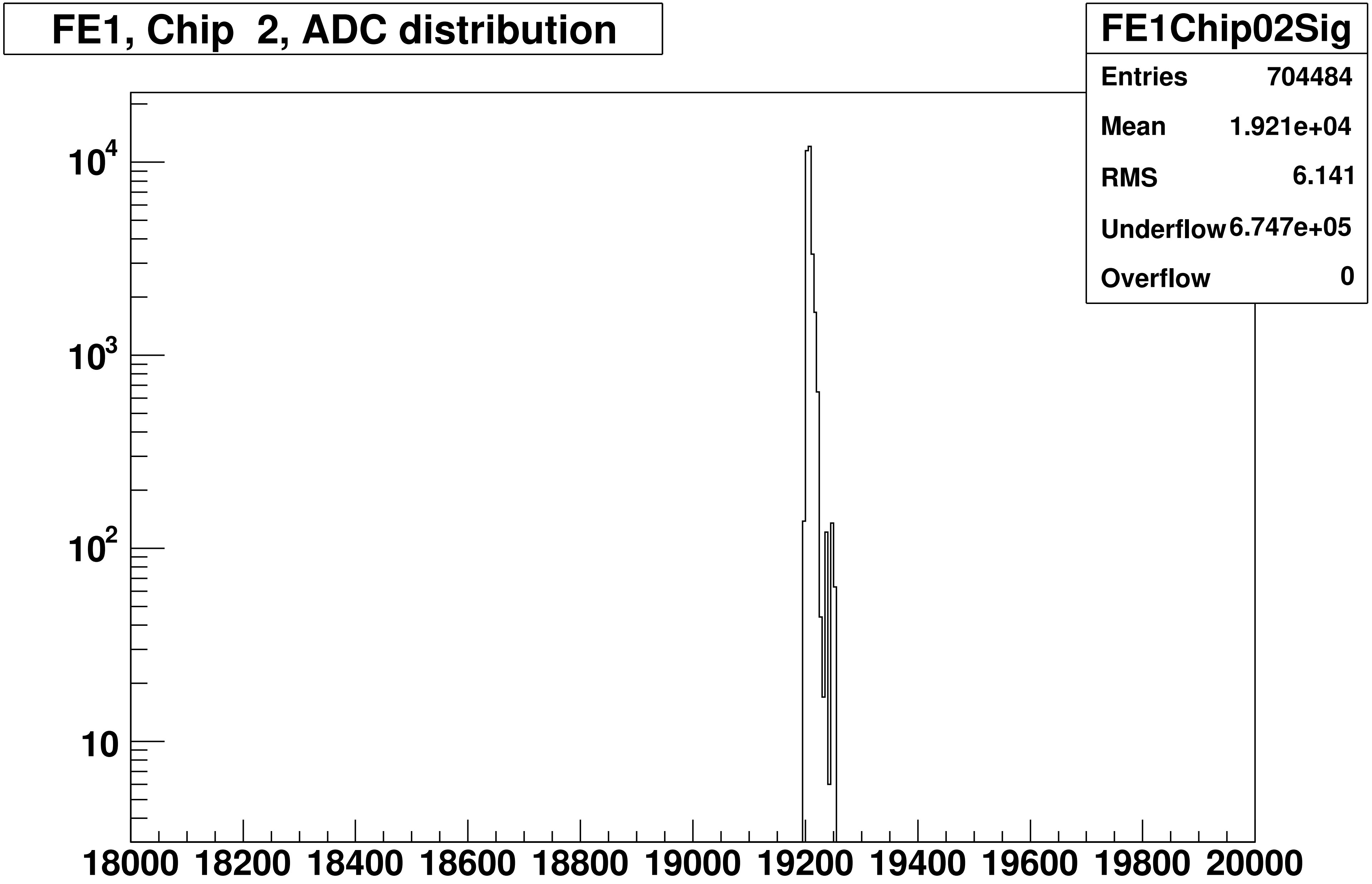
FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 704484
Mean 1.921e+04
RMS 6.141
Underflow 6.747e+05
Overflow 0
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10^3
10^2
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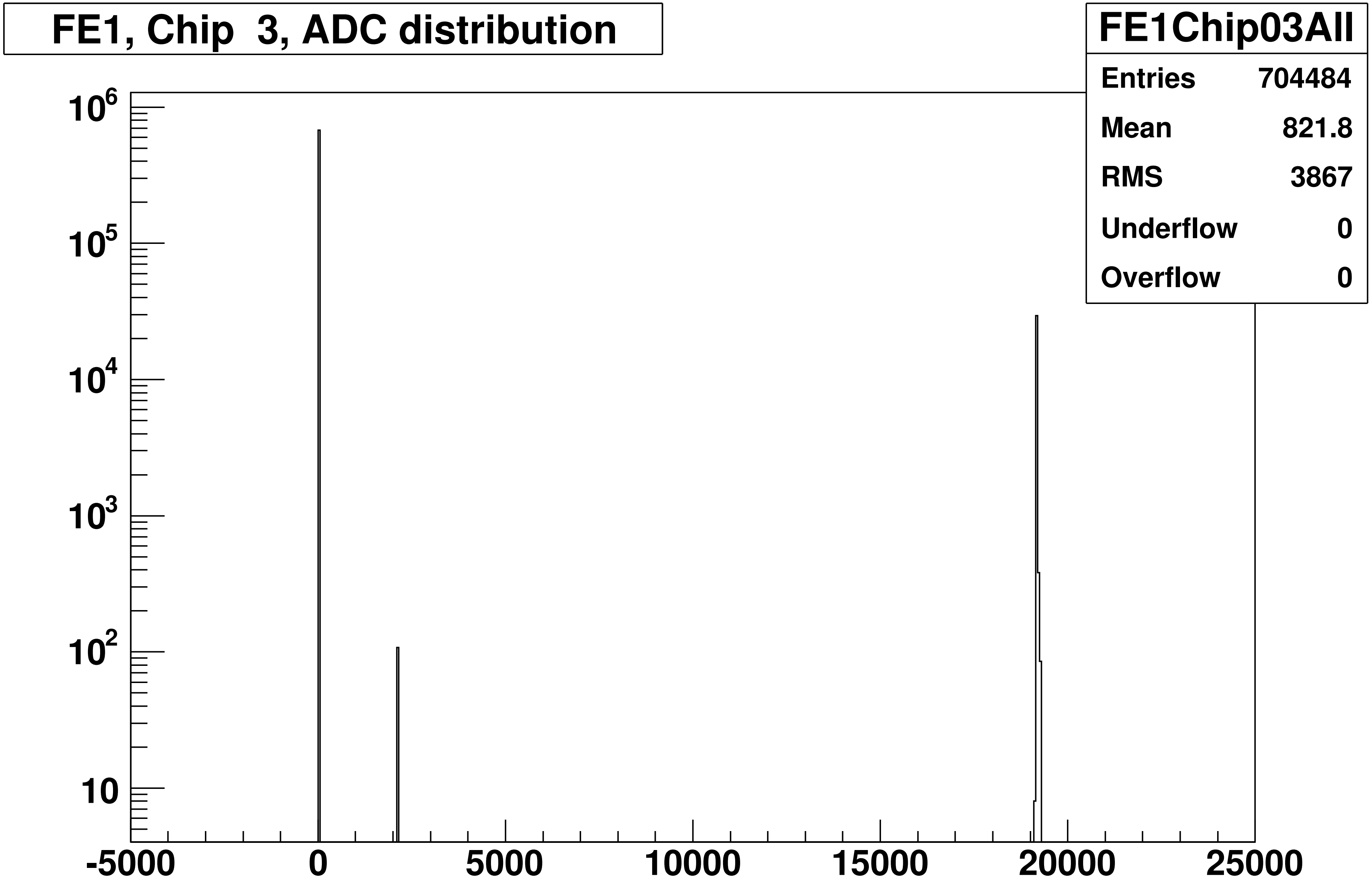
FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 704484
Mean 821.8
RMS 3867
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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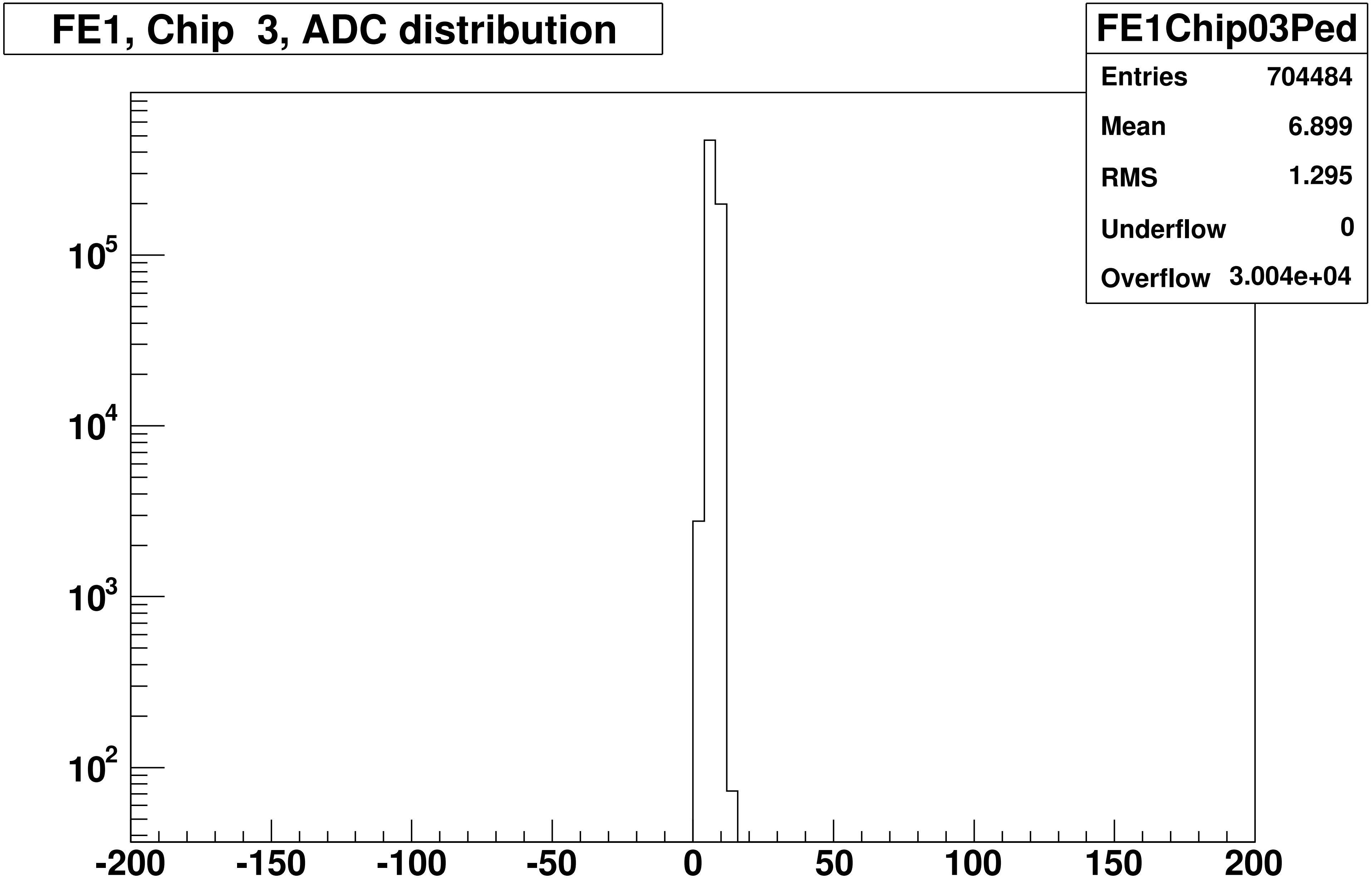

FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 704484
Mean 6.899
RMS 1.295
Underflow 0
Overflow 3.004e+04
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10^4
10^3
10^2
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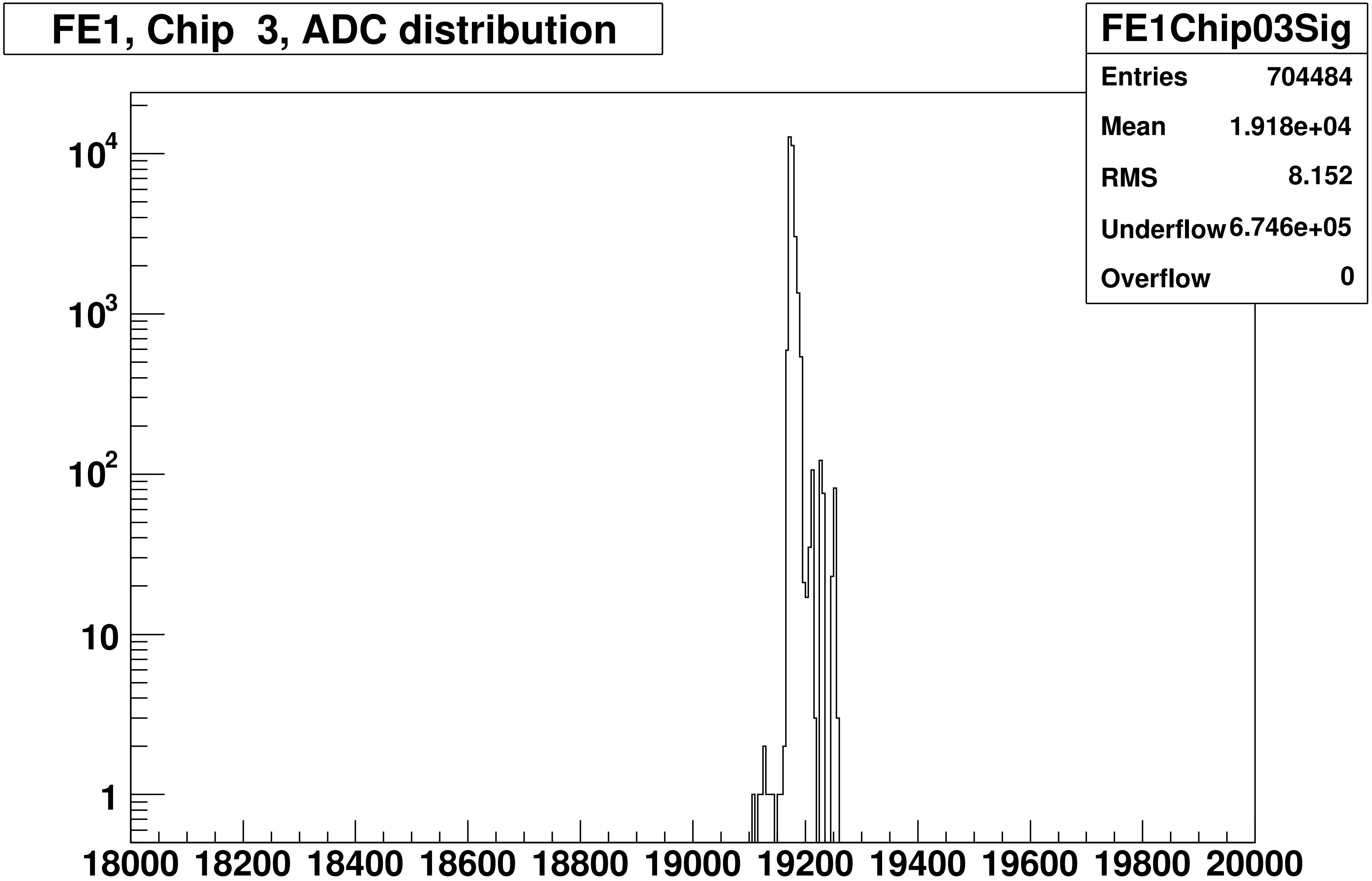
150
200


FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 704484
Mean 1.918e+04
RMS 8.152
Underflow 6.746e+05
Overflow 0
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10^3
10^2
10
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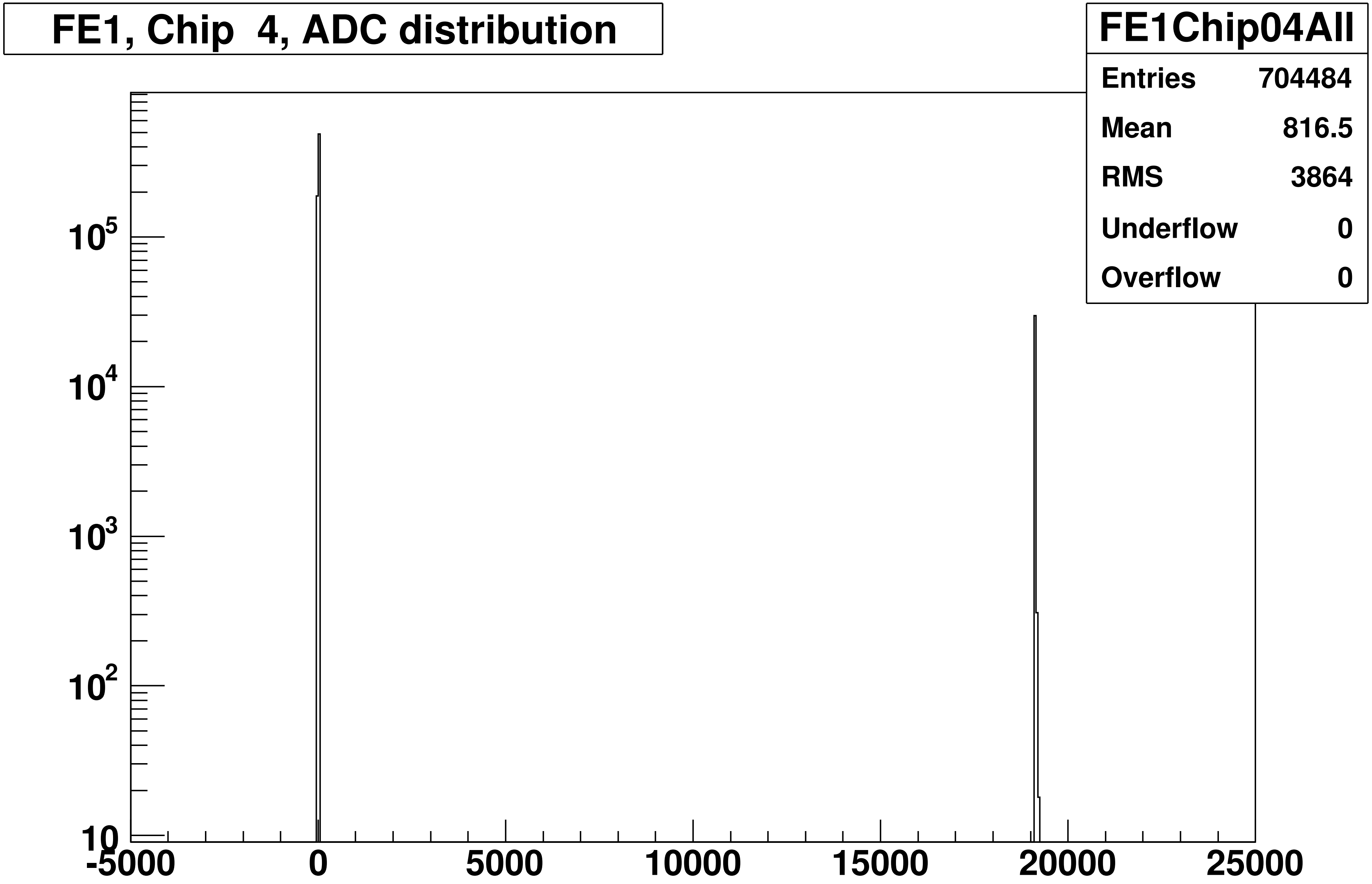
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 704484
Mean 816.5
RMS 3864
Underflow 0
Overflow 0
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10^4
10^3
10^2
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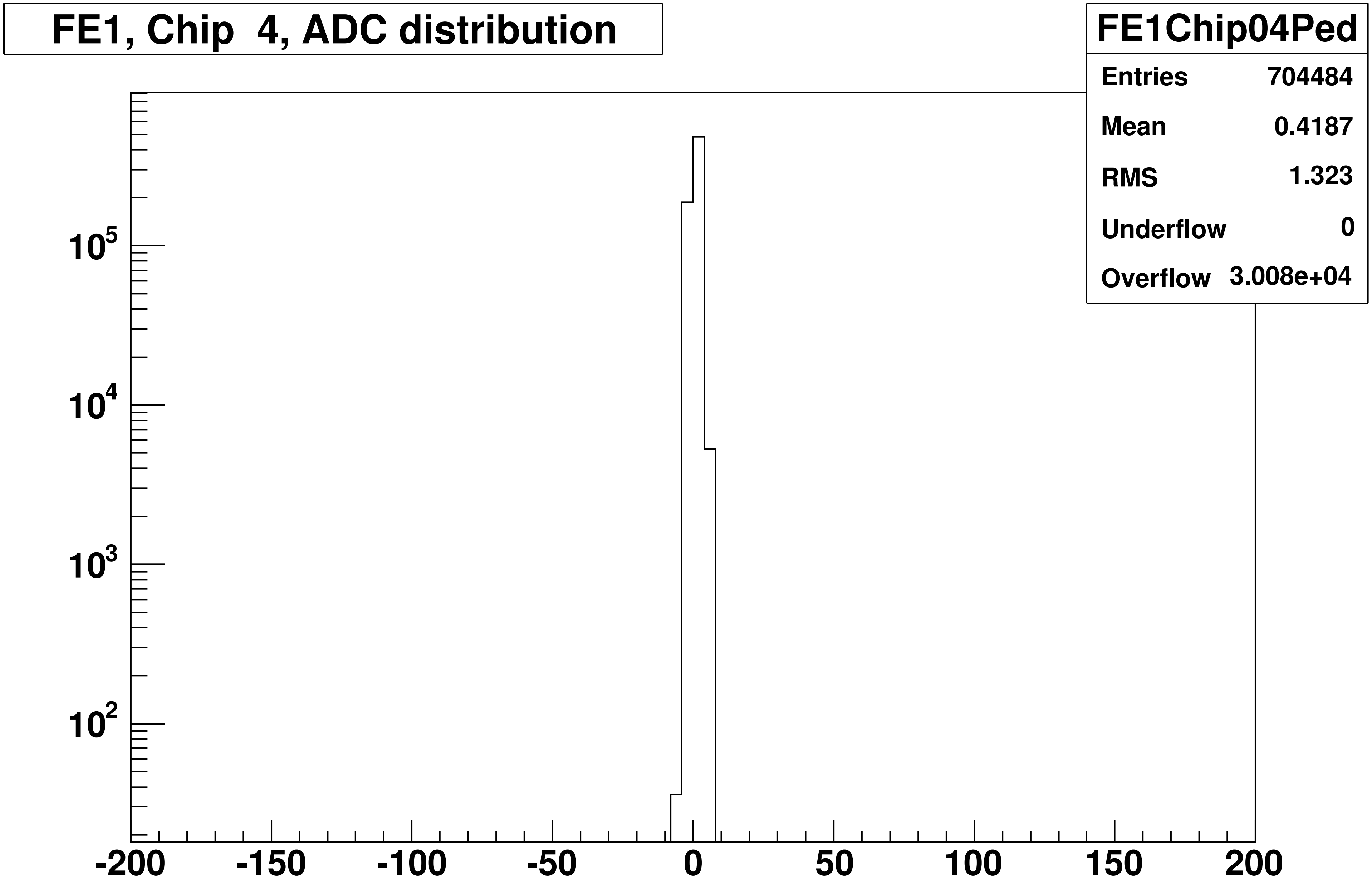
25000


FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 704484
Mean 0.4187
RMS 1.323
Underflow 0
Overflow 3.008e+04
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10^3
10^2
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0
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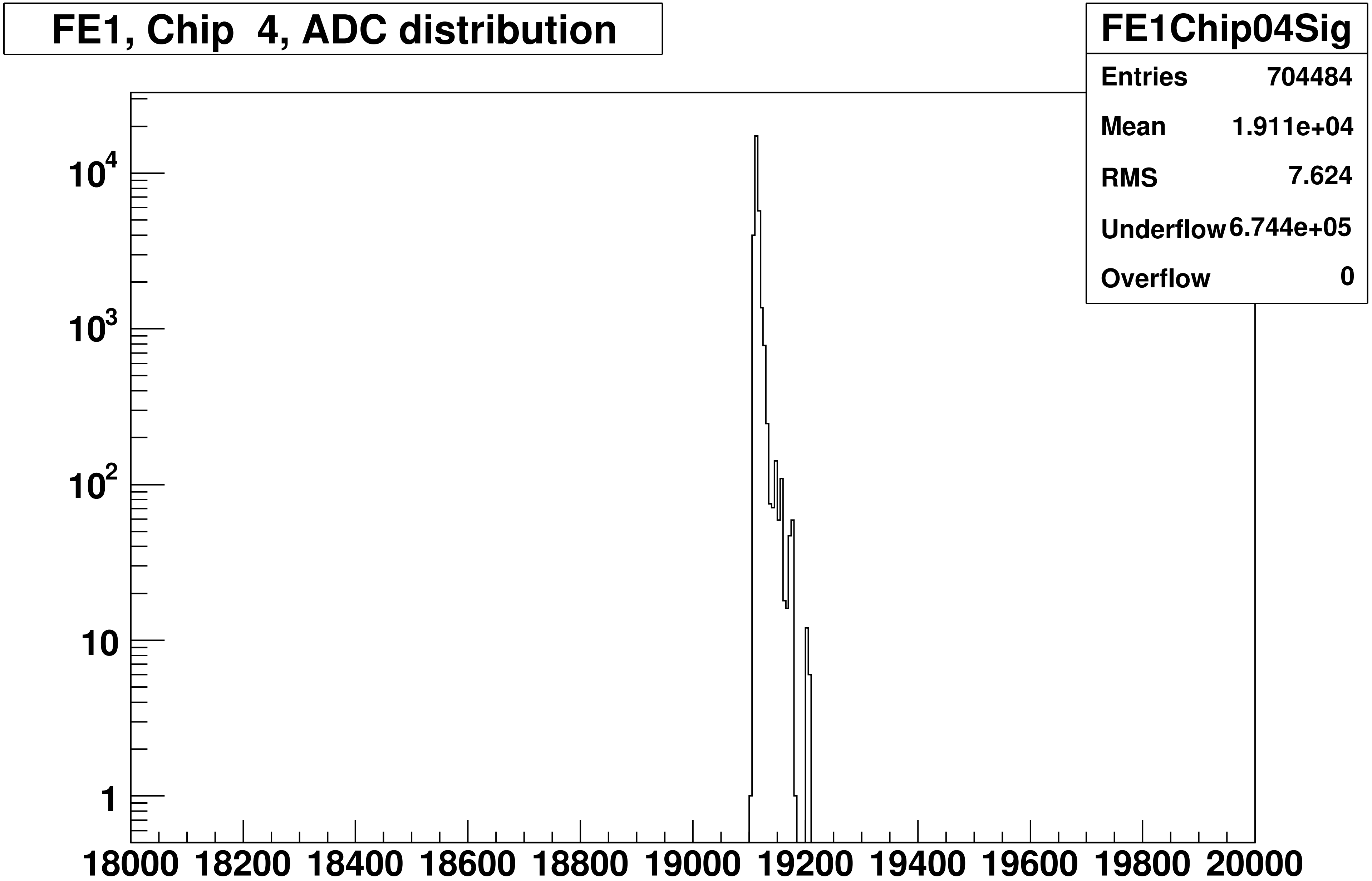
150
200



FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 704484
Mean 1.911e+04
RMS 7.624
Underflow 6.744e+05
Overflow 0
10^4
10^3
10^2
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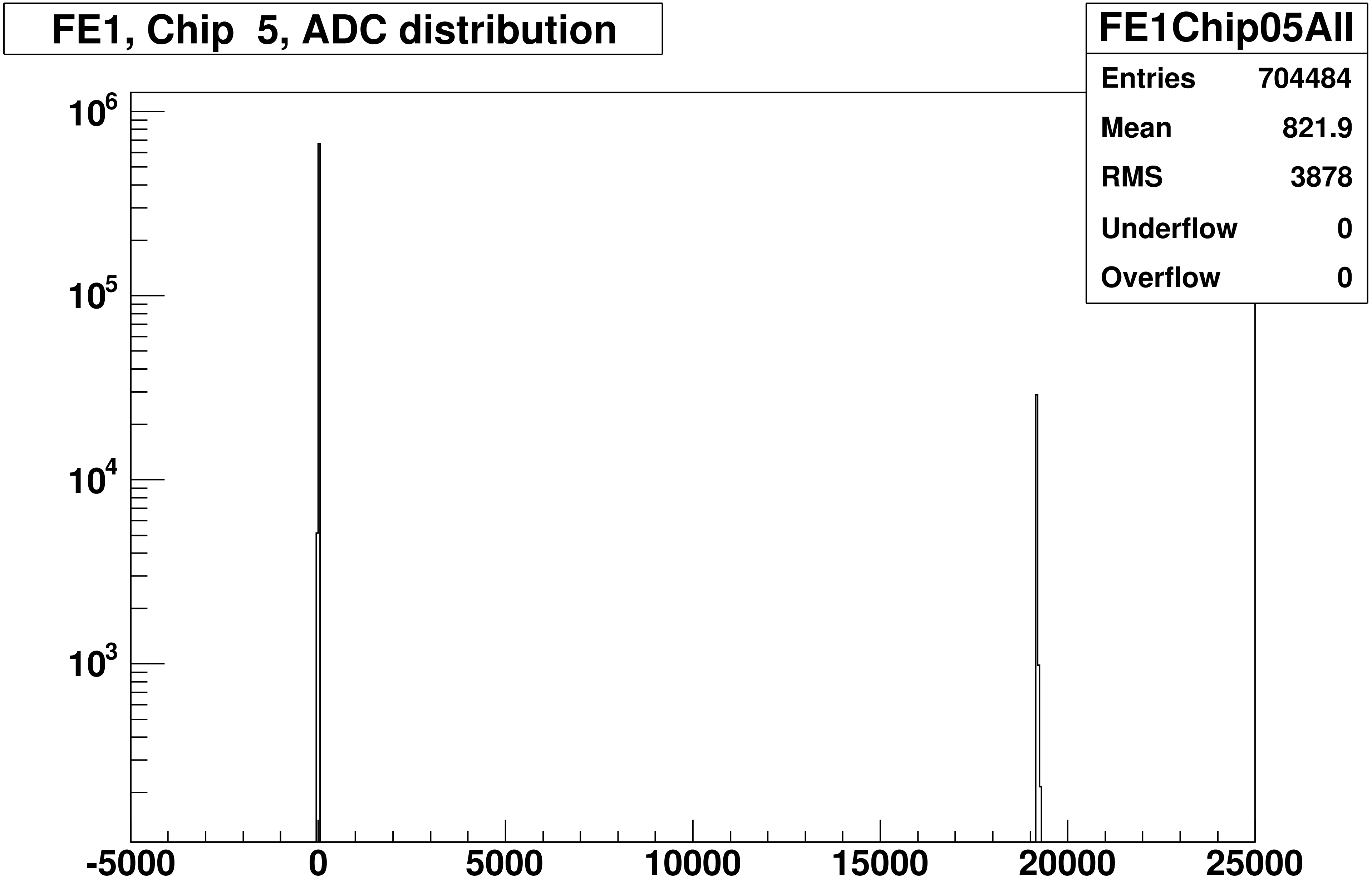
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 704484
Mean 821.9
RMS 3878
Underflow 0
Overflow 0
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10^5
10^4
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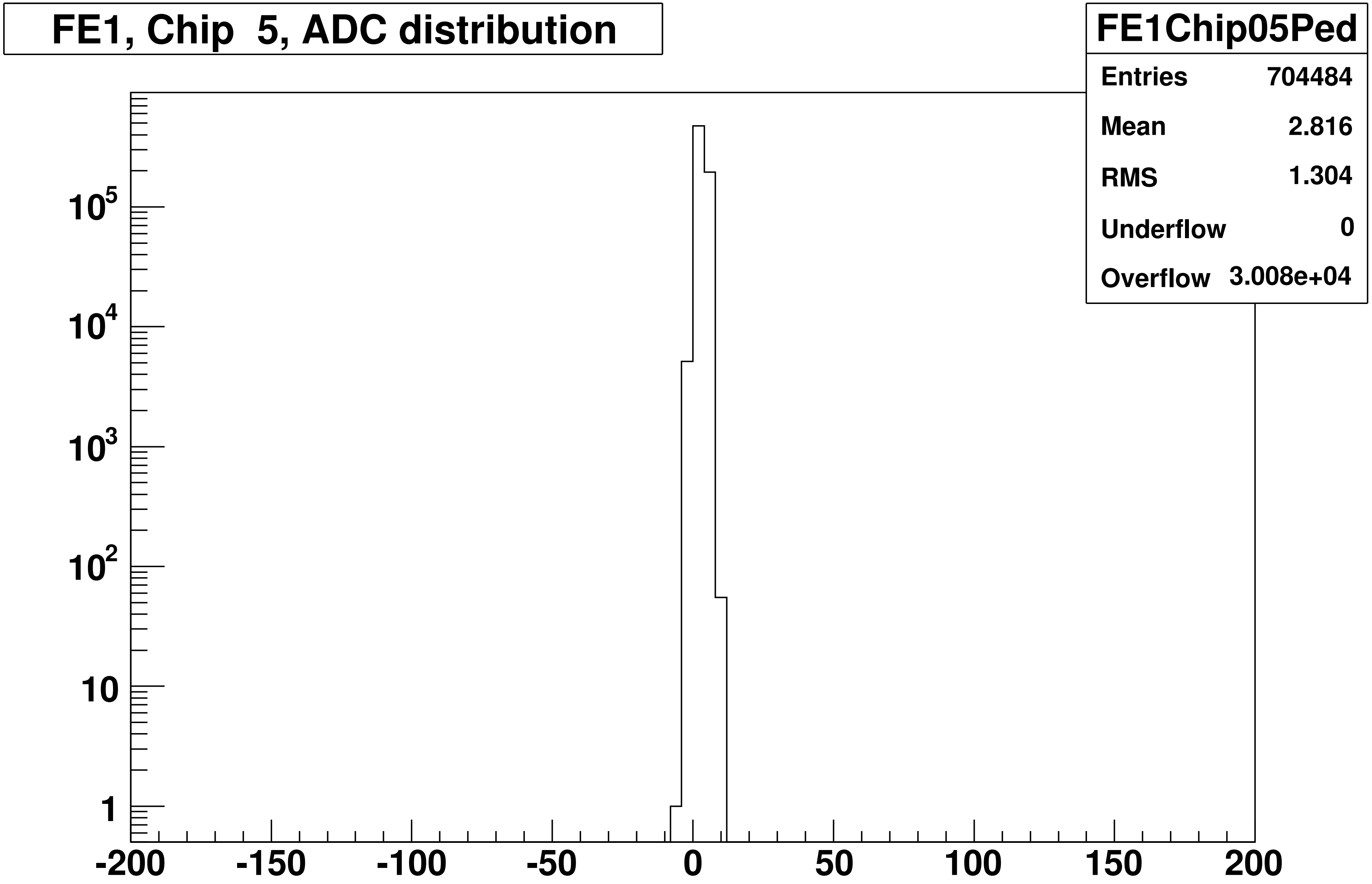
FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 704484
Mean 2.816
RMS 1.304
Underflow 0
Overflow 3.008e+04
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10^3
10^2
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150
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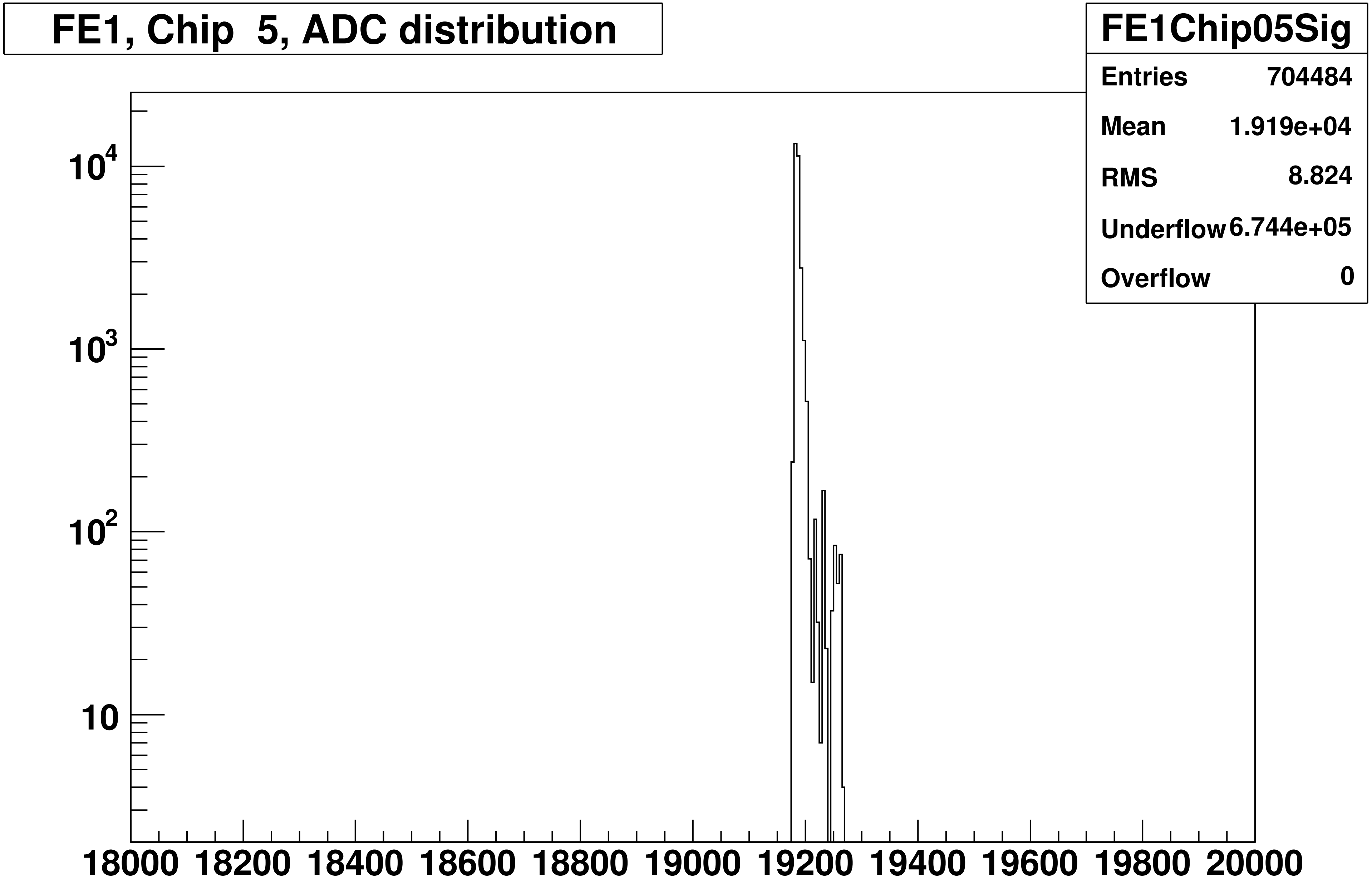
FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 704484
Mean 1.919e+04
RMS 8.824
Underflow 6.744e+05
Overflow 0
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10^3
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10
18000
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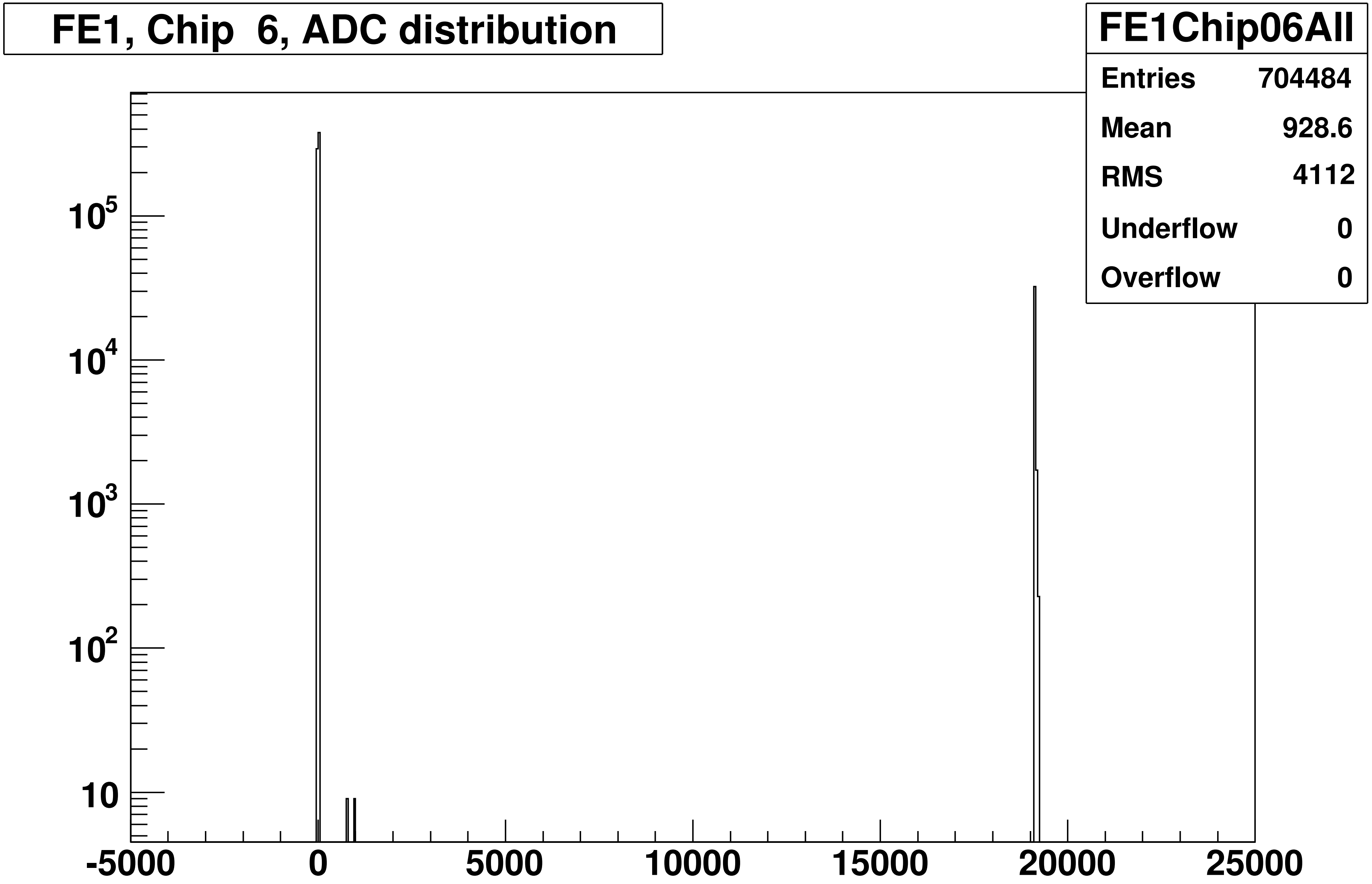
20000



FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 704484
Mean 928.6
RMS 4112
Underflow 0
Overflow 0
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10^4
10^3
10^2
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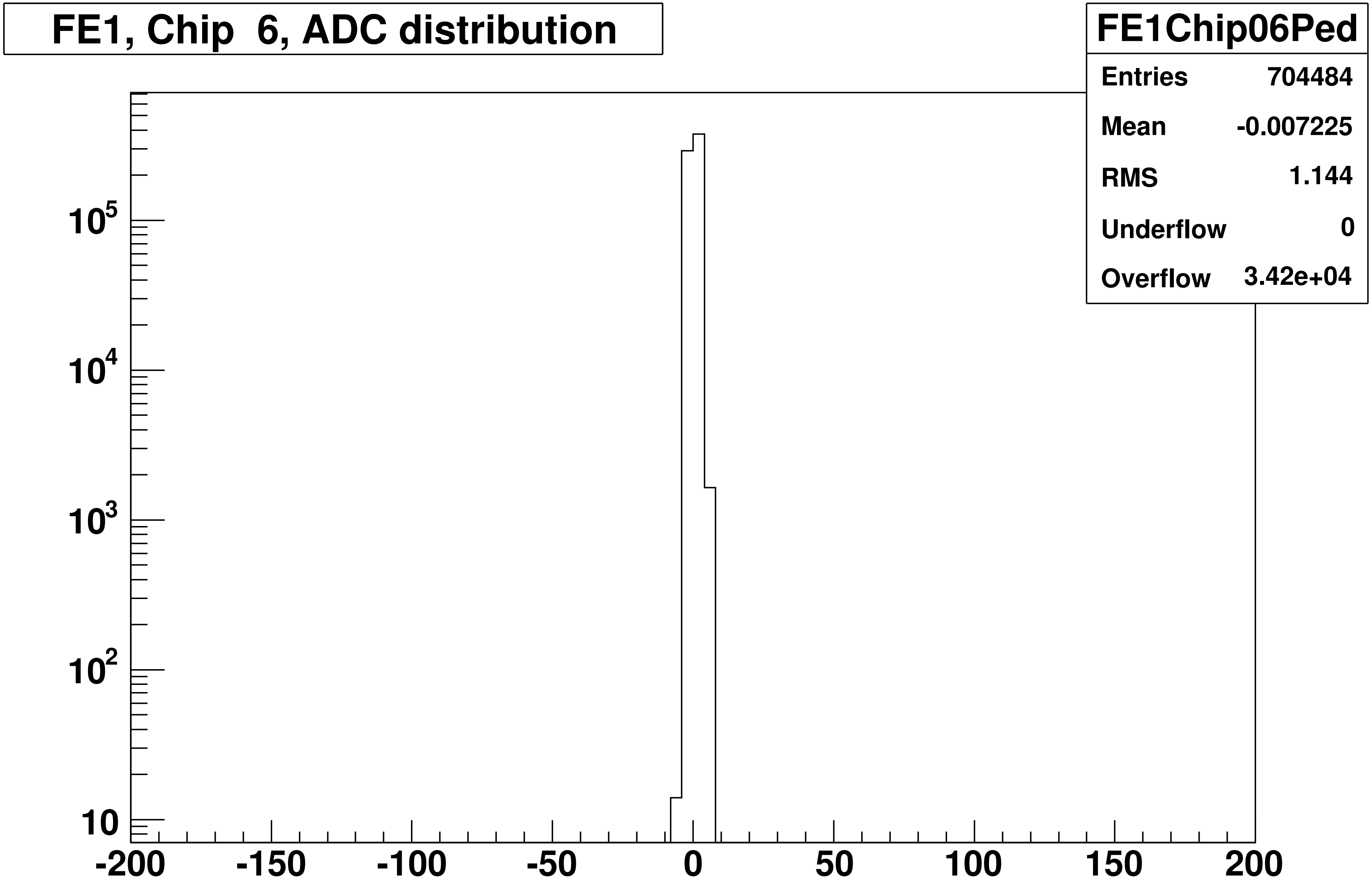
0
5000
10000
15000
20000
25000


FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 704484
Mean -0.007225
RMS 1.144
Underflow 0
Overflow 3.42e+04
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10⁴
10³
10²
10
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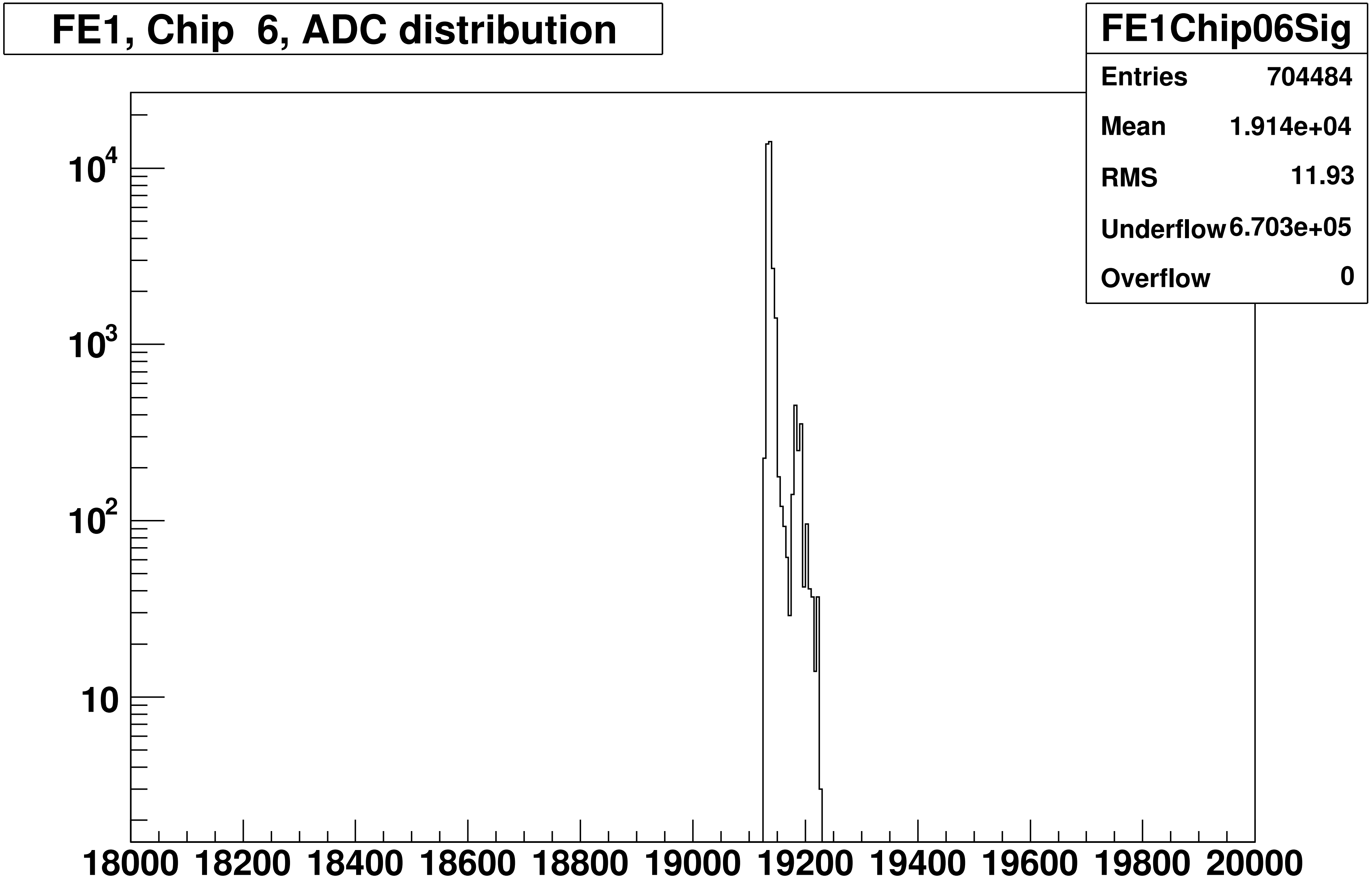
-150
-100
-50
0
50
100
150
200


FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 704484
Mean 1.914e+04
RMS 11.93
Underflow 6.703e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
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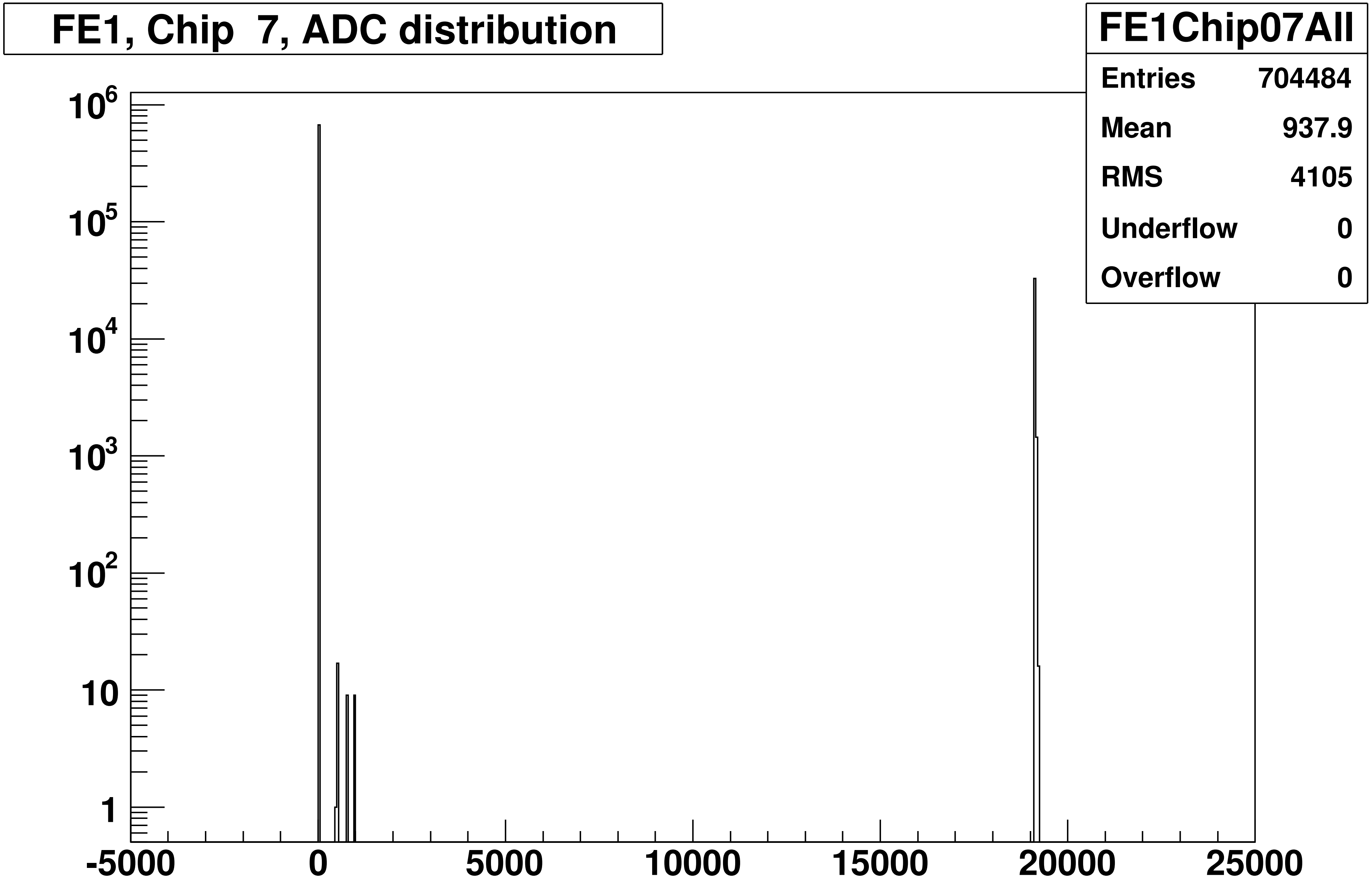
19600
19800
20000


FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 704484
Mean 937.9
RMS 4105
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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-5000
0
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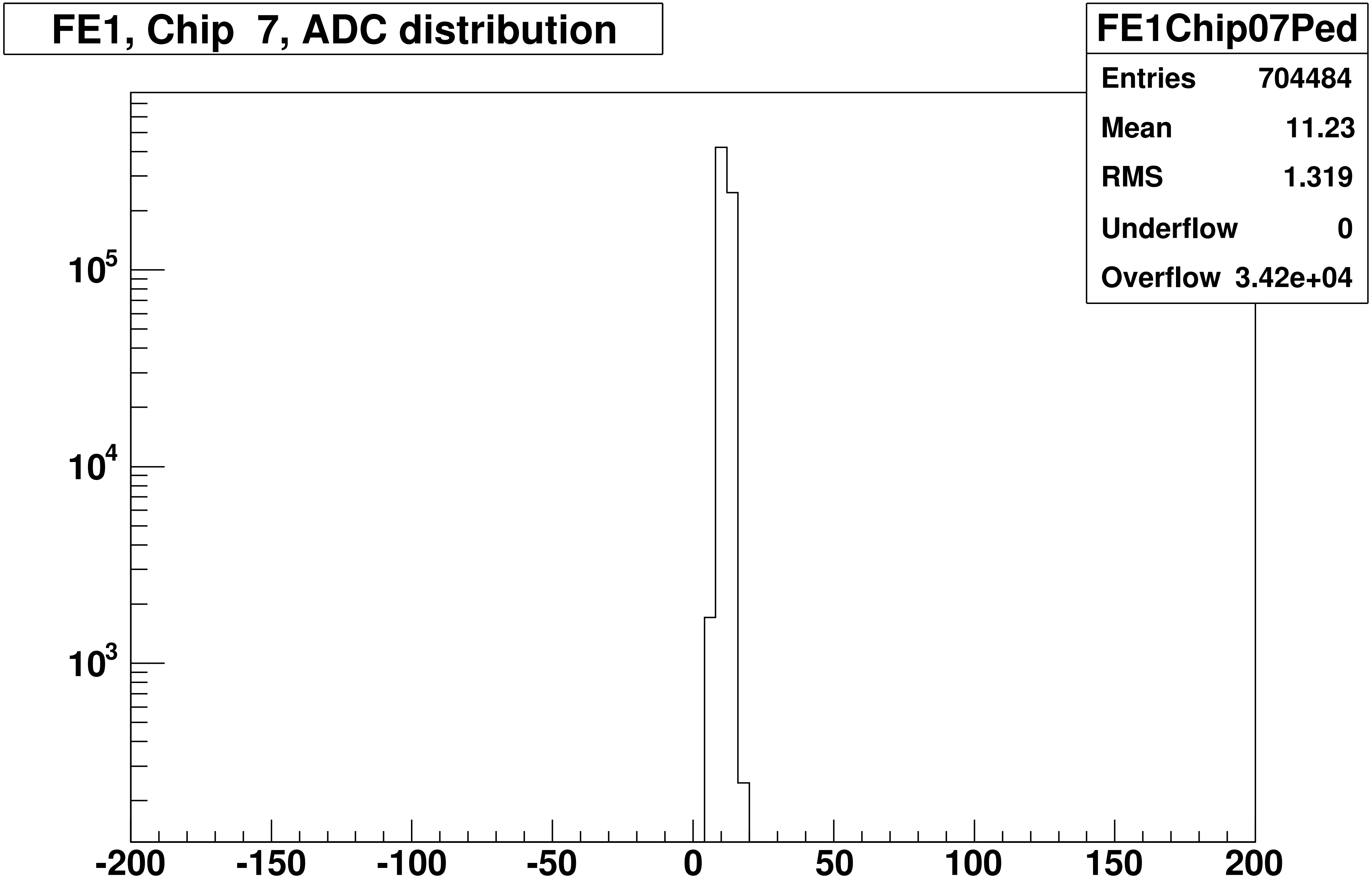
10000
15000
20000
25000


FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 704484
Mean 11.23
RMS 1.319
Underflow 0
Overflow 3.42e+04
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10^4
10^3
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-100
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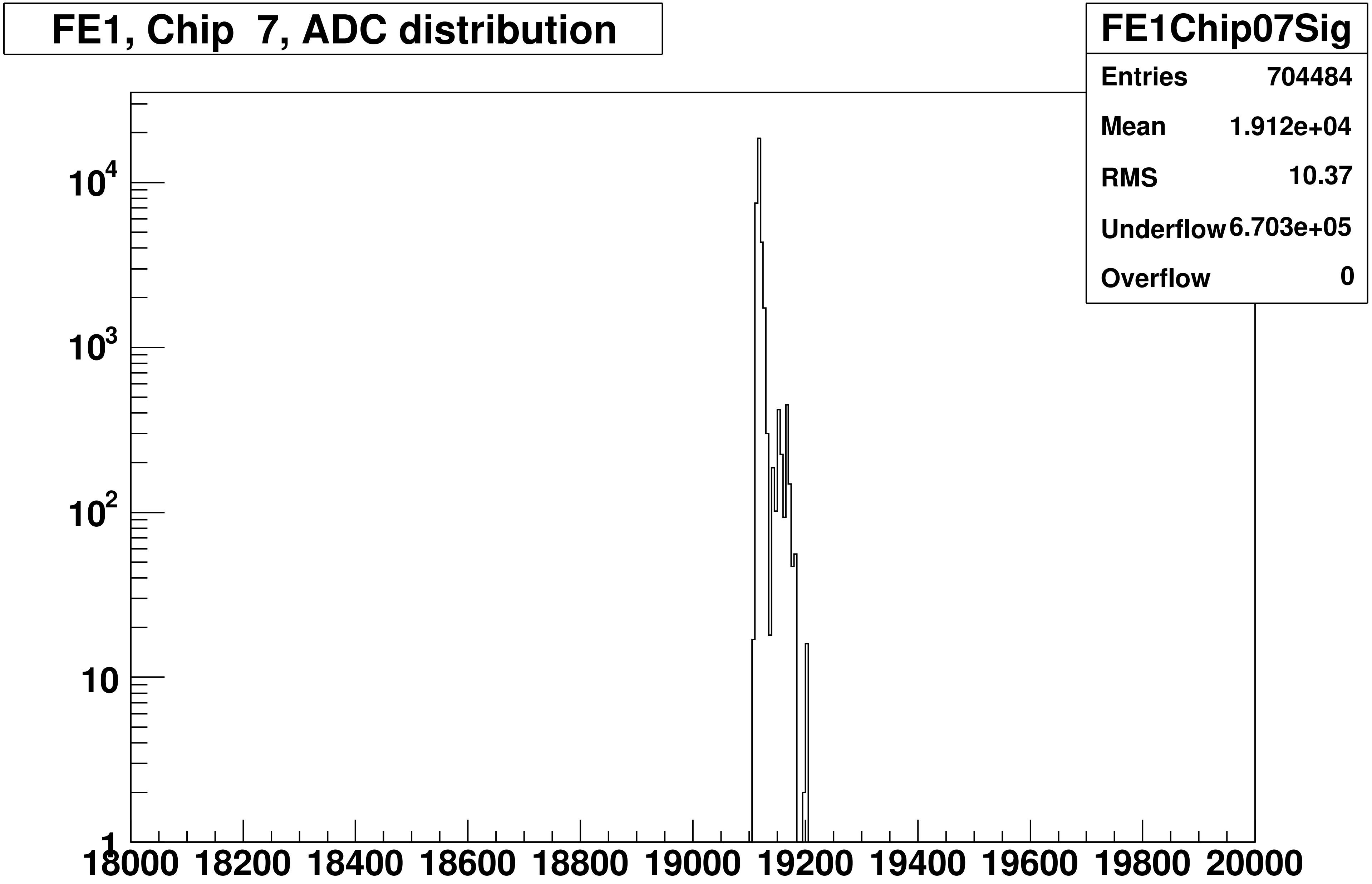
FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 704484
Mean 1.912e+04
RMS 10.37
Underflow 6.703e+05
Overflow 0
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10^3
10^2
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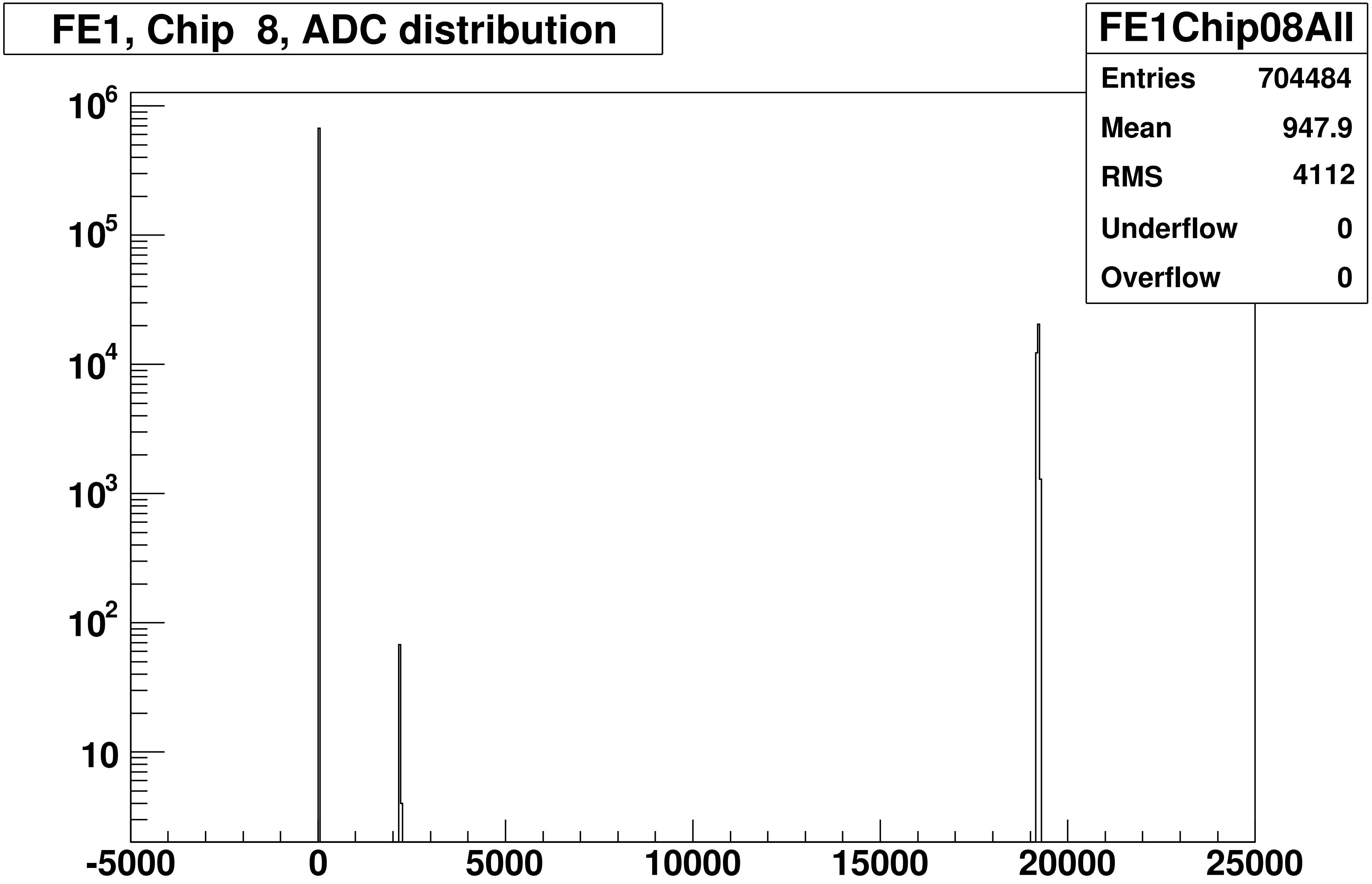
FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 704484
Mean 947.9
RMS 4112
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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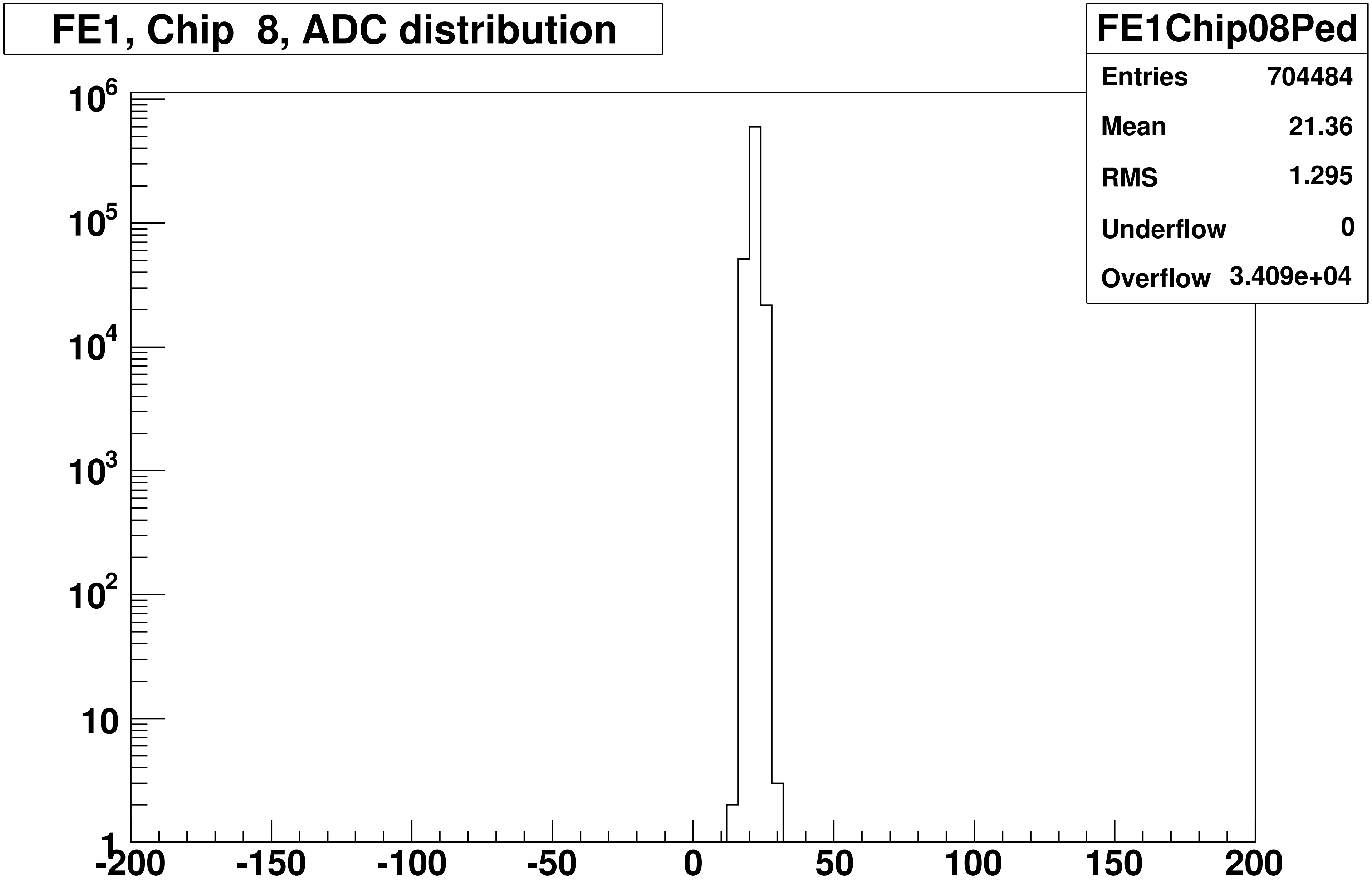
15000
20000
25000


FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 704484
Mean 21.36
RMS 1.295
Underflow 0
Overflow 3.409e+04
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10^5
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10^3
10^2
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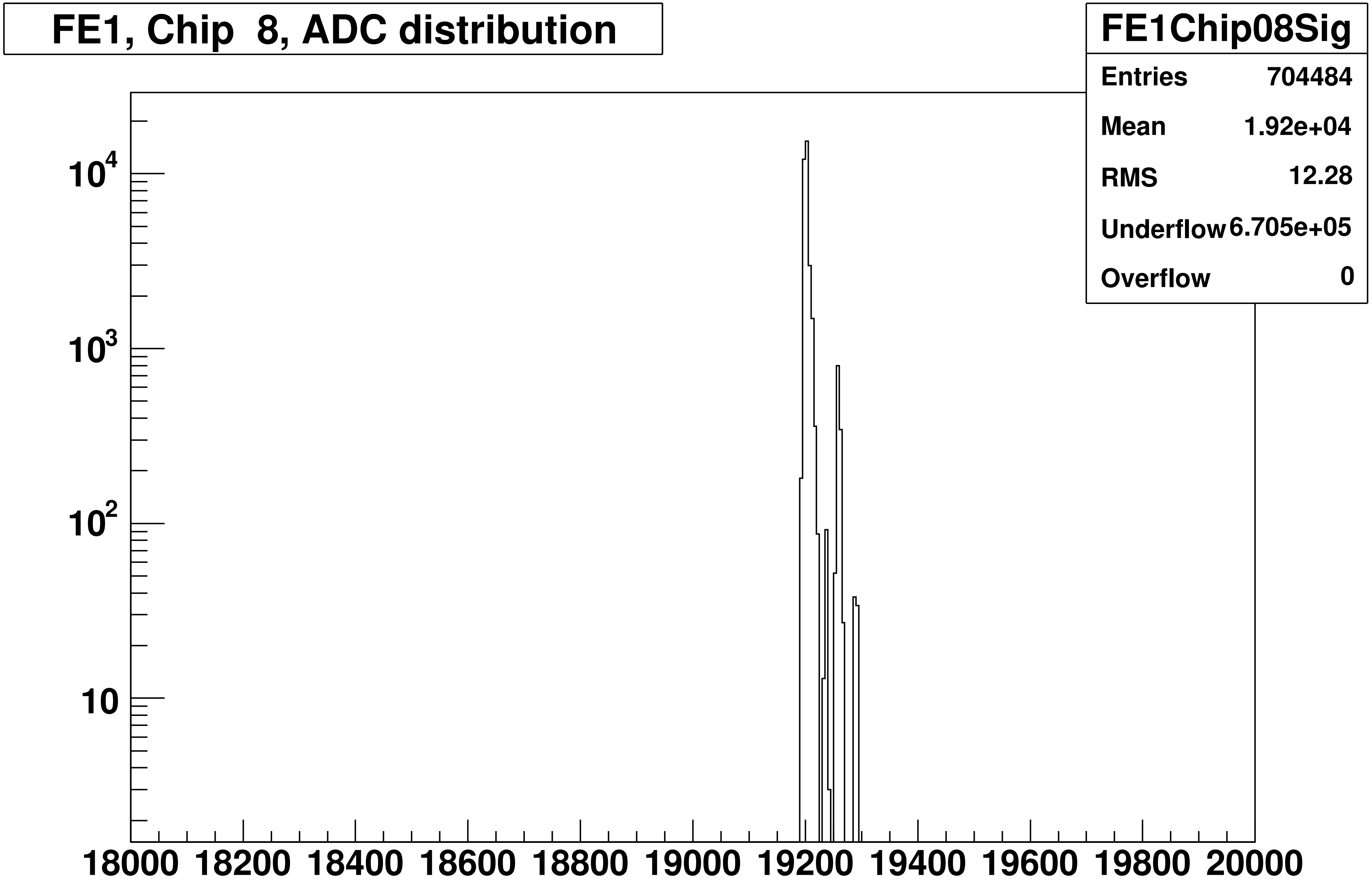
50
100
150
200



FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 704484
Mean 1.92e+04
RMS 12.28
Underflow 6.705e+05
Overflow 0
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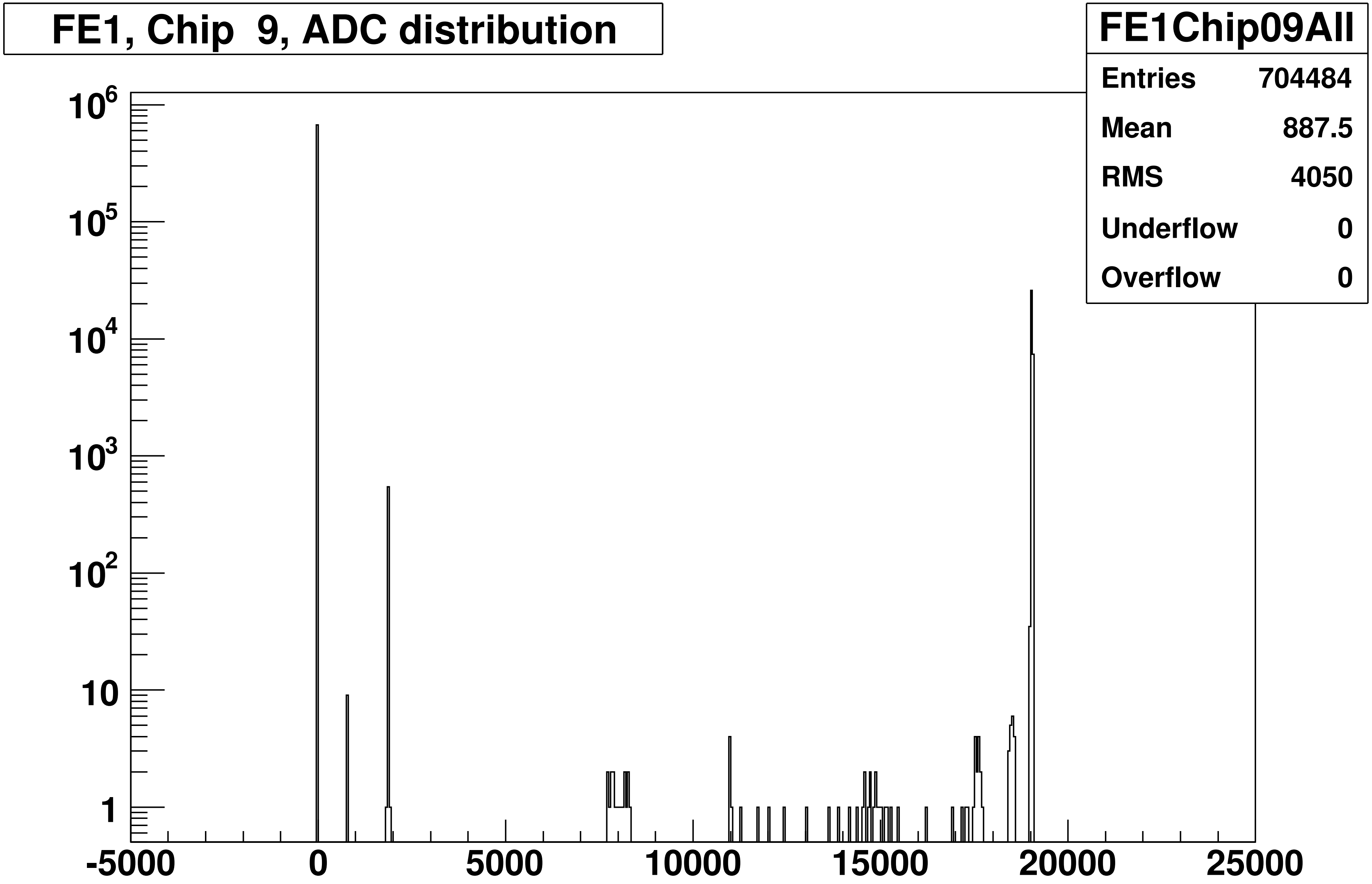
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 704484
Mean 887.5
RMS 4050
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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20000
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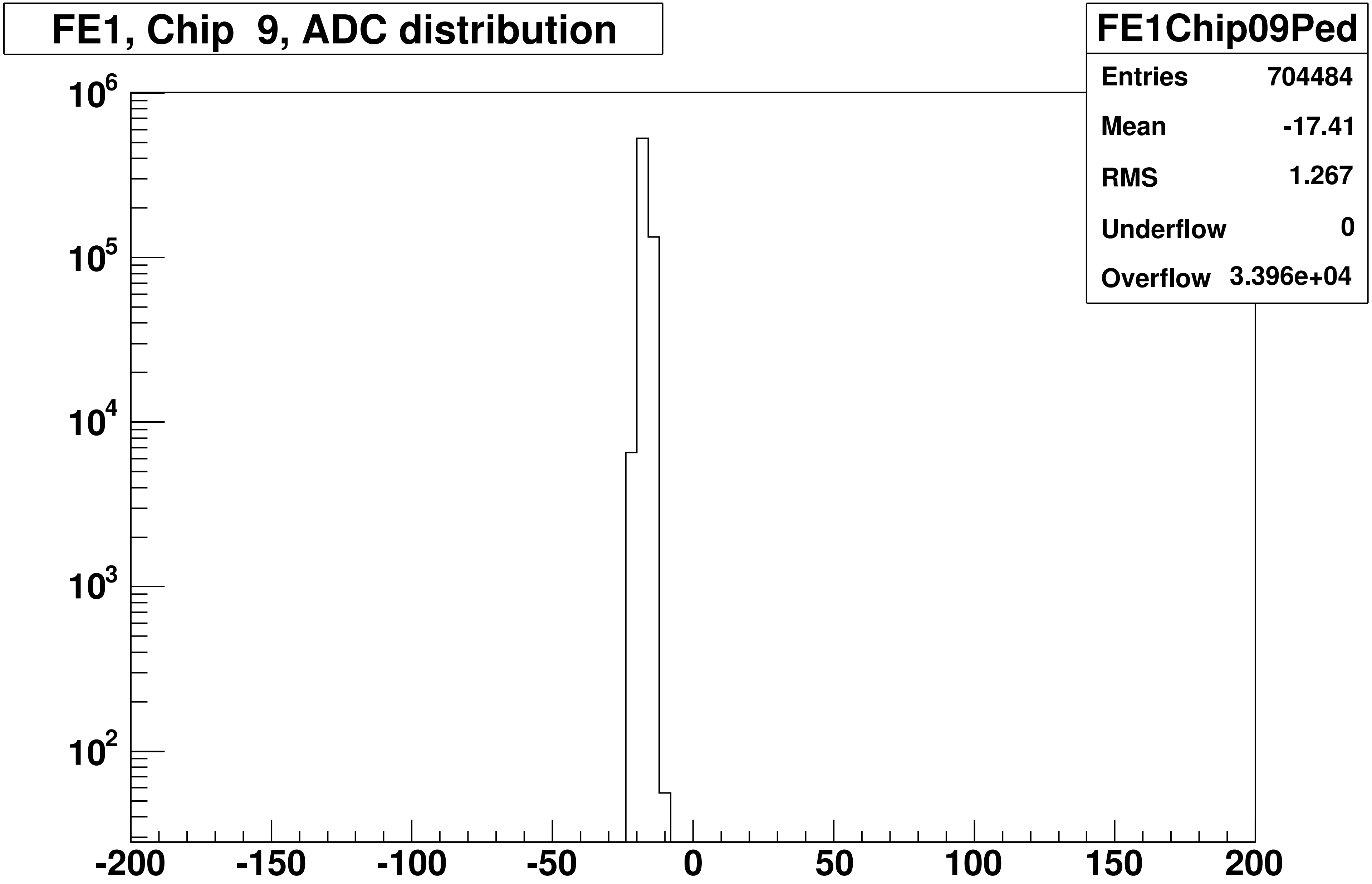
FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 704484
Mean -17.41
RMS 1.267
Underflow 0
Overflow 3.396e+04
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10^5
10^4
10^3
10^2
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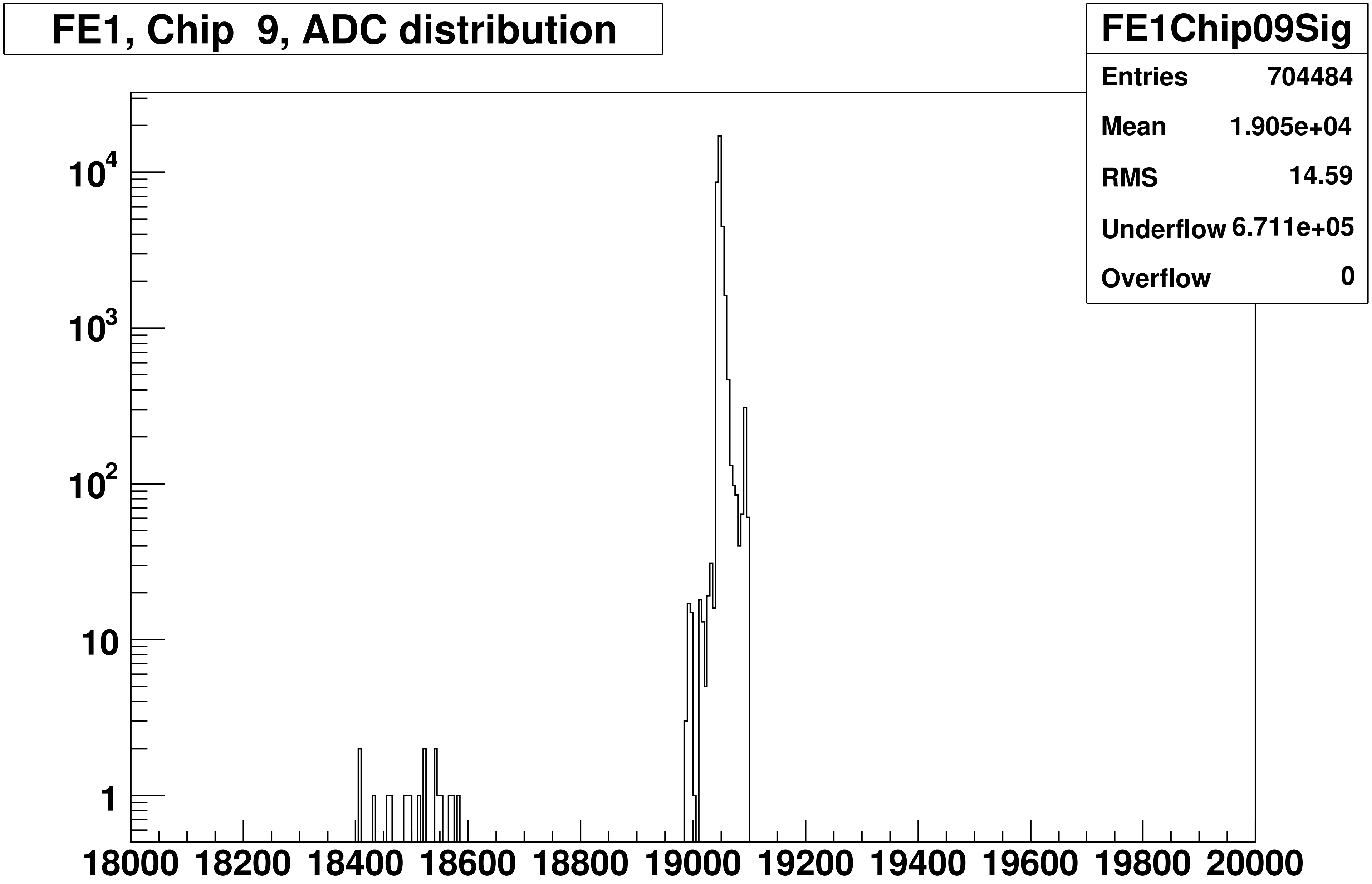
150
200


FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 704484
Mean 1.905e+04
RMS 14.59
Underflow 6.711e+05
Overflow 0
10^4
10^3
10^2
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1
18000
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18400
18600
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19000
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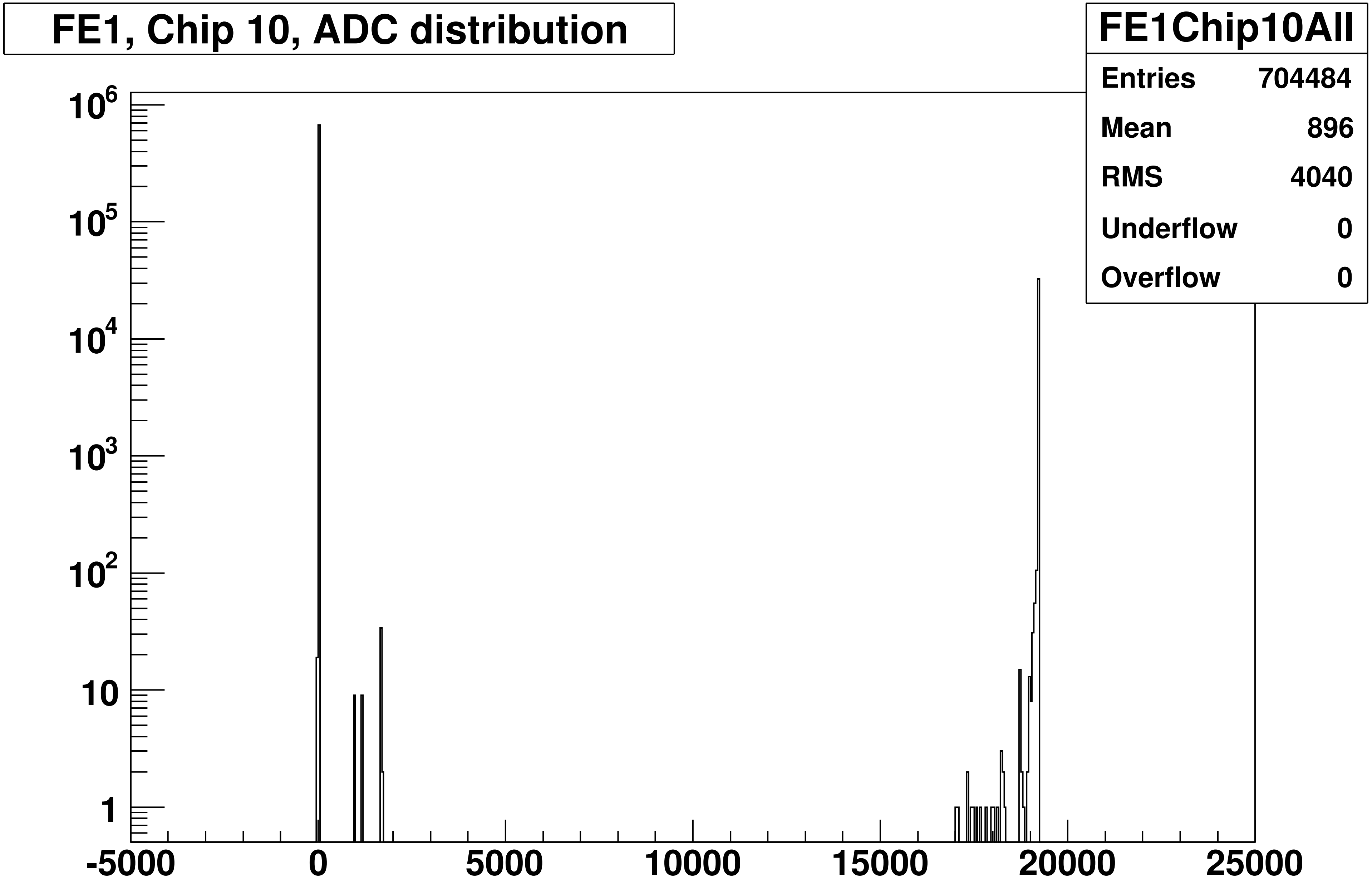
FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 704484
Mean 896
RMS 4040
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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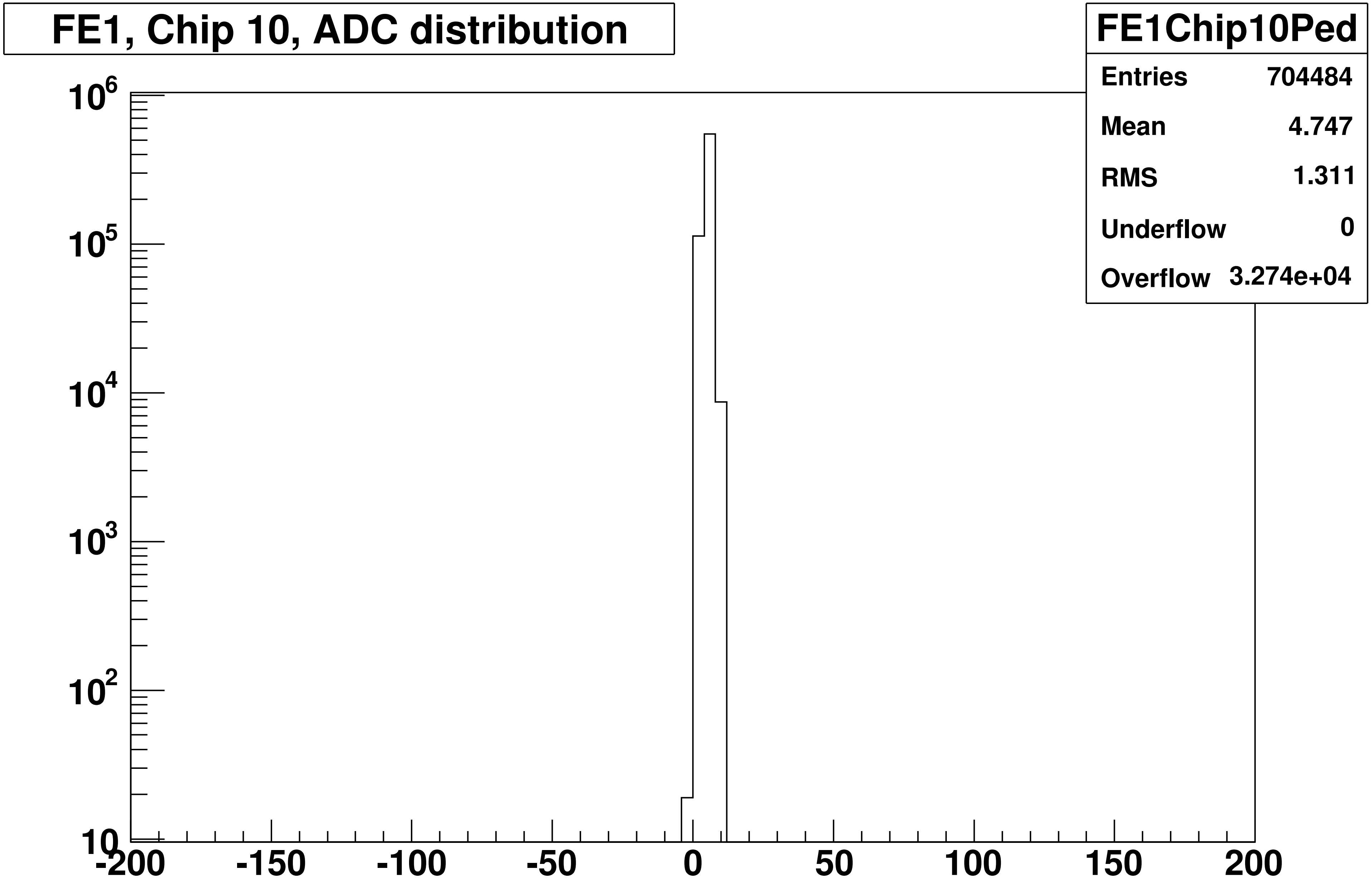

FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 704484
Mean 4.747
RMS 1.311
Underflow 0
Overflow 3.274e+04
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10^3
10^4
10^5
10^6
-200
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-100
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0
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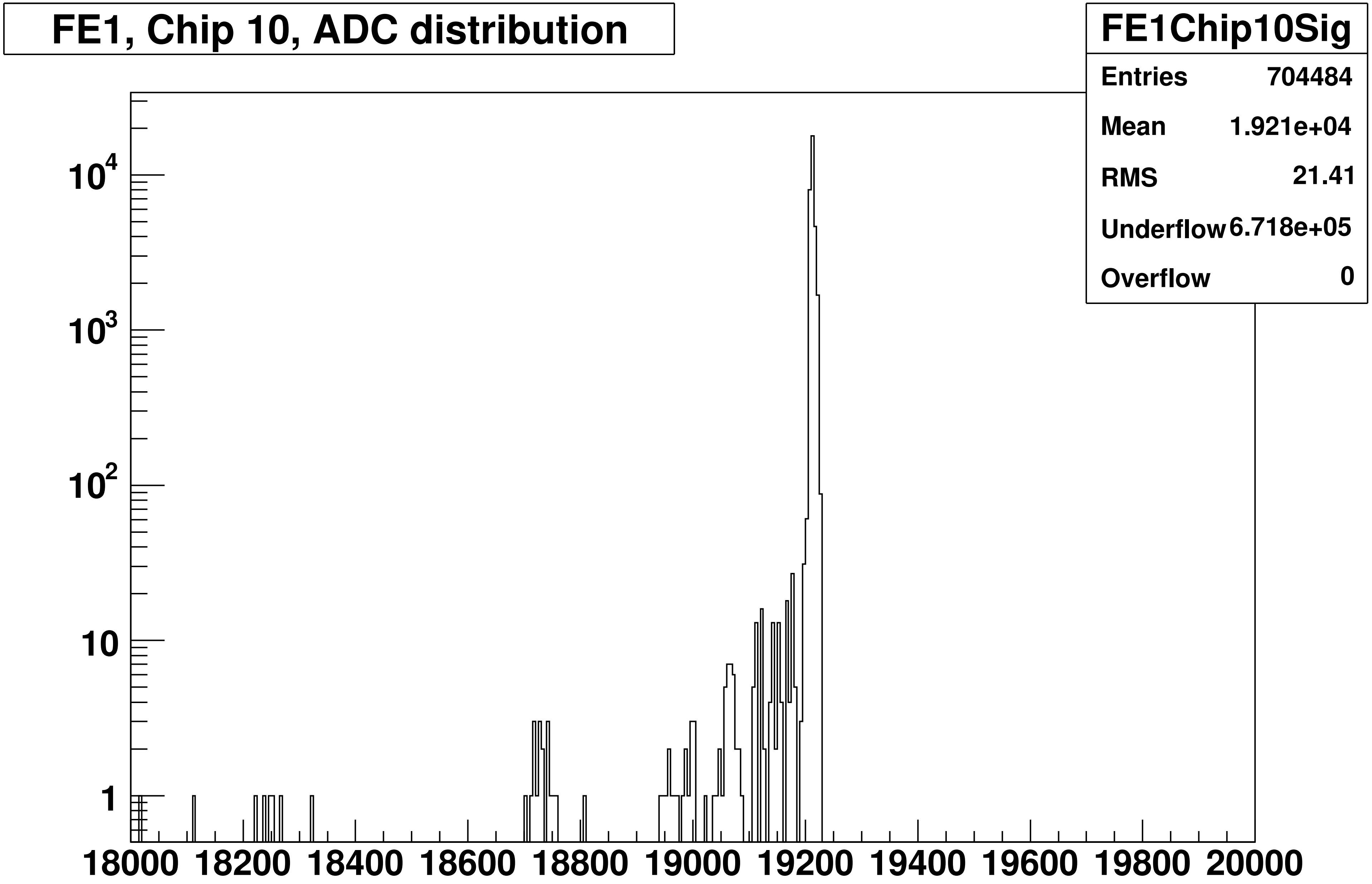
100
150
200


FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 704484
Mean 1.921e+04
RMS 21.41
Underflow 6.718e+05
Overflow 0
10^4
10^3
10^2
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1
18000
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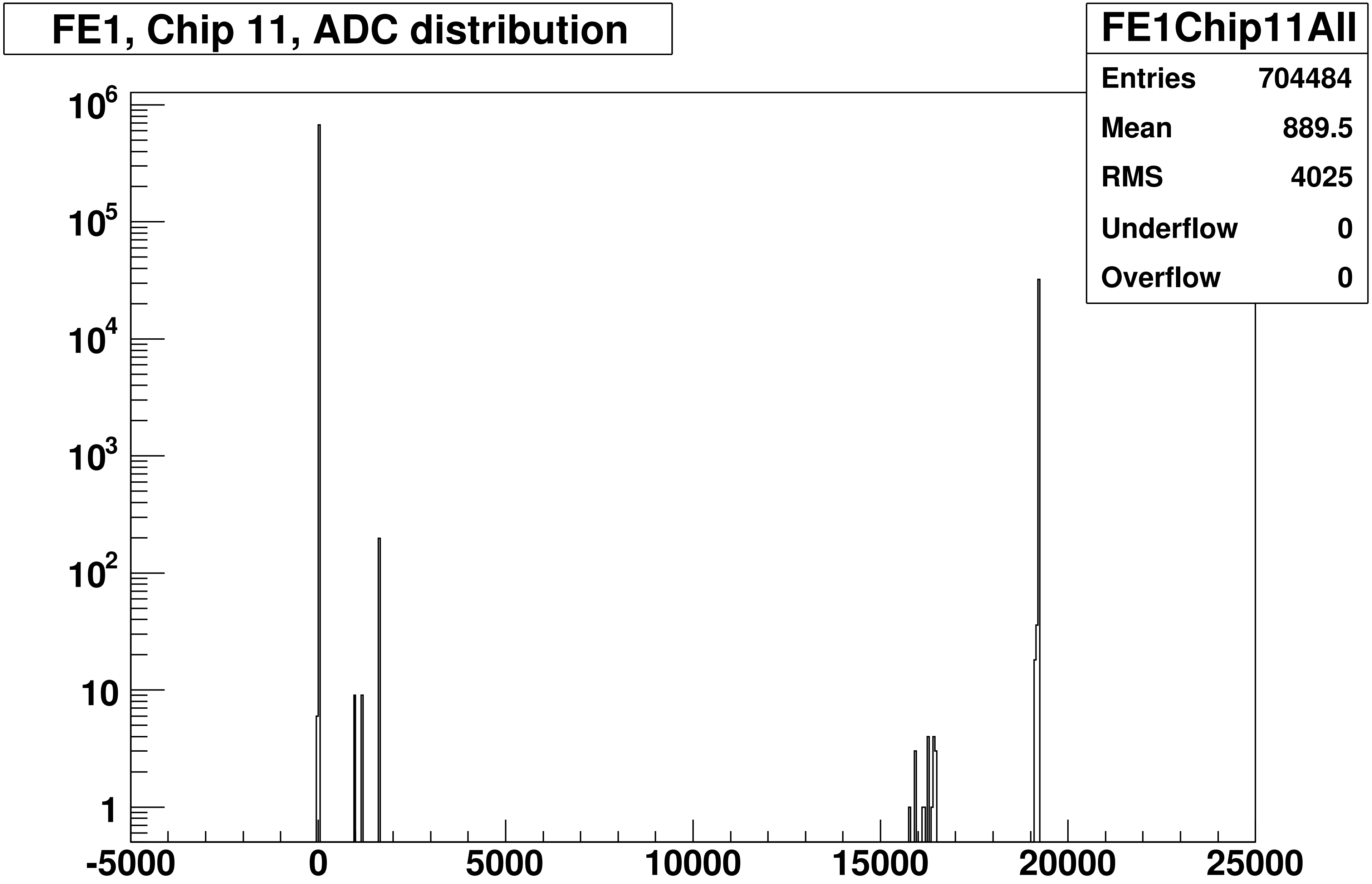
20000


FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 704484
Mean 889.5
RMS 4025
Underflow 0
Overflow 0
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10^4
10^3
10^2
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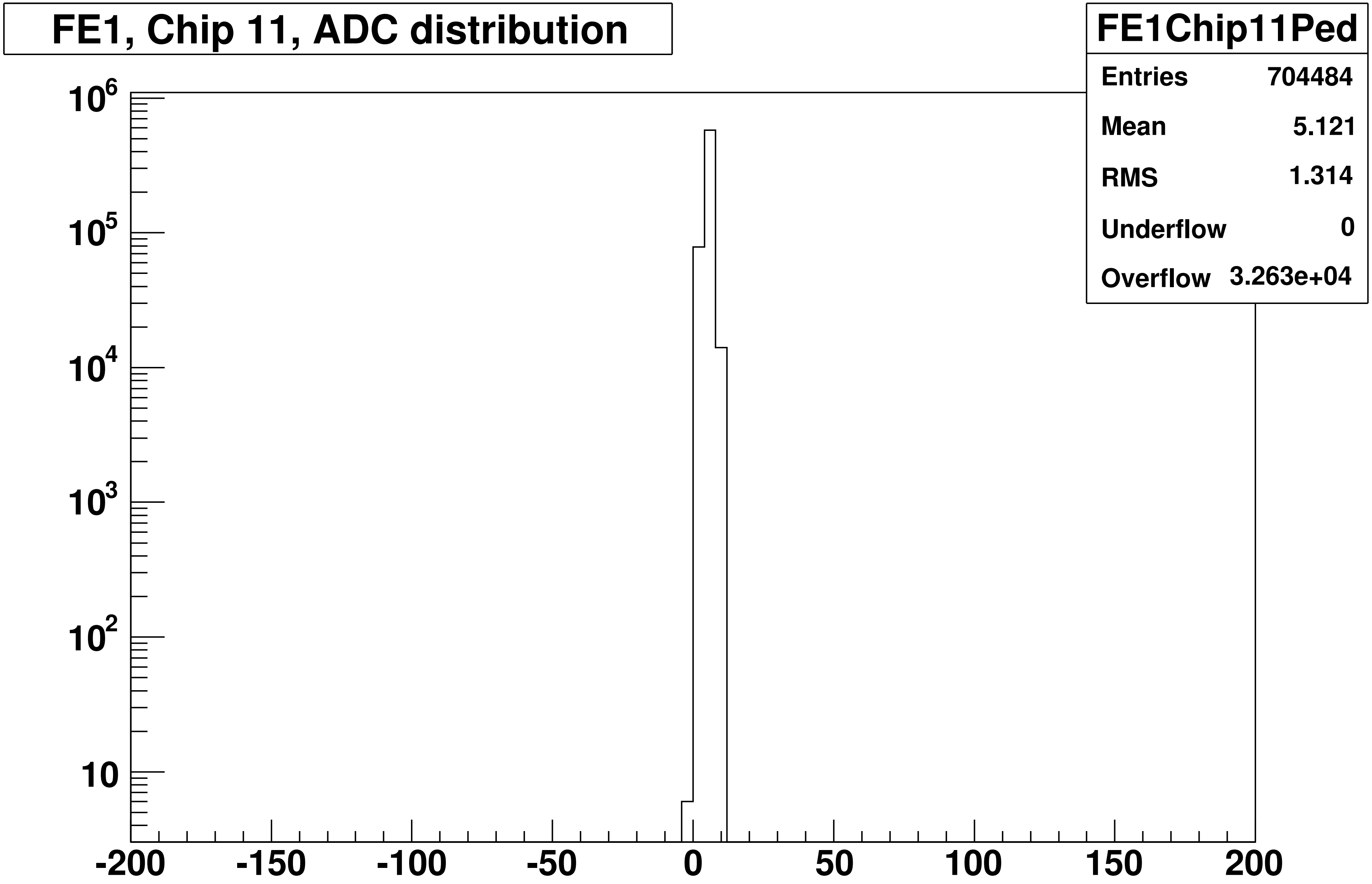

FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 704484
Mean 5.121
RMS 1.314
Underflow 0
Overflow 3.263e+04
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10^4
10^3
10^2
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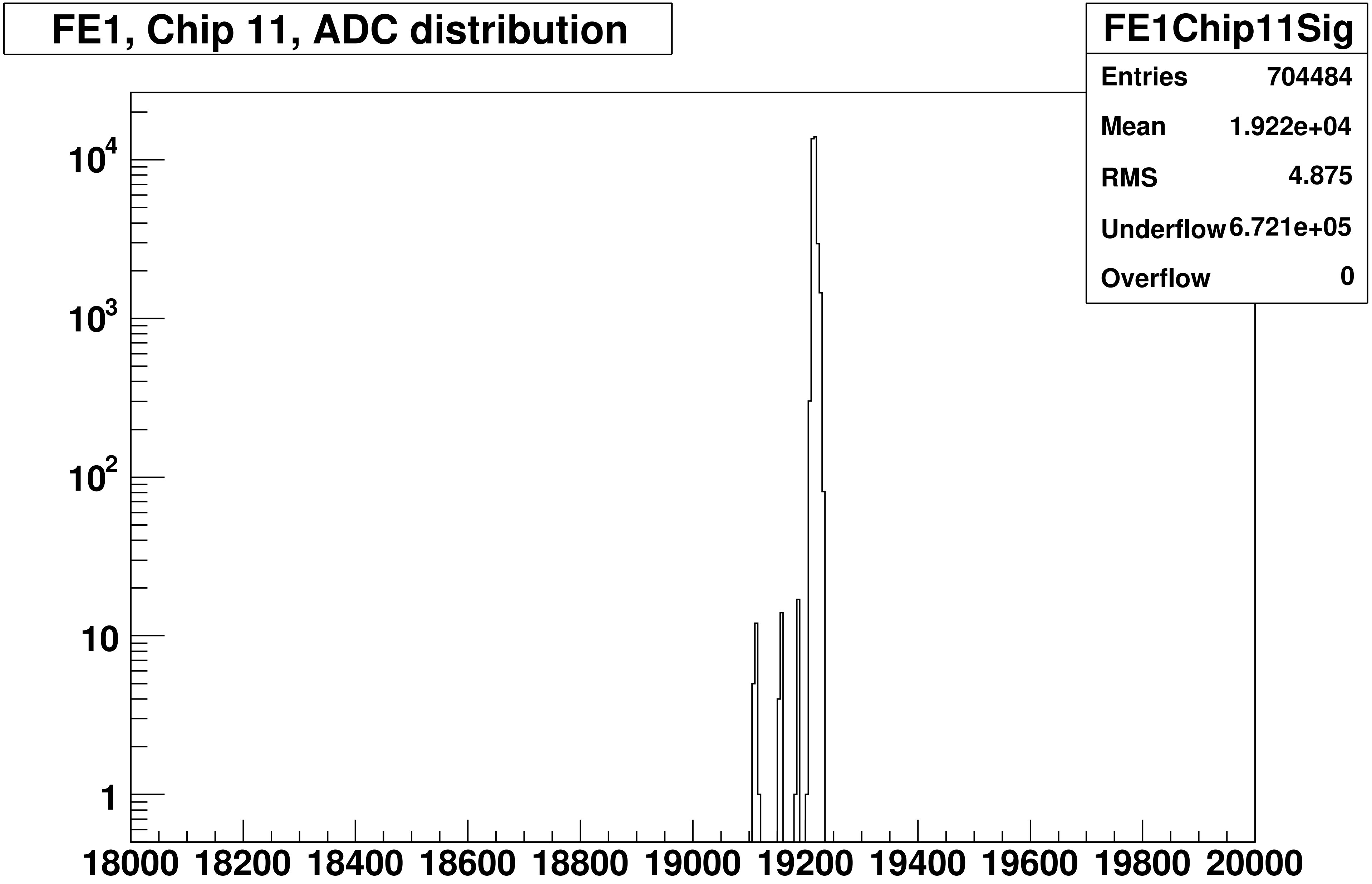
200


FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 704484
Mean 1.922e+04
RMS 4.875
Underflow 6.721e+05
Overflow 0
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10^3
10^2
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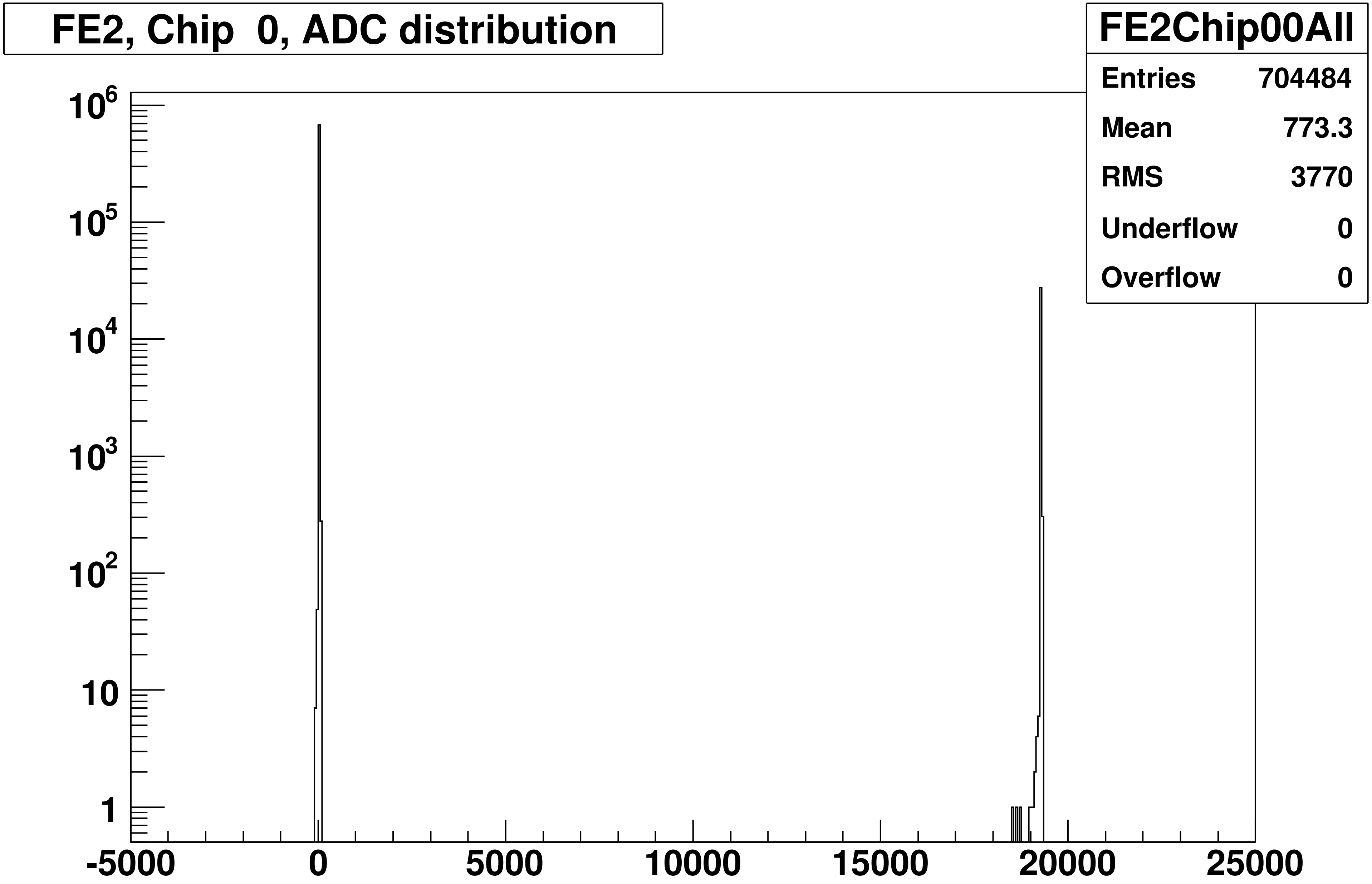
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 704484
Mean 773.3
RMS 3770
Underflow 0
Overflow 0
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10^4
10^3
10^2
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0
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20000
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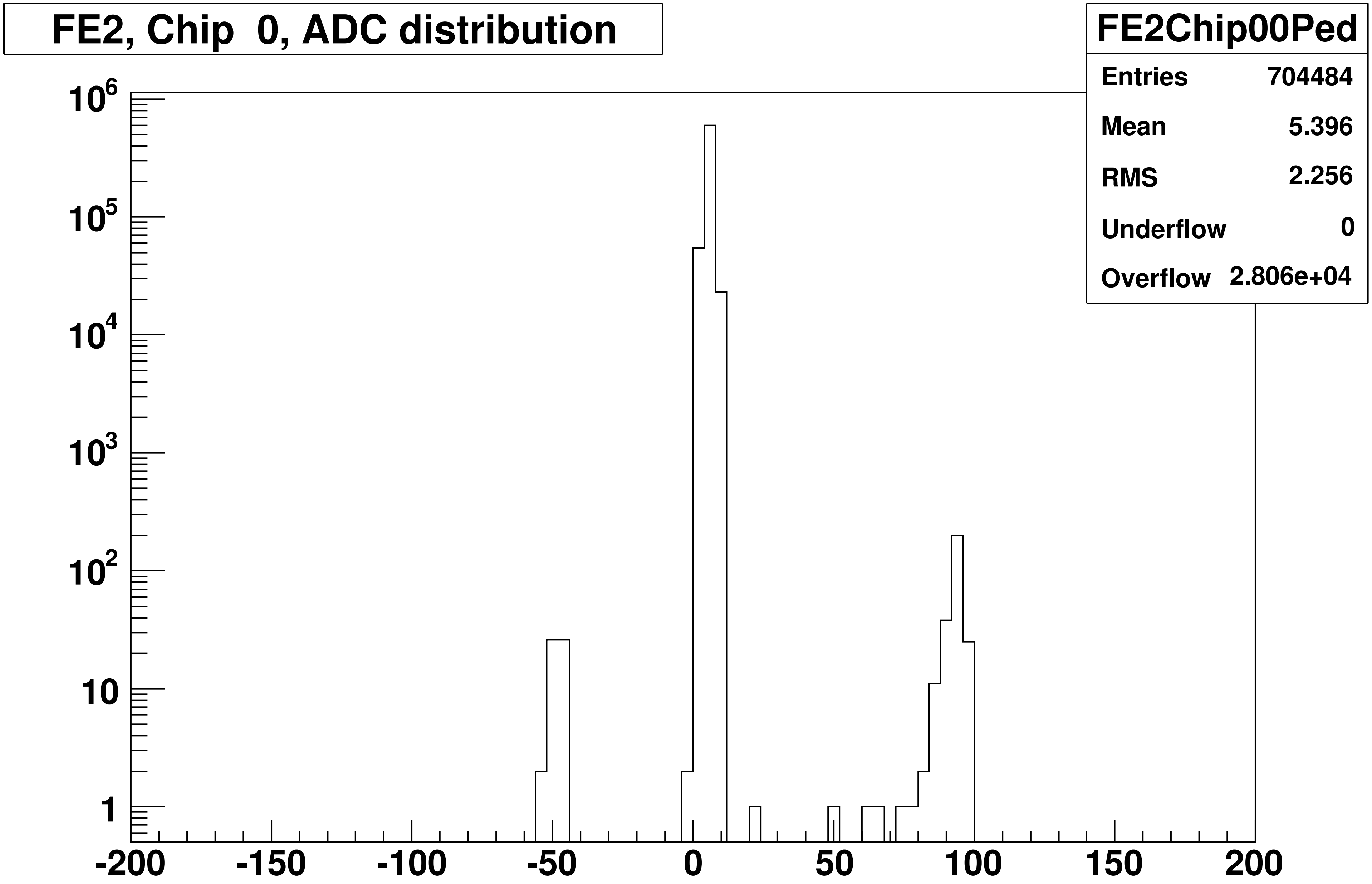

FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 704484
Mean 5.396
RMS 2.256
Underflow 0
Overflow 2.806e+04
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10^3
10^2
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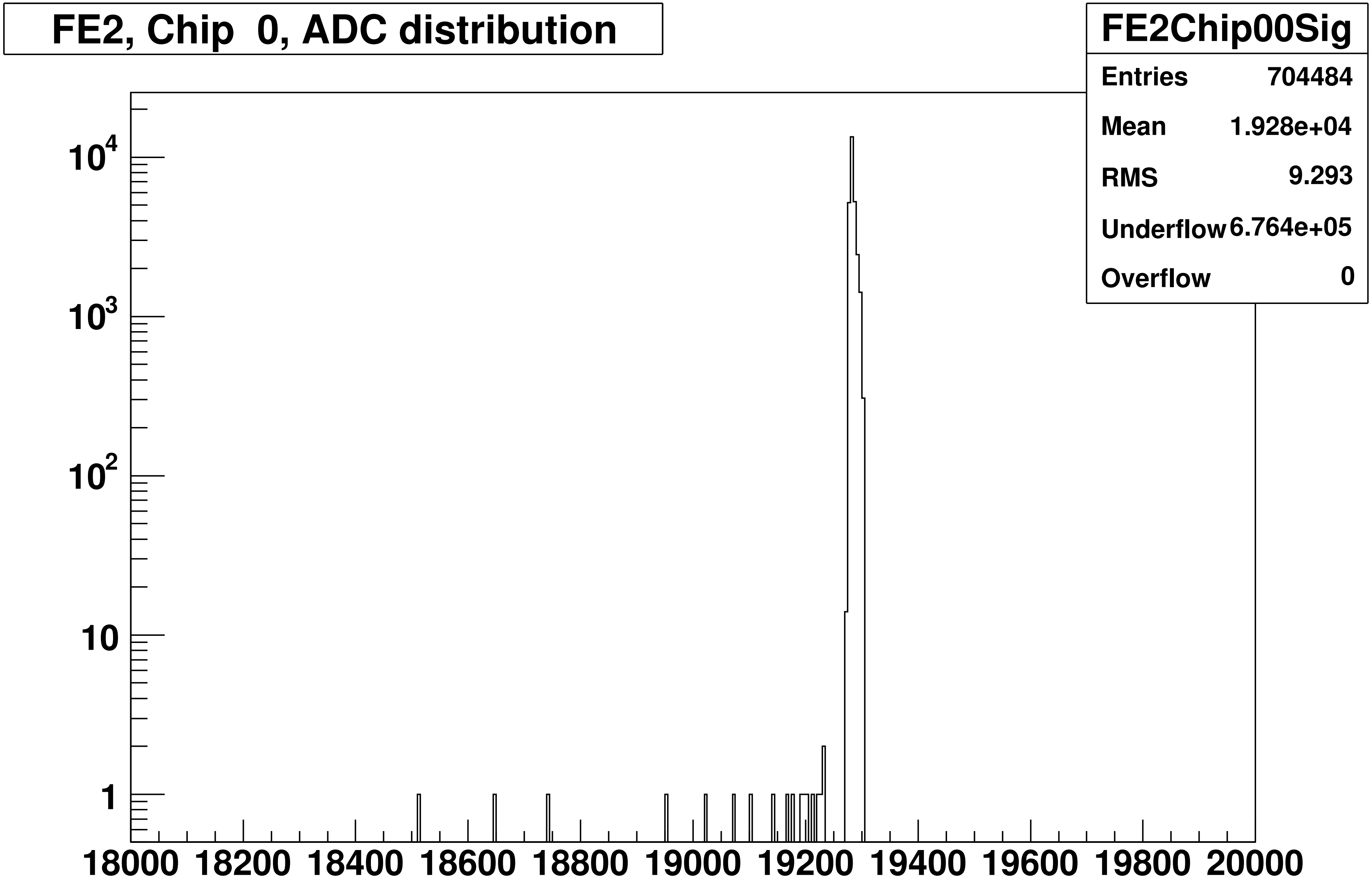
150
200


FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 704484
Mean 1.928e+04
RMS 9.293
Underflow 6.764e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
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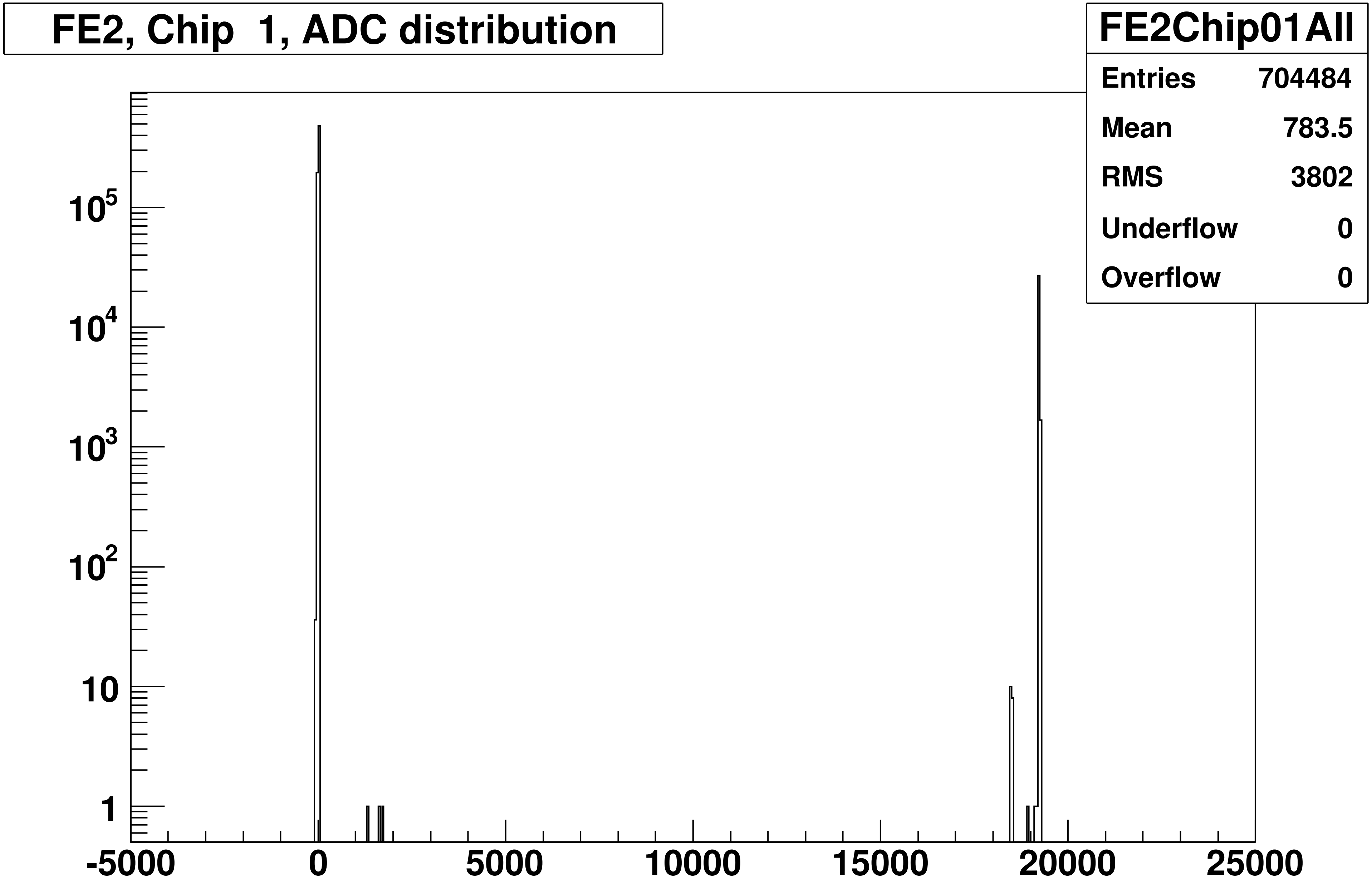
19600
19800
20000


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 704484
Mean 783.5
RMS 3802
Underflow 0
Overflow 0
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10^4
10^3
10^2
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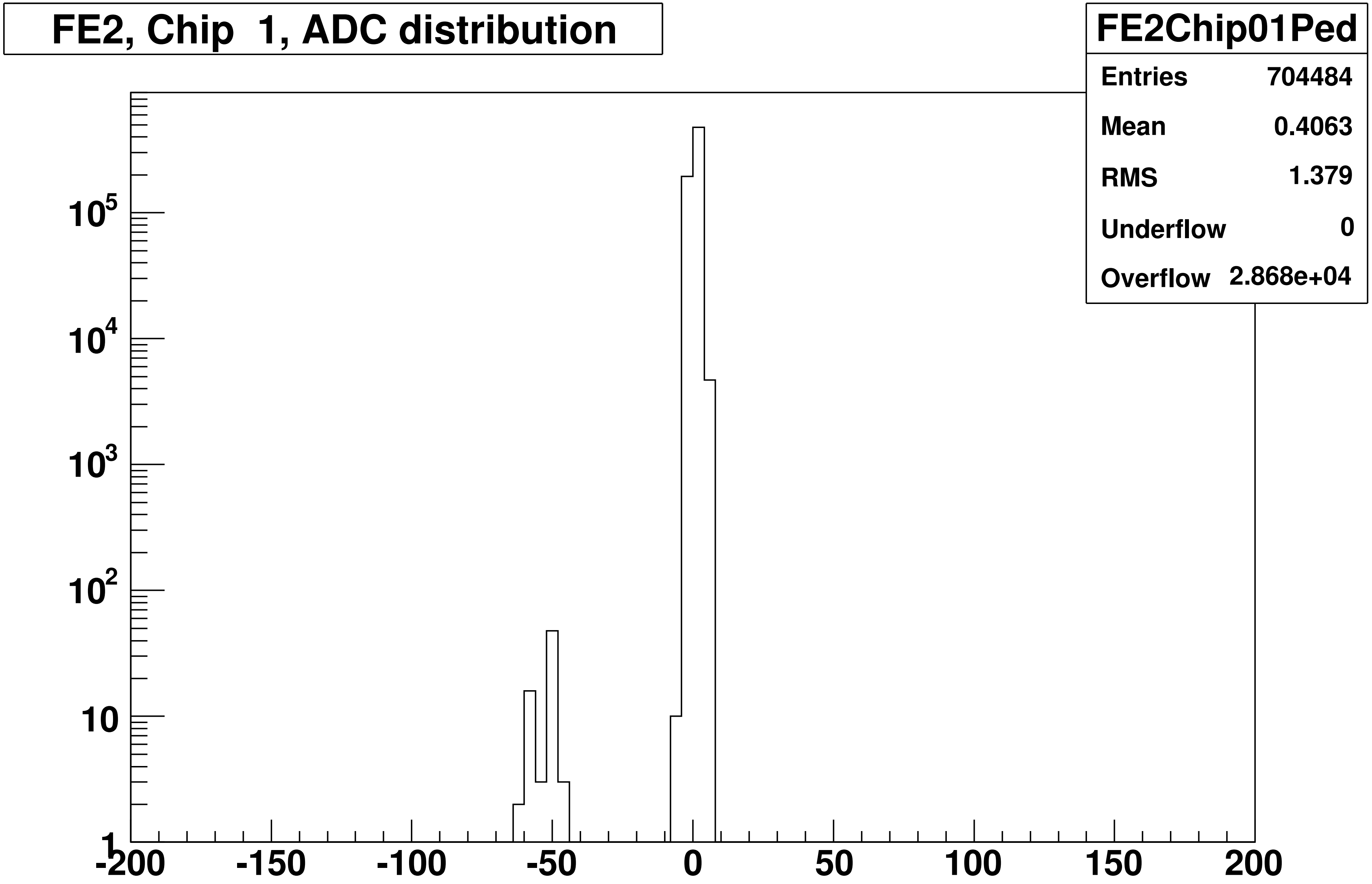
FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 704484
Mean 0.4063
RMS 1.379
Underflow 0
Overflow 2.868e+04
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10^3
10^2
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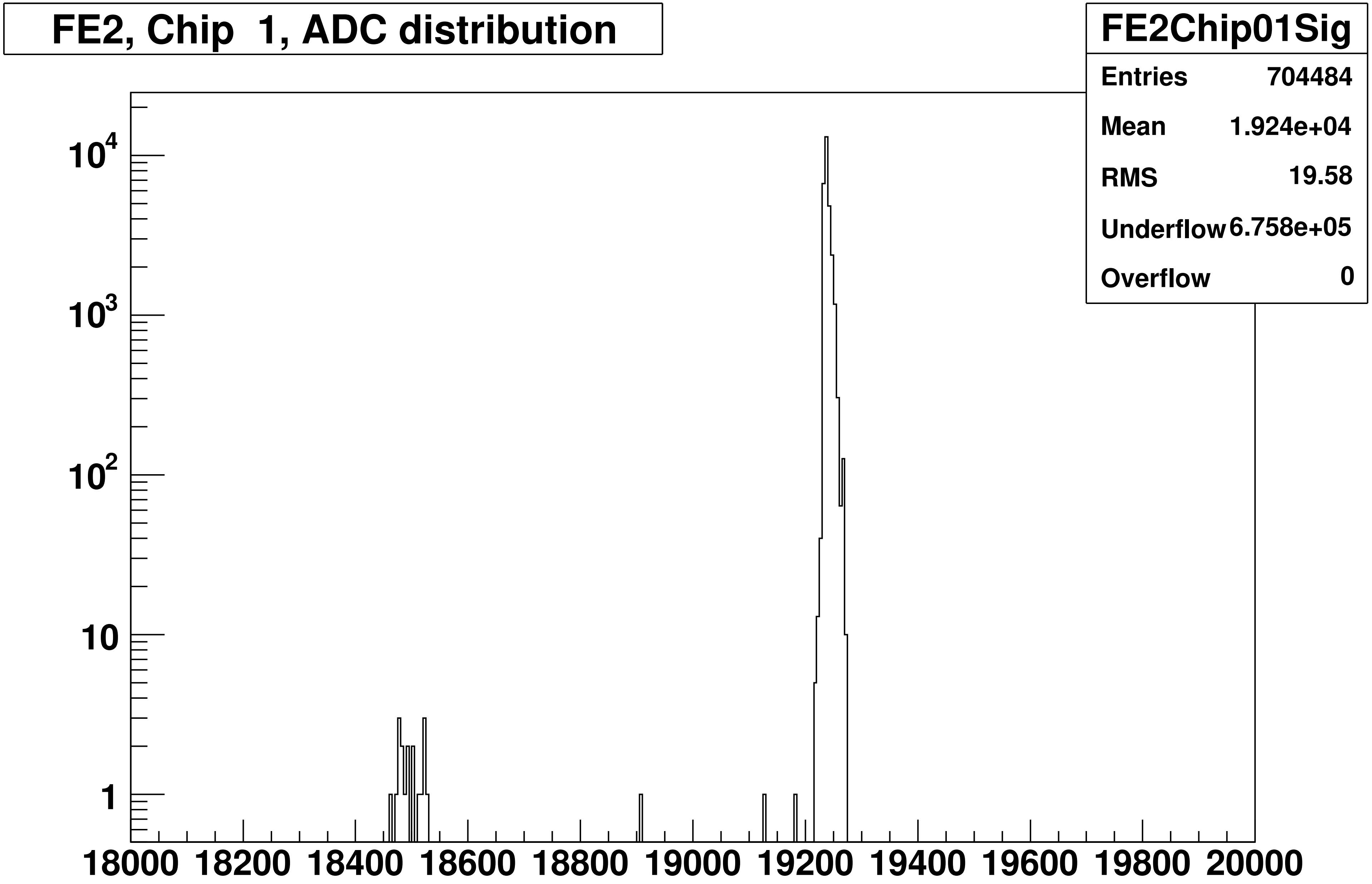
-100
-50
0
50
100
150
200


FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 704484
Mean 1.924e+04
RMS 19.58
Underflow 6.758e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
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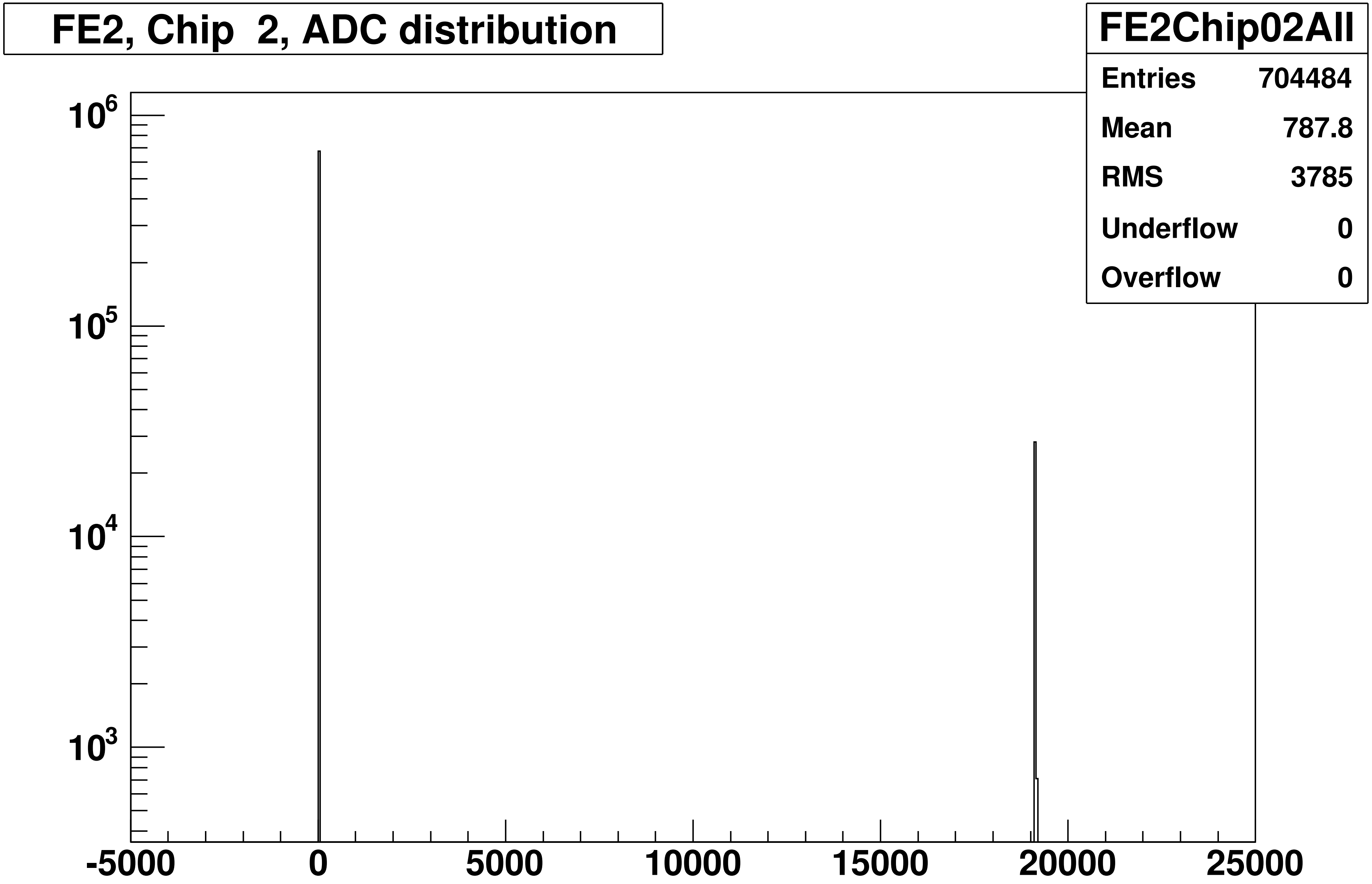
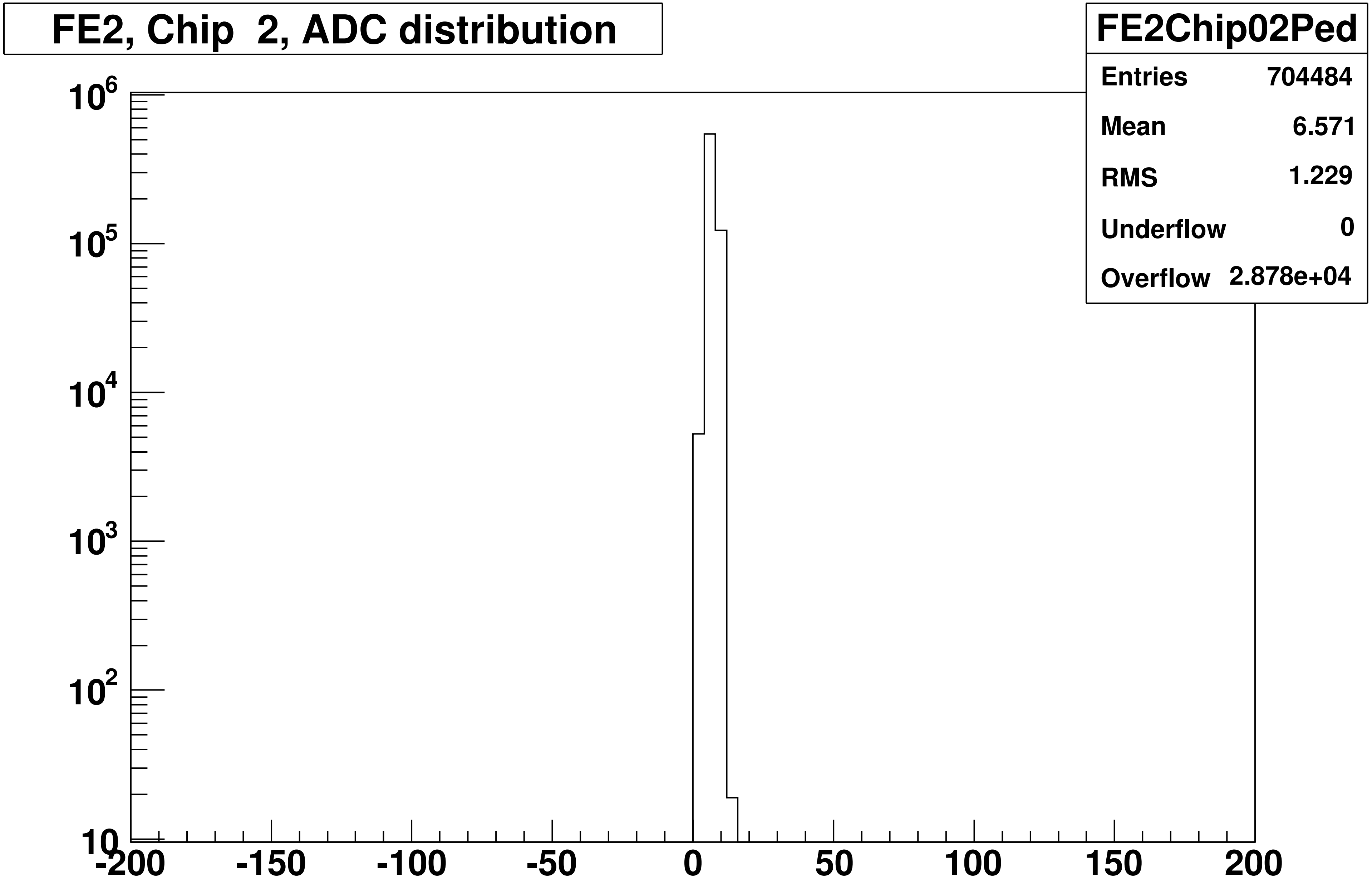
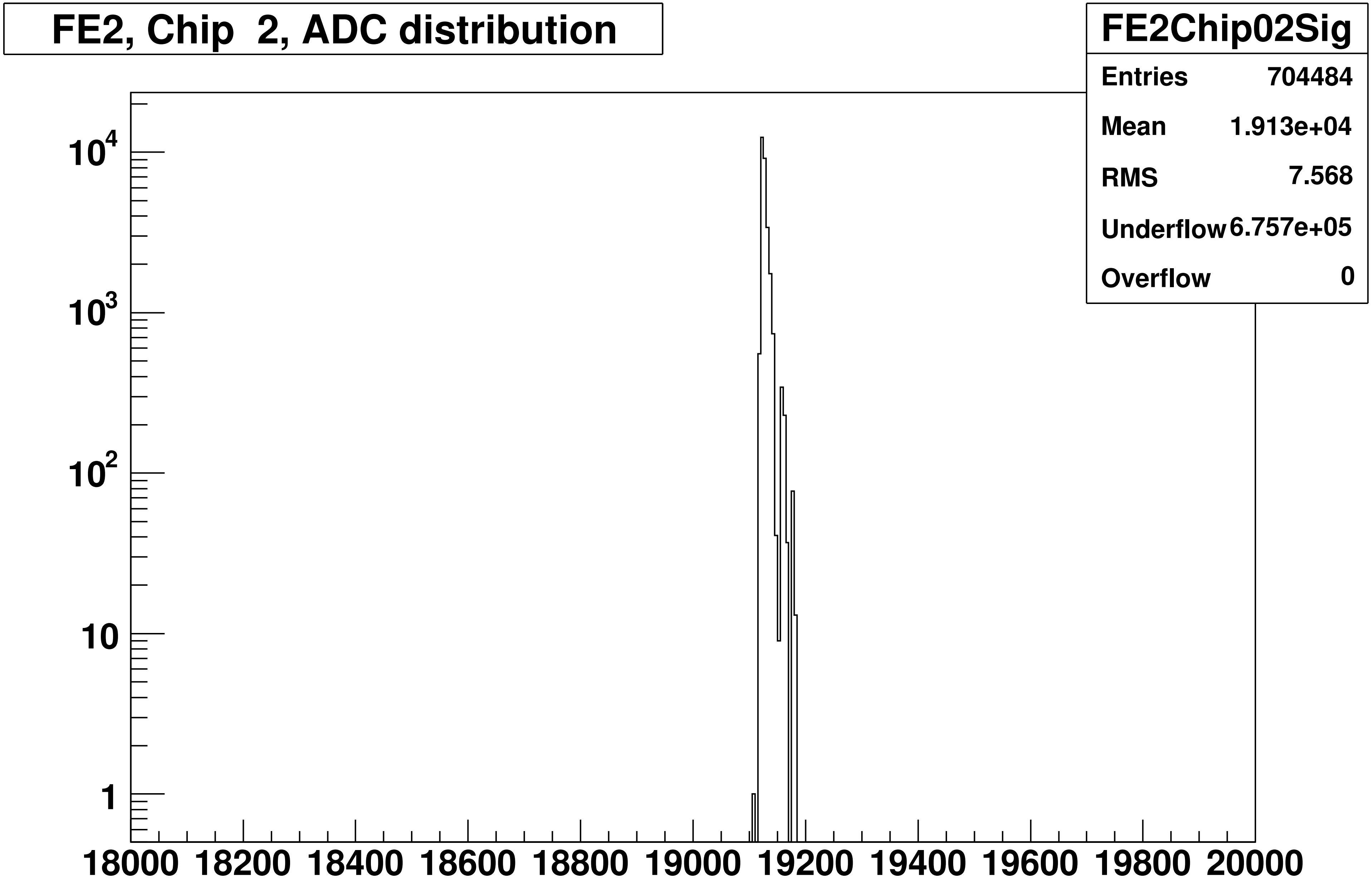
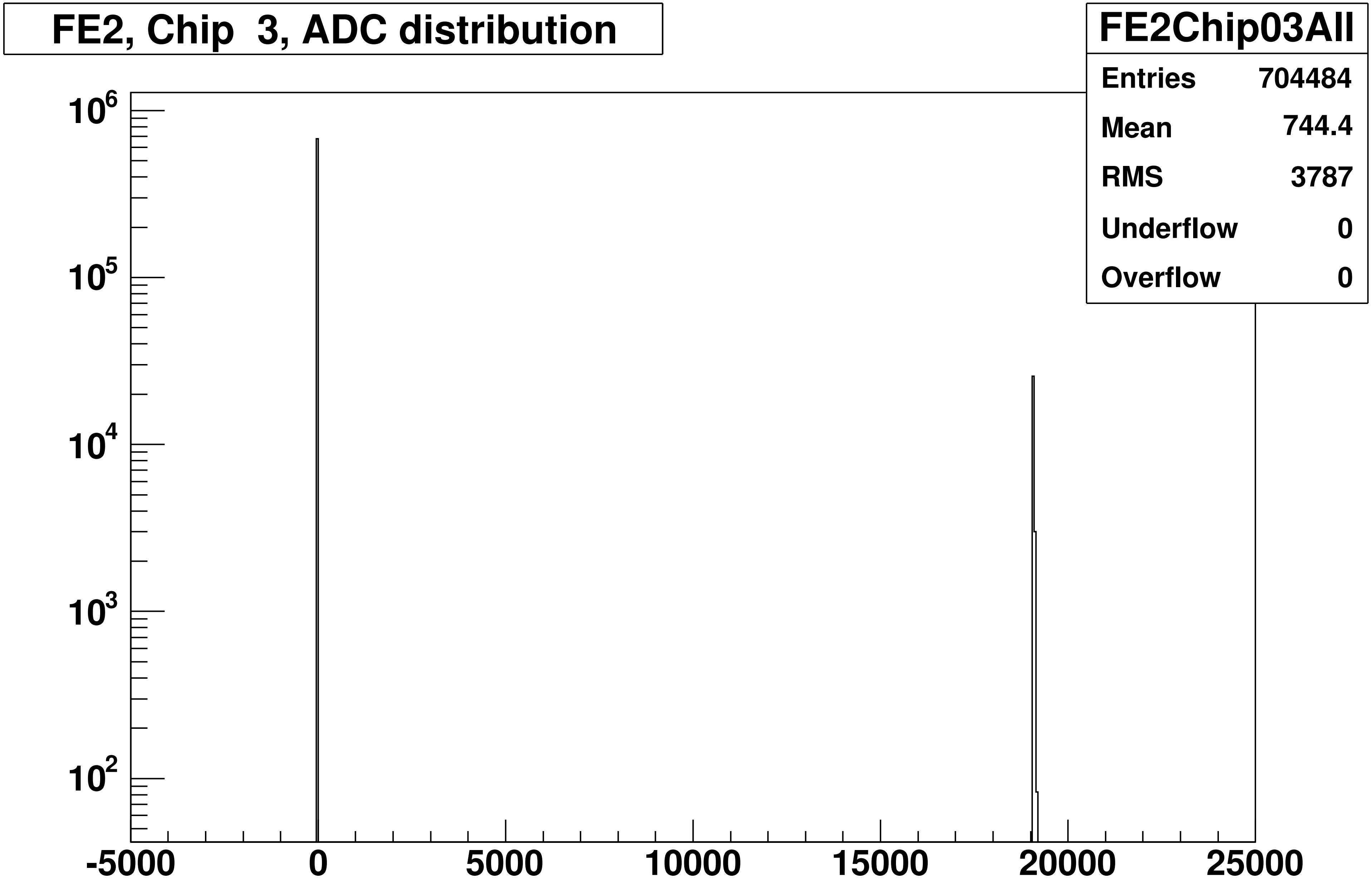
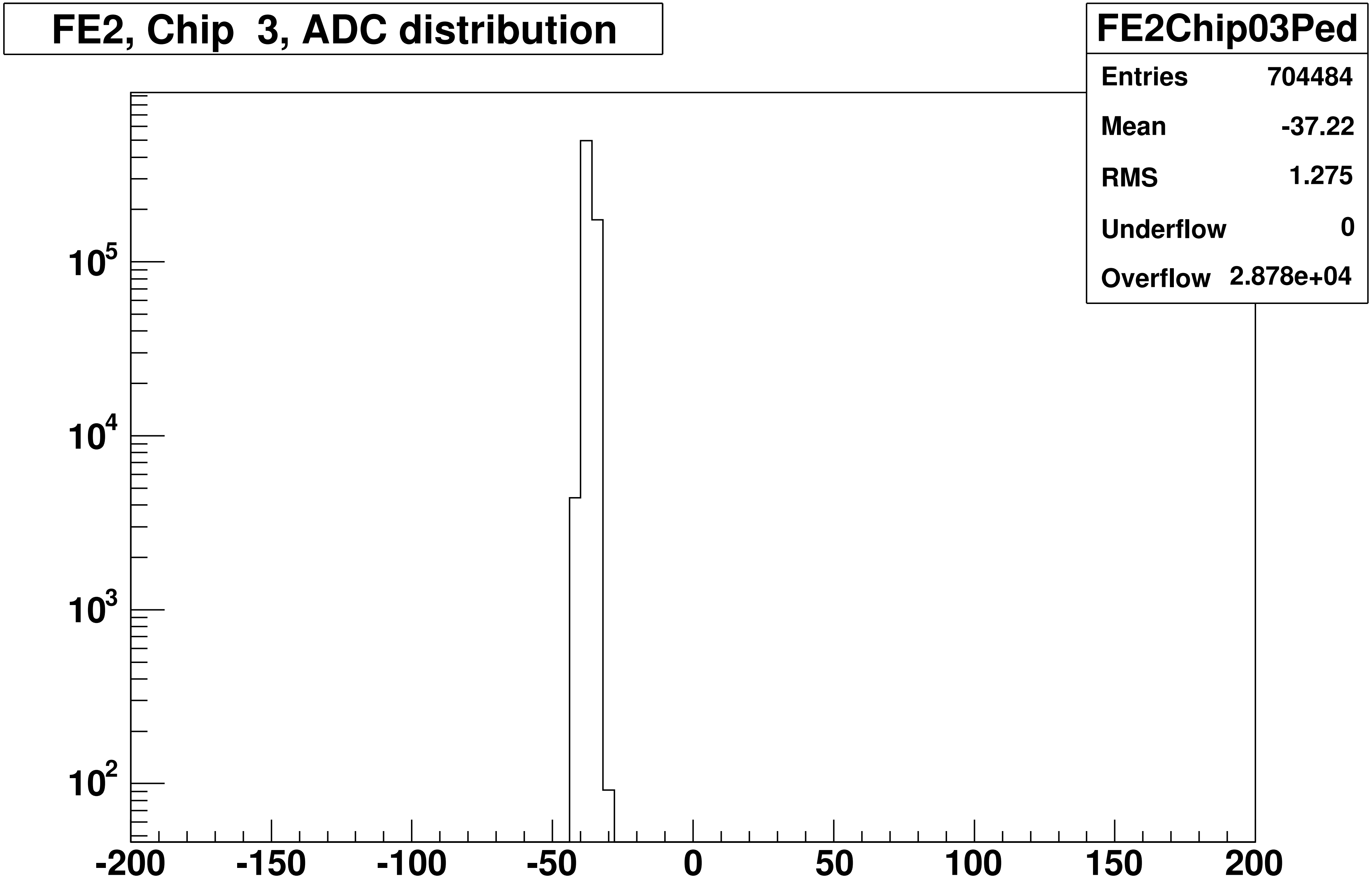
20000


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 704484
Mean 787.8
RMS 3785
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 704484
Mean 6.571
RMS 1.229
Underflow 0
Overflow 2.878e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 704484
Mean 1.913e+04
RMS 7.568
Underflow 6.757e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 704484
Mean 744.4
RMS 3787
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 704484
Mean -37.22
RMS 1.275
Underflow 0
Overflow 2.878e+04
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10^4
10^3
10^2
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150
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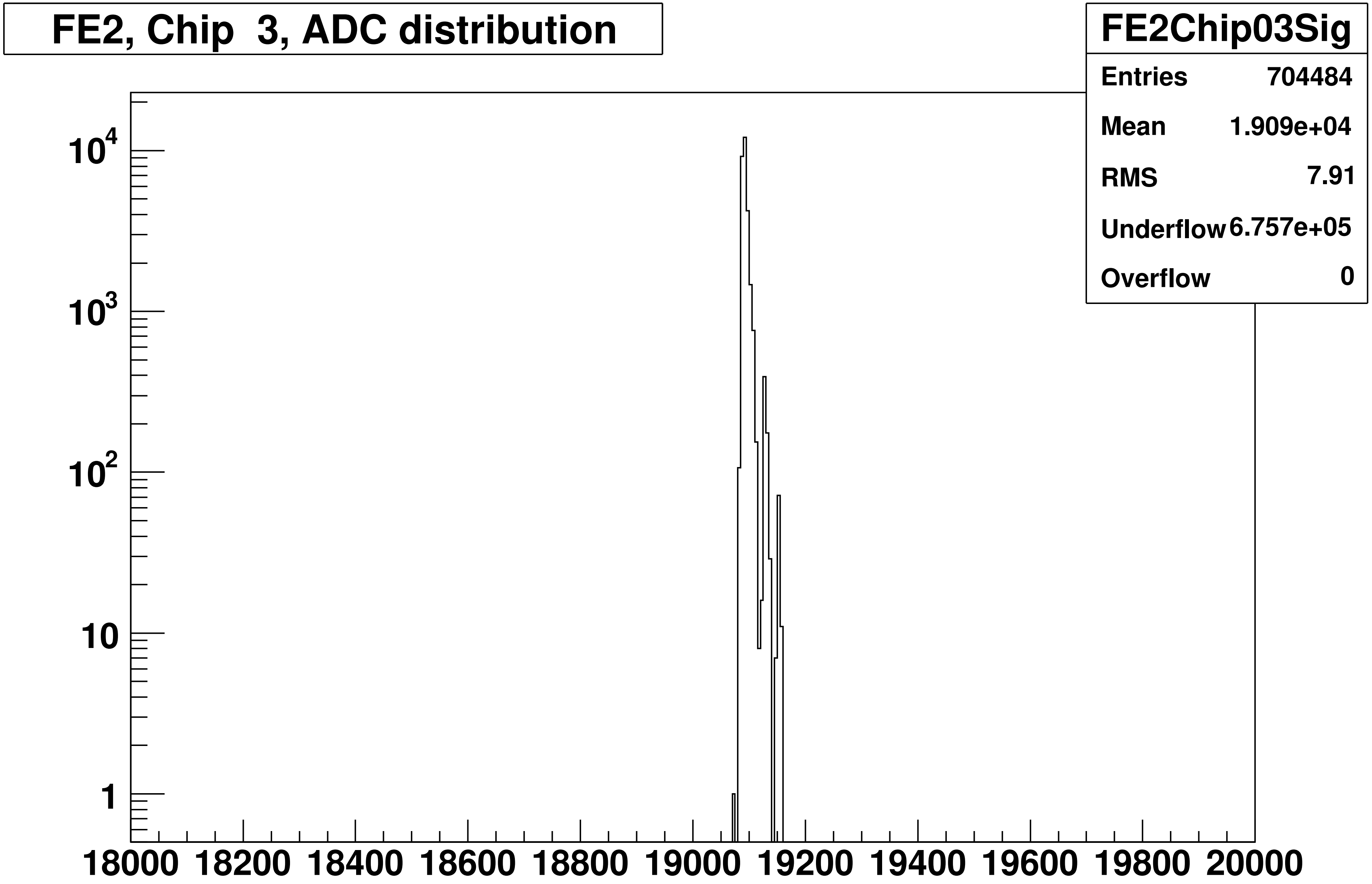

FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 704484
Mean 1.909e+04
RMS 7.91
Underflow 6.757e+05
Overflow 0
10^4
10^3
10^2
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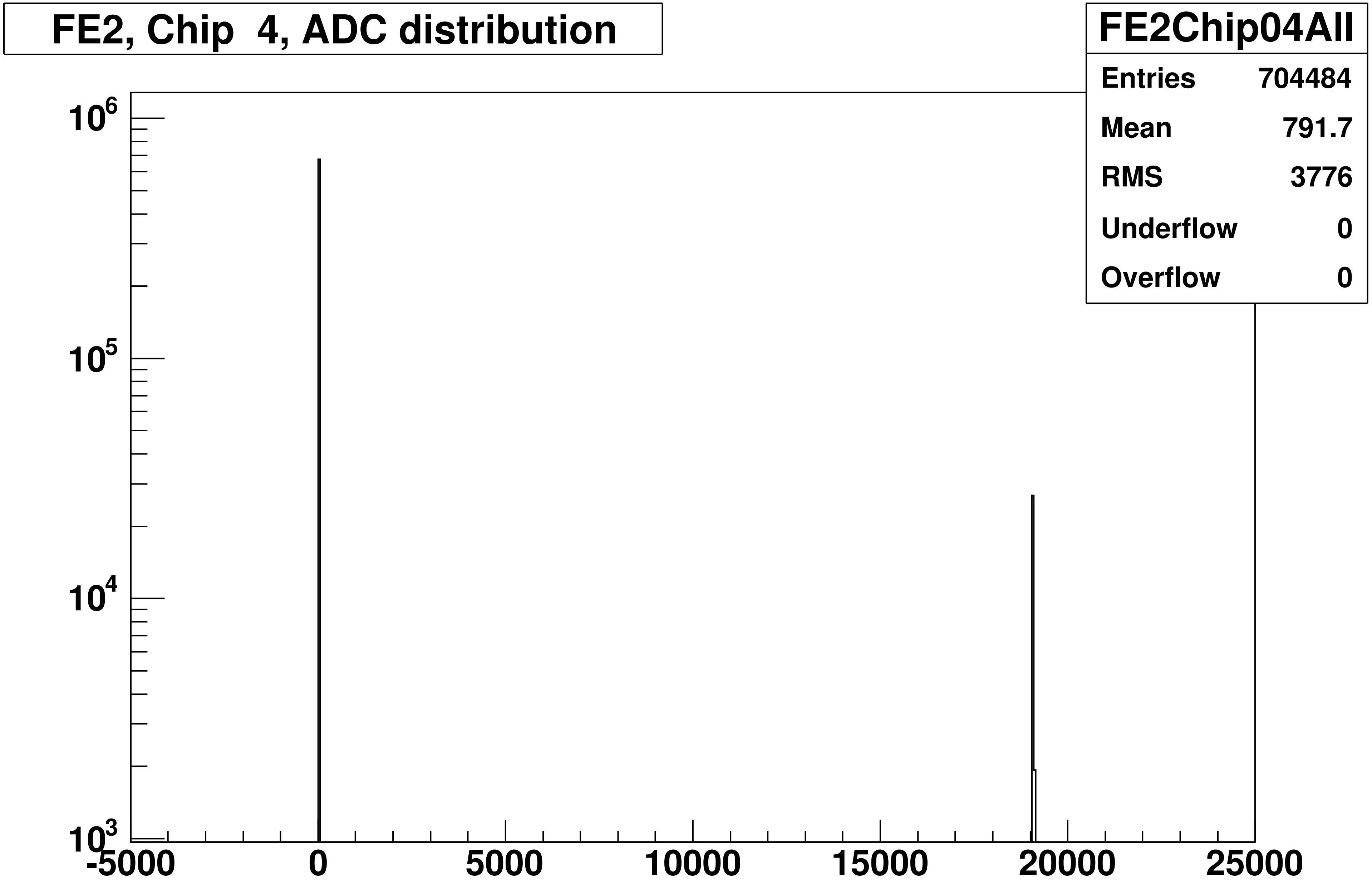
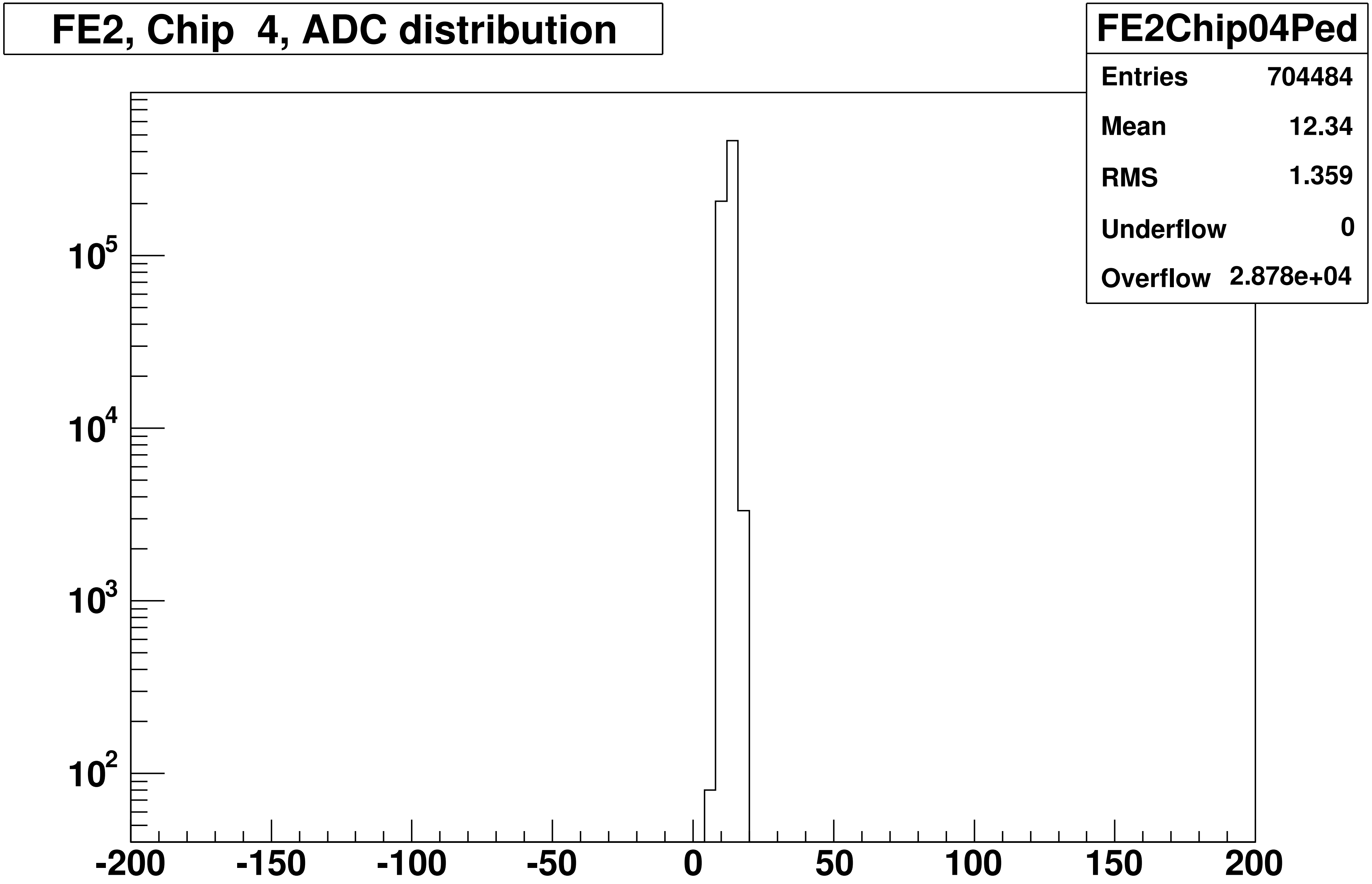
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 704484
Mean 791.7
RMS 3776
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 704484
Mean 12.34
RMS 1.359
Underflow 0
Overflow 2.878e+04
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10^4
10^3
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150
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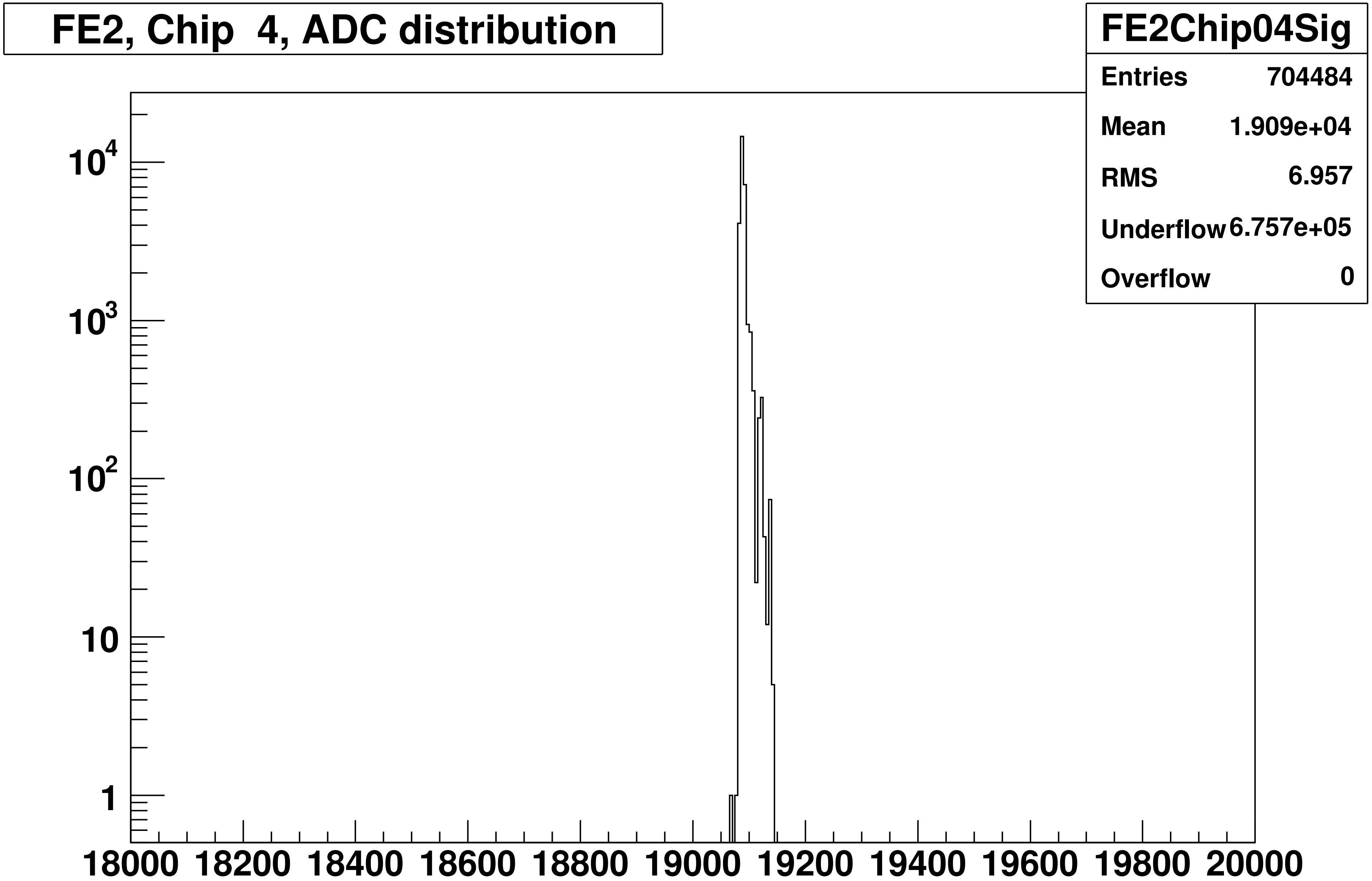
FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 704484
Mean 1.909e+04
RMS 6.957
Underflow 6.757e+05
Overflow 0
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10^3
10^2
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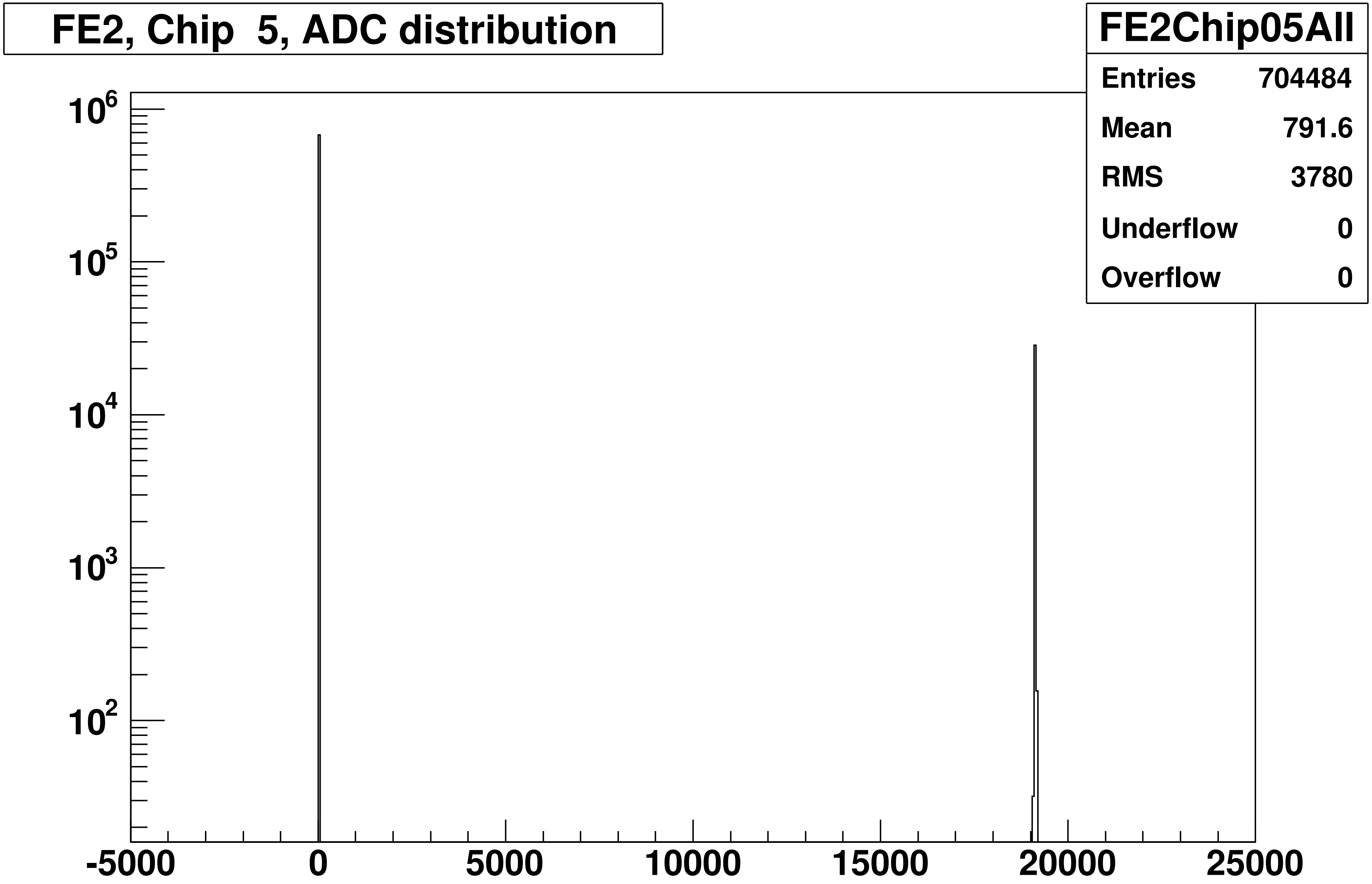
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 704484
Mean 791.6
RMS 3780
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
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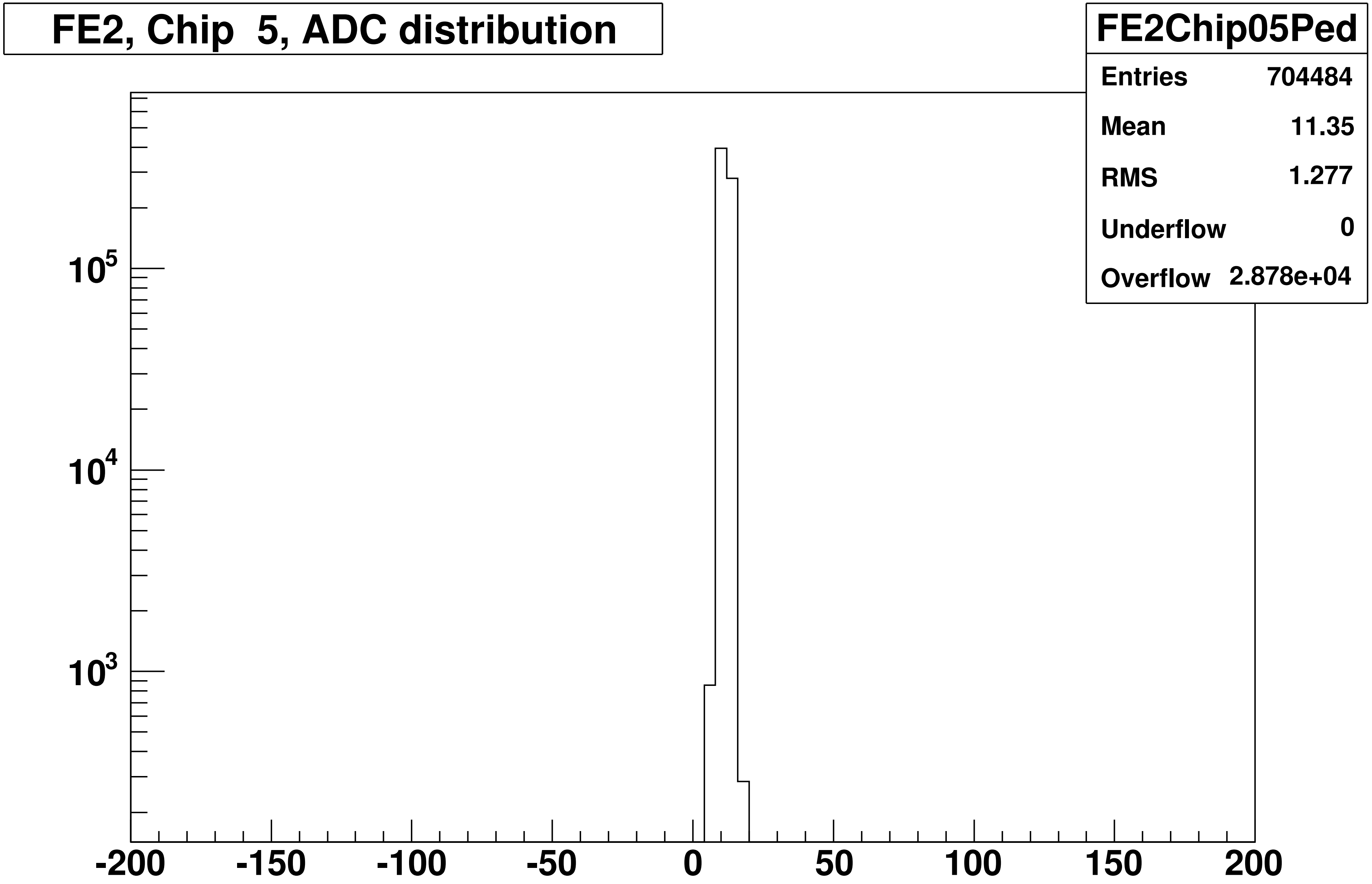
10000
15000
20000
25000


FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 704484
Mean 11.35
RMS 1.277
Underflow 0
Overflow 2.878e+04
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10^4
10^3
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-100
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150
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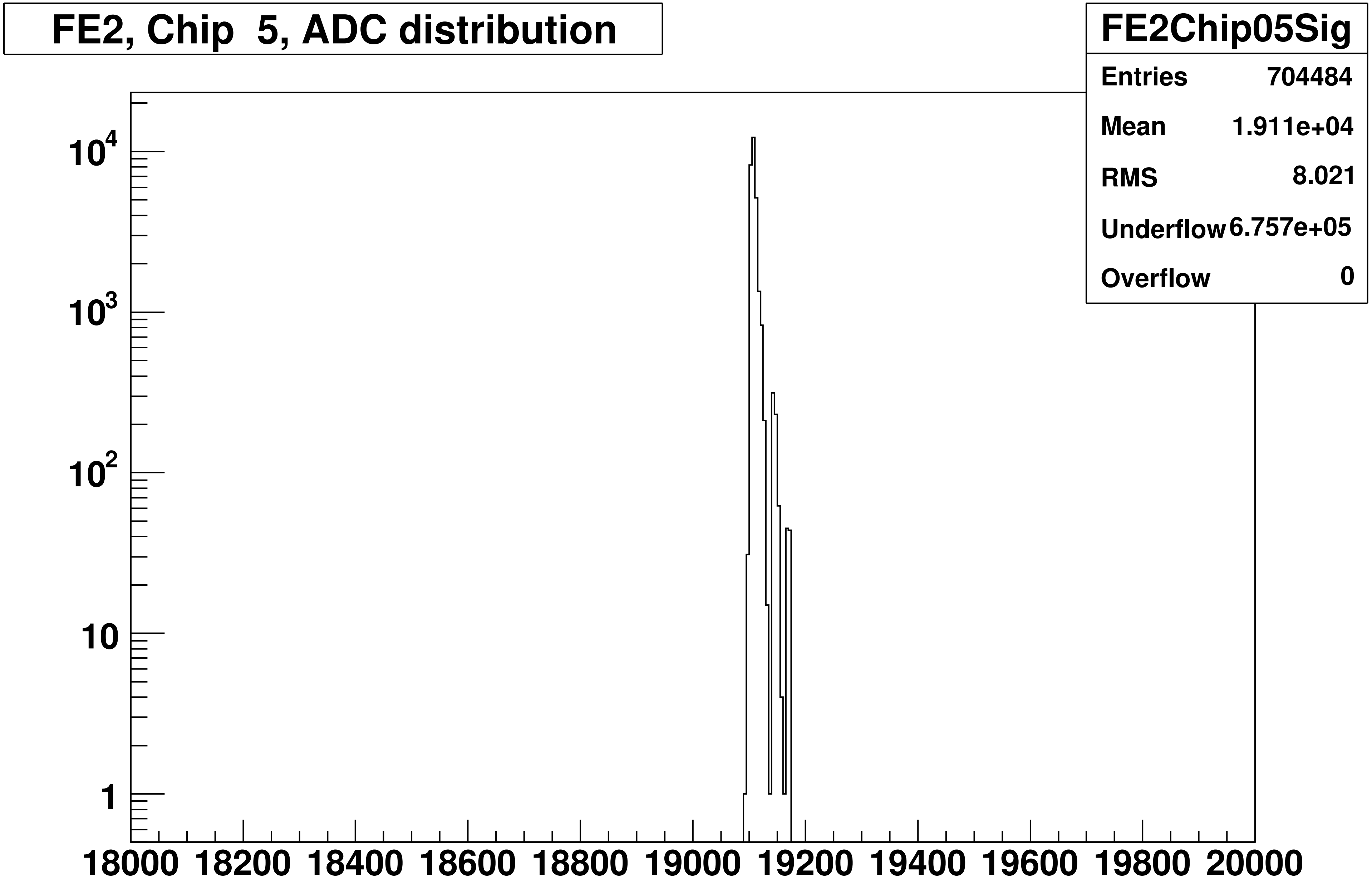
FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 704484
Mean 1.911e+04
RMS 8.021
Underflow 6.757e+05
Overflow 0
10^4
10^3
10^2
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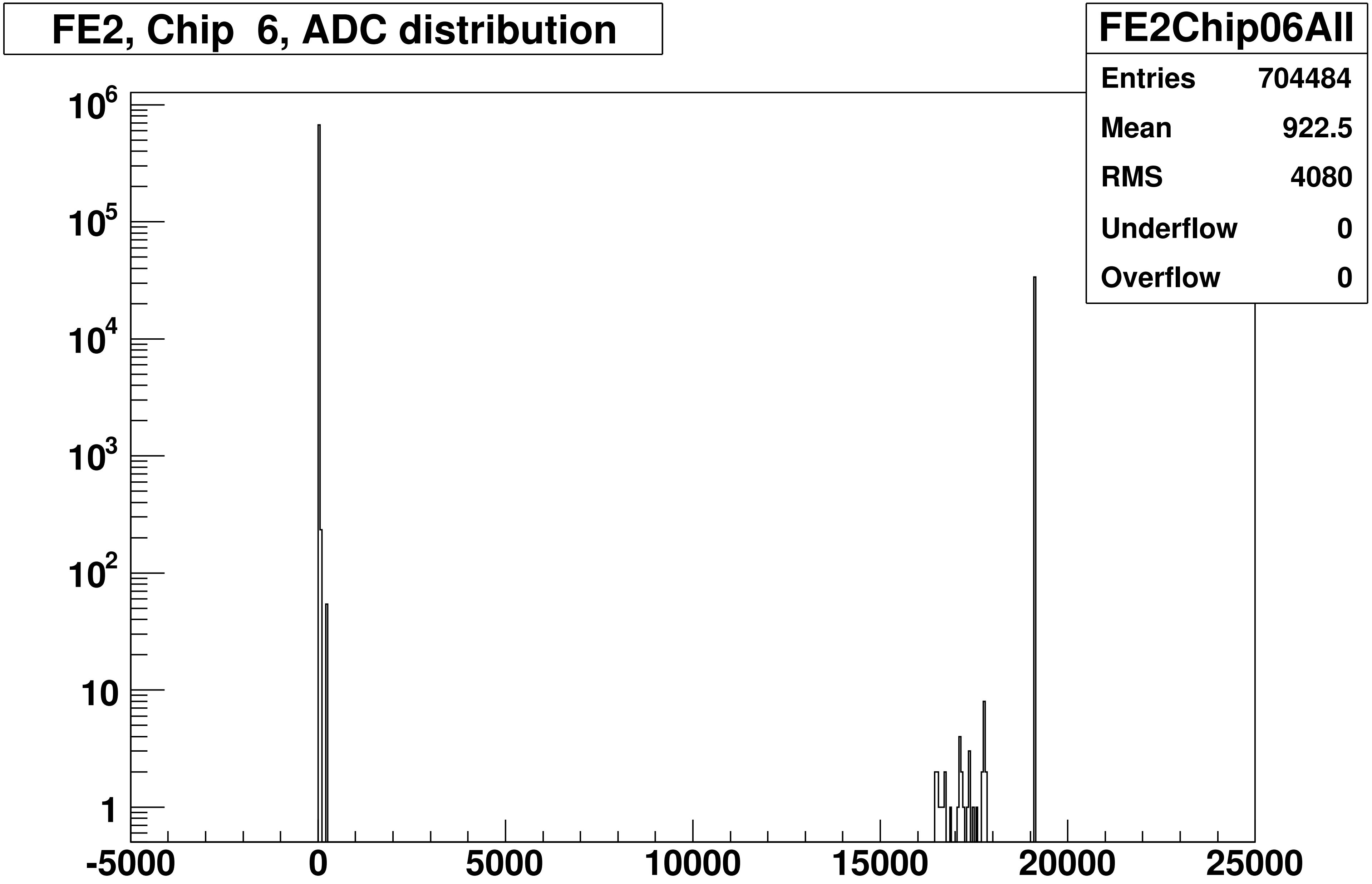
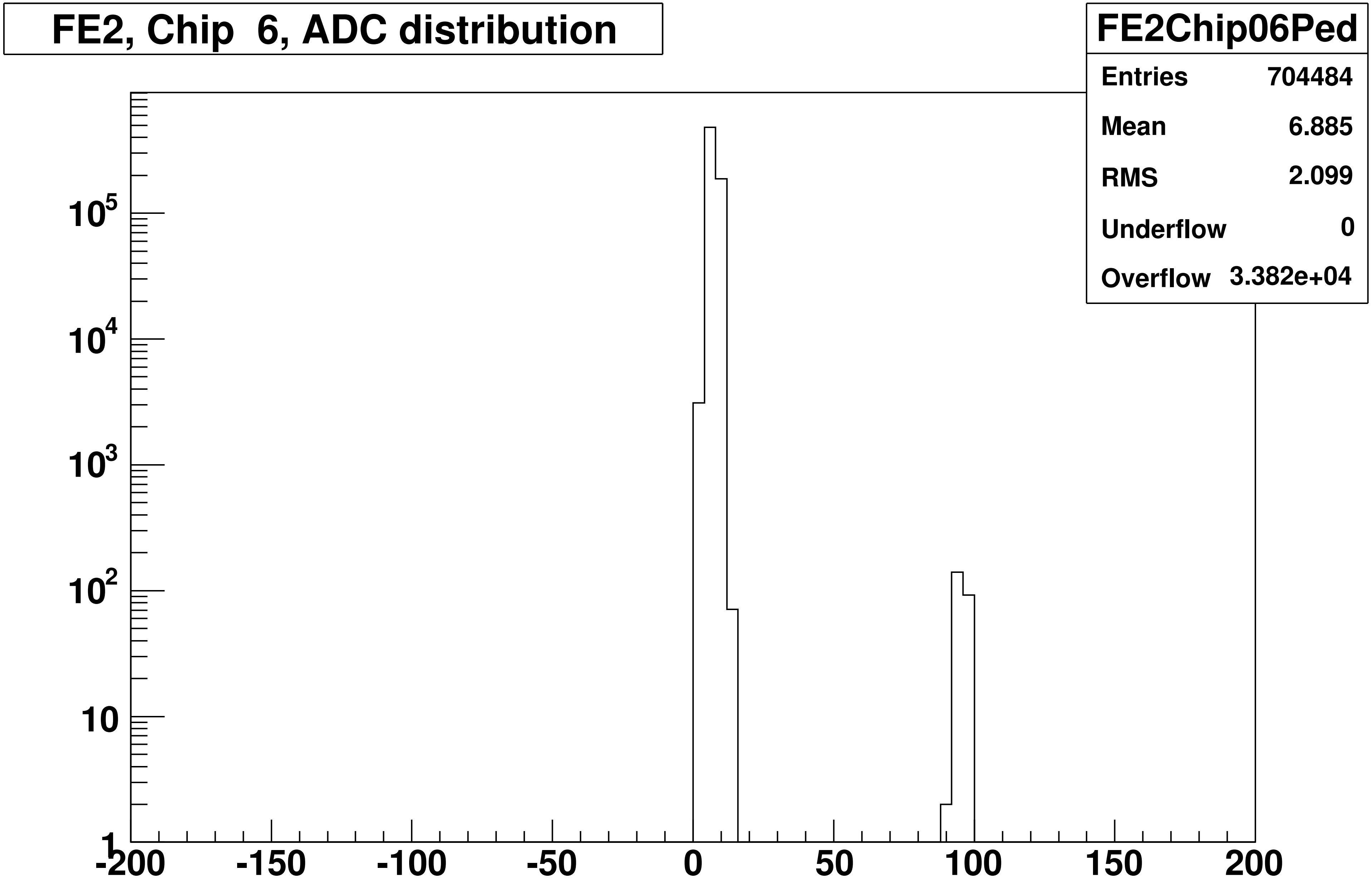
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 704484
Mean 922.5
RMS 4080
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 704484
Mean 6.885
RMS 2.099
Underflow 0
Overflow 3.382e+04
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10^4
10^3
10^2
10
1
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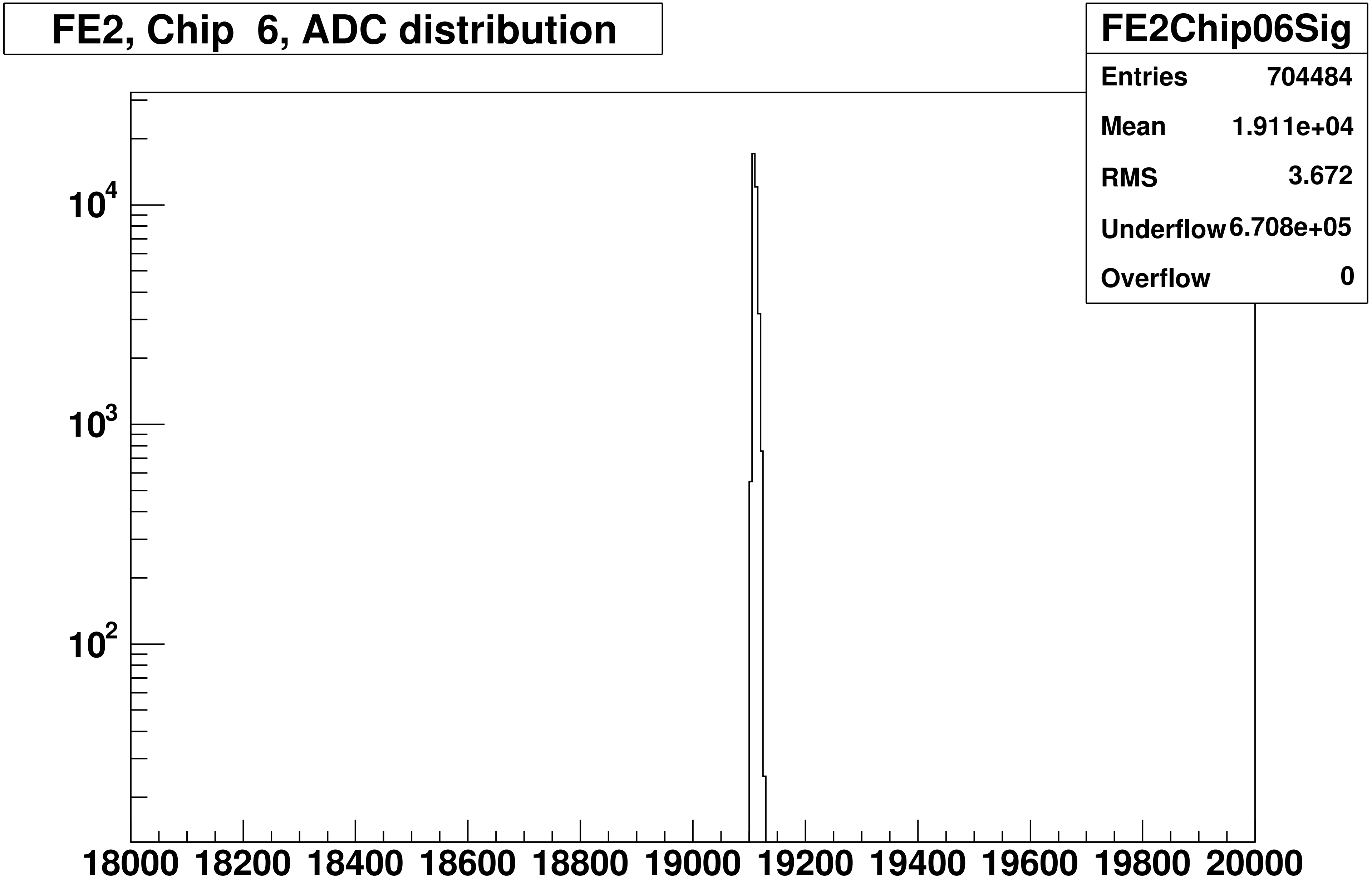
100
150
200


FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 704484
Mean 1.911e+04
RMS 3.672
Underflow 6.708e+05
Overflow 0
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10^3
10^2
18000
18200
18400
18600
18800
19000
19200
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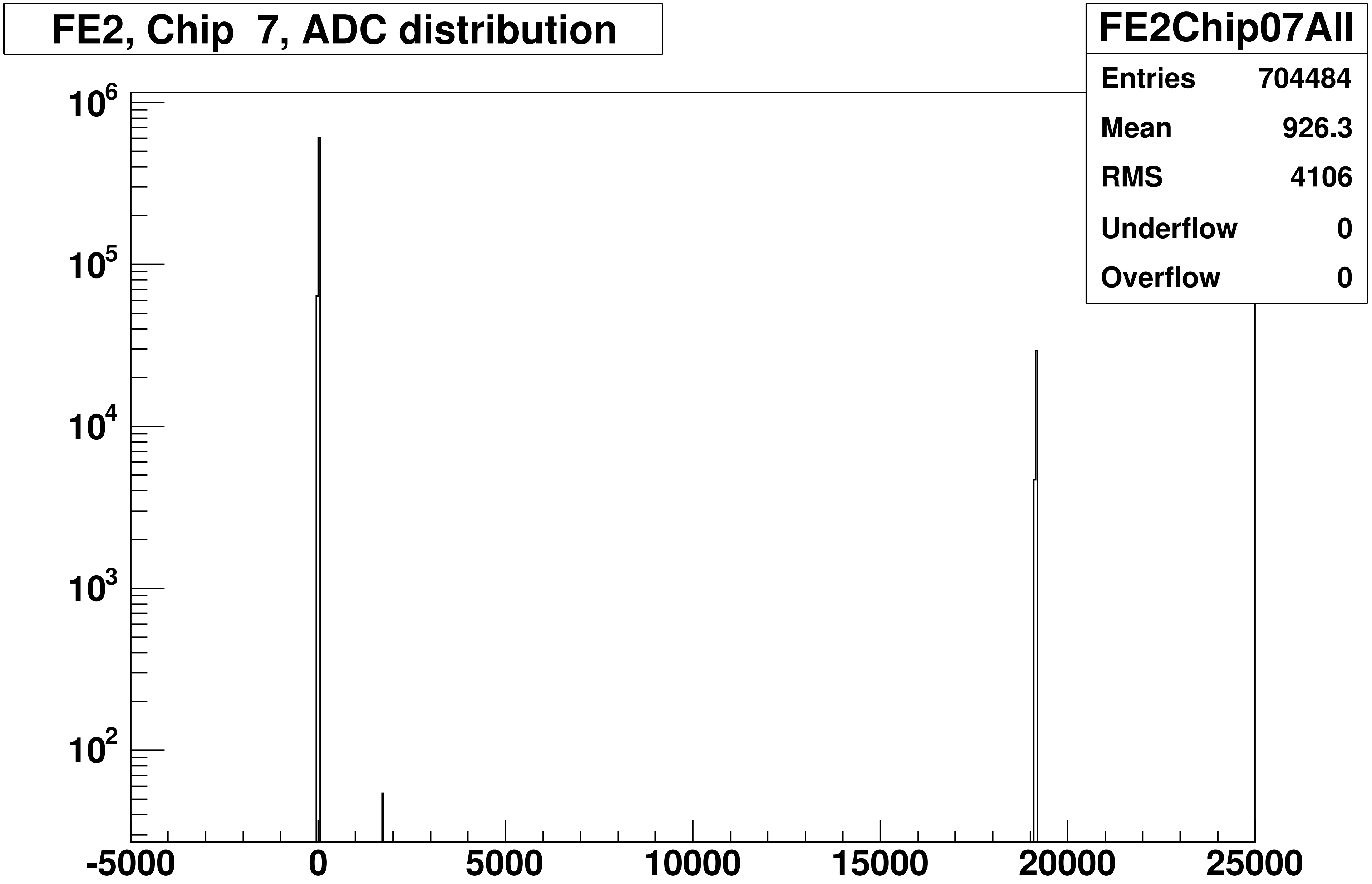
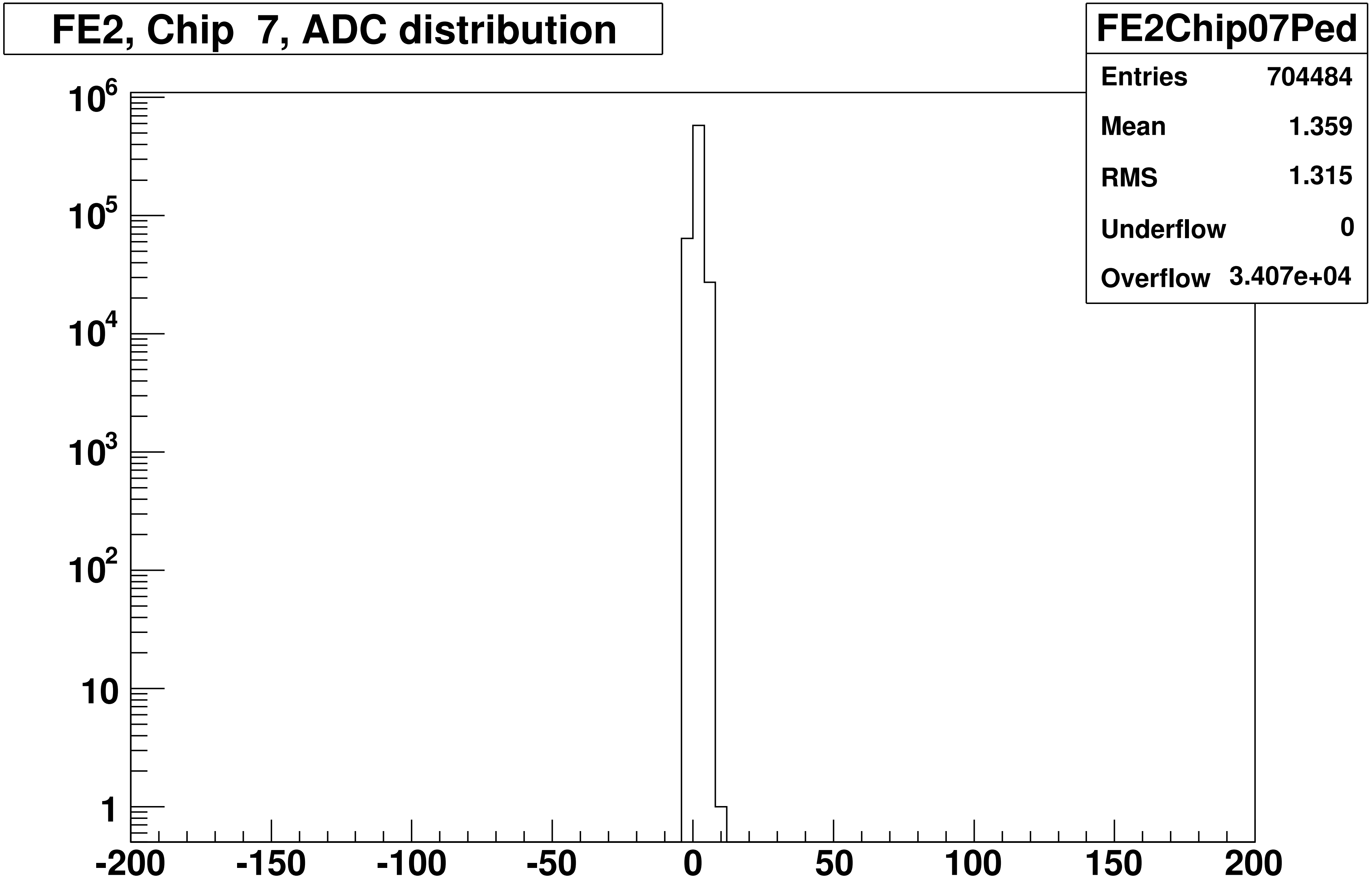
19800
20000


FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 704484
Mean 926.3
RMS 4106
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 704484
Mean 1.359
RMS 1.315
Underflow 0
Overflow 3.407e+04
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10^5
10^4
10^3
10^2
10
1
-200
-150
-100
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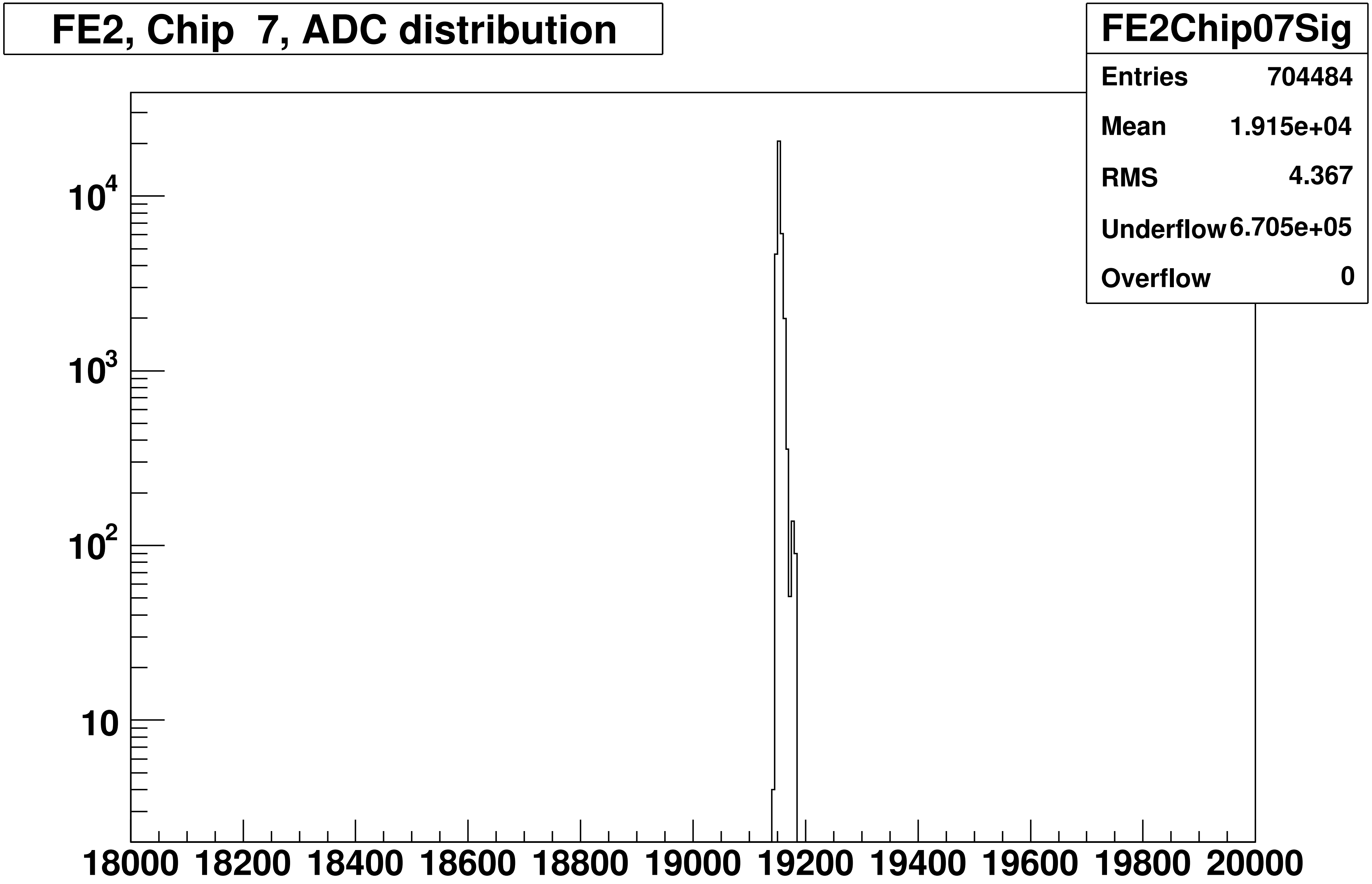
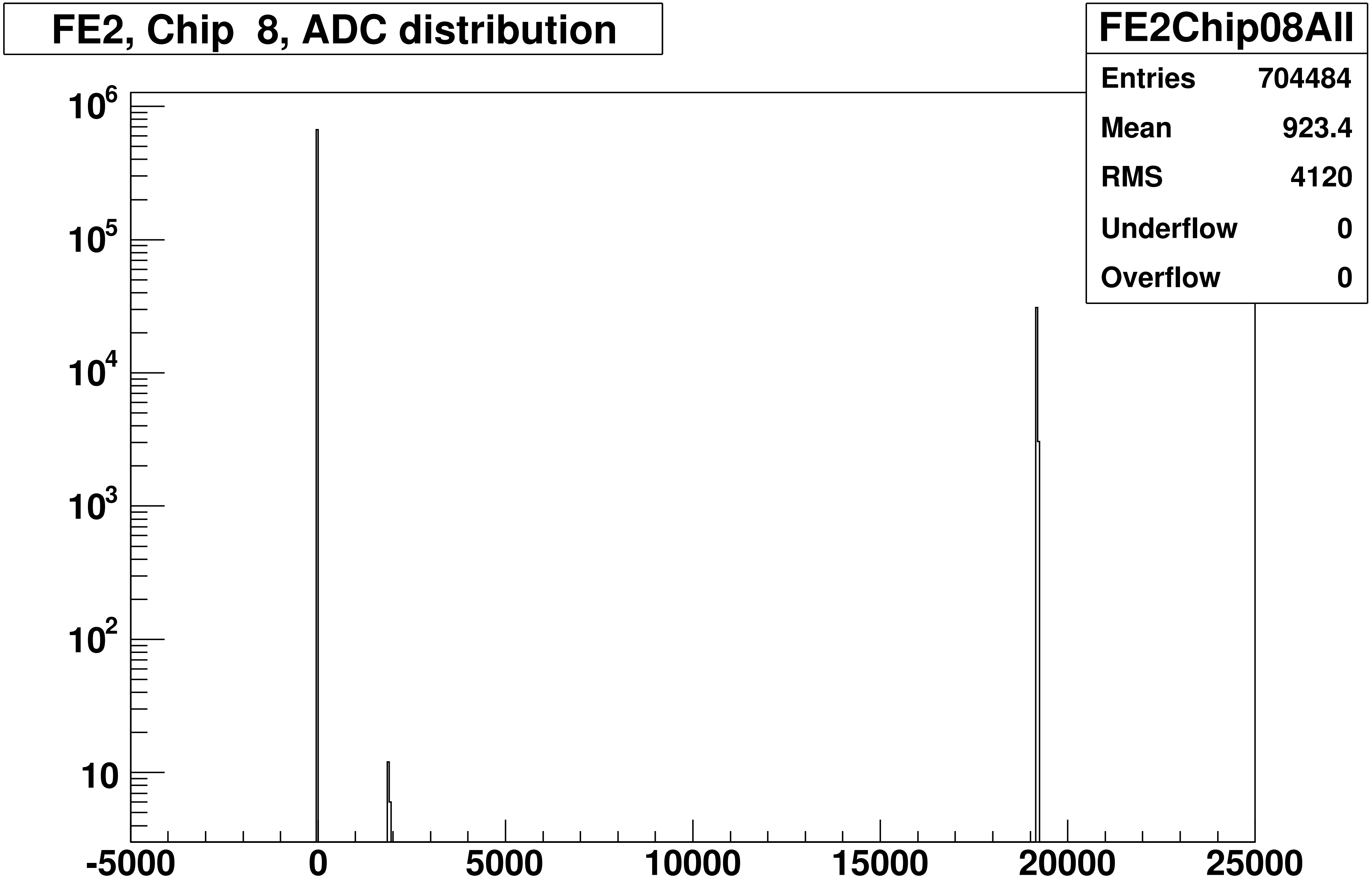
0
50
100
150
200


FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 704484
Mean 1.915e+04
RMS 4.367
Underflow 6.705e+05
Overflow 0
10
10^2
10^3
10^4
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 704484
Mean 923.4
RMS 4120
Underflow 0
Overflow 0
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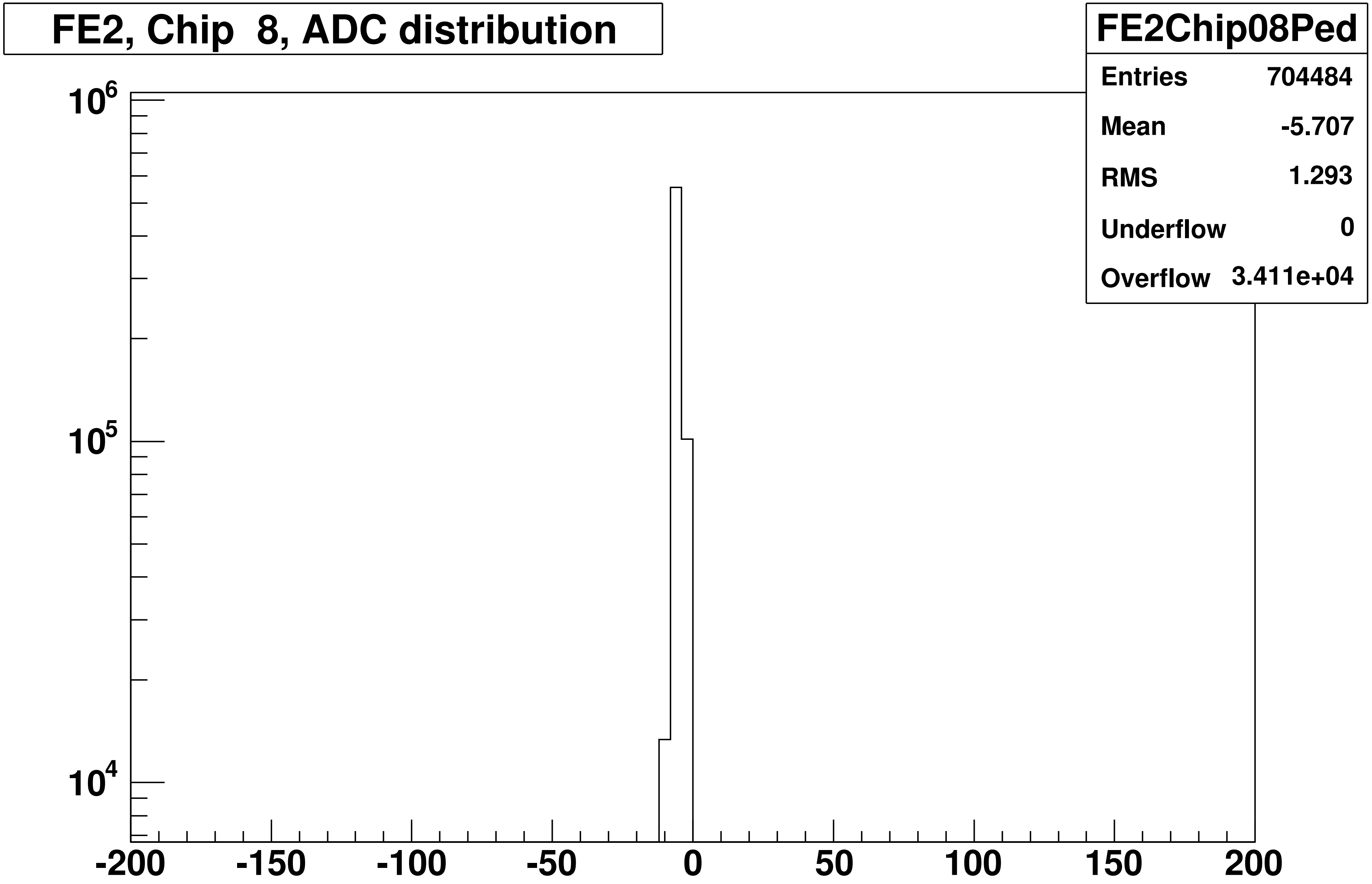
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 704484
Mean -5.707
RMS 1.293
Underflow 0
Overflow 3.411e+04
10^6
10^5
10^4
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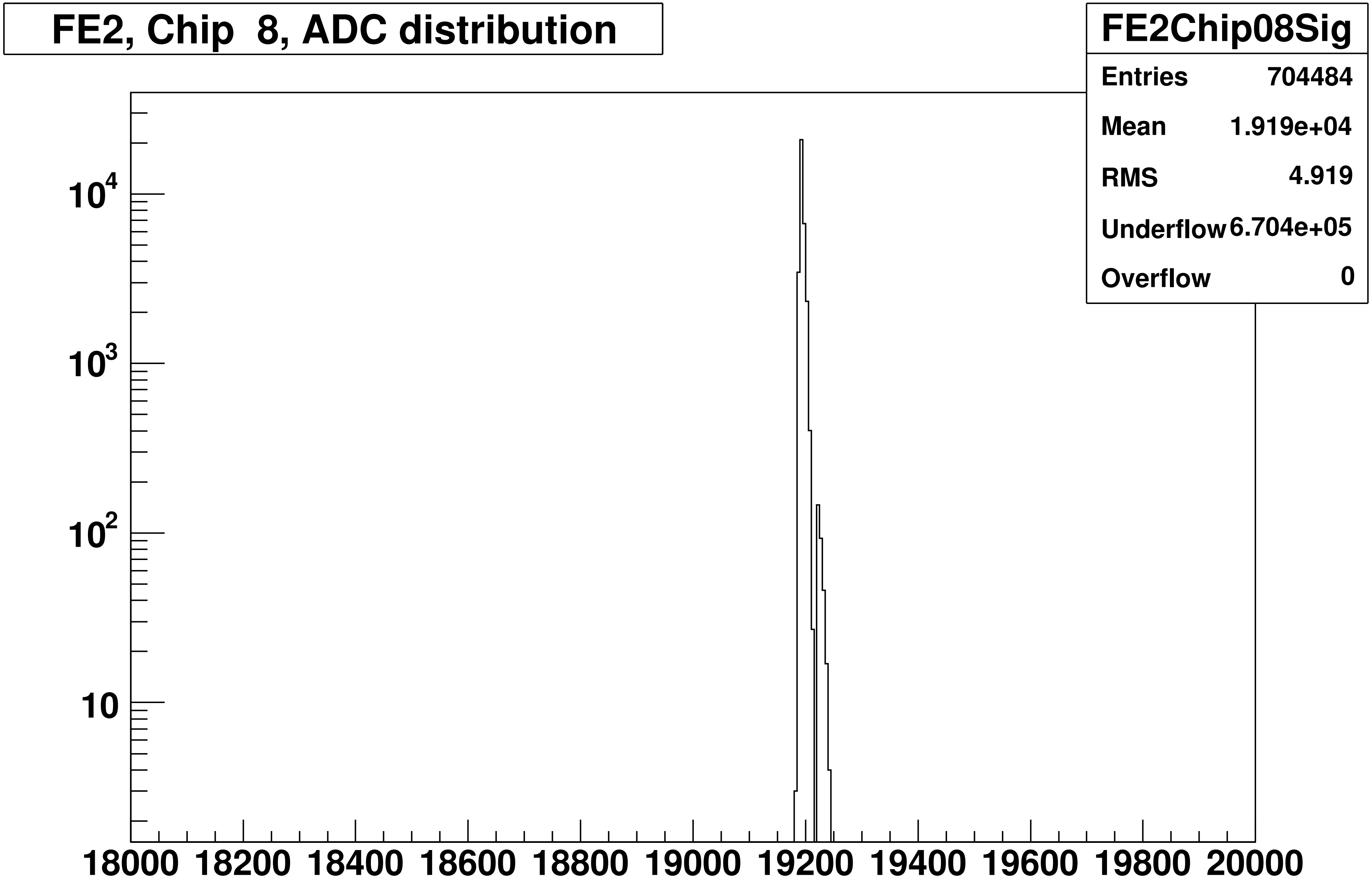
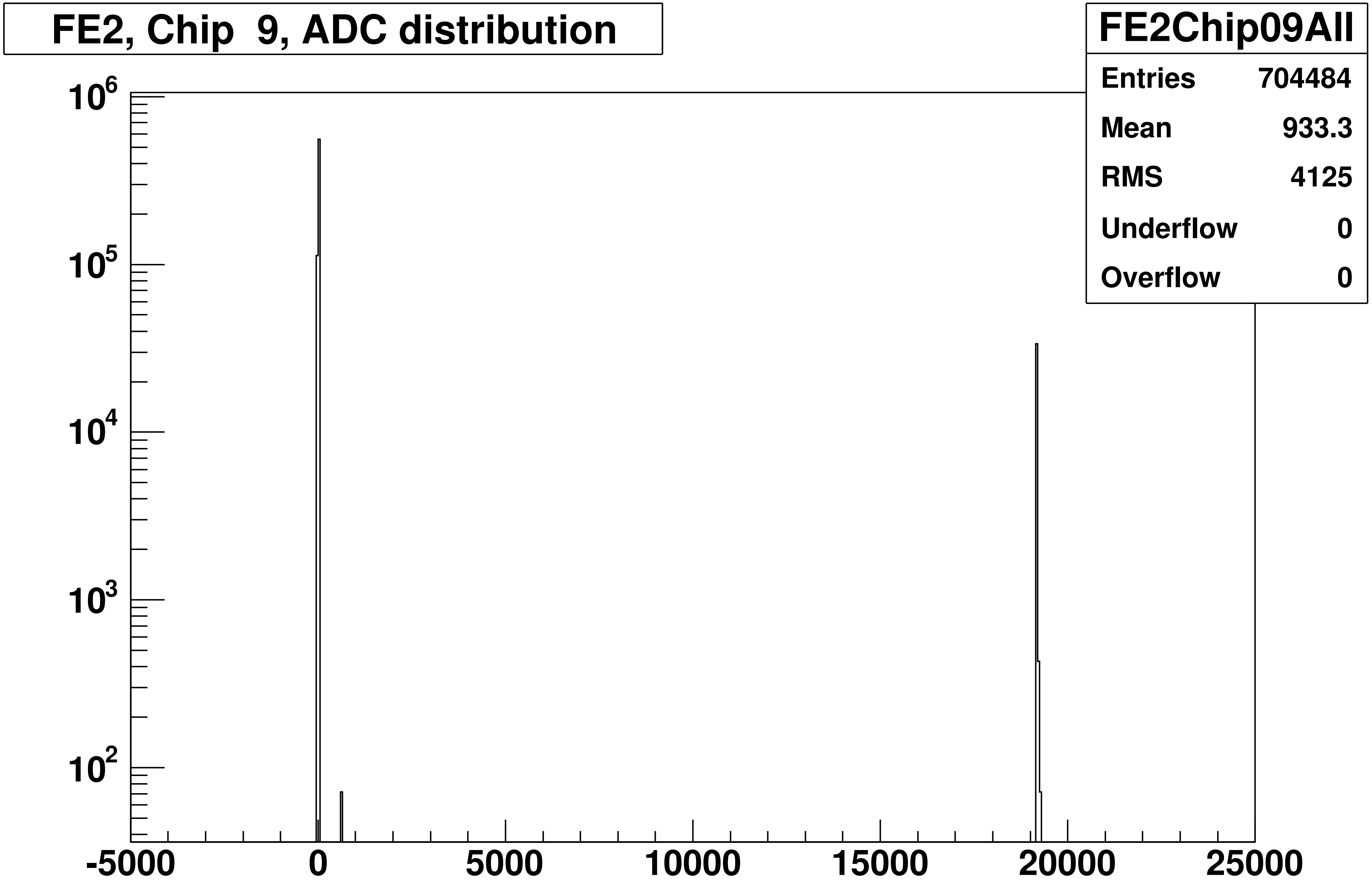
-100
-50
0
50
100
150
200


FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 704484
Mean 1.919e+04
RMS 4.919
Underflow 6.704e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 704484
Mean 933.3
RMS 4125
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
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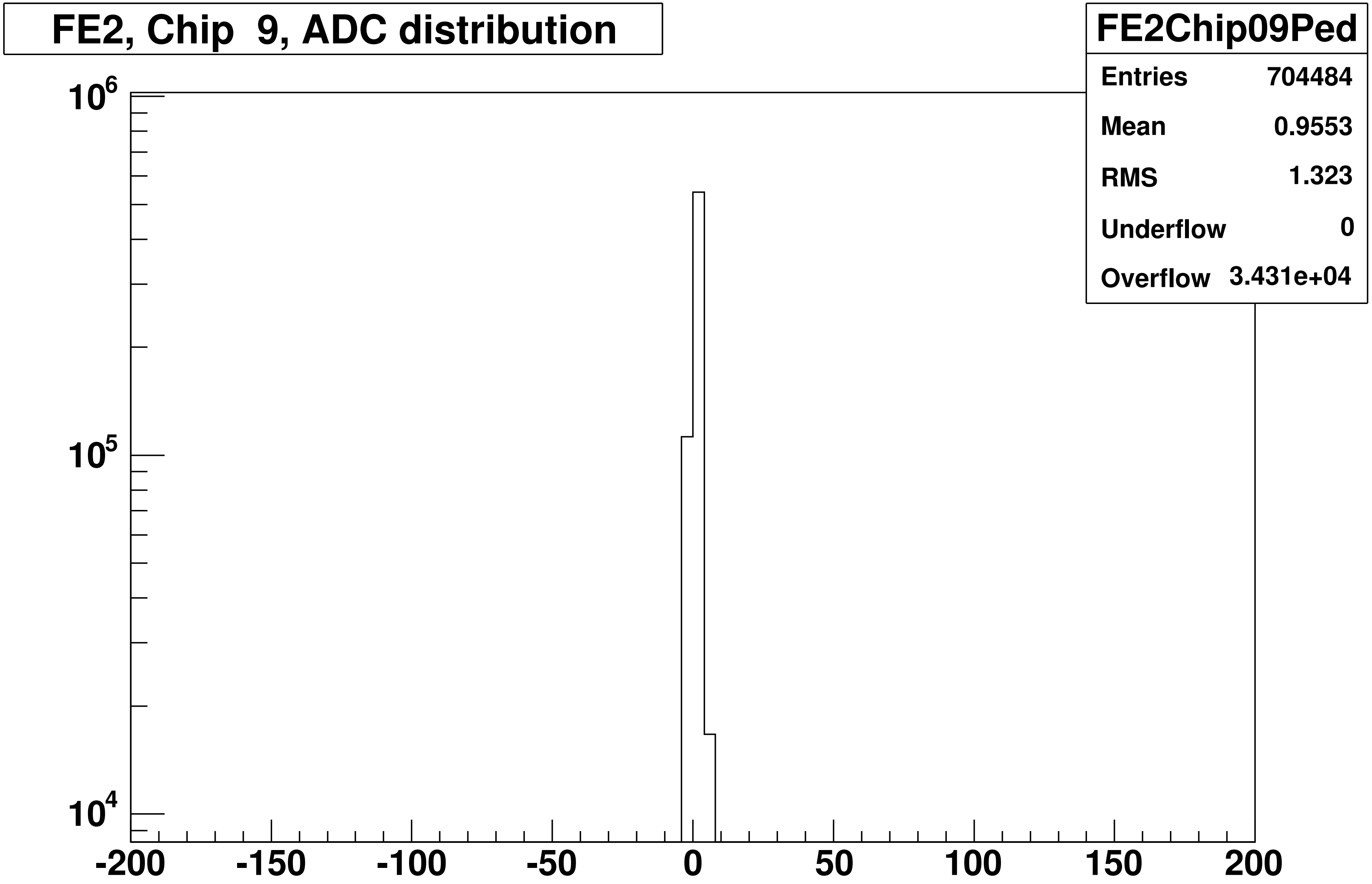
10000
15000
20000
25000



FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 704484
Mean 0.9553
RMS 1.323
Underflow 0
Overflow 3.431e+04
10^6
10^5
10^4
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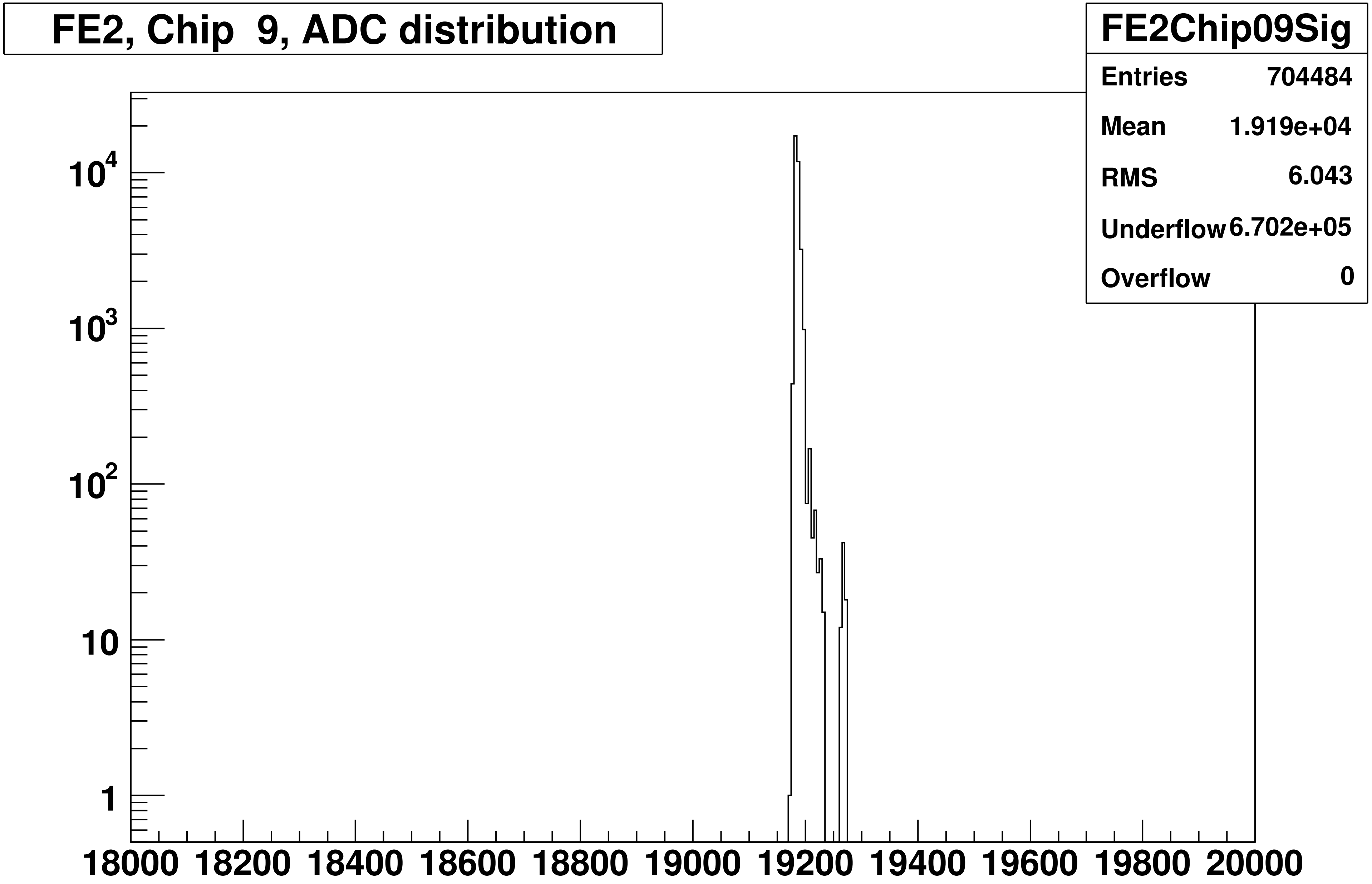
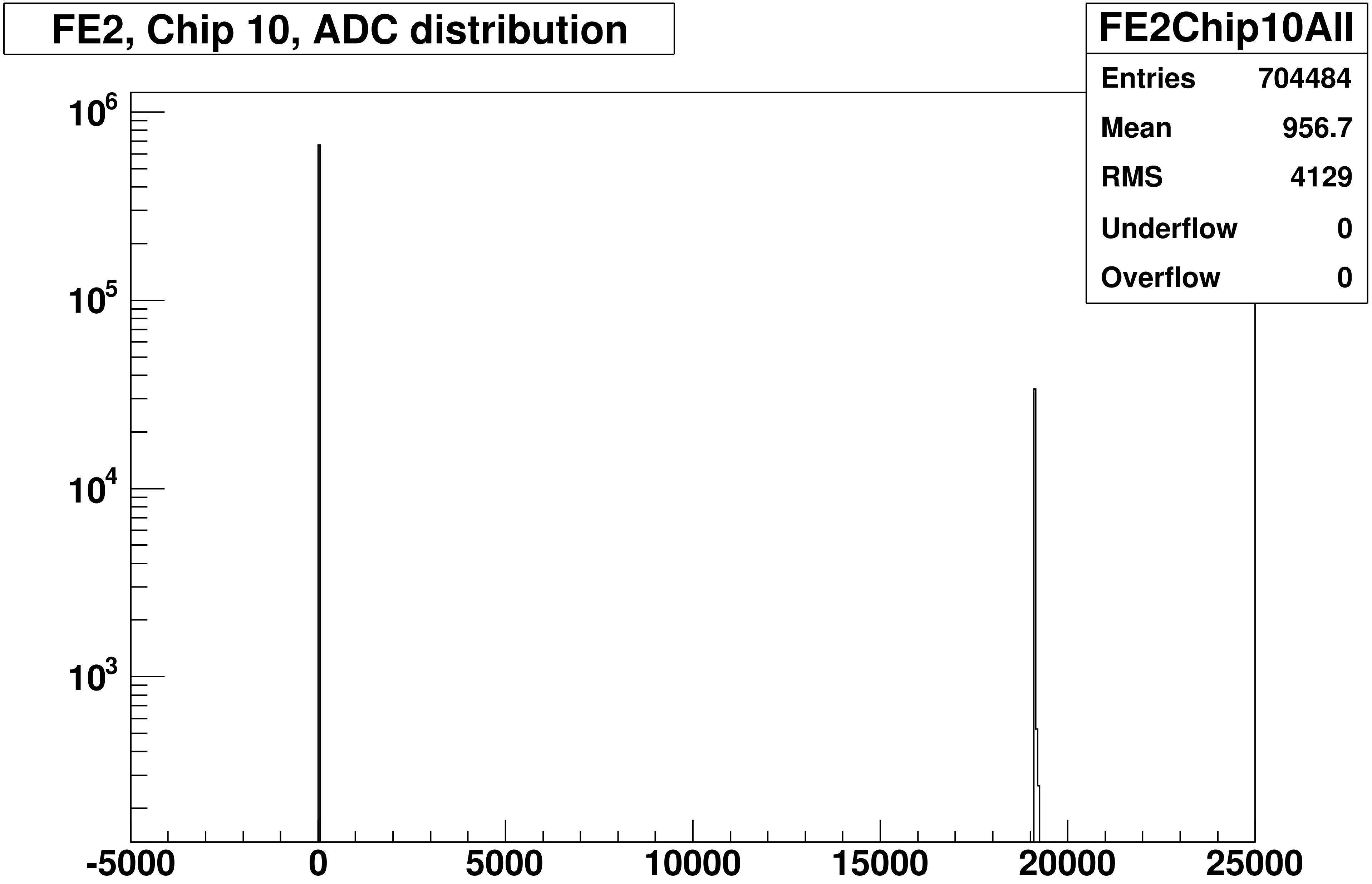
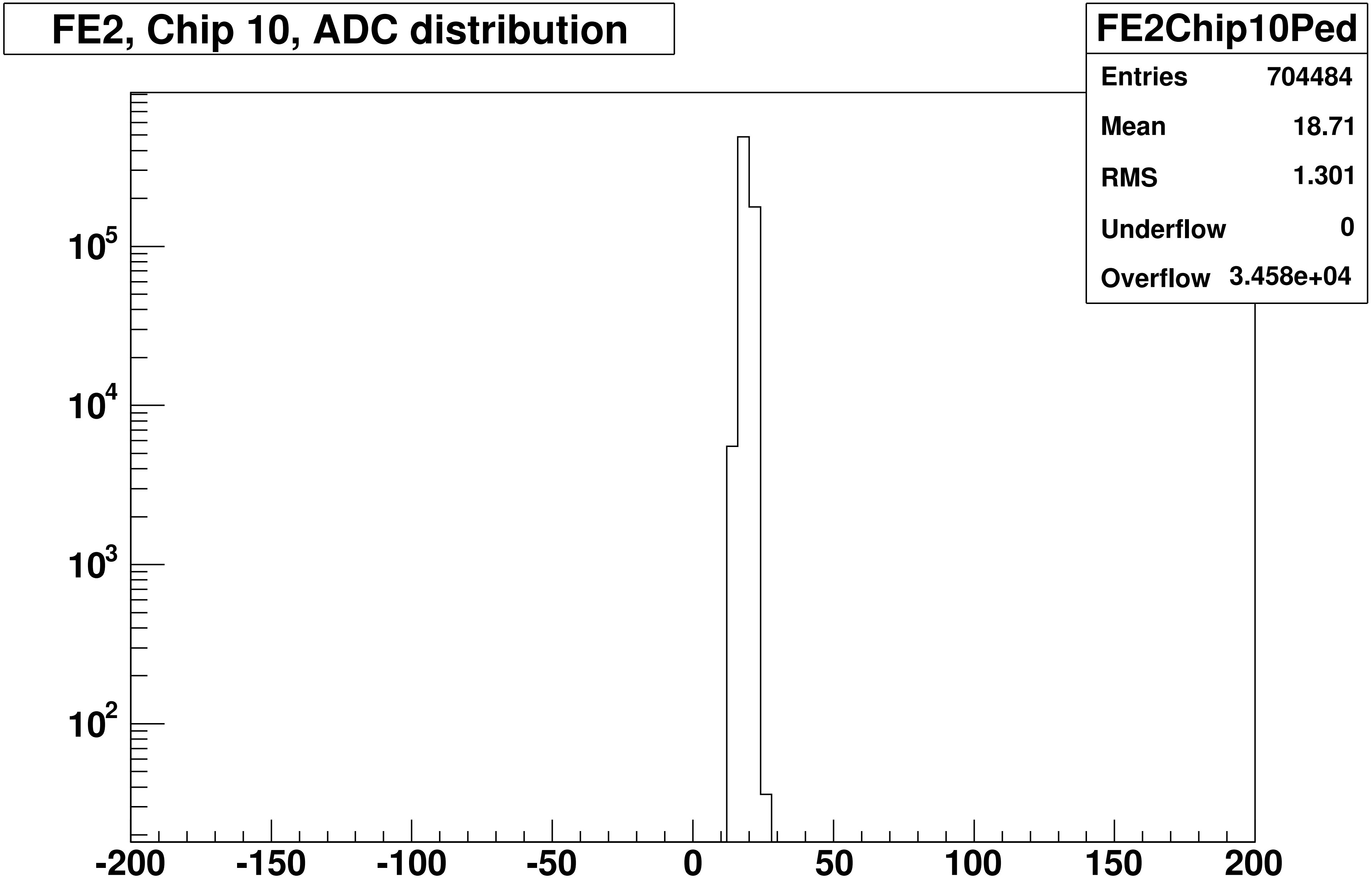
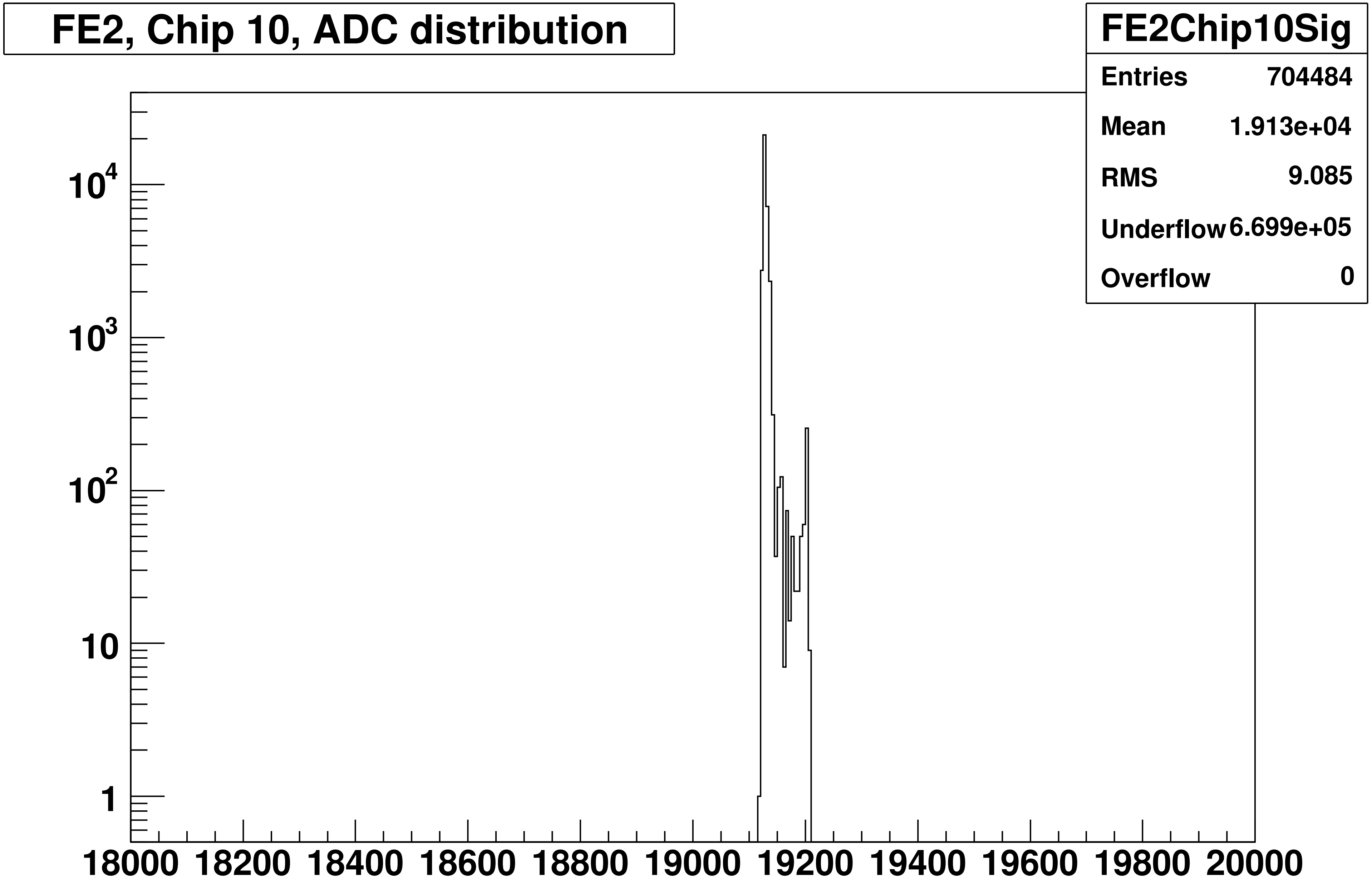
-100
-50
0
50
100
150
200

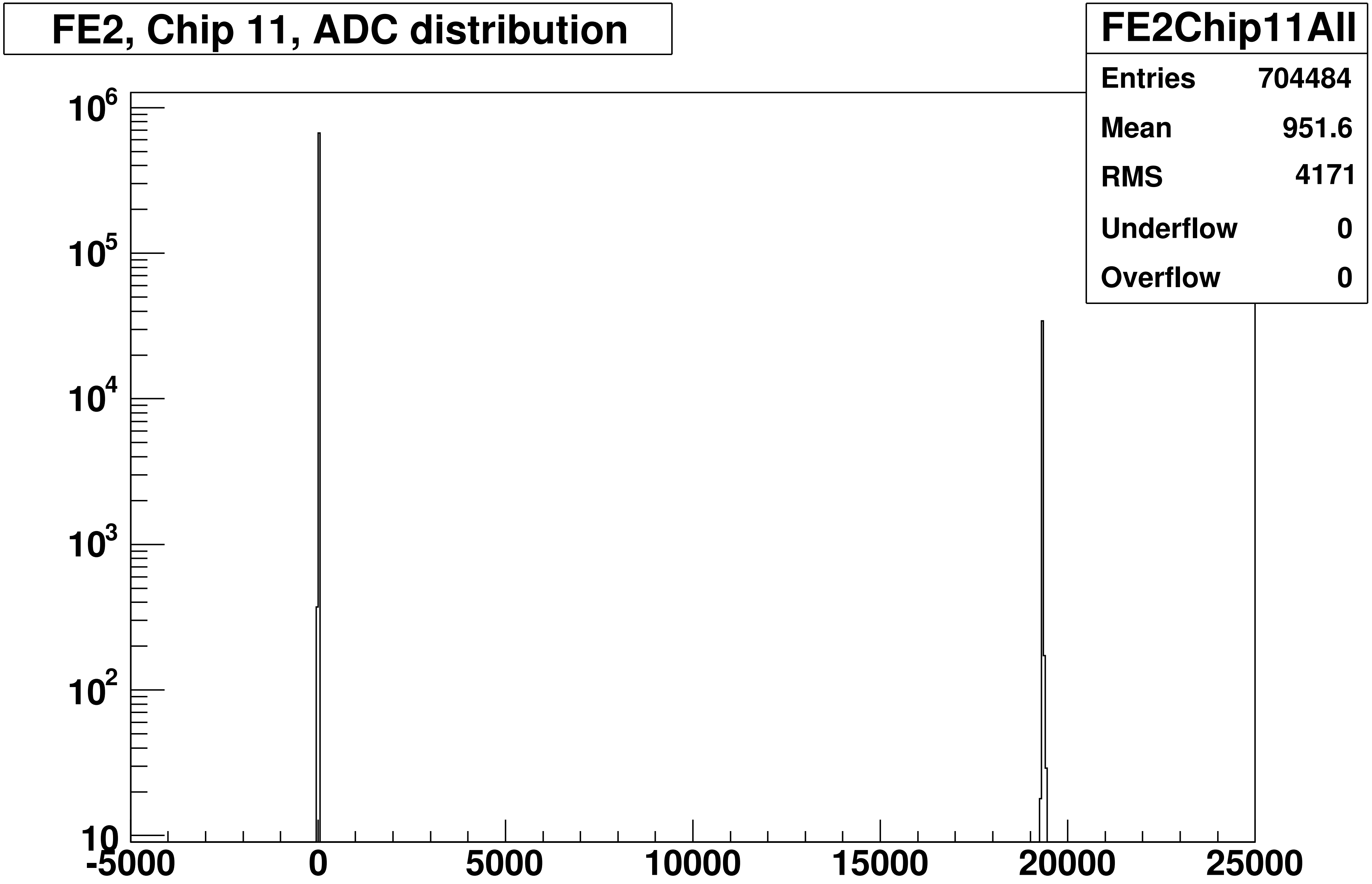


FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 704484
Mean 1.919e+04
RMS 6.043
Underflow 6.702e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 704484
Mean 956.7
RMS 4129
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 704484
Mean 18.71
RMS 1.301
Underflow 0
Overflow 3.458e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 704484
Mean 1.913e+04
RMS 9.085
Underflow 6.699e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 704484
Mean 951.6
RMS 4171
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
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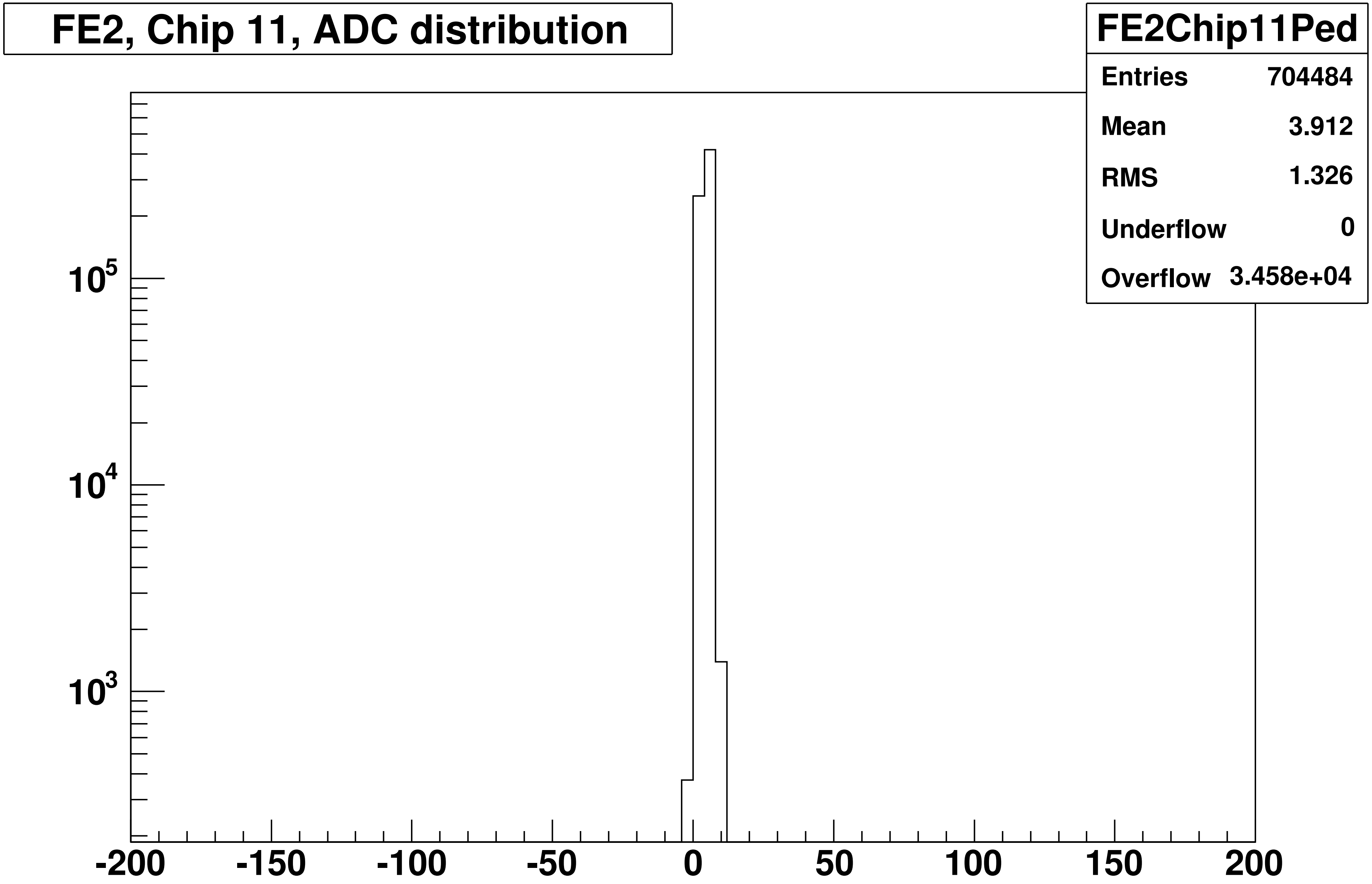
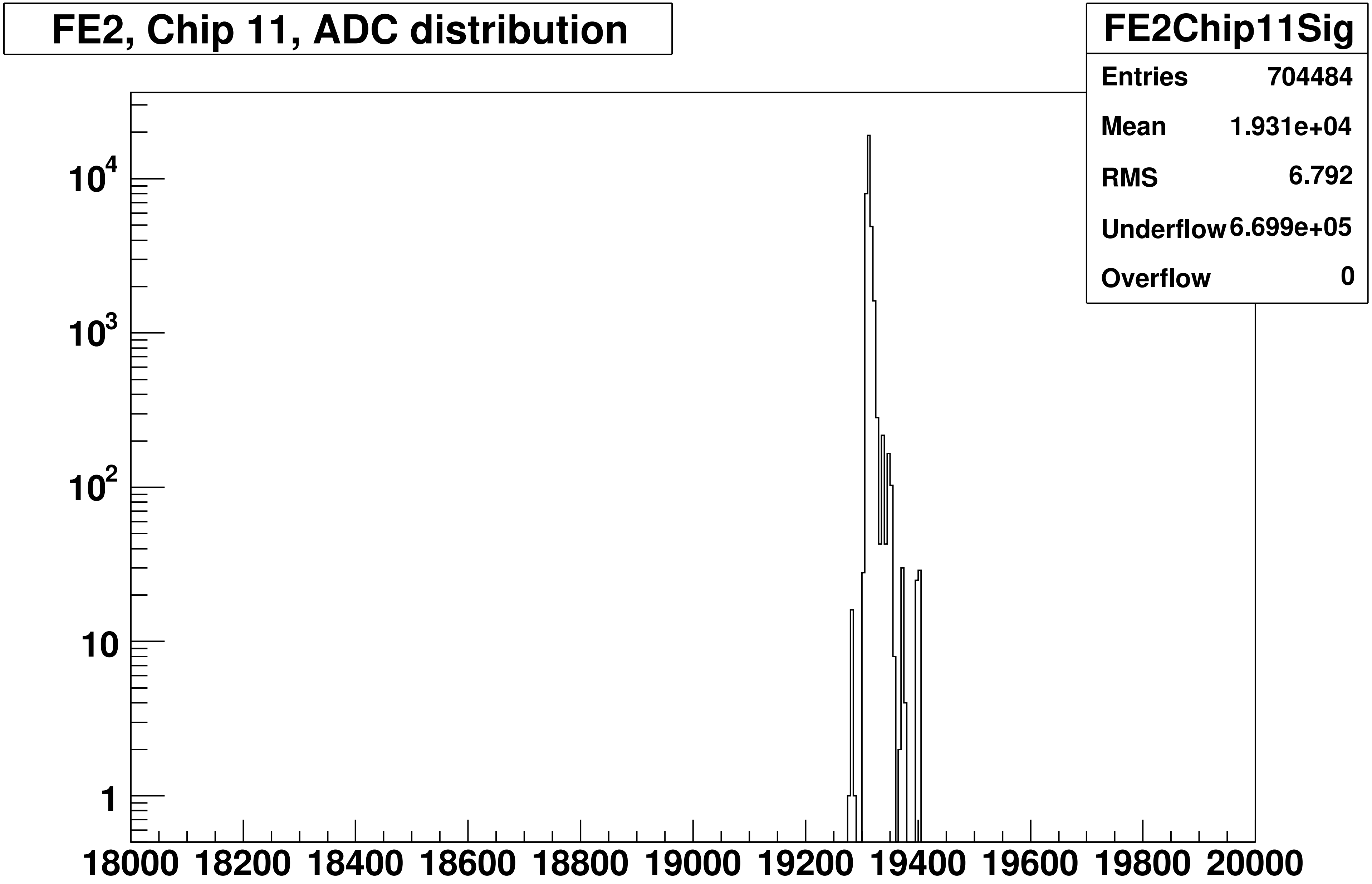
10000
15000
20000
25000


FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 704484
Mean 3.912
RMS 1.326
Underflow 0
Overflow 3.458e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 704484
Mean 1.931e+04
RMS 6.792
Underflow 6.699e+05
Overflow 0
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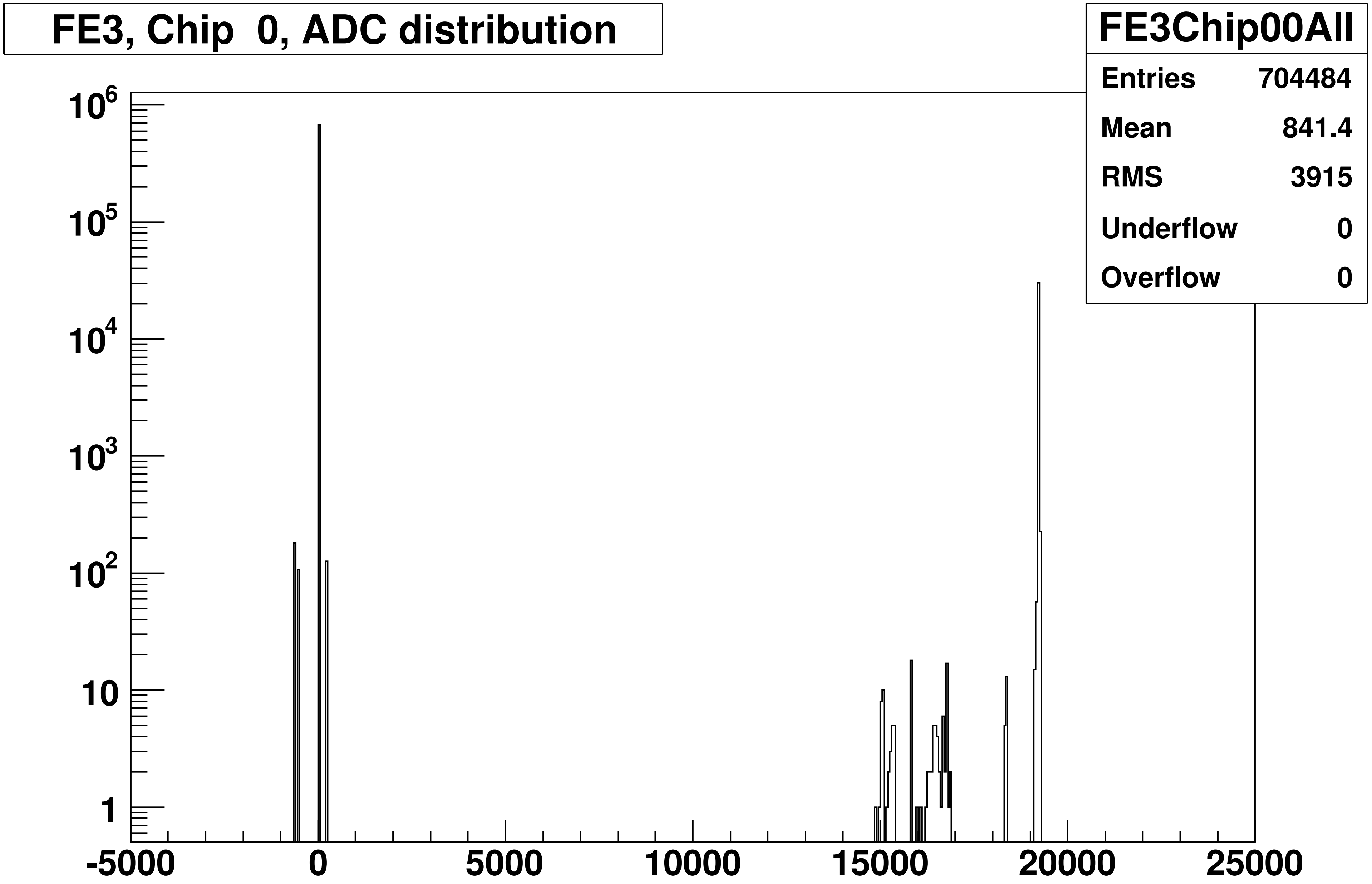
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 704484
Mean 841.4
RMS 3915
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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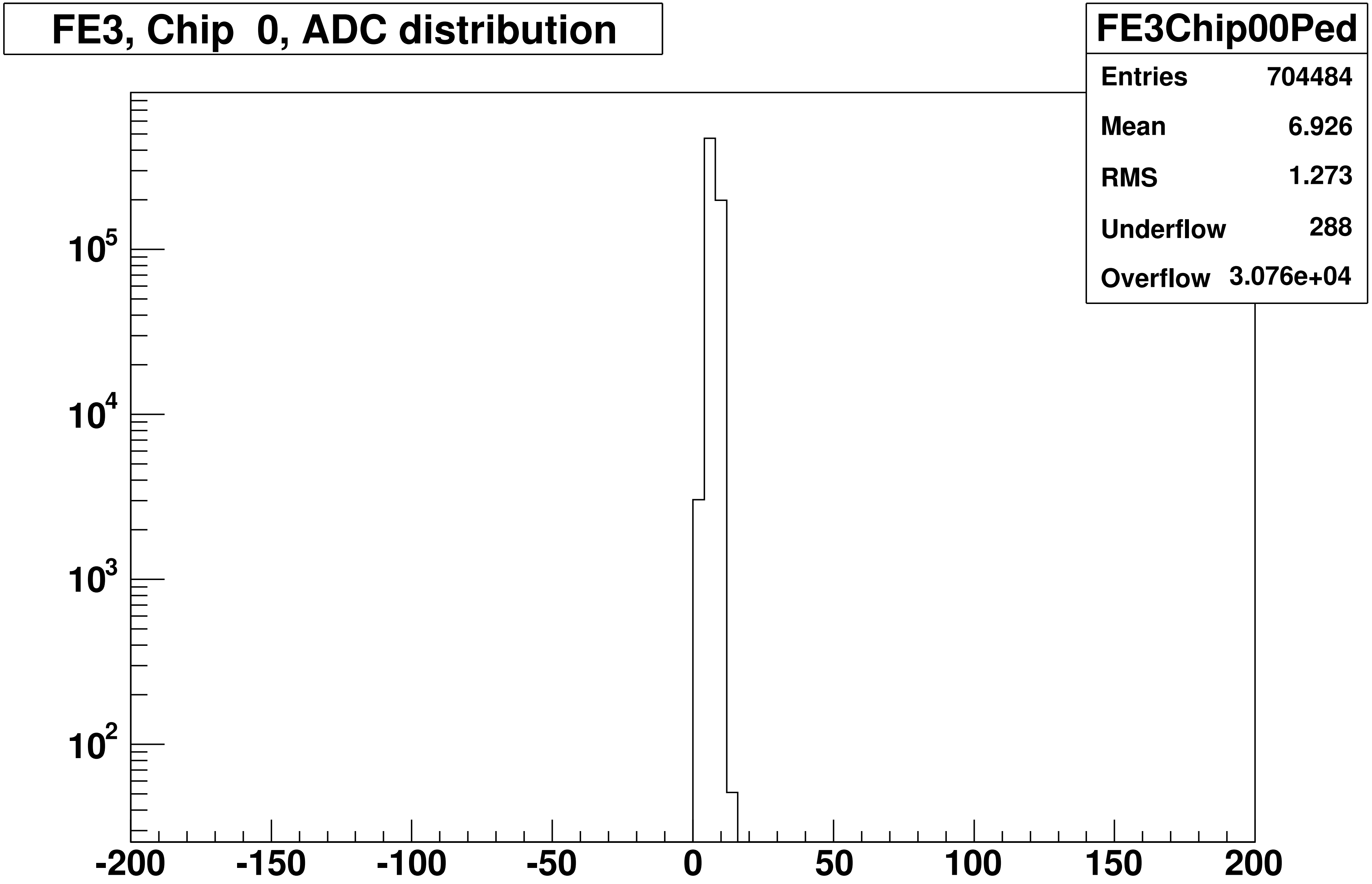
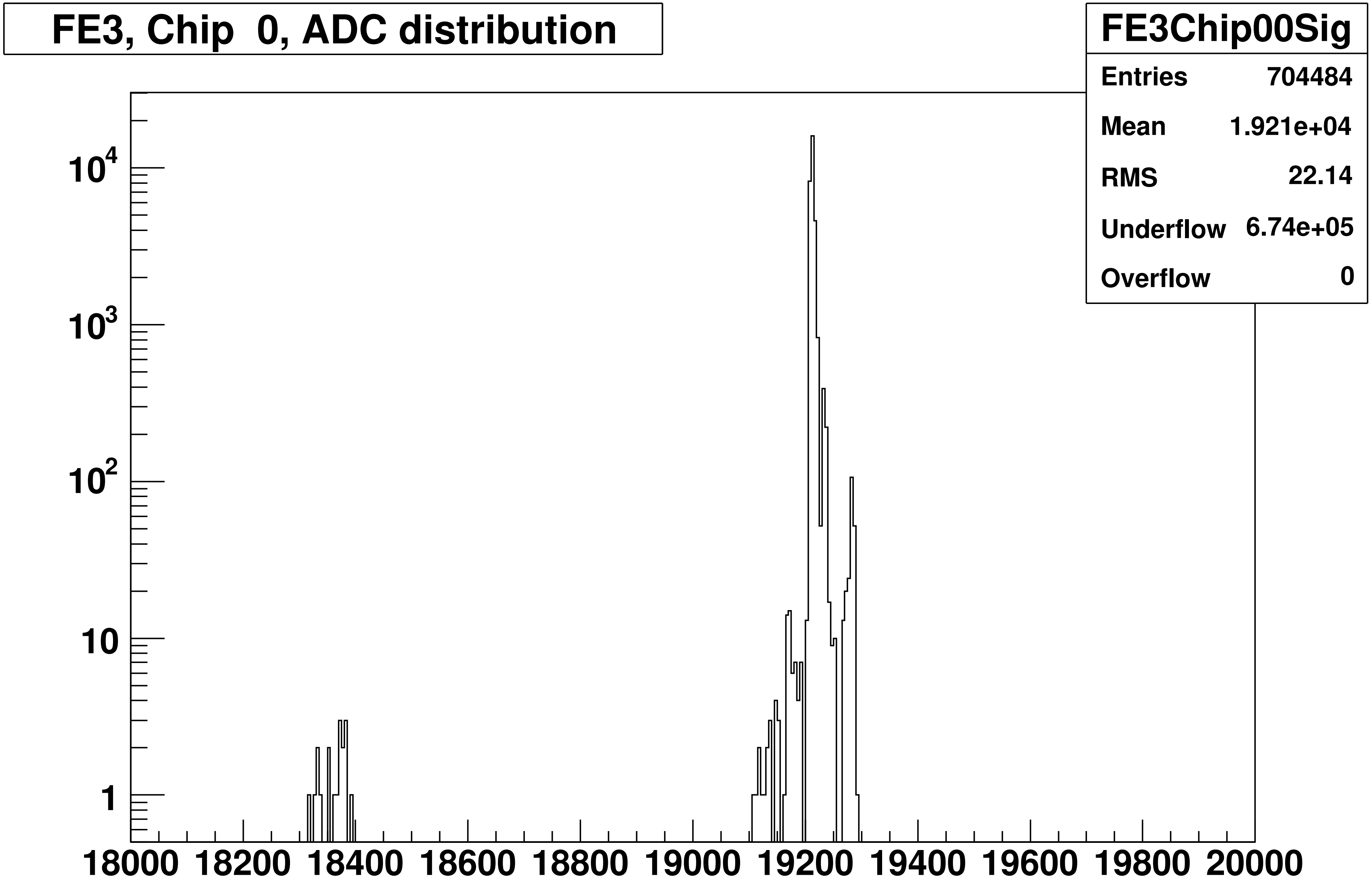
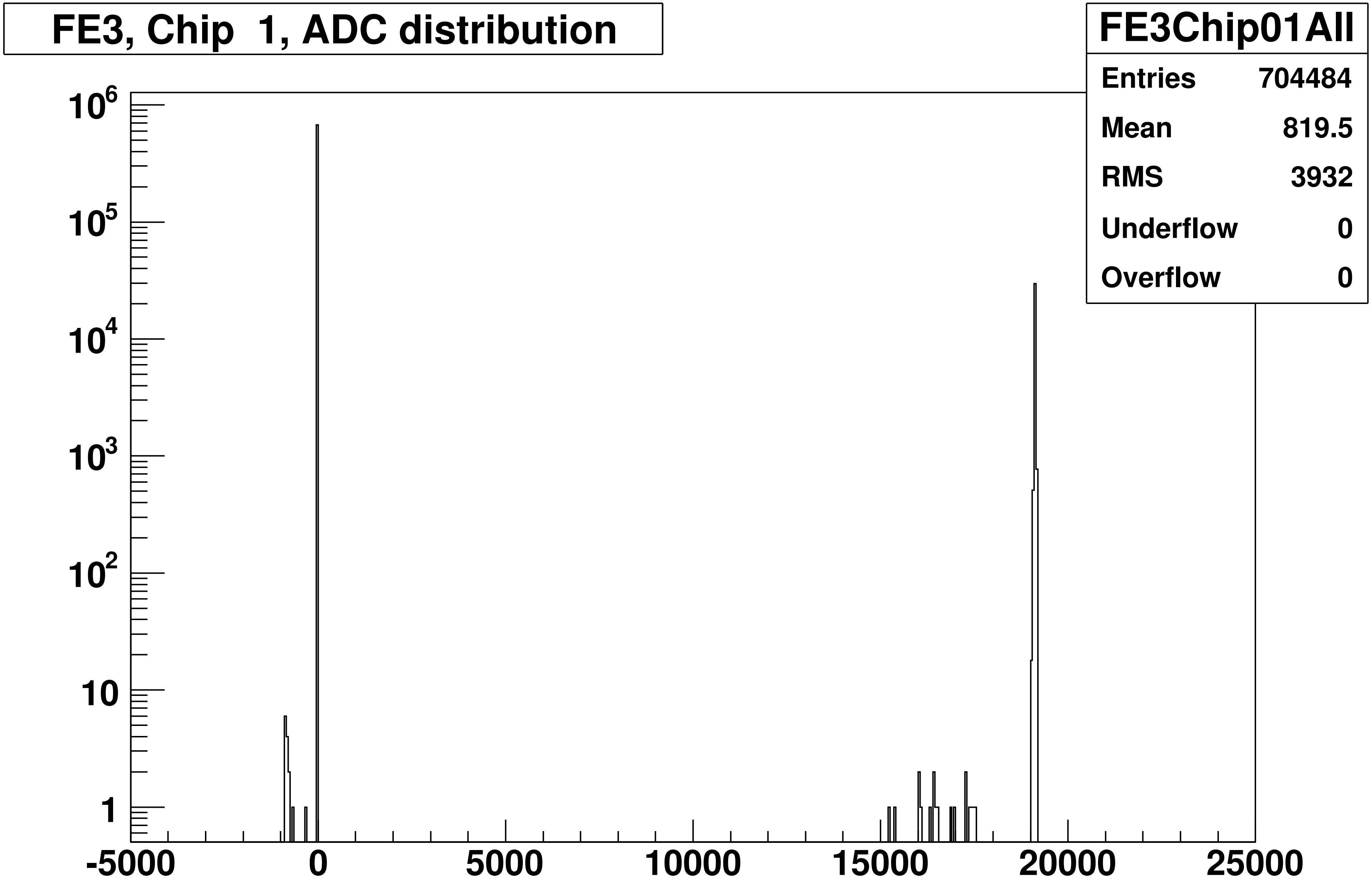
10000
15000
20000
25000



FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 704484
Mean 6.926
RMS 1.273
Underflow 288
Overflow 3.076e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 704484
Mean 1.921e+04
RMS 22.14
Underflow 6.74e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 704484
Mean 819.5
RMS 3932
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
5000
10000
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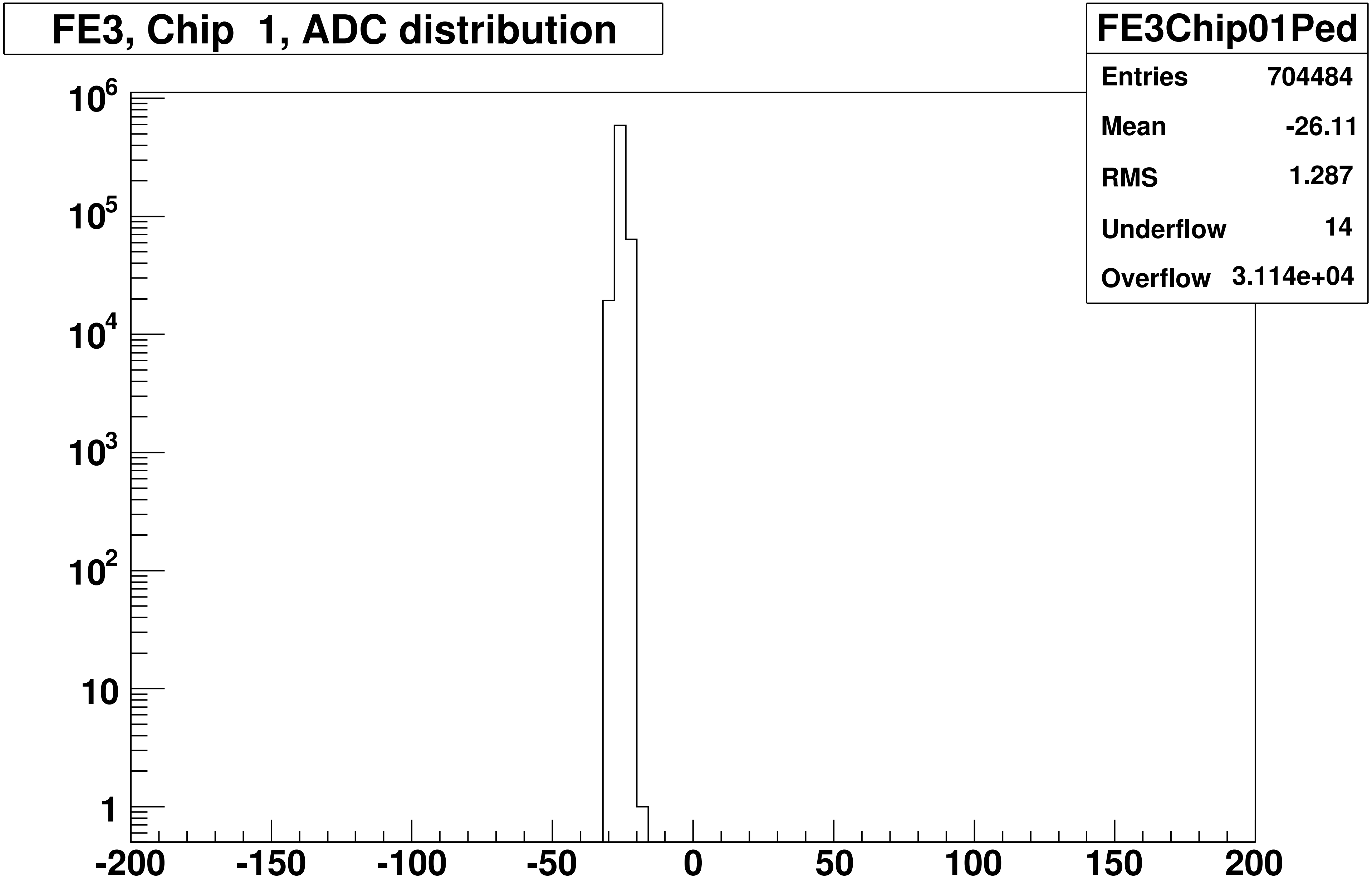
20000
25000


FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 704484
Mean -26.11
RMS 1.287
Underflow 14
Overflow 3.114e+04
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10^5
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10^3
10^2
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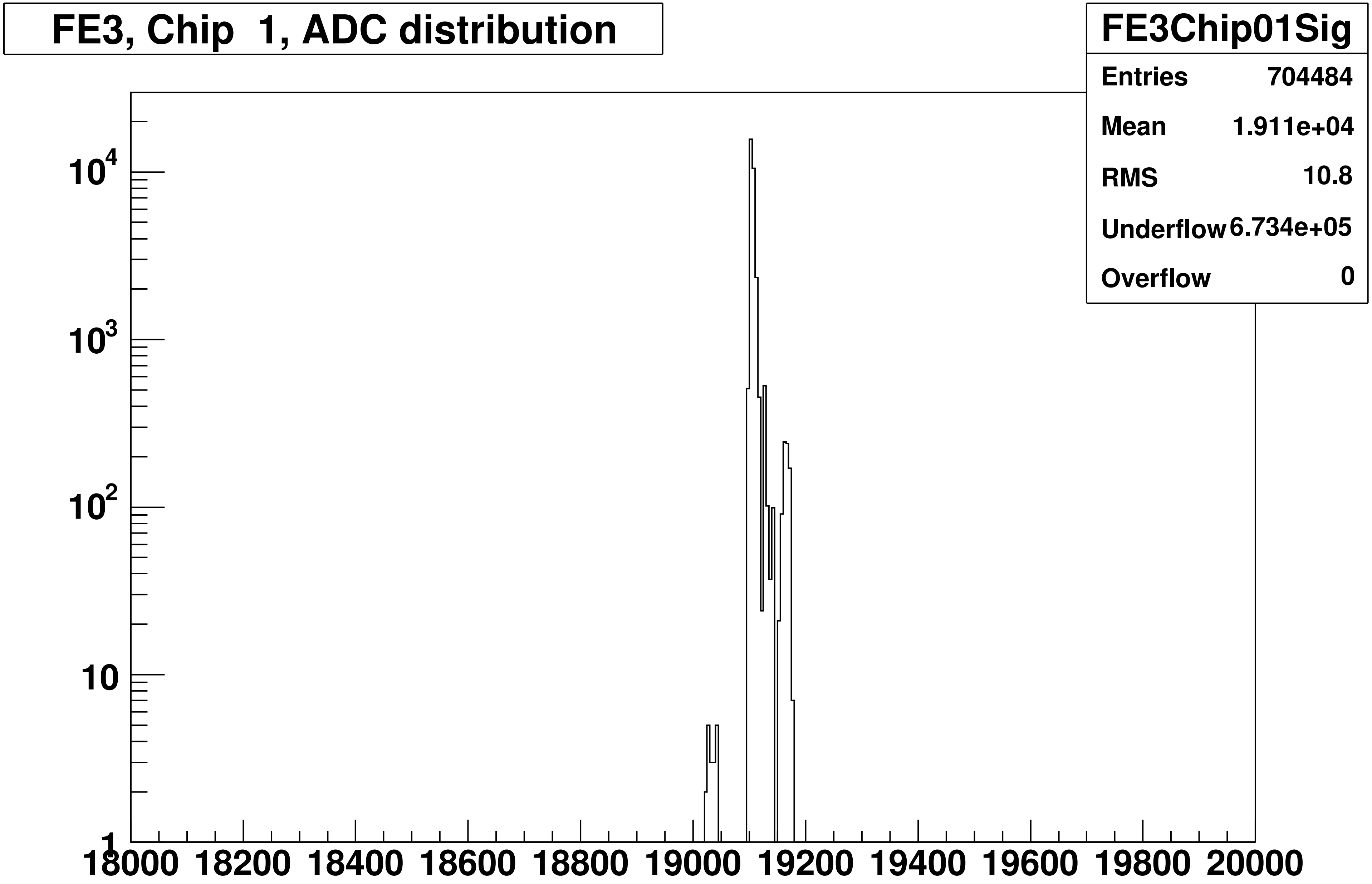
150
200


FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 704484
Mean 1.911e+04
RMS 10.8
Underflow 6.734e+05
Overflow 0
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10^3
10^2
10
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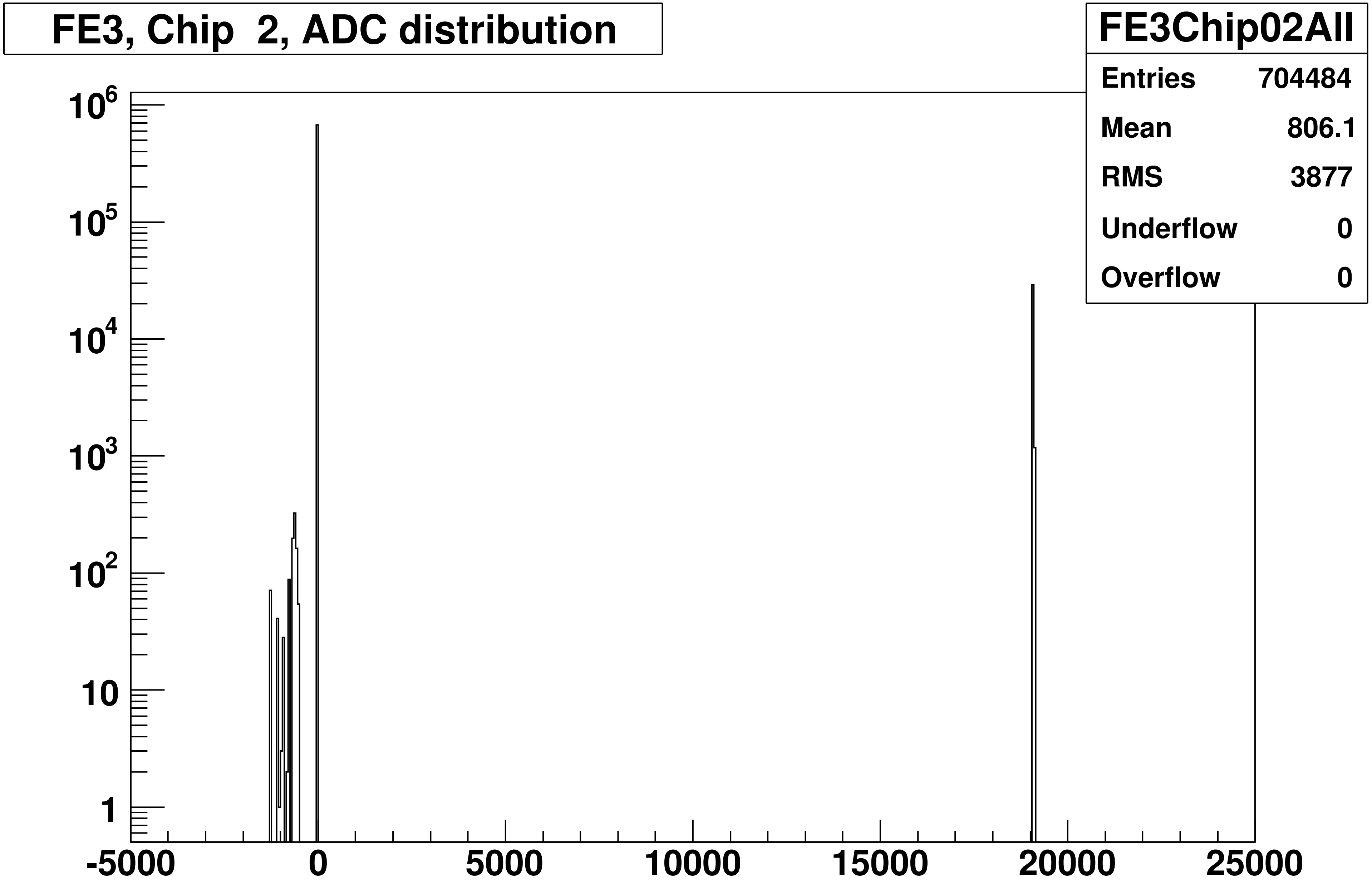
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 704484
Mean 806.1
RMS 3877
Underflow 0
Overflow 0
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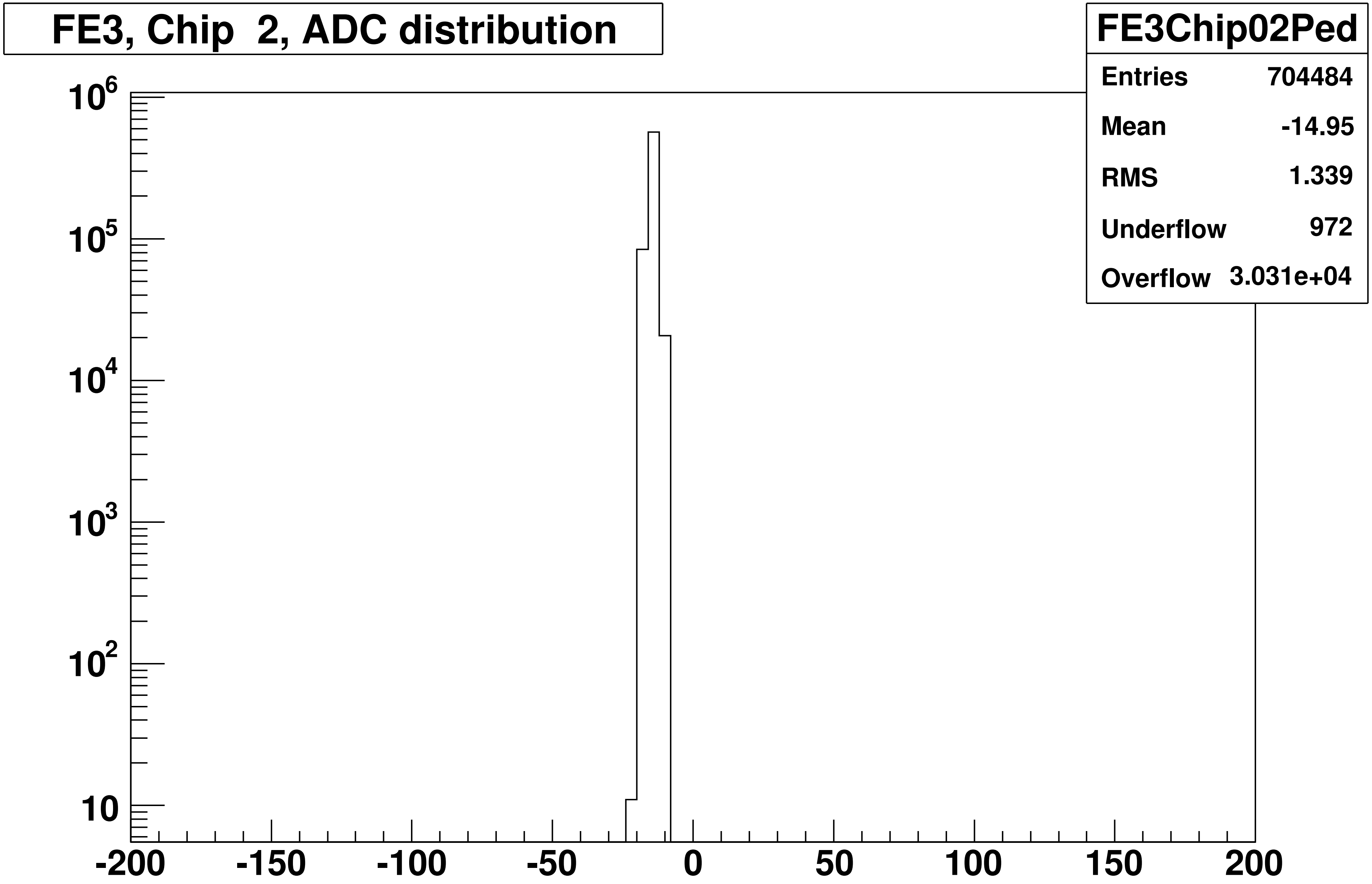
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 704484
Mean -14.95
RMS 1.339
Underflow 972
Overflow 3.031e+04
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10^5
10^4
10^3
10^2
10
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-100
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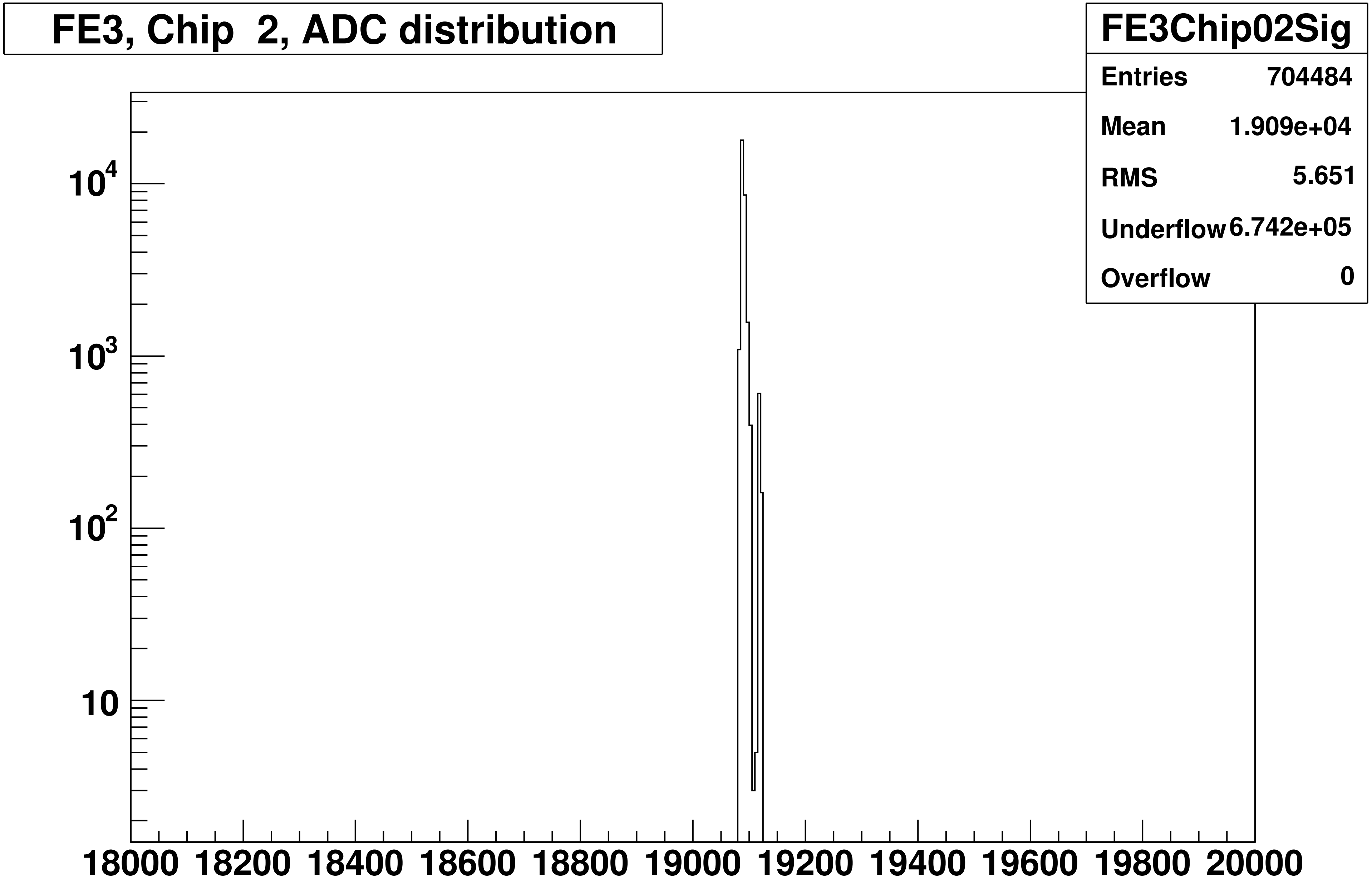
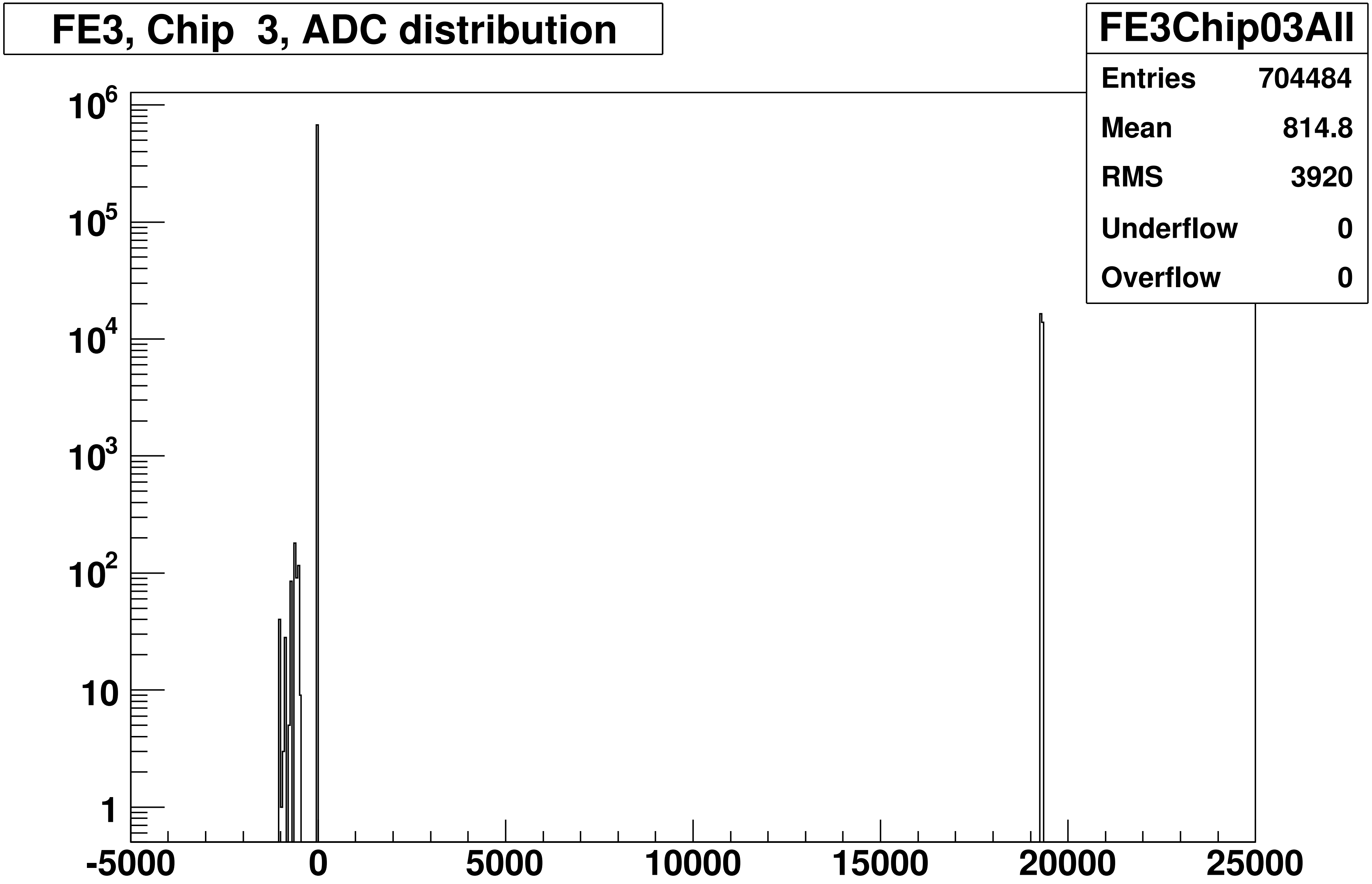
50
100
150
200


FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 704484
Mean 1.909e+04
RMS 5.651
Underflow 6.742e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 704484
Mean 814.8
RMS 3920
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
5000
10000
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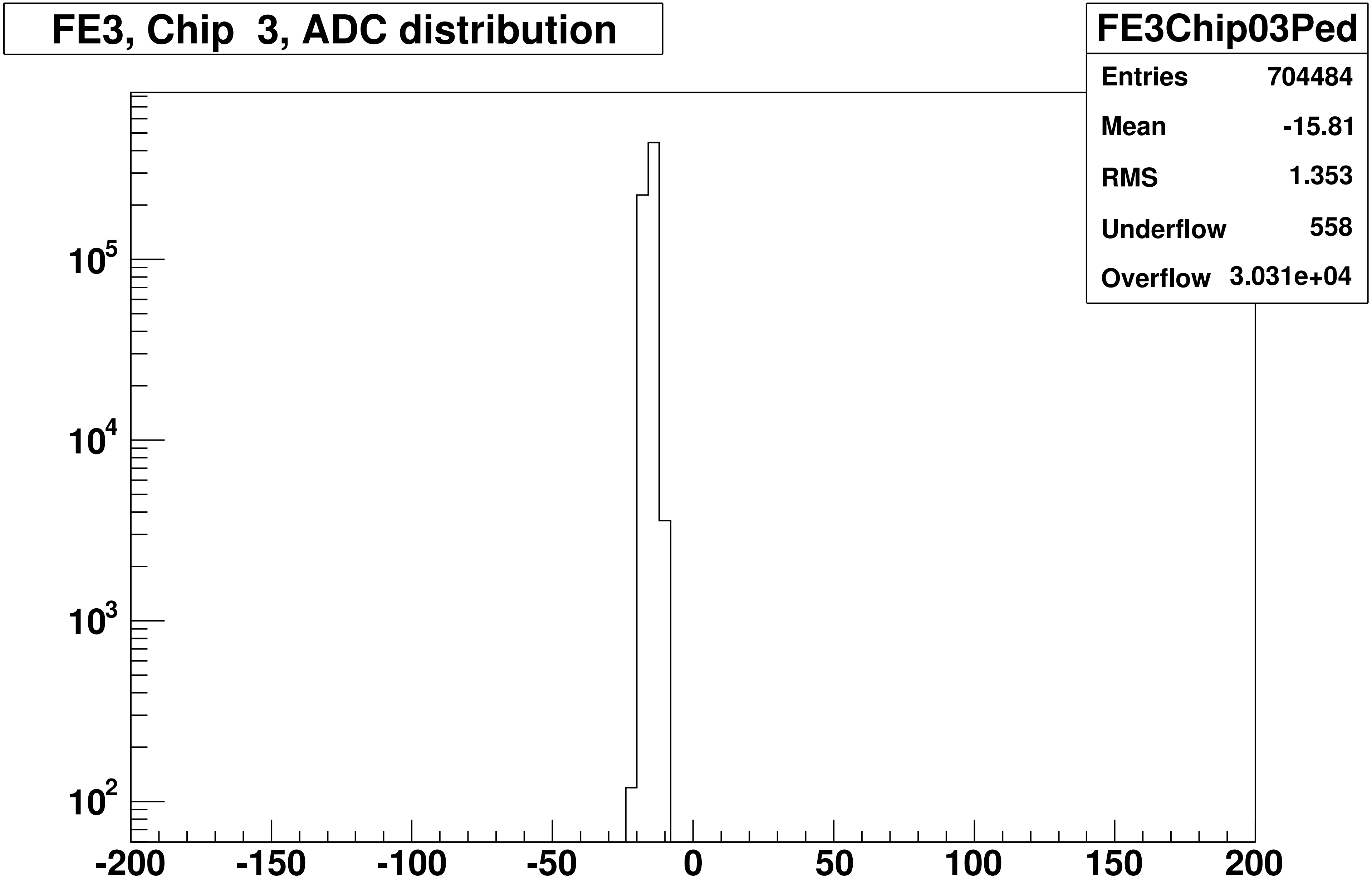
20000
25000



FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 704484
Mean -15.81
RMS 1.353
Underflow 558
Overflow 3.031e+04
10^5
10^4
10^3
10^2
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150
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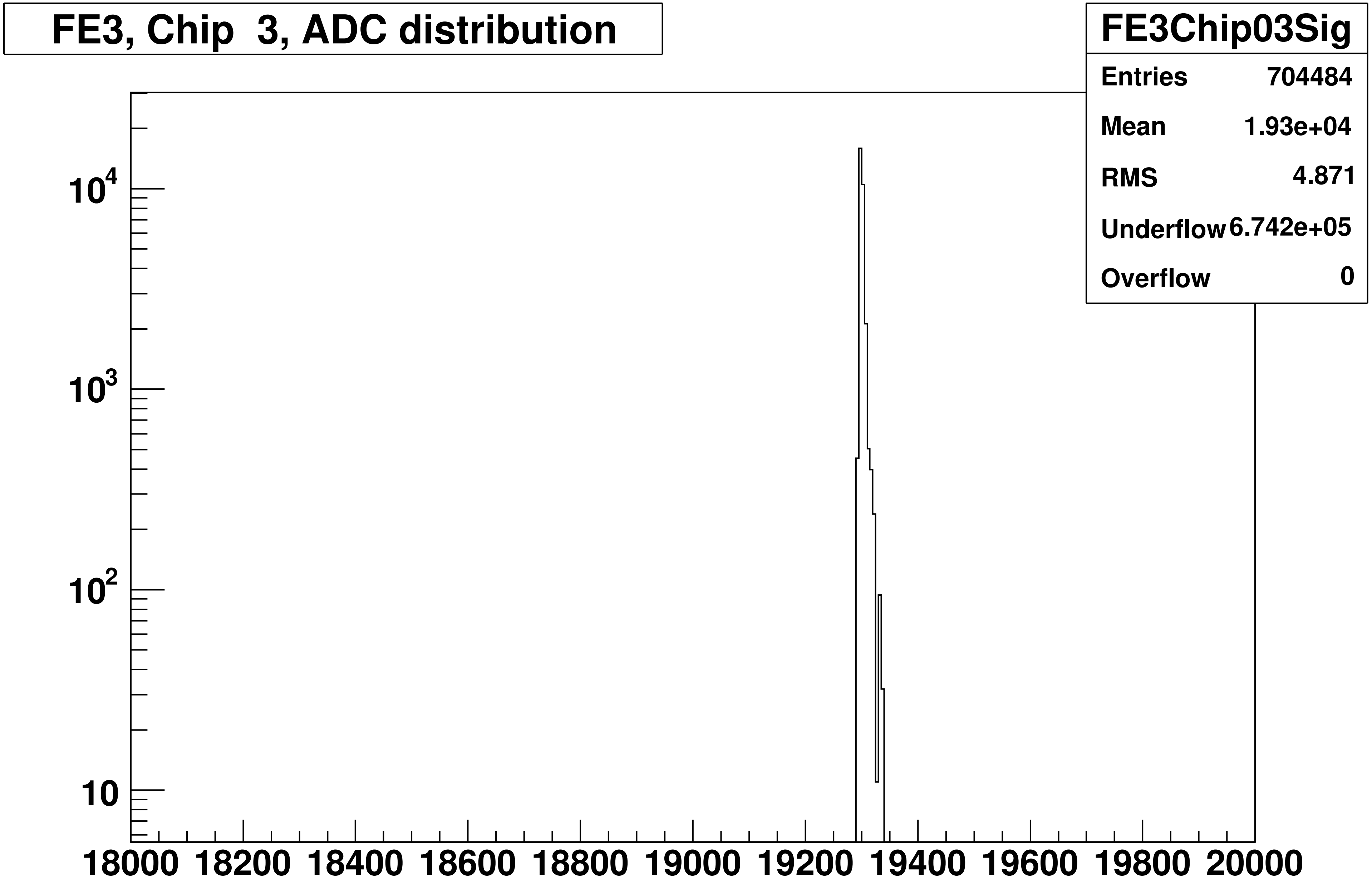
FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 704484
Mean 1.93e+04
RMS 4.871
Underflow 6.742e+05
Overflow 0
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10^2
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10^4
18000
18200
18400
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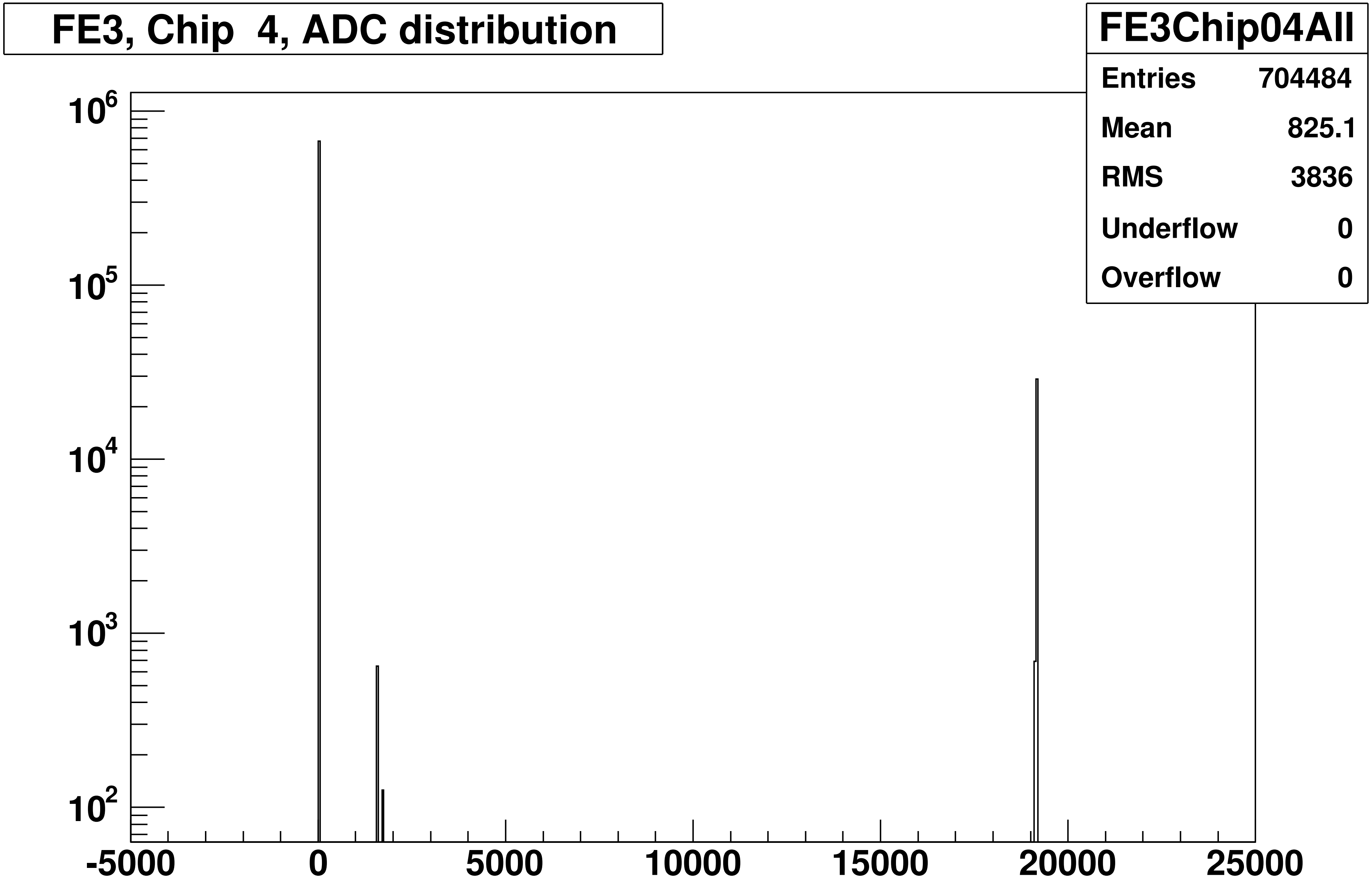
FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 704484
Mean 825.1
RMS 3836
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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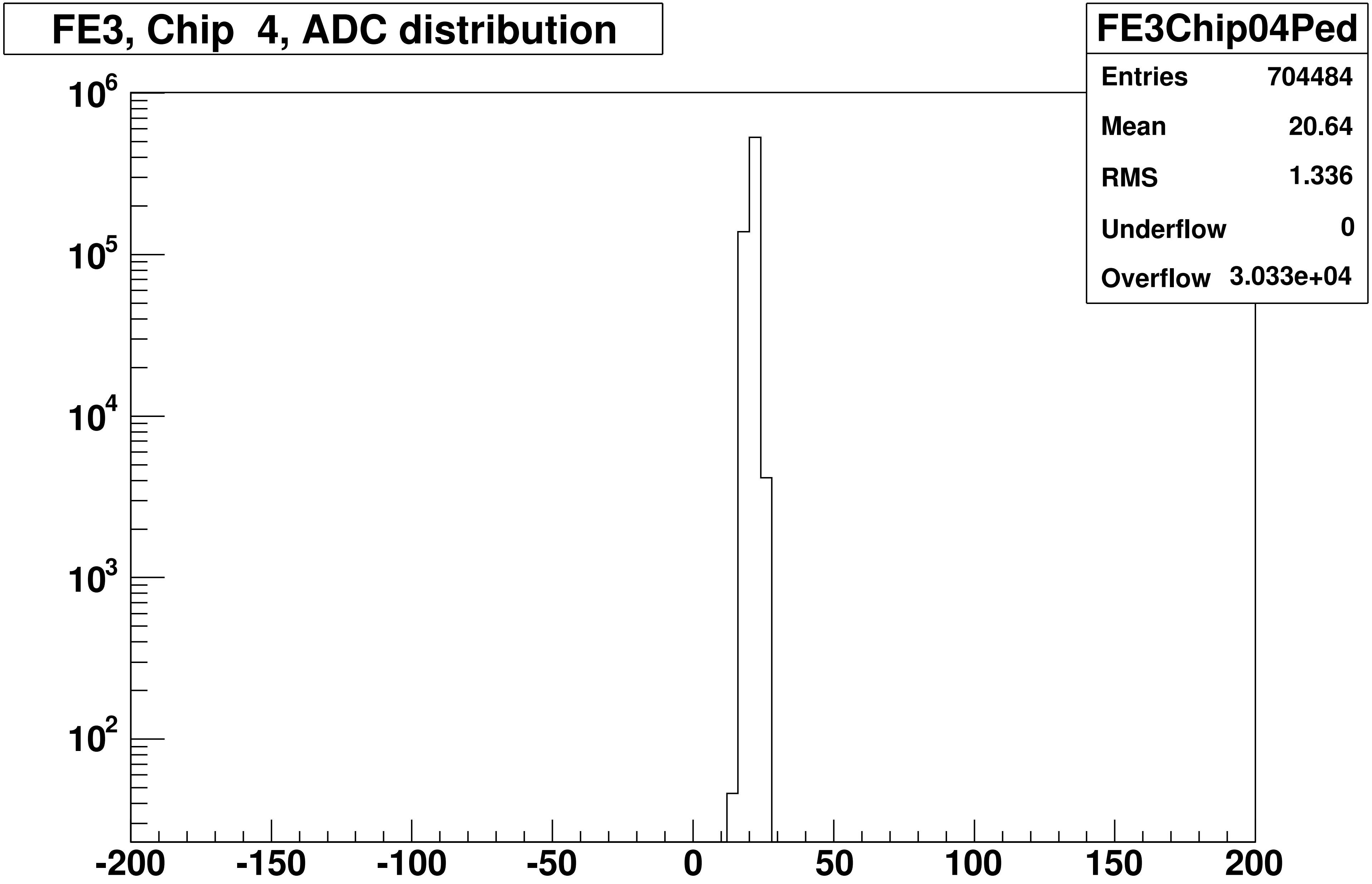
FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 704484
Mean 20.64
RMS 1.336
Underflow 0
Overflow 3.033e+04
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10^3
10^2
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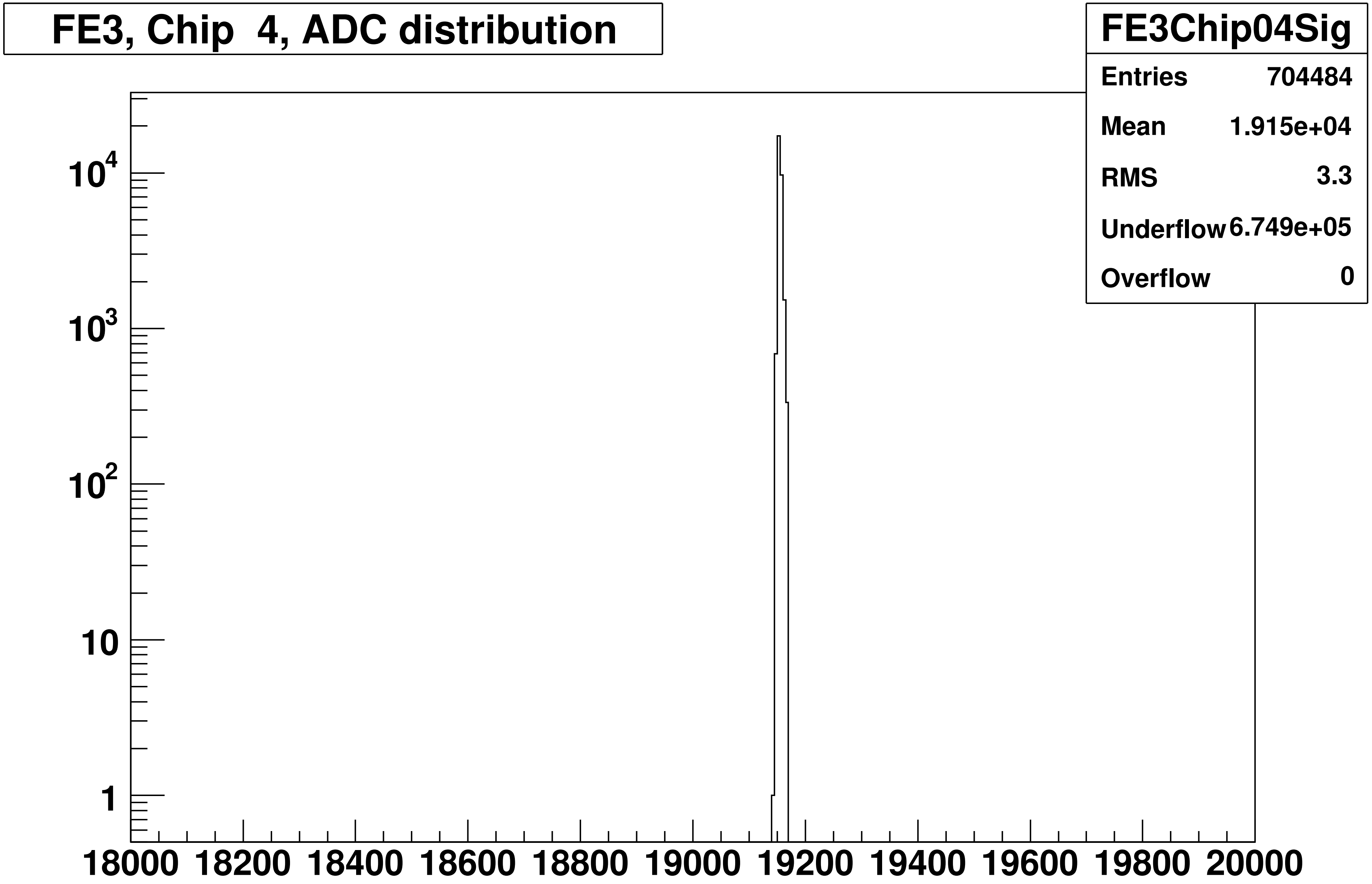

FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 704484
Mean 1.915e+04
RMS 3.3
Underflow 6.749e+05
Overflow 0
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10^3
10^2
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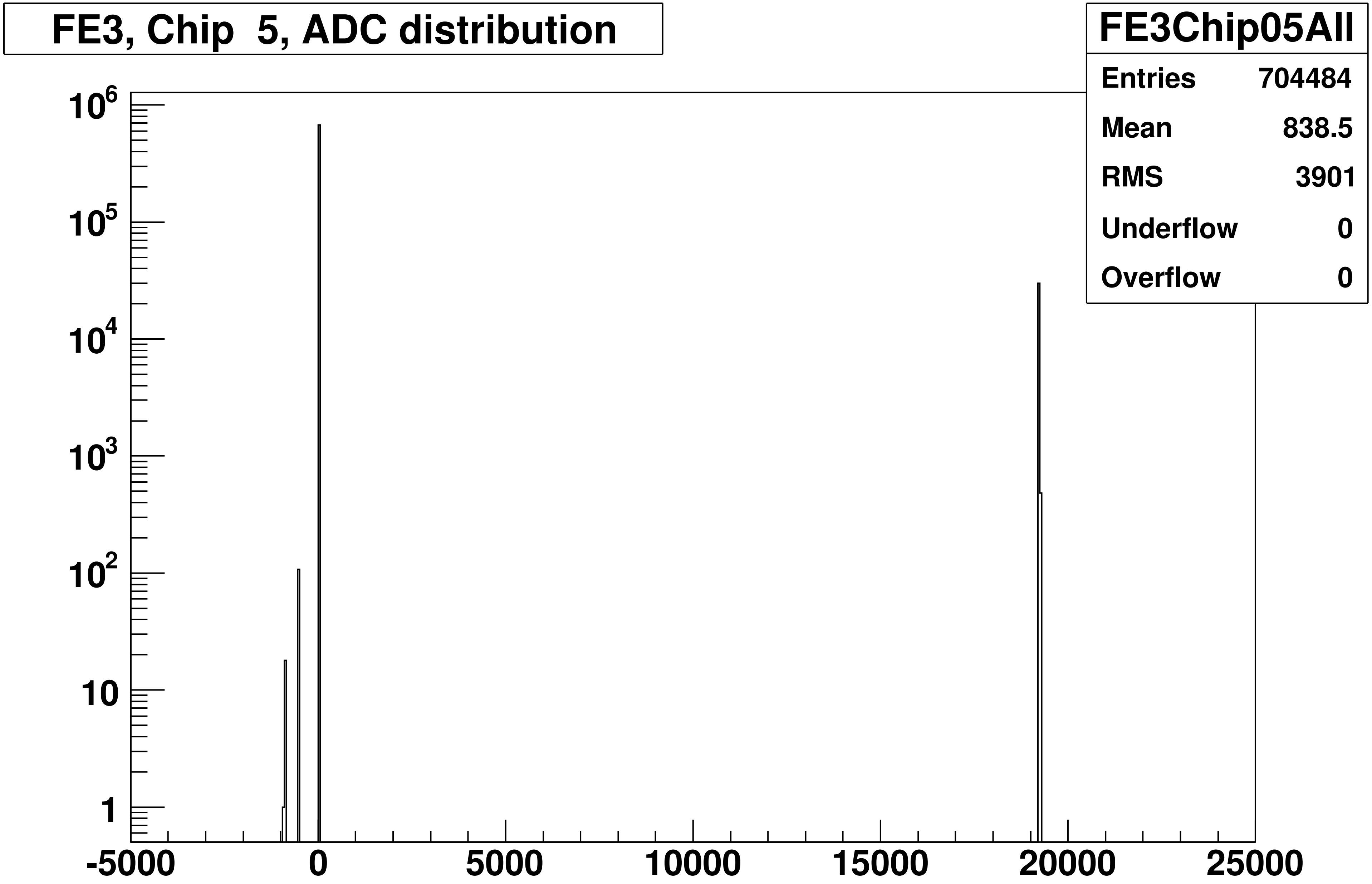
FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 704484
Mean 838.5
RMS 3901
Underflow 0
Overflow 0
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10^4
10^3
10^2
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20000
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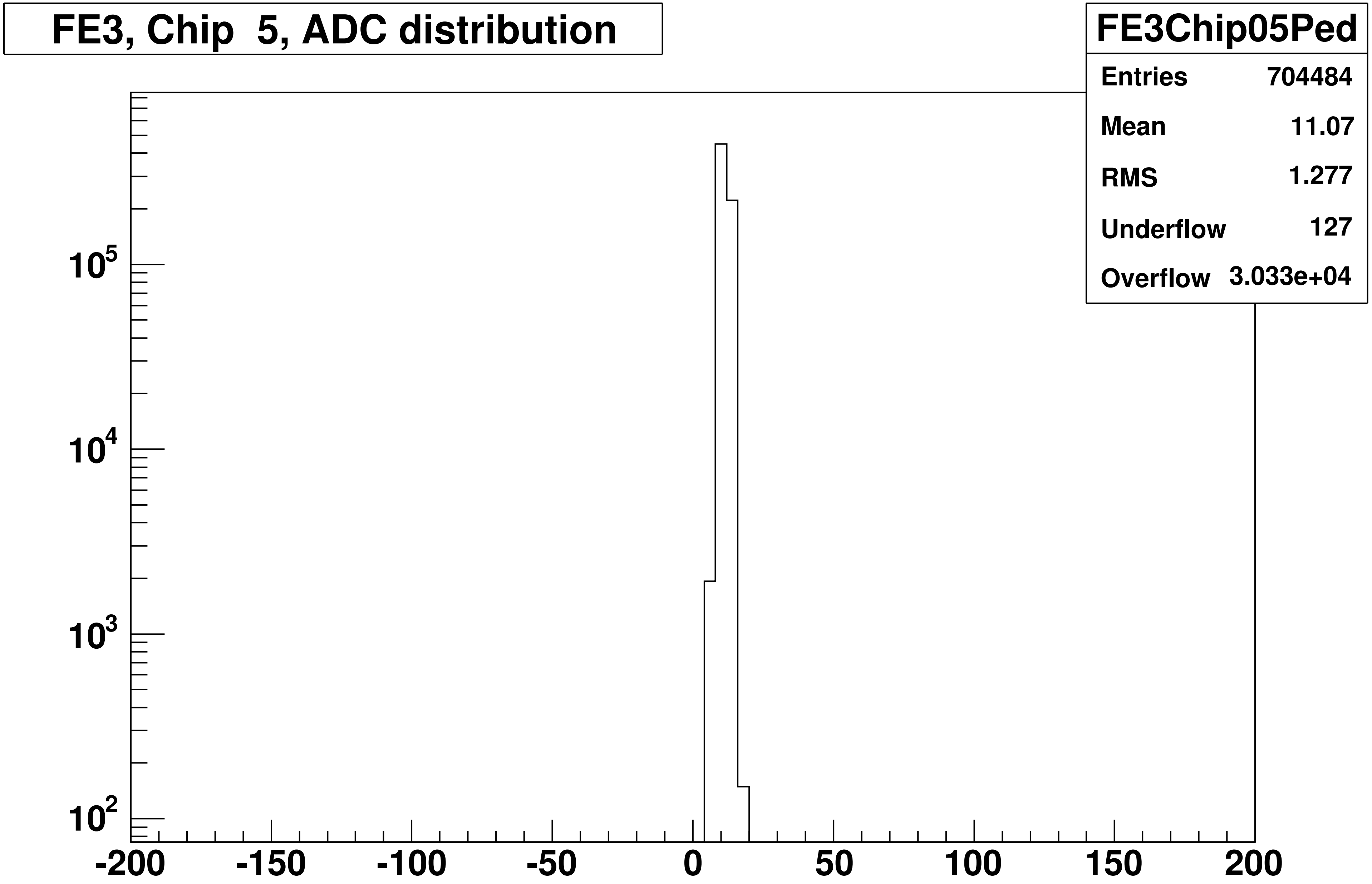
FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 704484
Mean 11.07
RMS 1.277
Underflow 127
Overflow 3.033e+04
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10^3
10^2
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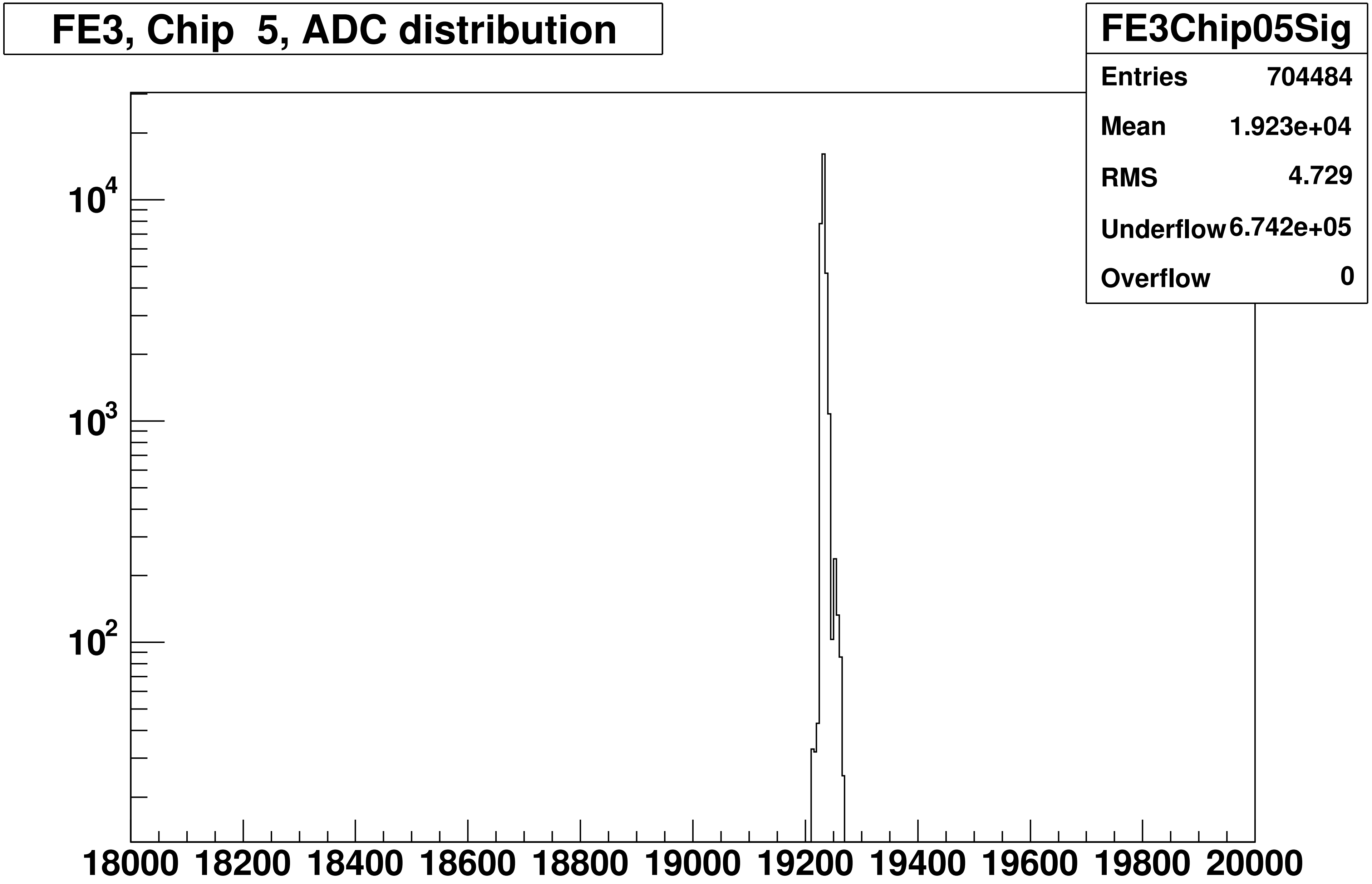
FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 704484
Mean 1.923e+04
RMS 4.729
Underflow 6.742e+05
Overflow 0
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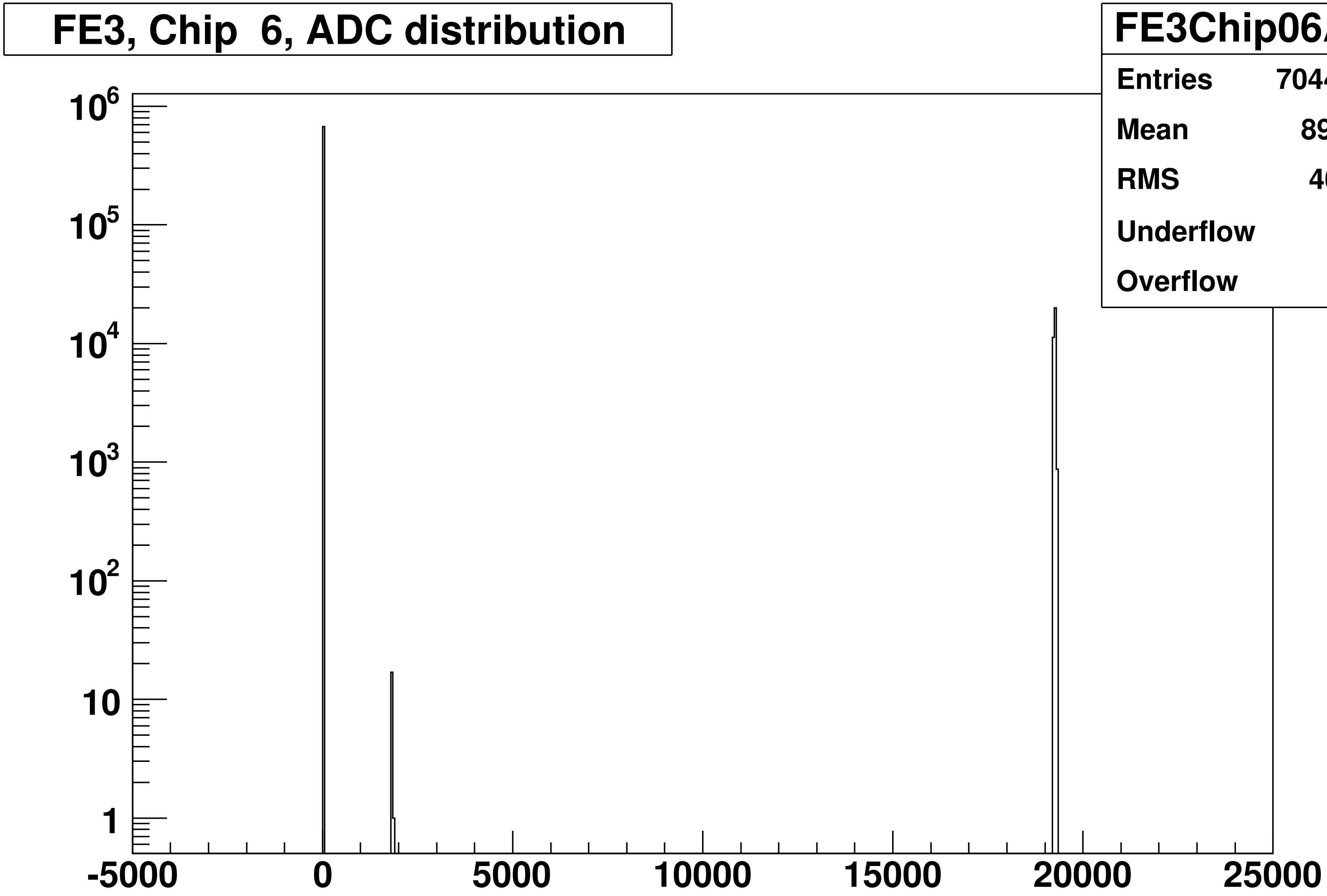
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 704484
Mean 890.3
RMS 4008
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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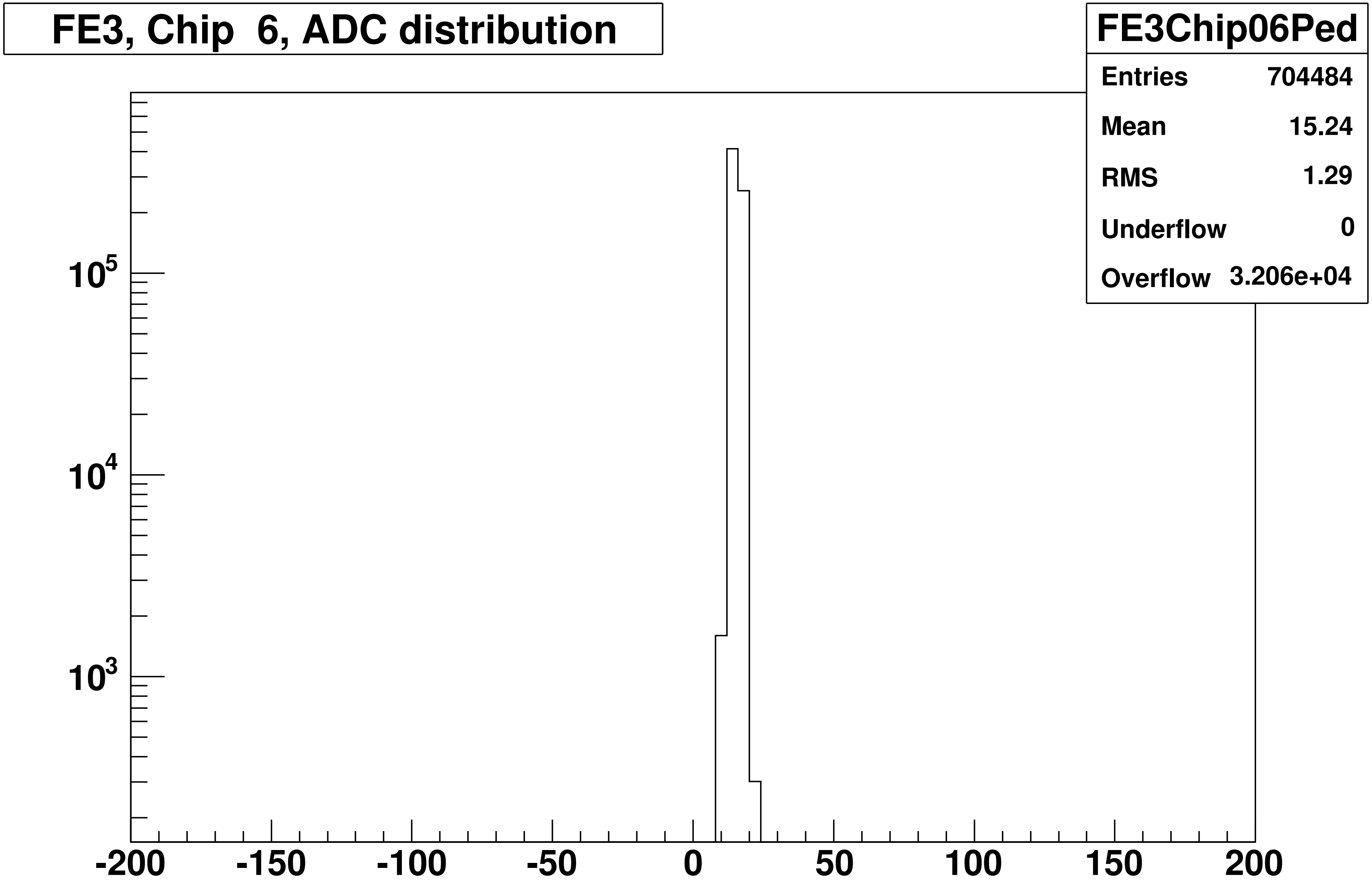
FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 704484
Mean 15.24
RMS 1.29
Underflow 0
Overflow 3.206e+04
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10^4
10^3
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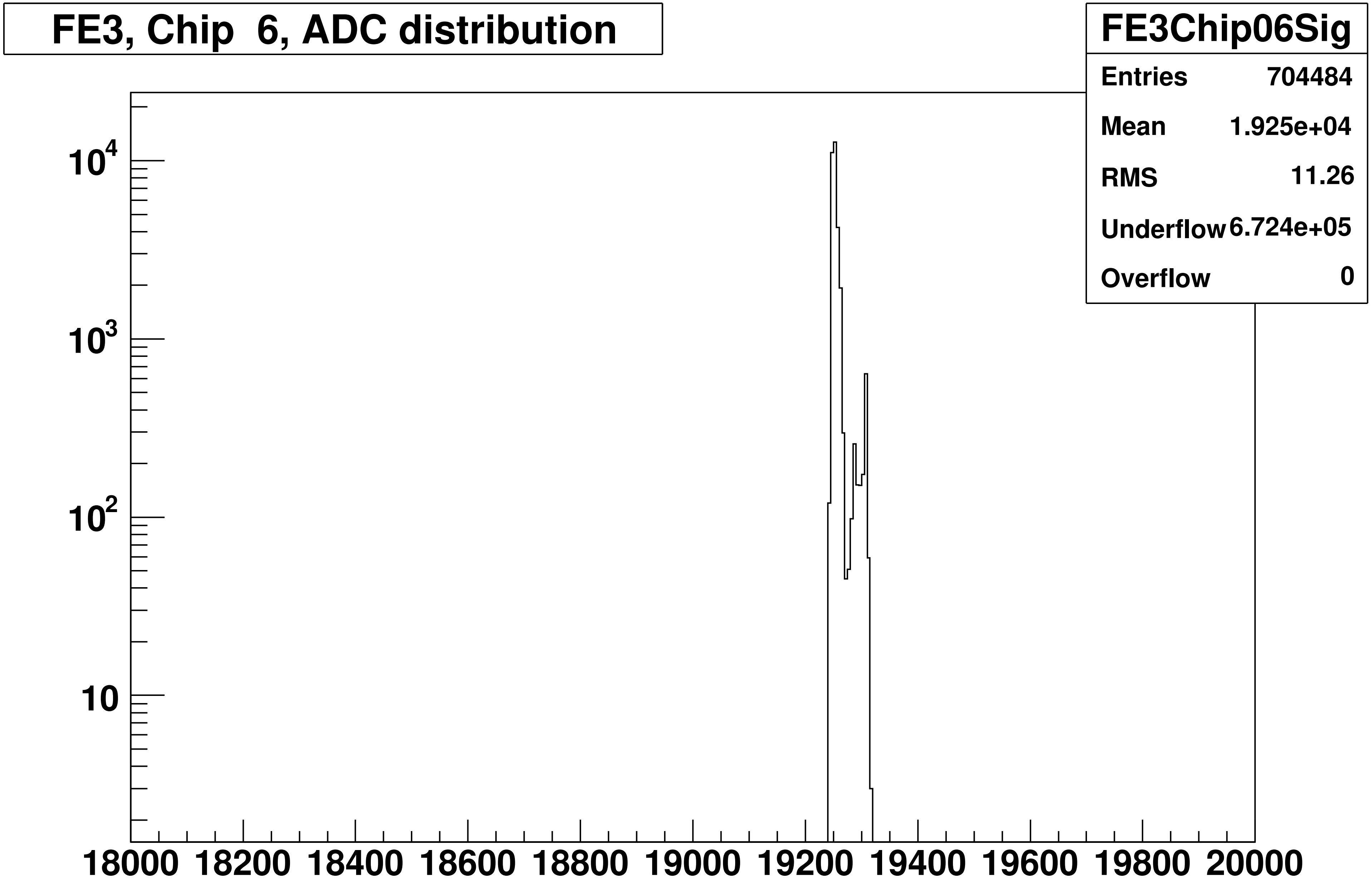
FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 704484
Mean 1.925e+04
RMS 11.26
Underflow 6.724e+05
Overflow 0
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10^3
10^2
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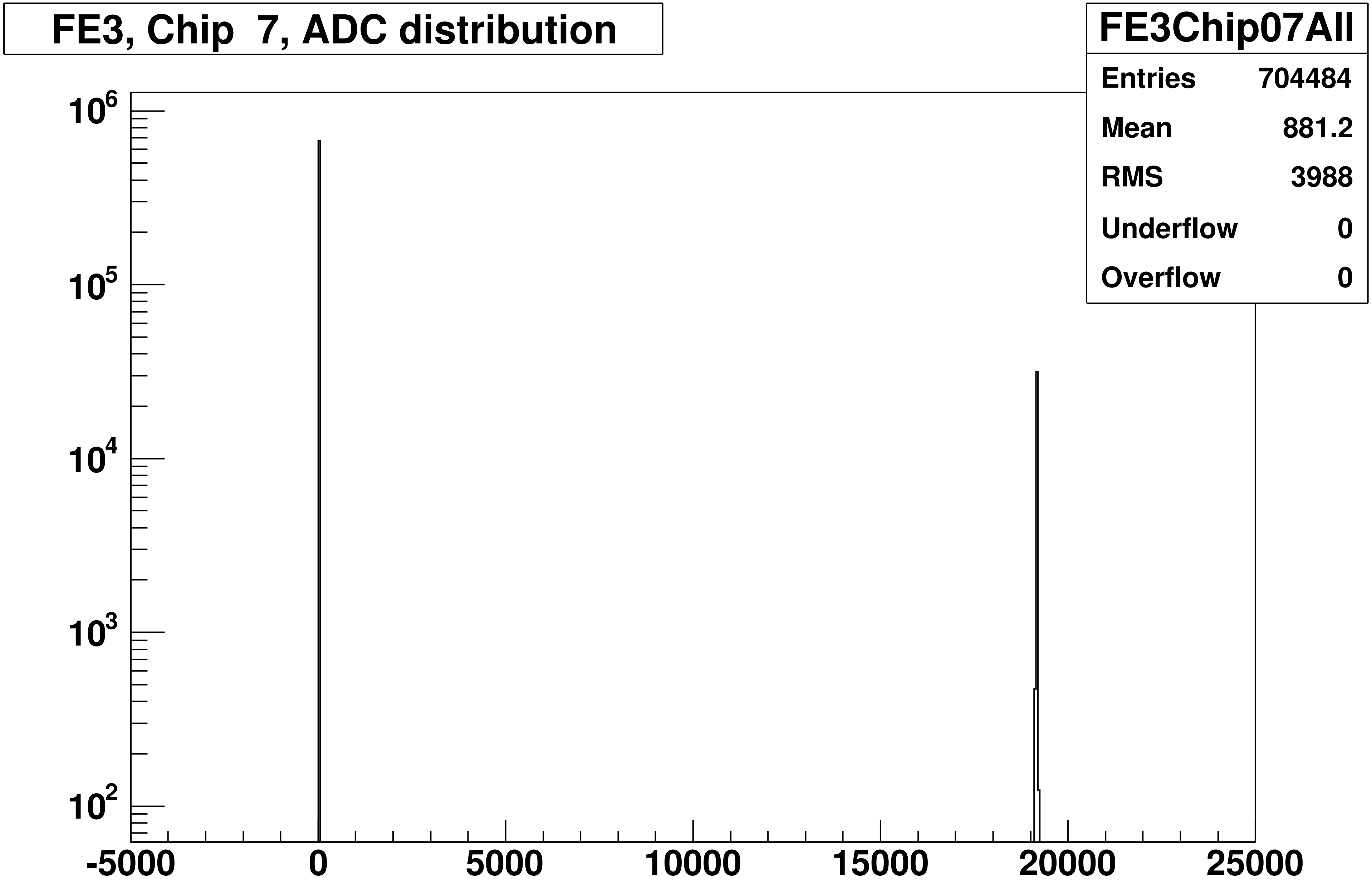
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 704484
Mean 881.2
RMS 3988
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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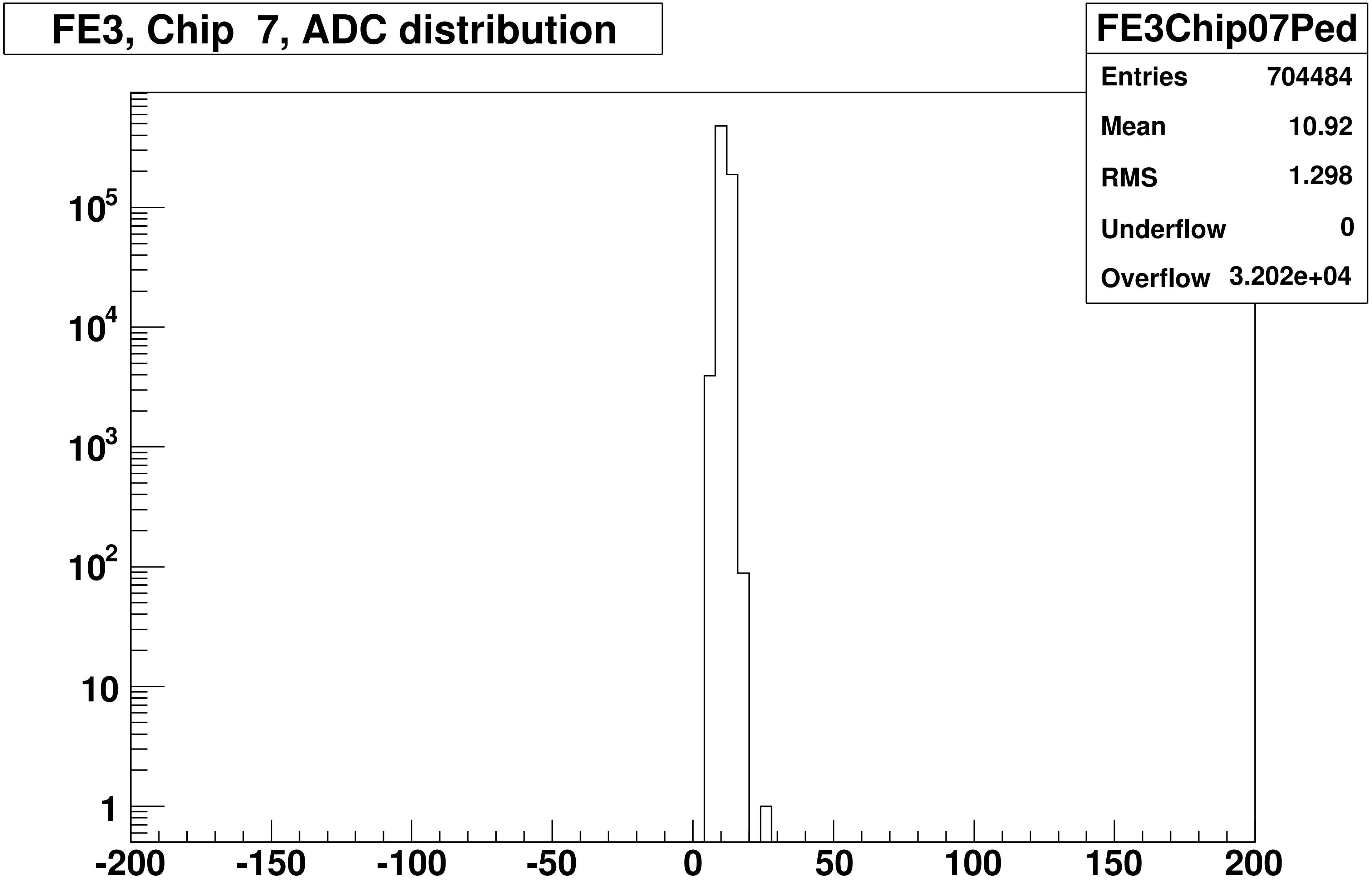

FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 704484
Mean 10.92
RMS 1.298
Underflow 0
Overflow 3.202e+04
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10^4
10^3
10^2
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150
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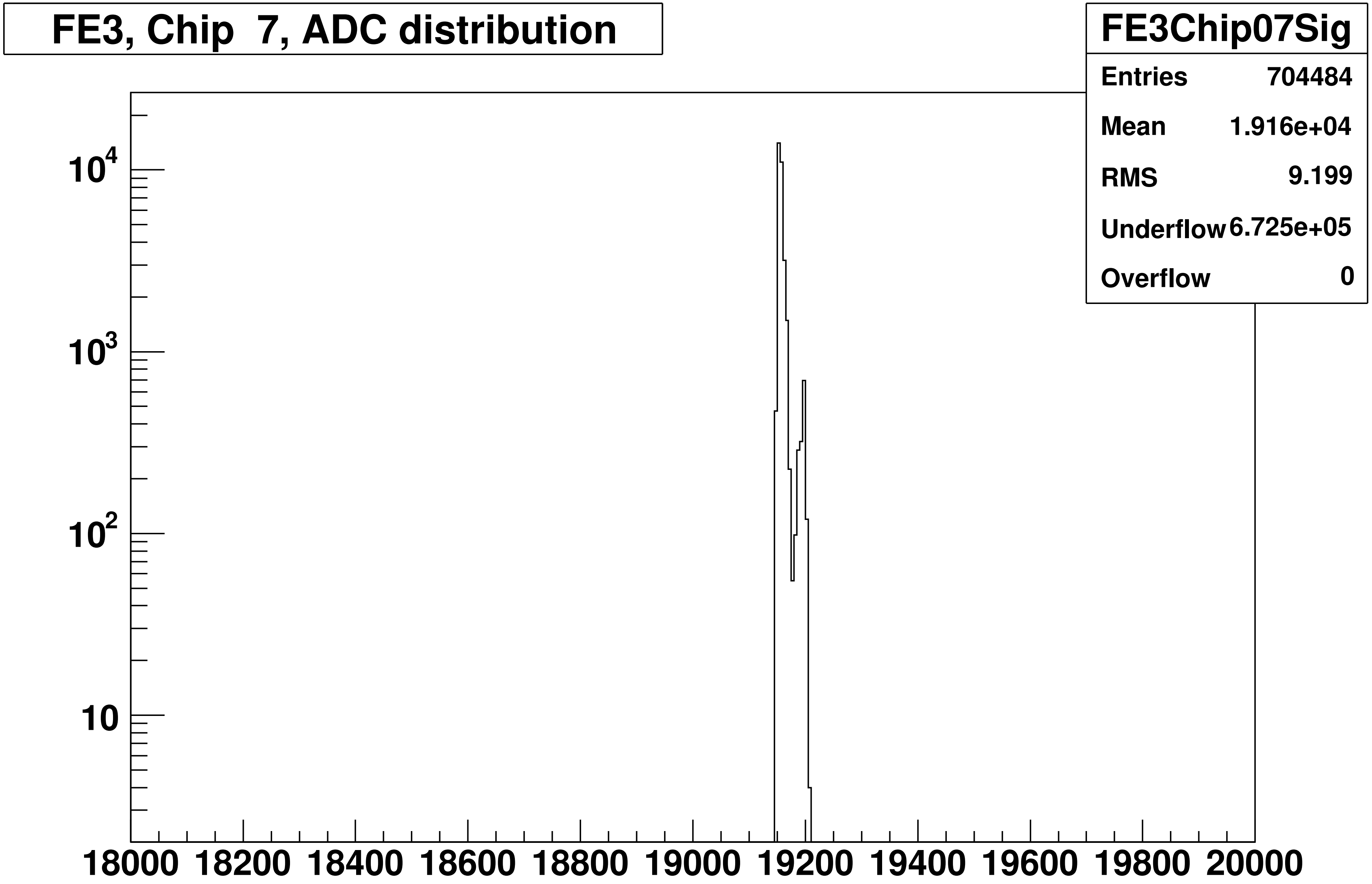
FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 704484
Mean 1.916e+04
RMS 9.199
Underflow 6.725e+05
Overflow 0
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10^3
10^2
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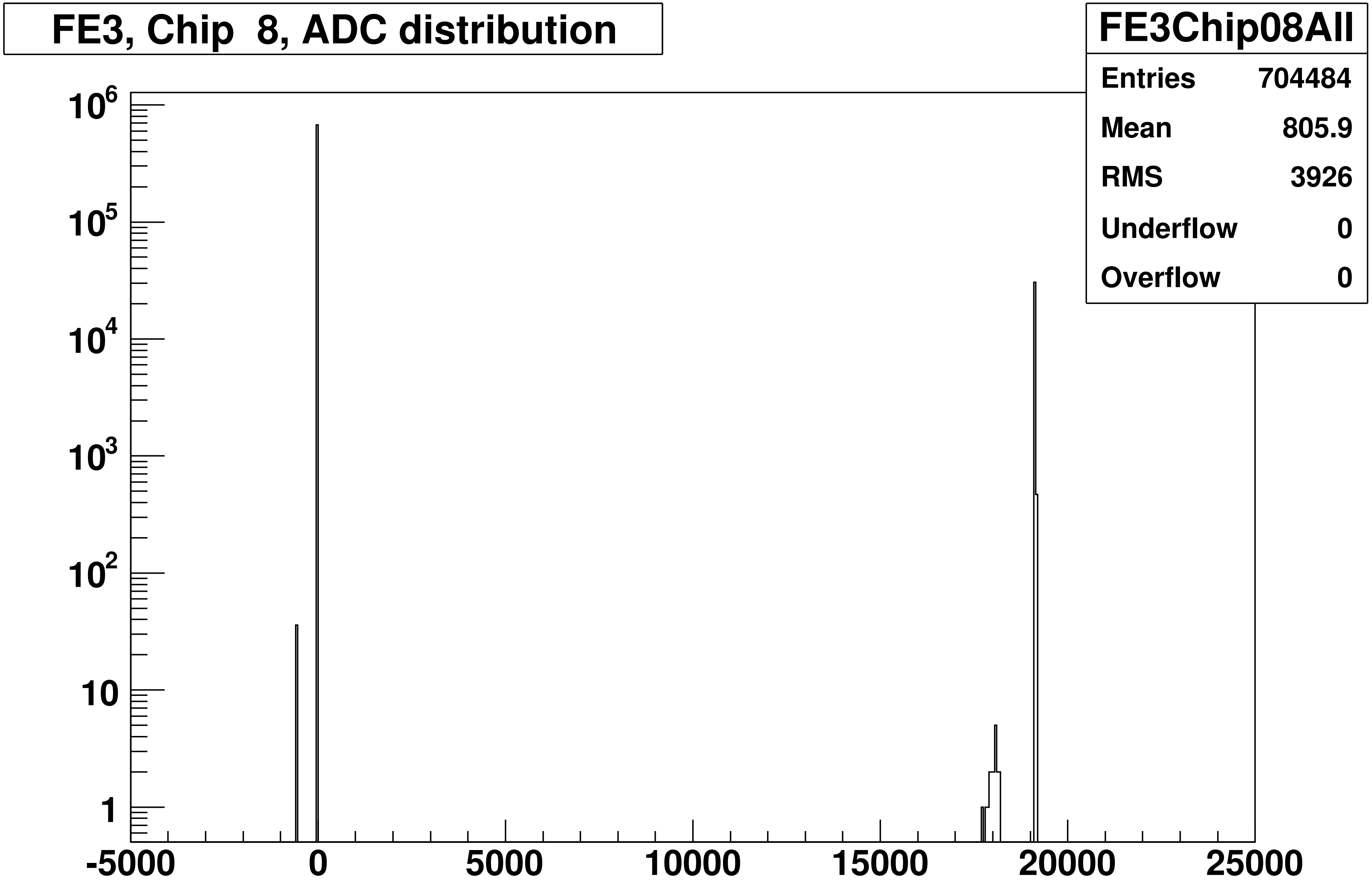
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 704484
Mean 805.9
RMS 3926
Underflow 0
Overflow 0
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10^4
10^3
10^2
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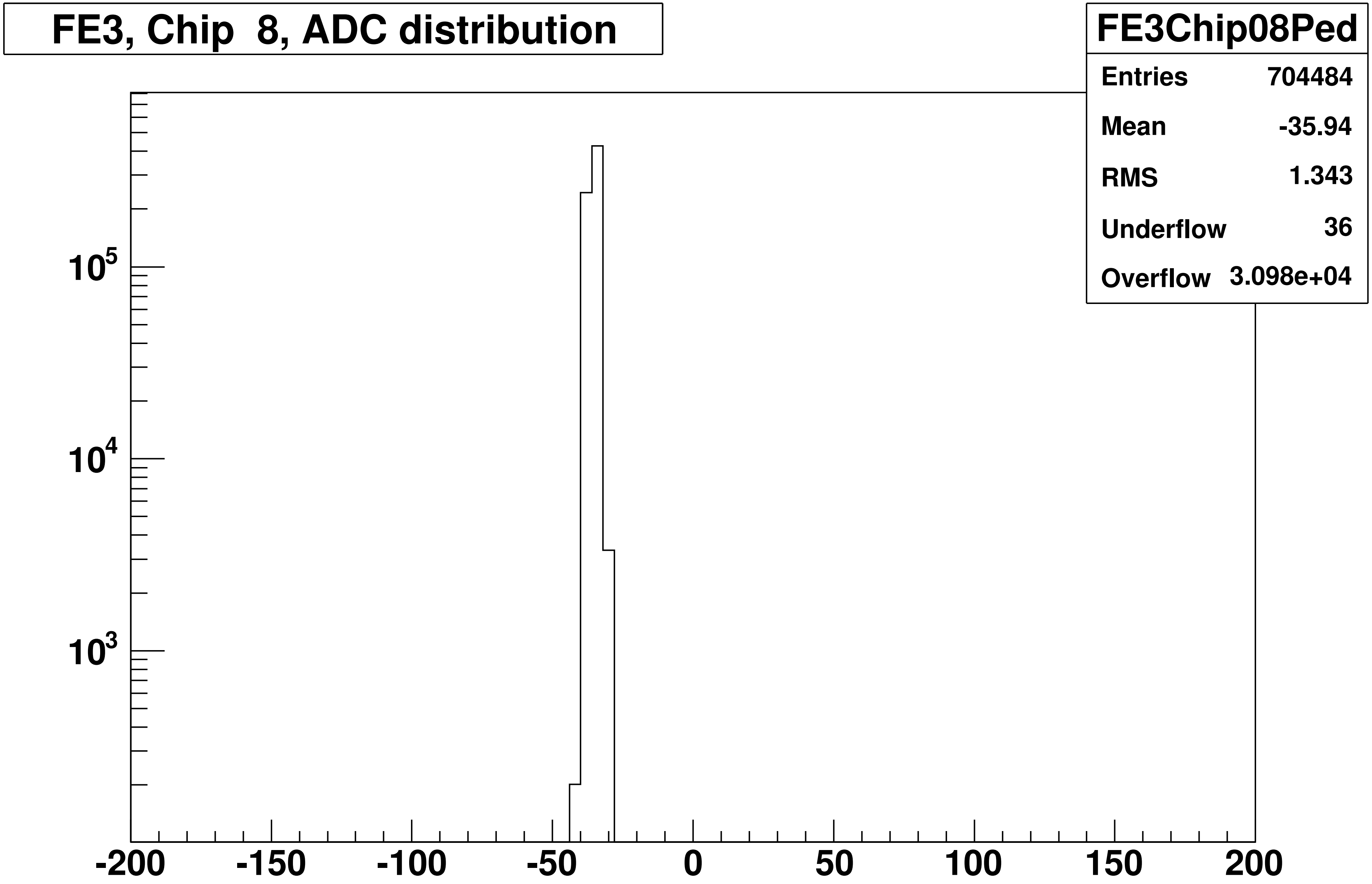
FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 704484
Mean -35.94
RMS 1.343
Underflow 36
Overflow 3.098e+04
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10^4
10^3
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0
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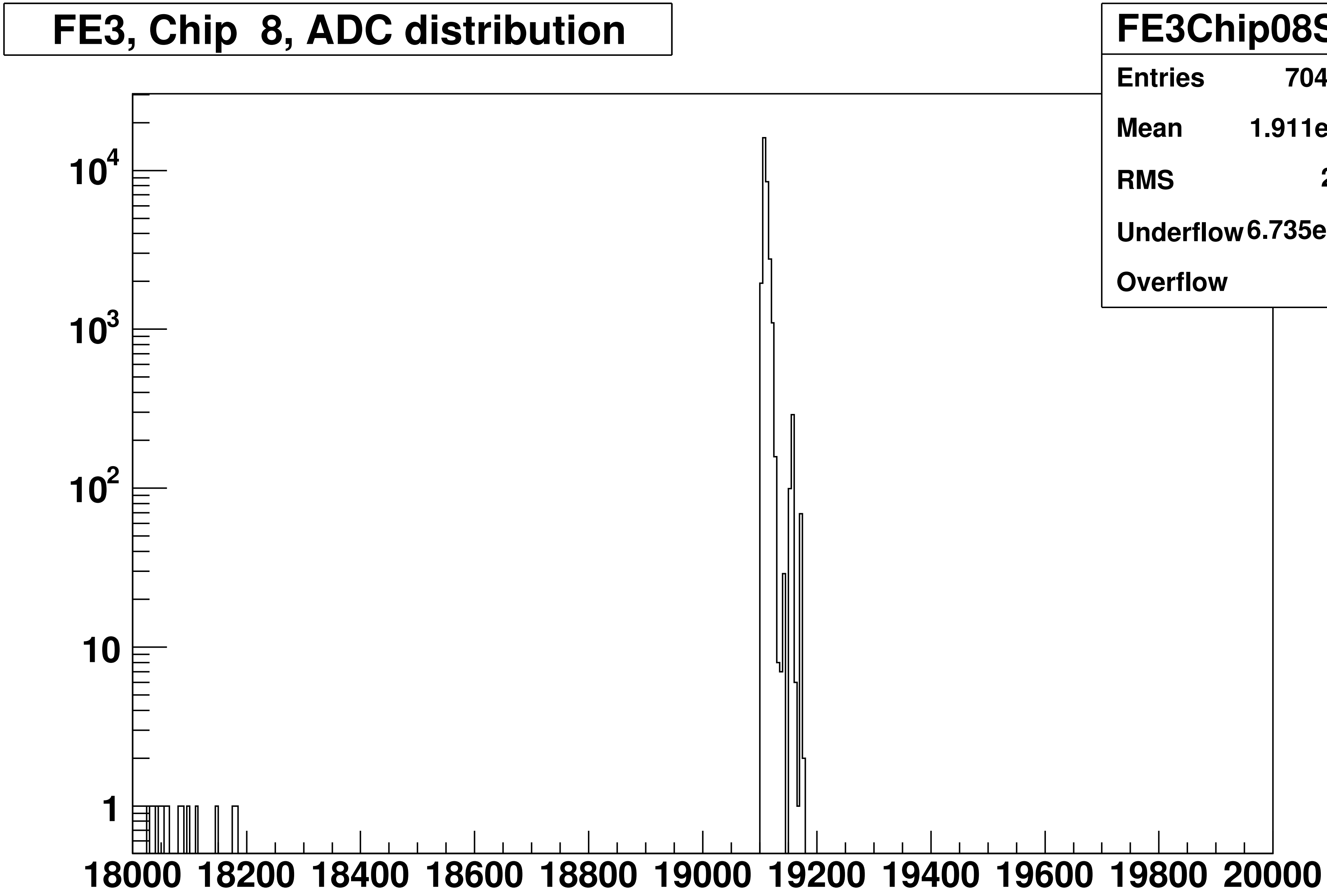
100
150
200


FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 704484
Mean 1.911e+04
RMS 20.5
Underflow 6.735e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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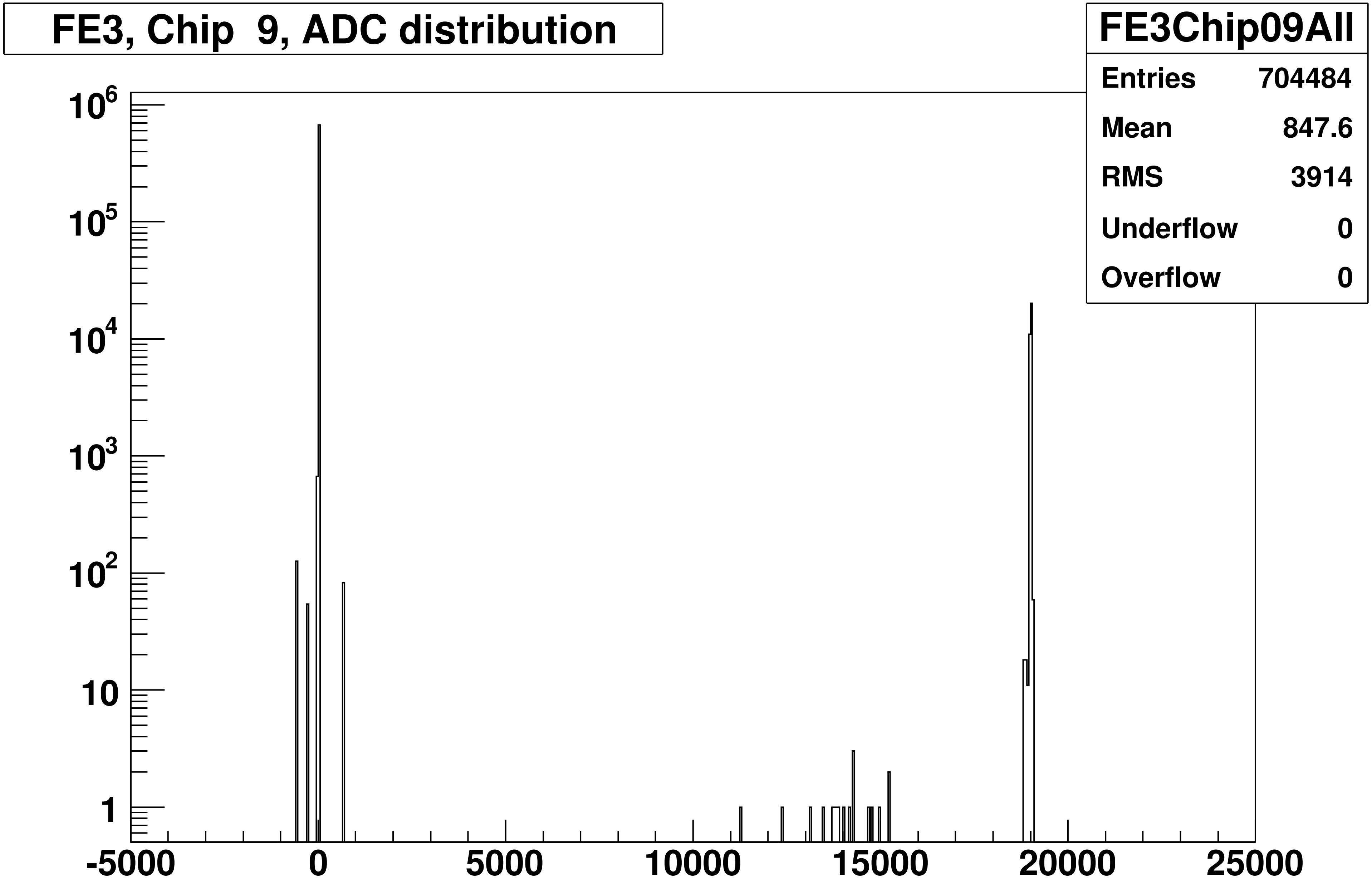
19600
19800
20000



FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 704484
Mean 847.6
RMS 3914
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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10000
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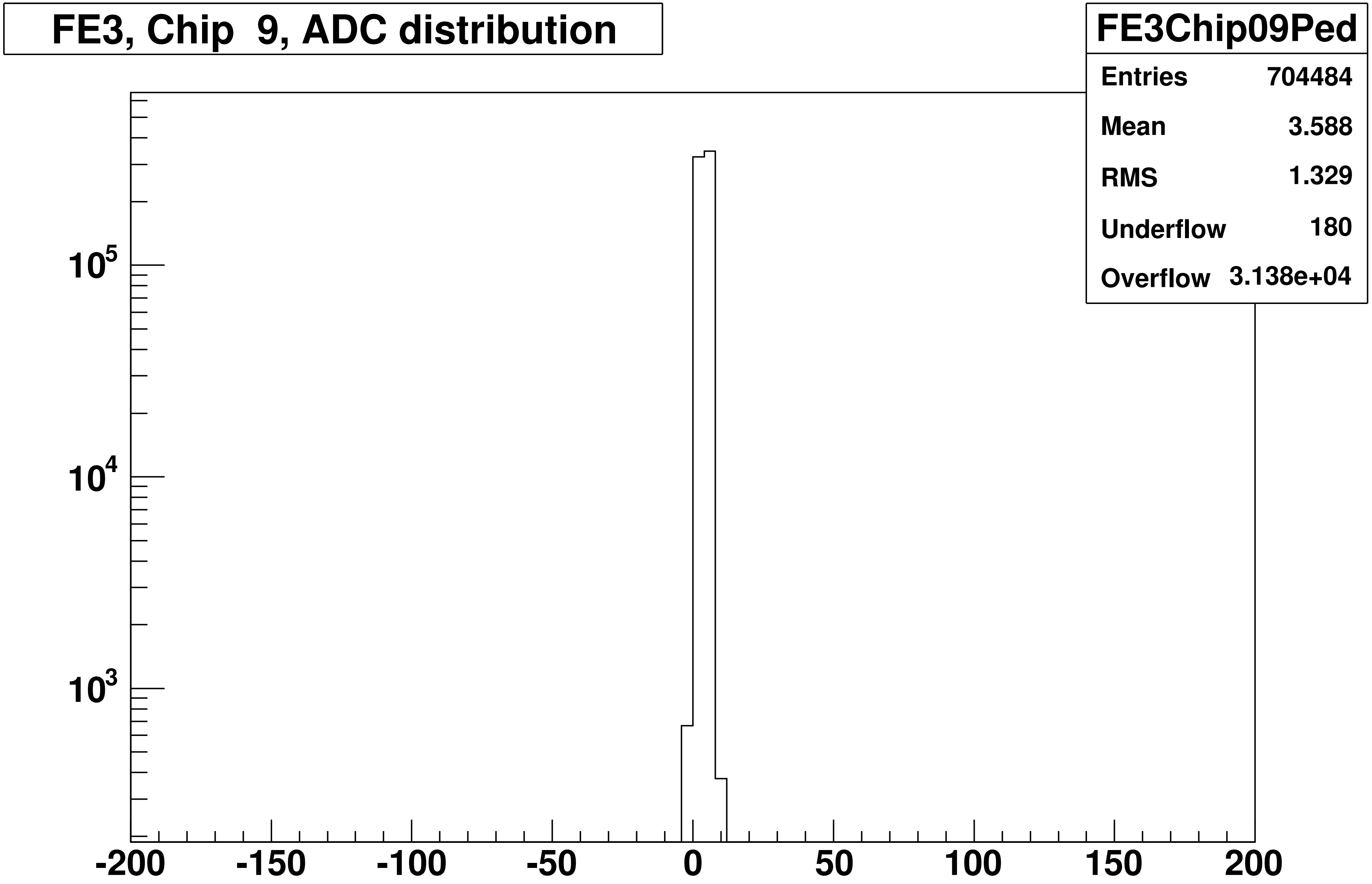
25000



FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 704484
Mean 3.588
RMS 1.329
Underflow 180
Overflow 3.138e+04
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10^4
10^3
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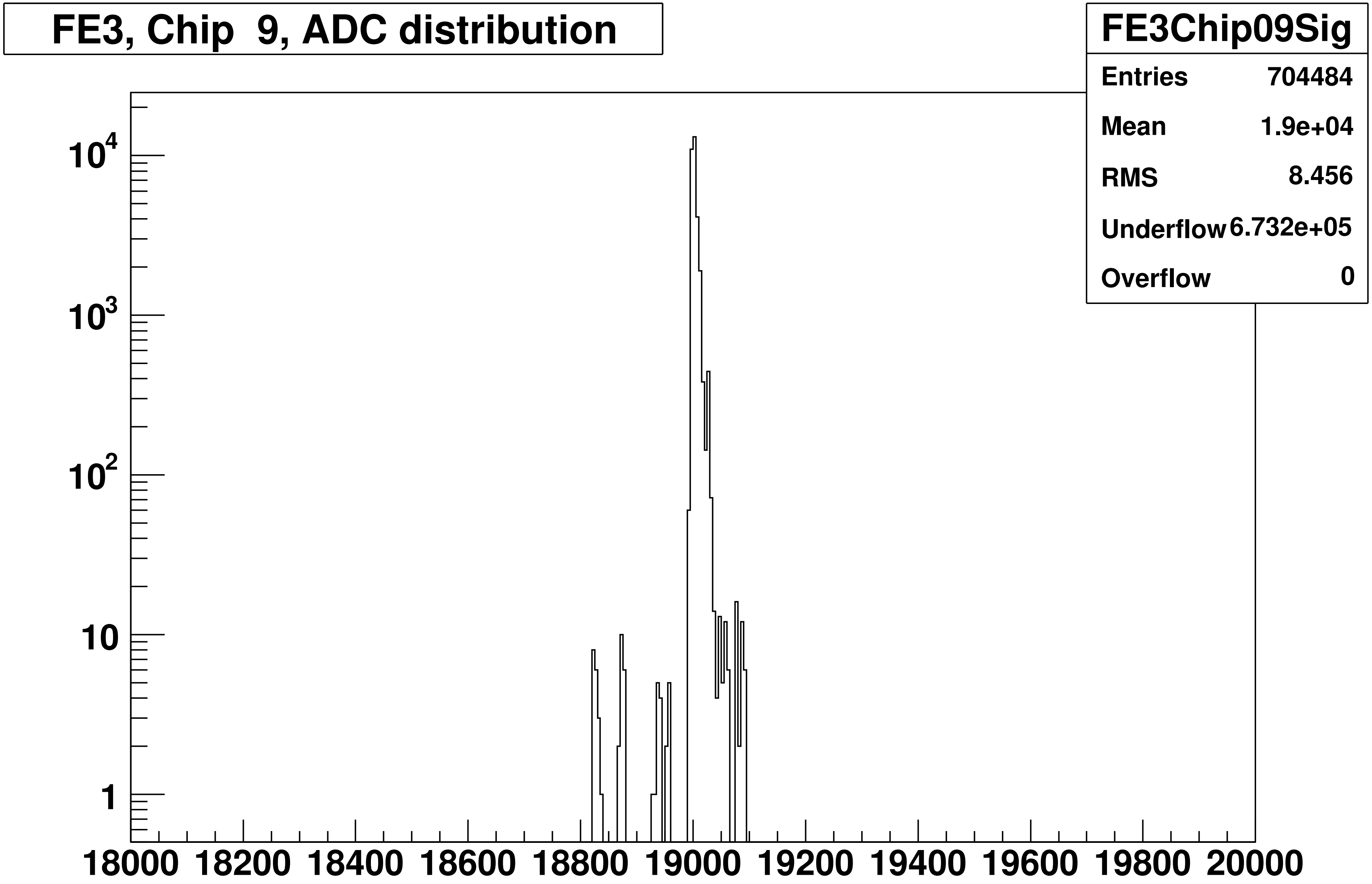
0
50
100
150
200


FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 704484
Mean 1.9e+04
RMS 8.456
Underflow 6.732e+05
Overflow 0
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10^3
10^2
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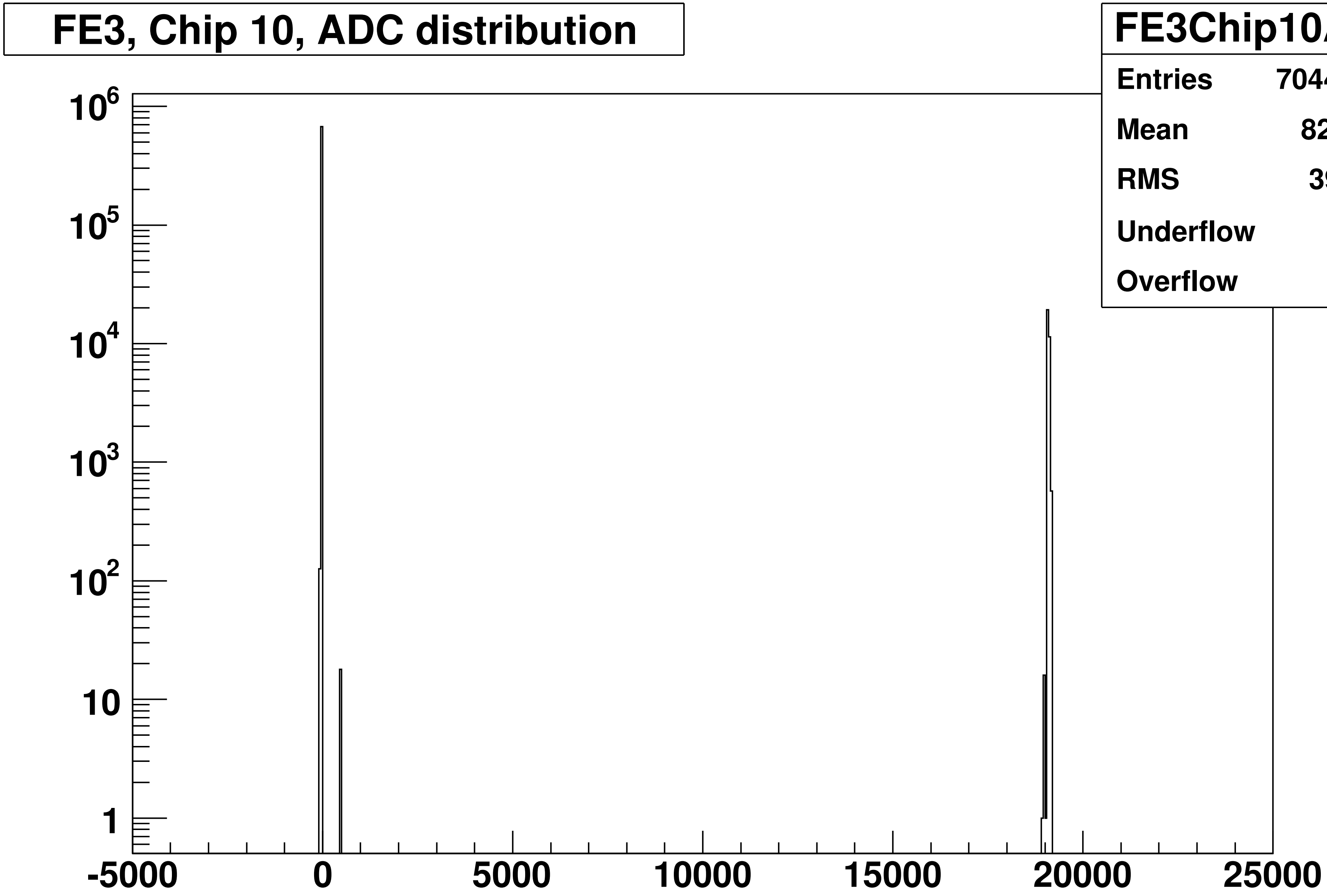
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 704484
Mean 828.4
RMS 3933
Underflow 0
Overflow 0
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10^4
10^3
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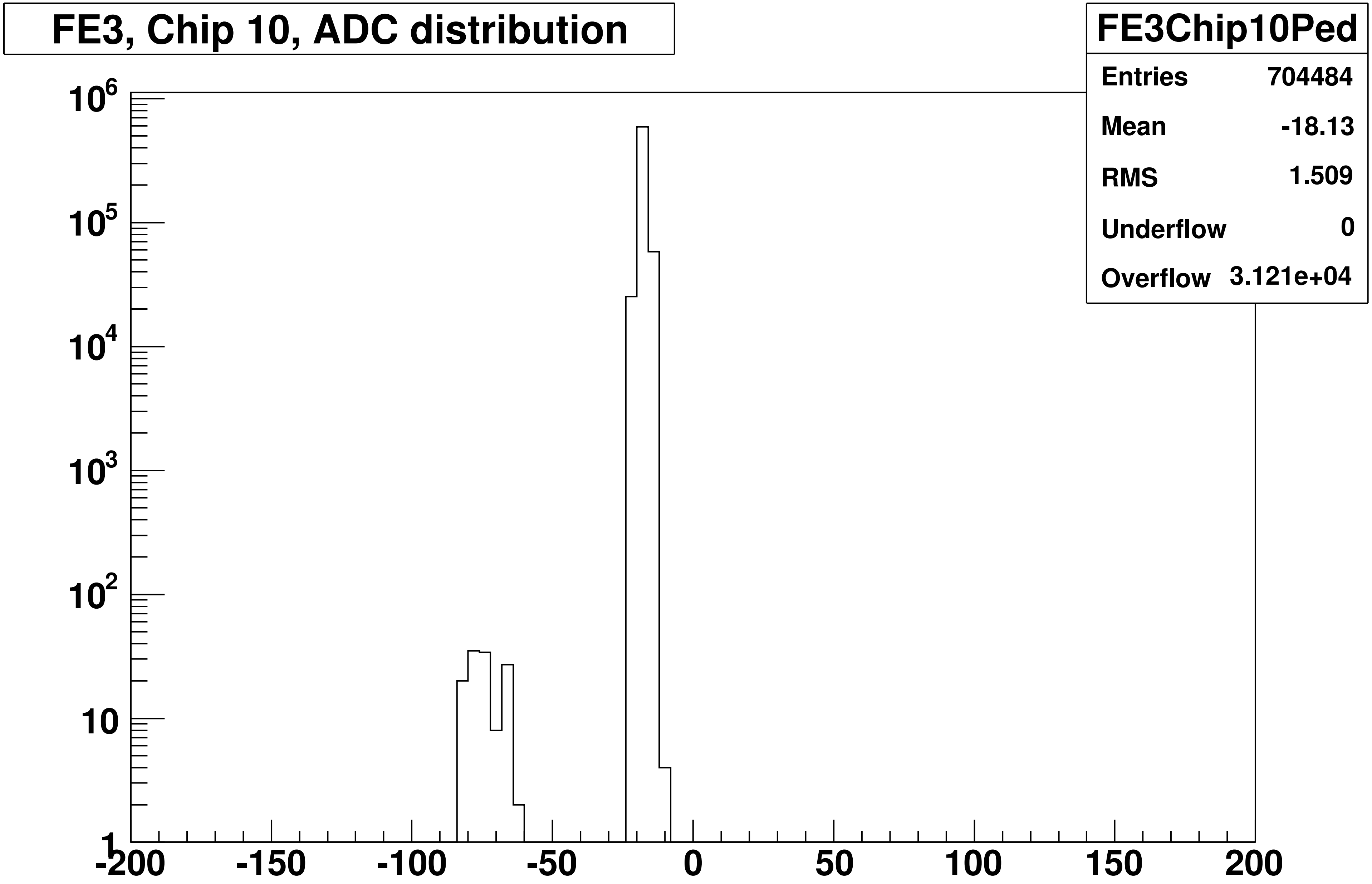

FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 704484
Mean -18.13
RMS 1.509
Underflow 0
Overflow 3.121e+04
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10^5
10^4
10^3
10^2
10
1
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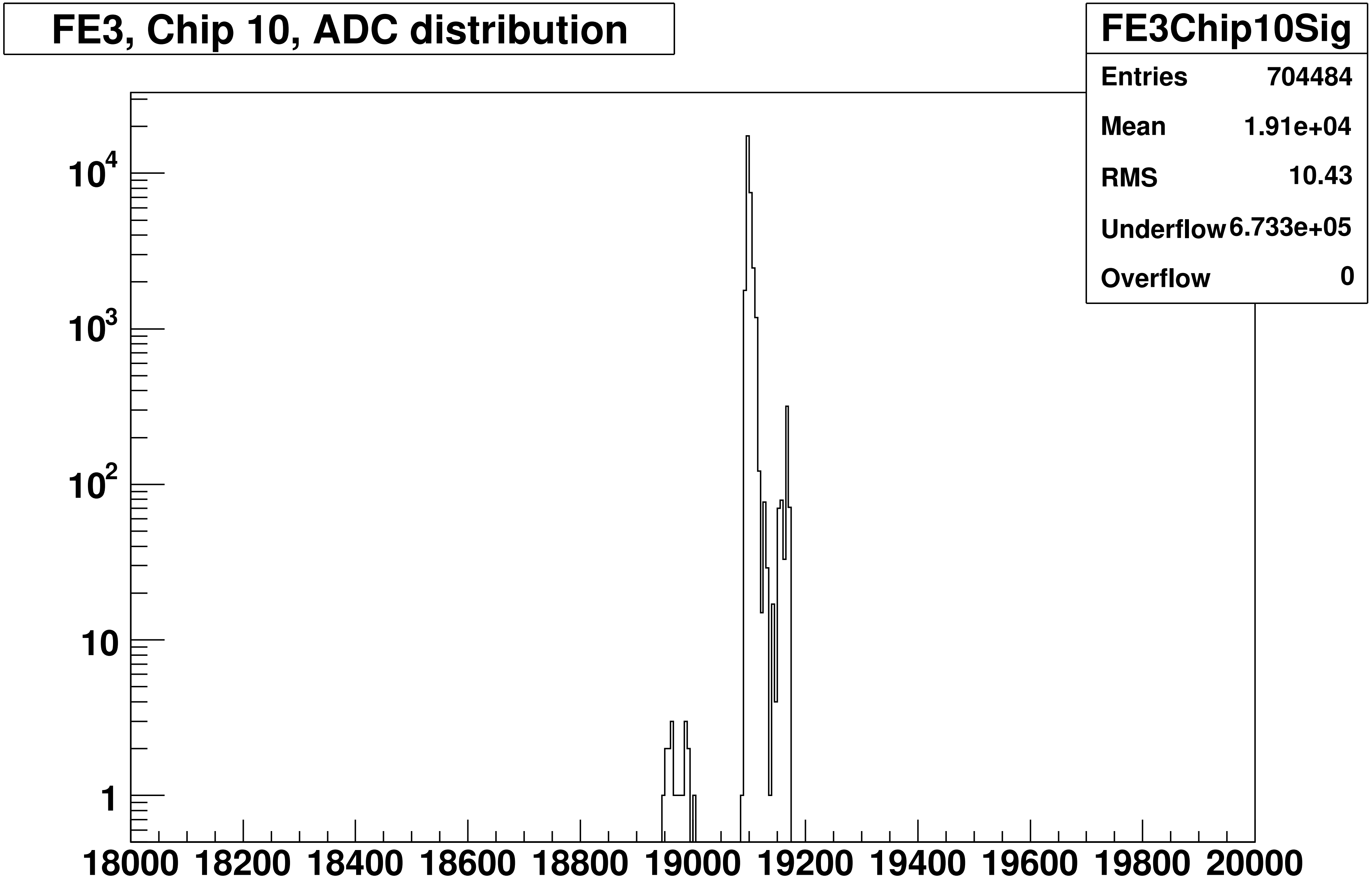
100
150
200



FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 704484
Mean 1.91e+04
RMS 10.43
Underflow 6.733e+05
Overflow 0
10^4
10^3
10^2
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18000
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18400
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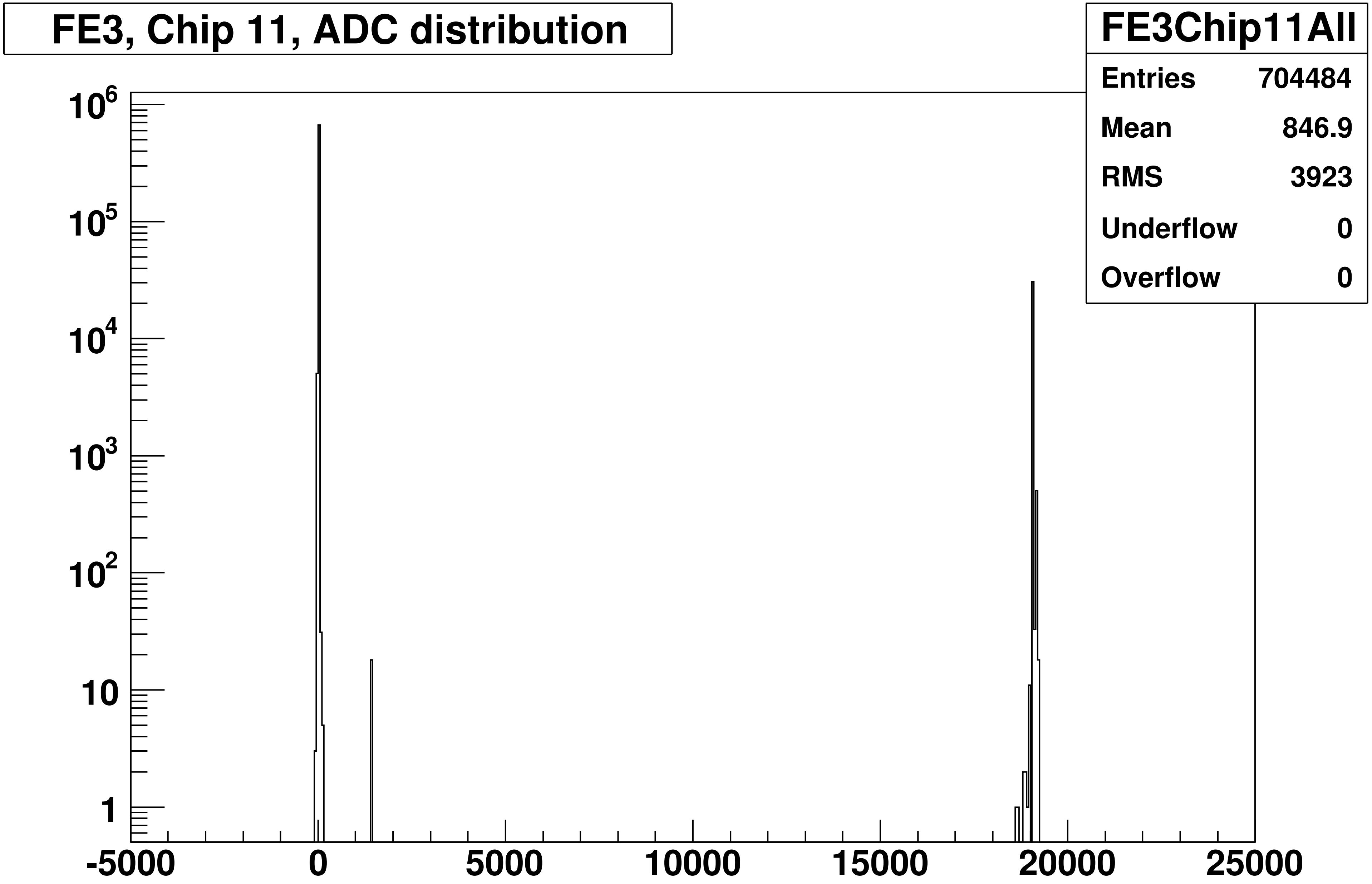
19800
20000



FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 704484
Mean 846.9
RMS 3923
Underflow 0
Overflow 0
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10^3
10^2
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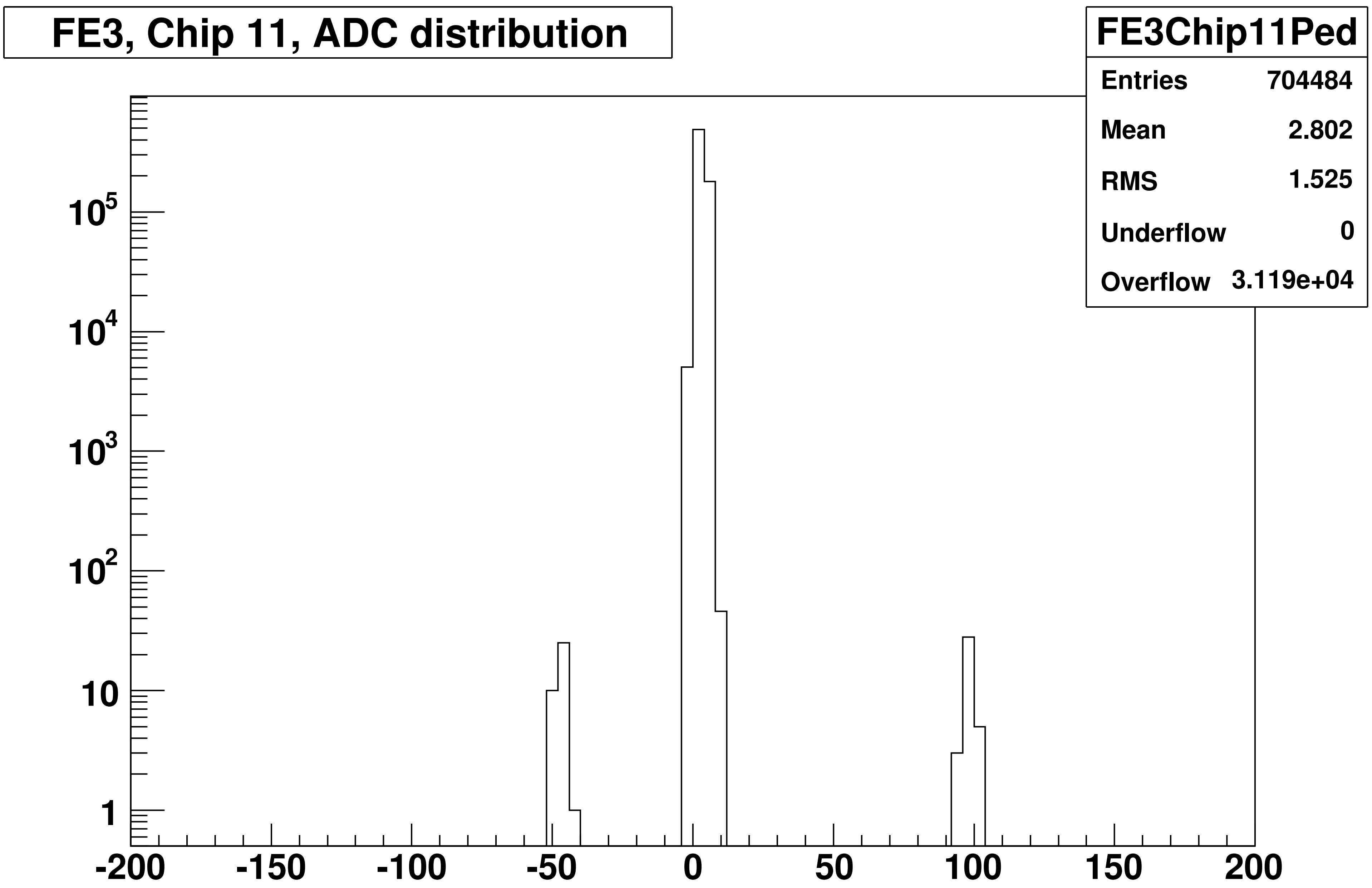
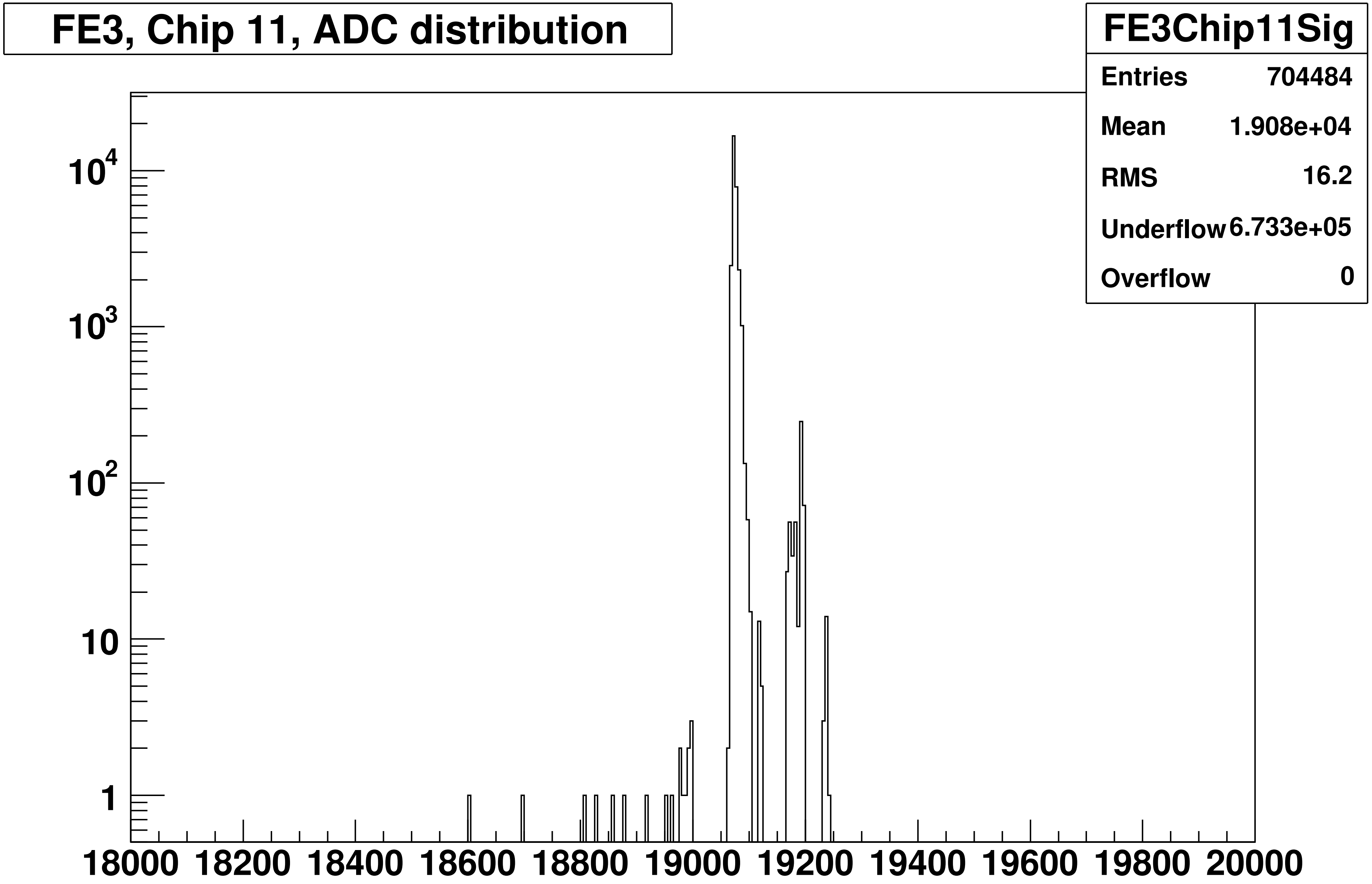


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 704484
Mean 2.802
RMS 1.525
Underflow 0
Overflow 3.119e+04


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 704484
Mean 1.908e+04
RMS 16.2
Underflow 6.733e+05
Overflow 0
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10^3
10^2
10
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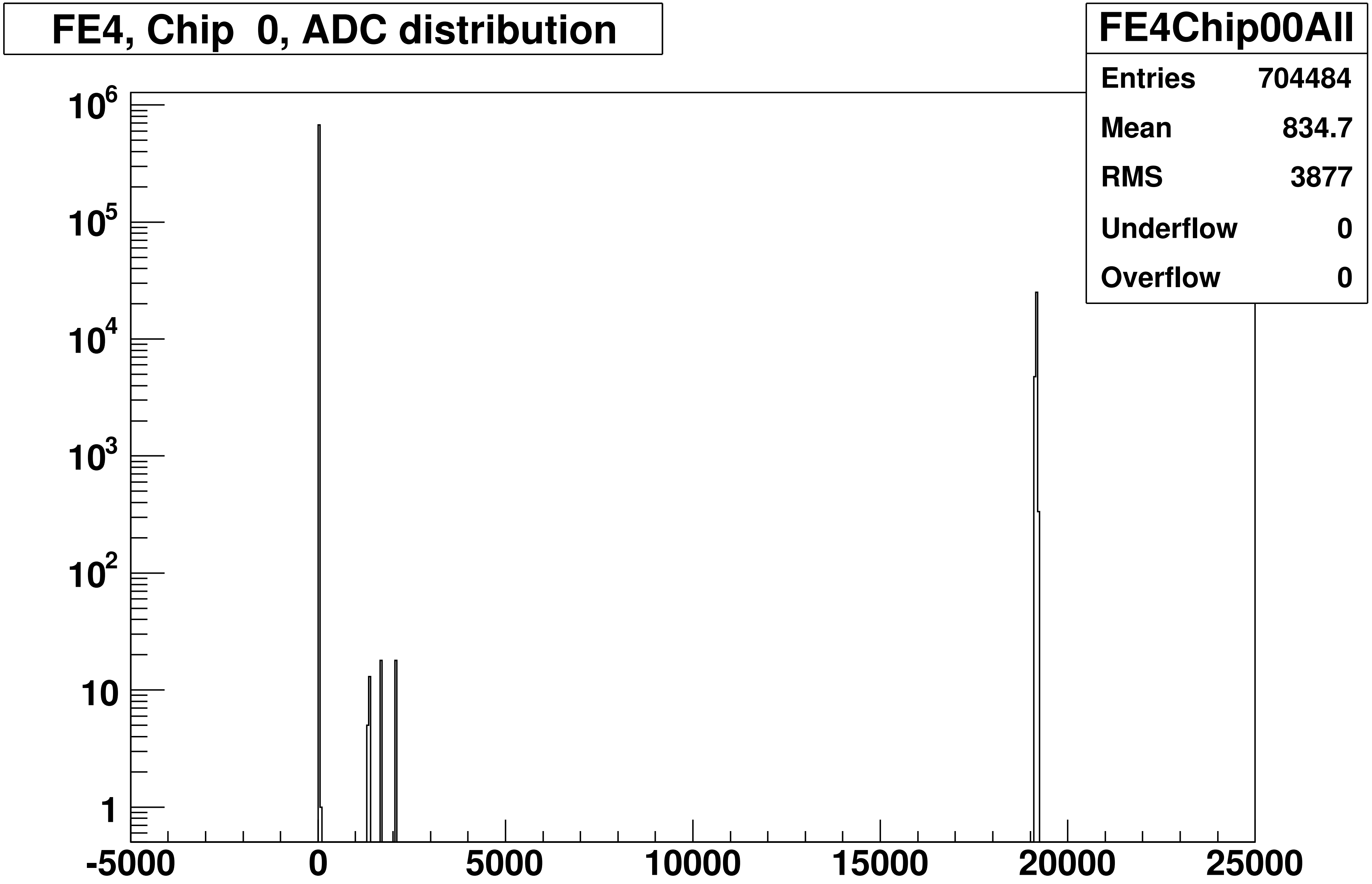
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 704484
Mean 834.7
RMS 3877
Underflow 0
Overflow 0
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10^4
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10^2
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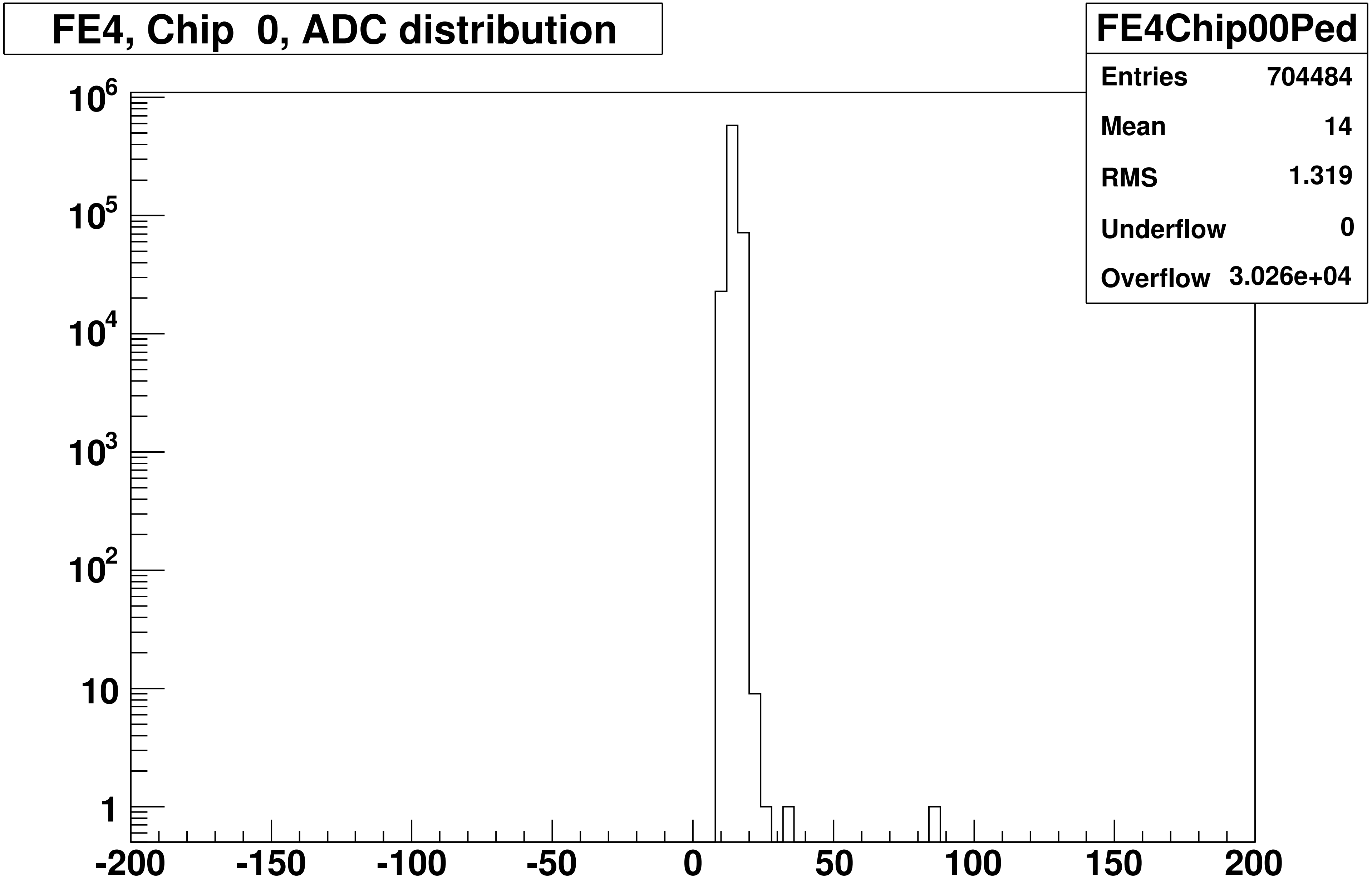
FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 704484
Mean 14
RMS 1.319
Underflow 0
Overflow 3.026e+04
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10^3
10^2
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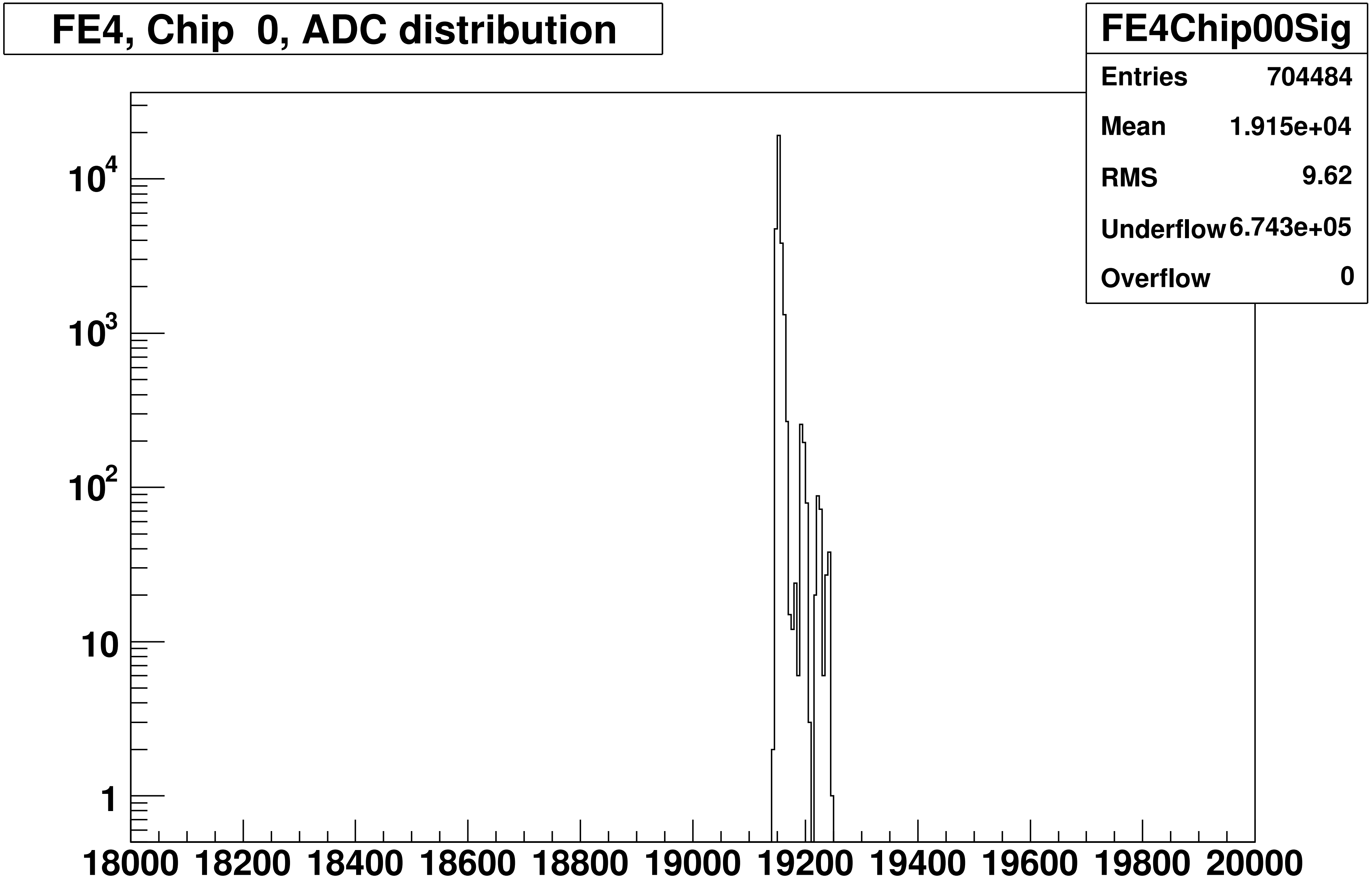

FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 704484
Mean 1.915e+04
RMS 9.62
Underflow 6.743e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
18400
18600
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19000
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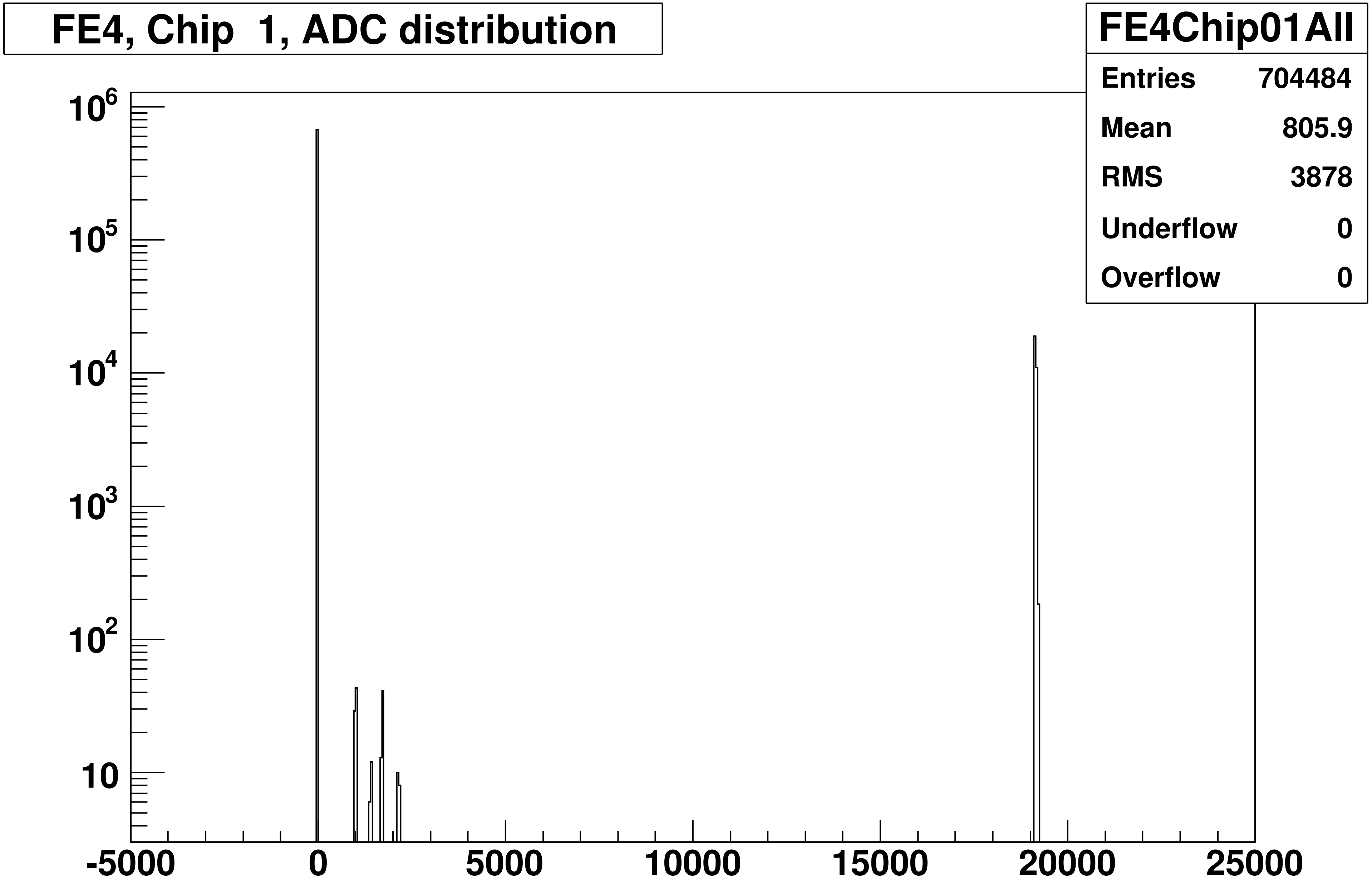
FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 704484
Mean 805.9
RMS 3878
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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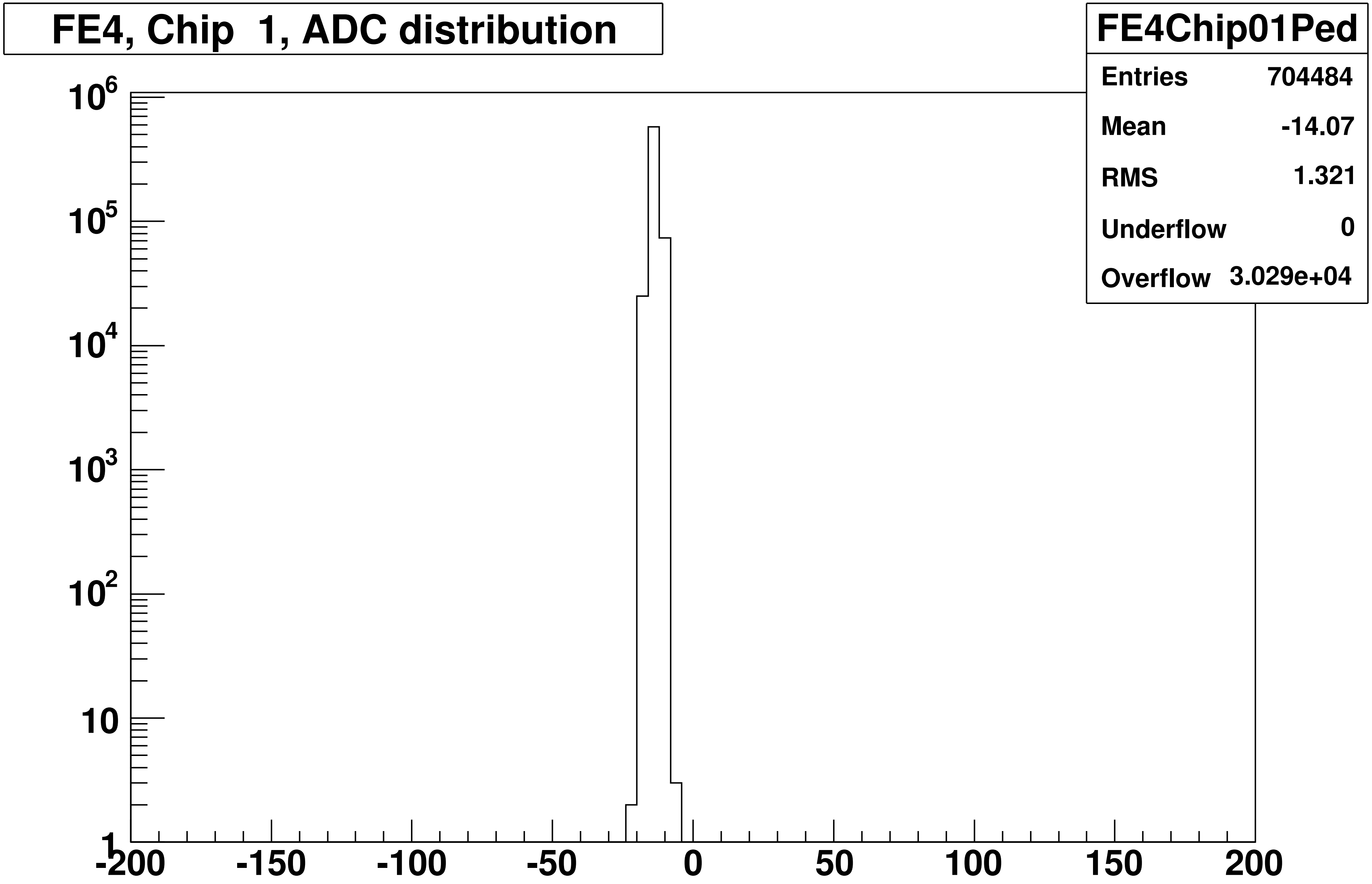
FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 704484
Mean -14.07
RMS 1.321
Underflow 0
Overflow 3.029e+04
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10^5
10^4
10^3
10^2
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150
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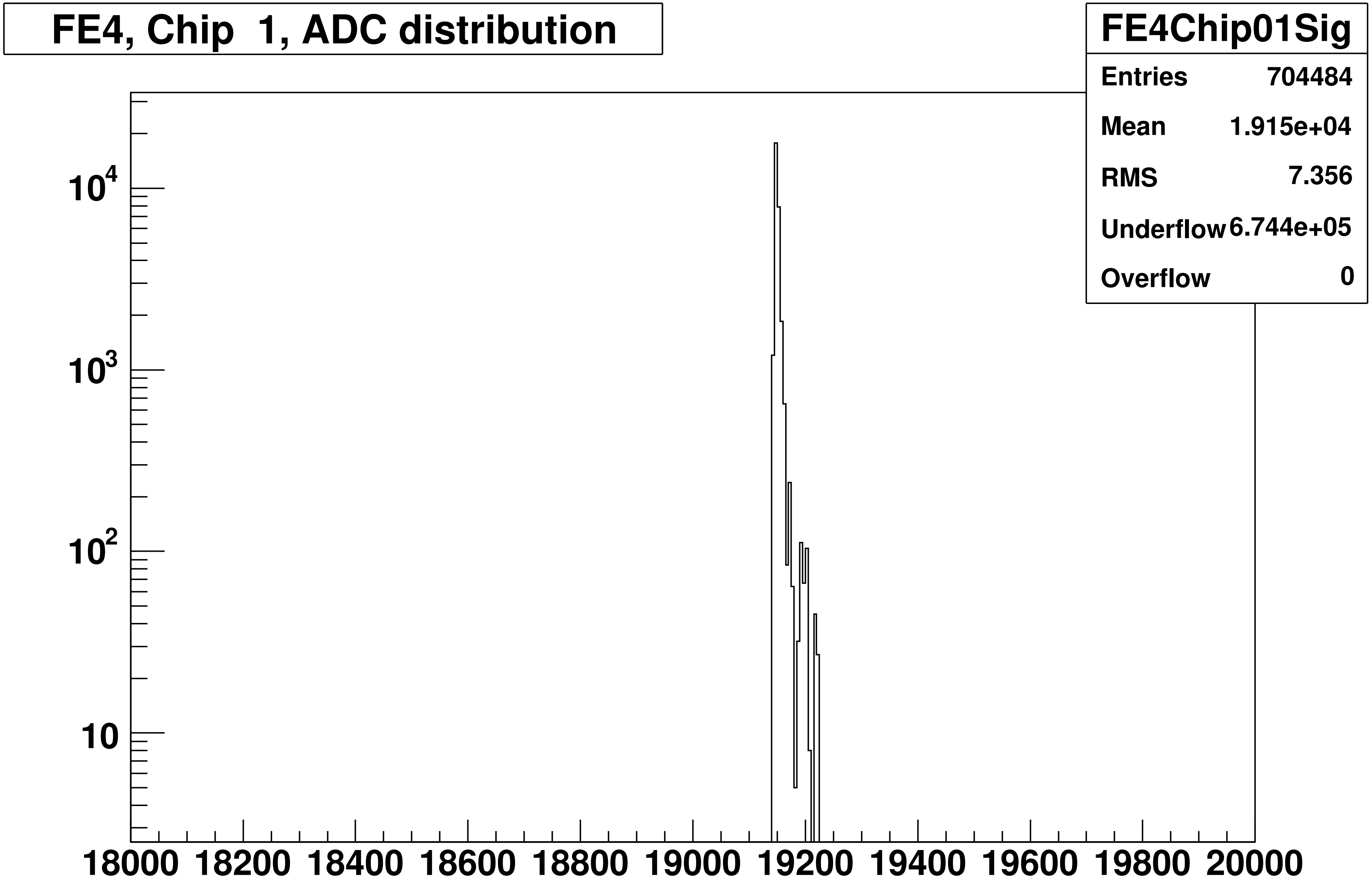

FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 704484
Mean 1.915e+04
RMS 7.356
Underflow 6.744e+05
Overflow 0
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10^3
10^2
10
18000
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18400
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19000
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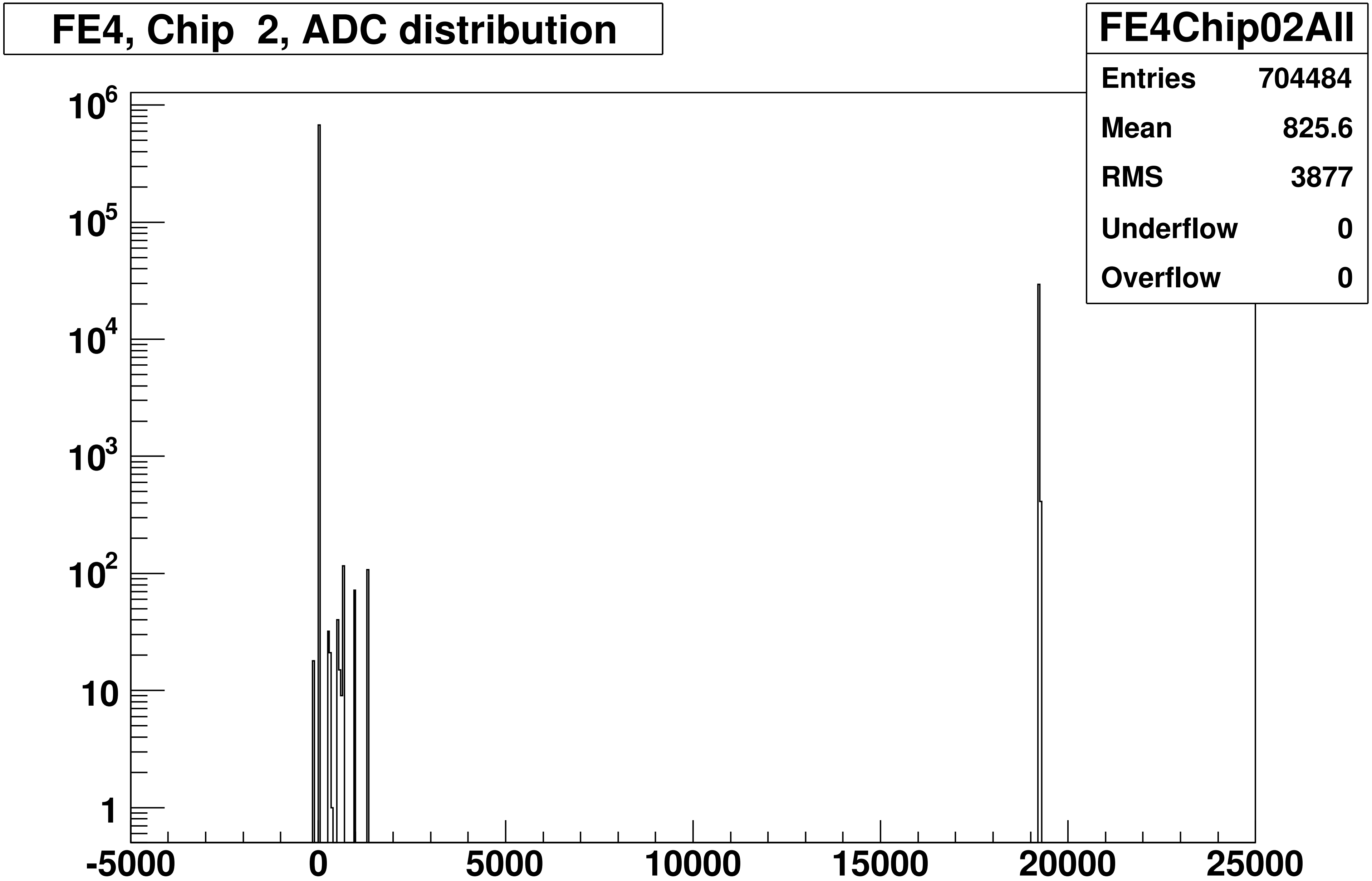
FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 704484
Mean 825.6
RMS 3877
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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20000
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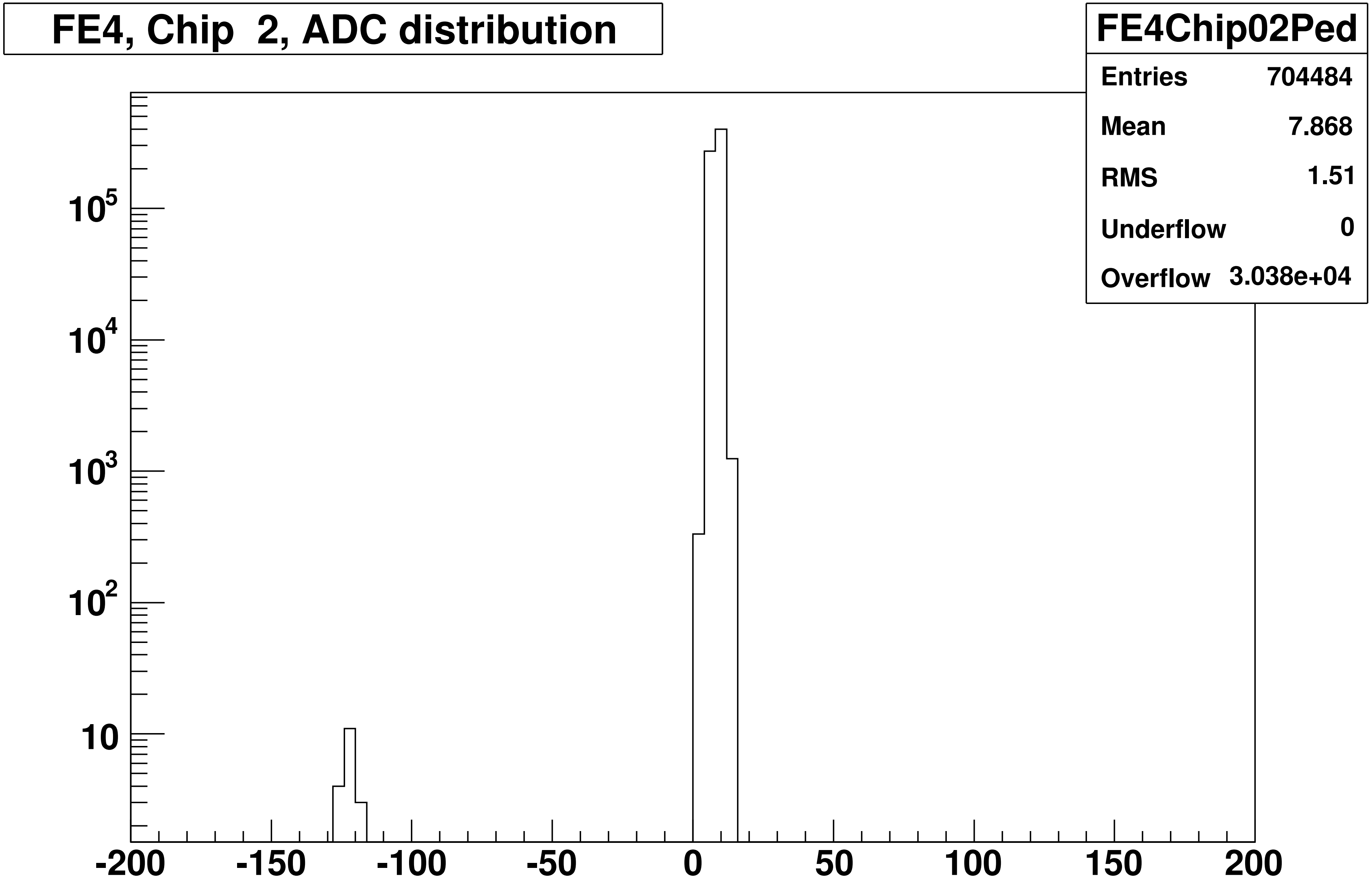

FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 704484
Mean 7.868
RMS 1.51
Underflow 0
Overflow 3.038e+04
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10^3
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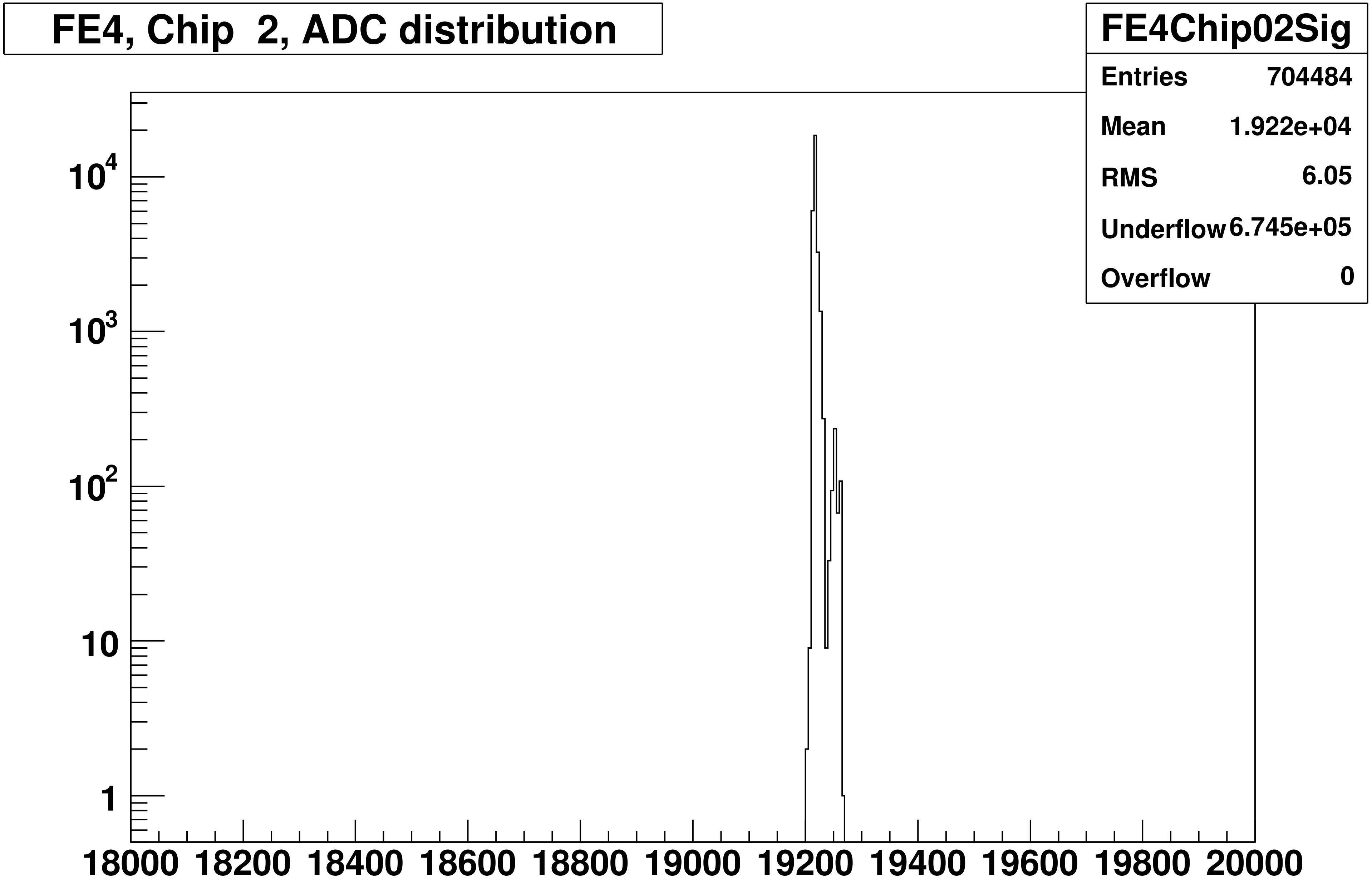
FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 704484
Mean 1.922e+04
RMS 6.05
Underflow 6.745e+05
Overflow 0
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10^3
10^2
10
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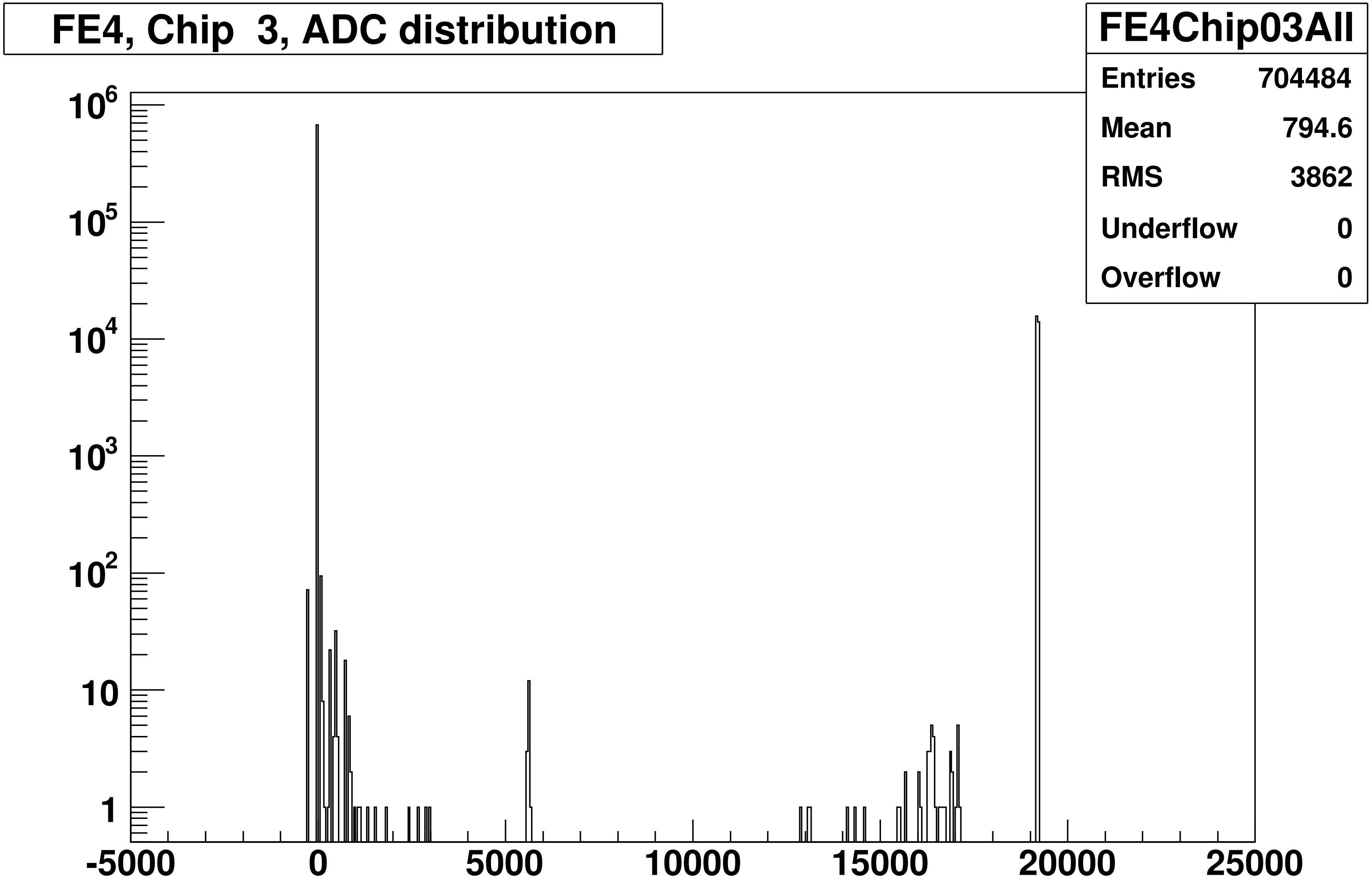
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 704484
Mean 794.6
RMS 3862
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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0
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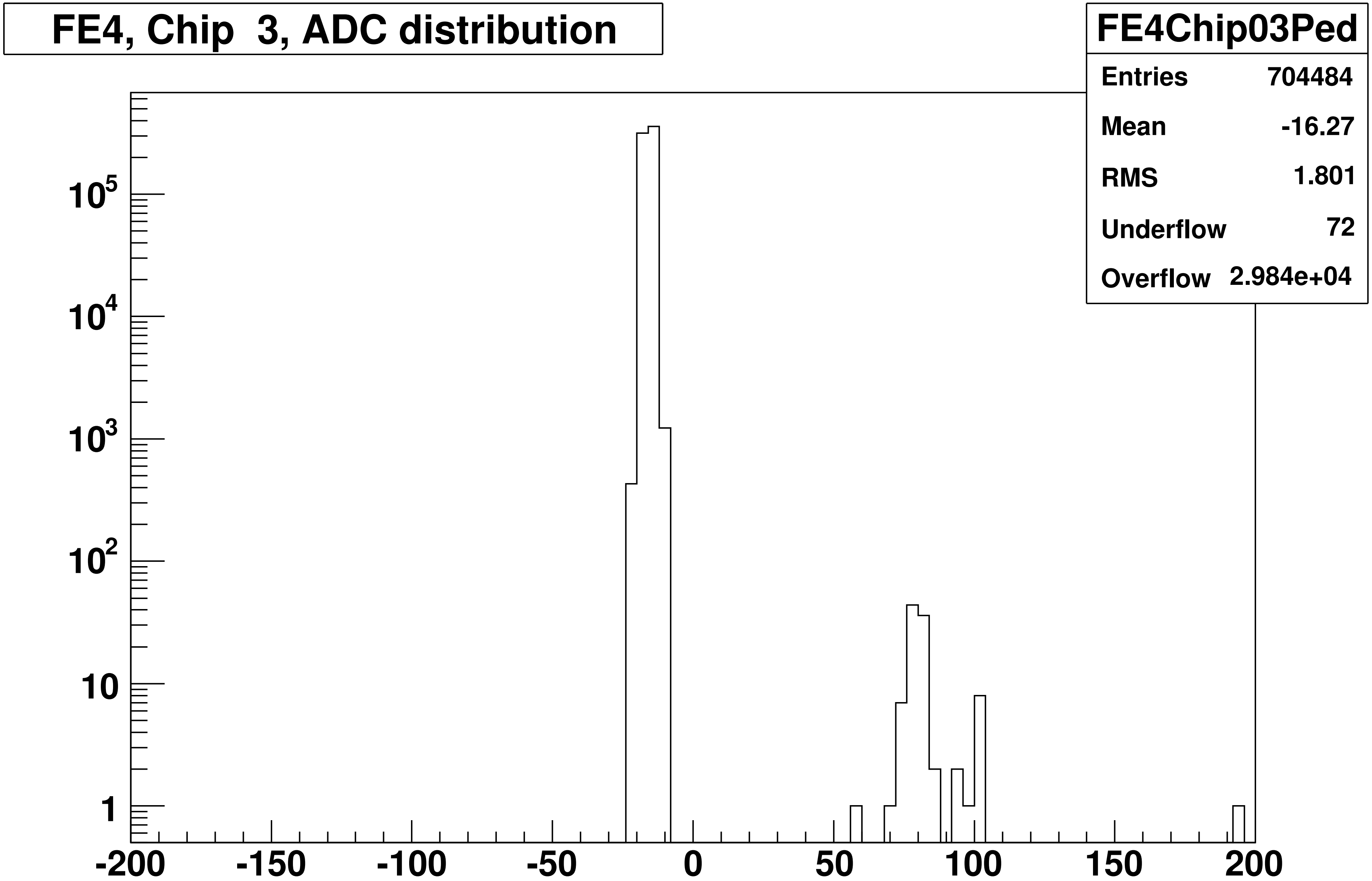
25000



FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 704484
Mean -16.27
RMS 1.801
Underflow 72
Overflow 2.984e+04
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10^4
10^3
10^2
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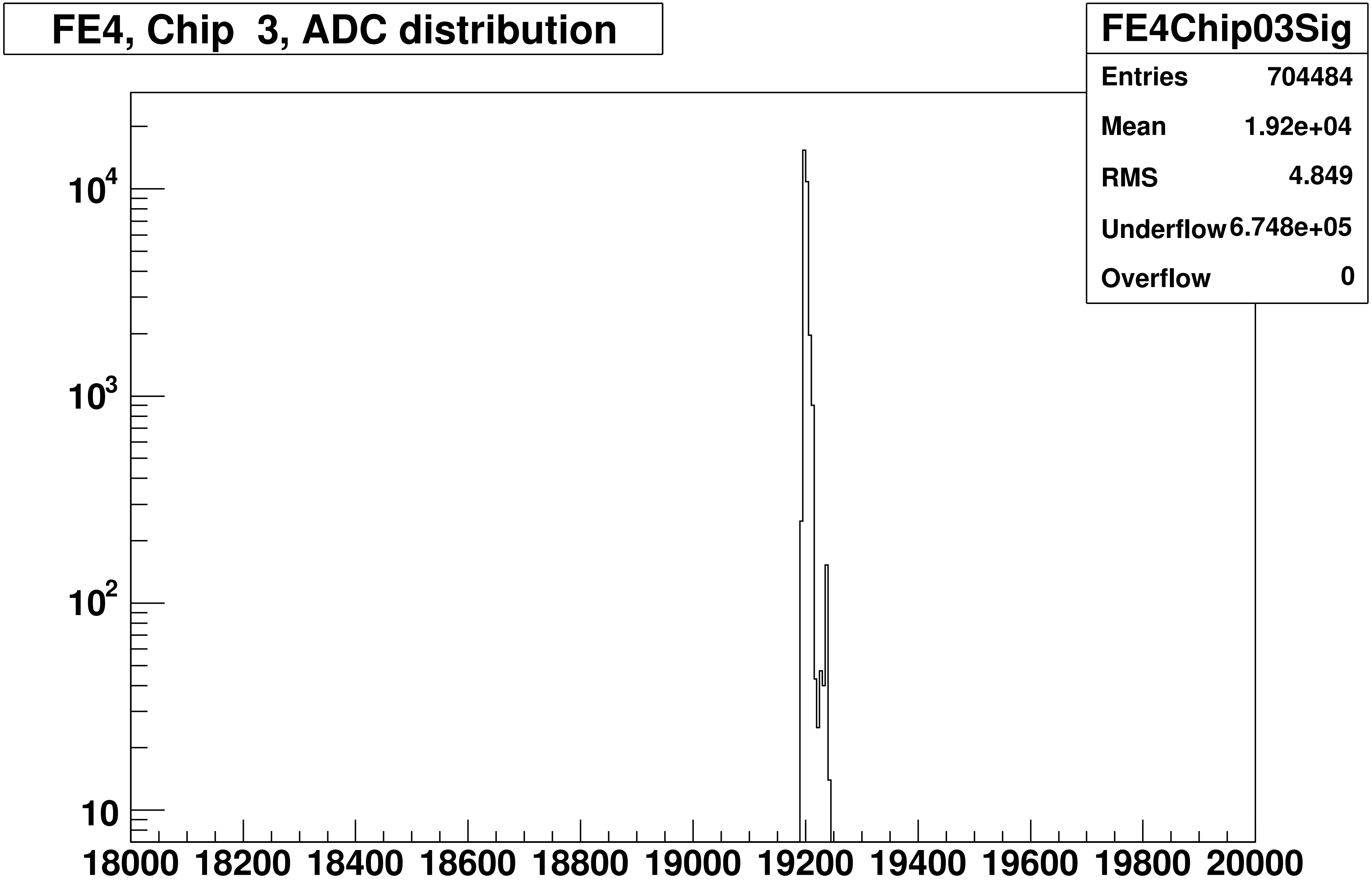
150
200



FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 704484
Mean 1.92e+04
RMS 4.849
Underflow 6.748e+05
Overflow 0
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10^3
10^2
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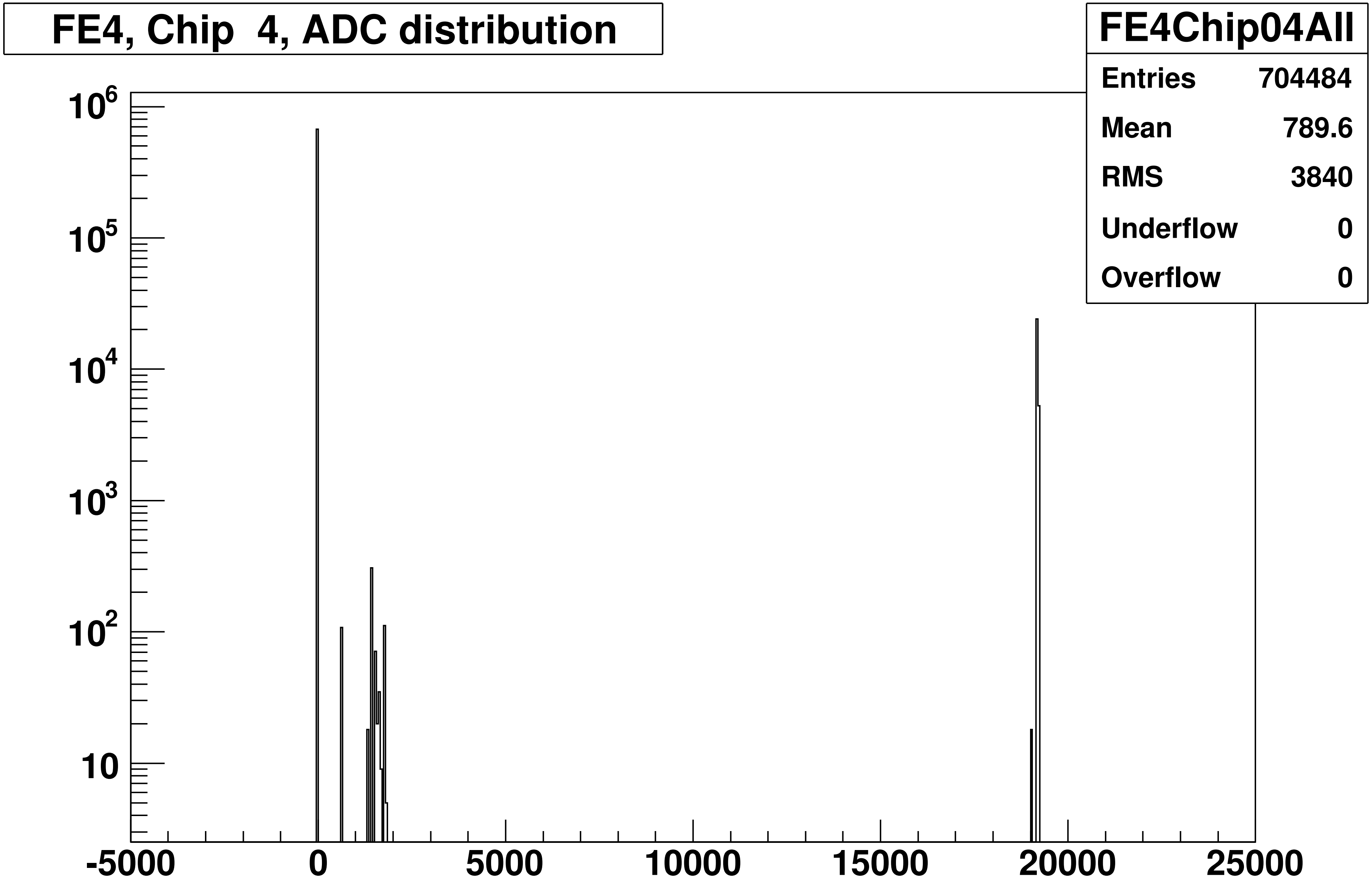
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 704484
Mean 789.6
RMS 3840
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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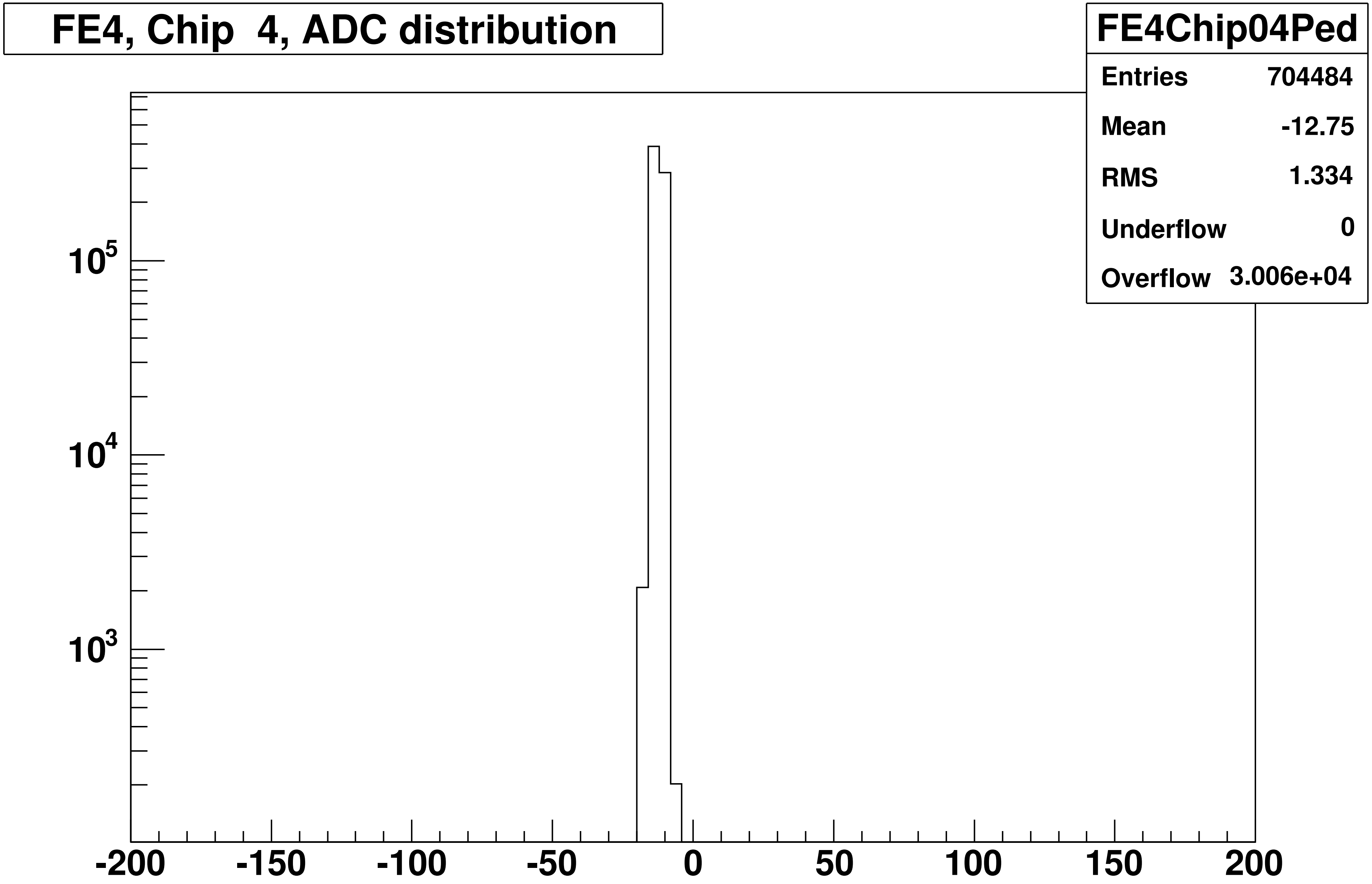
25000


FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 704484
Mean -12.75
RMS 1.334
Underflow 0
Overflow 3.006e+04
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10^4
10^3
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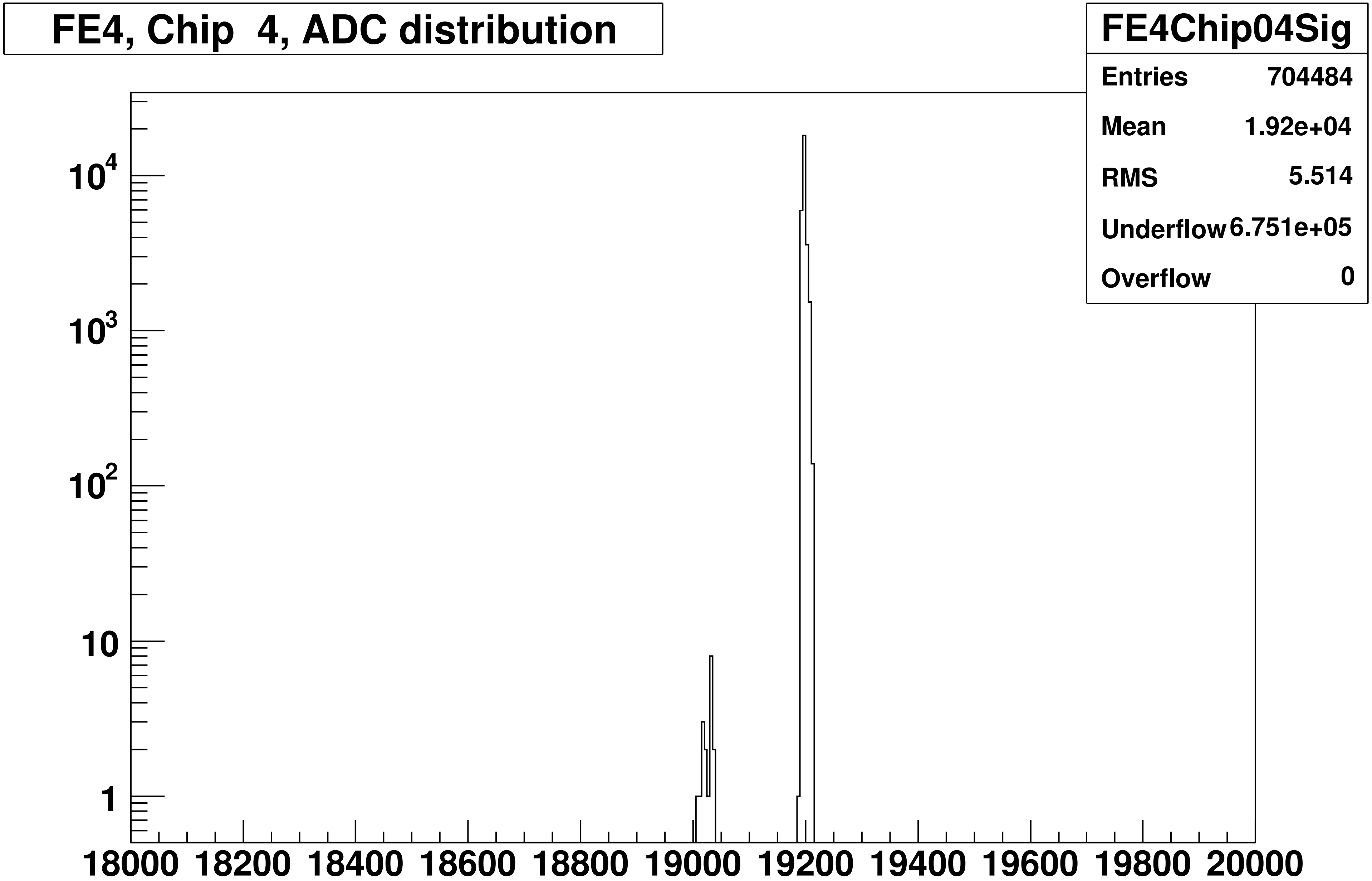
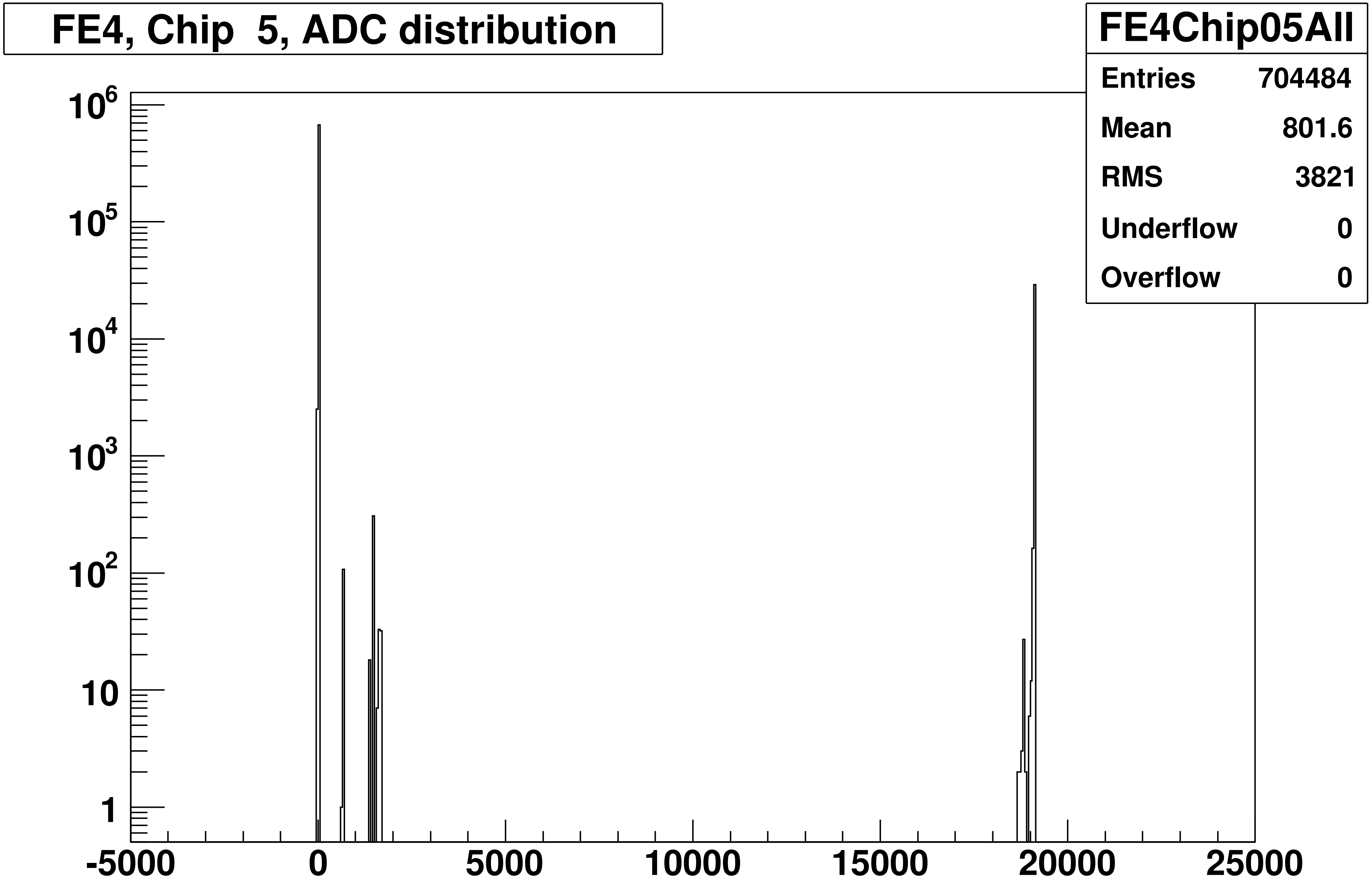
150
200


FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 704484
Mean 1.92e+04
RMS 5.514
Underflow 6.751e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 704484
Mean 801.6
RMS 3821
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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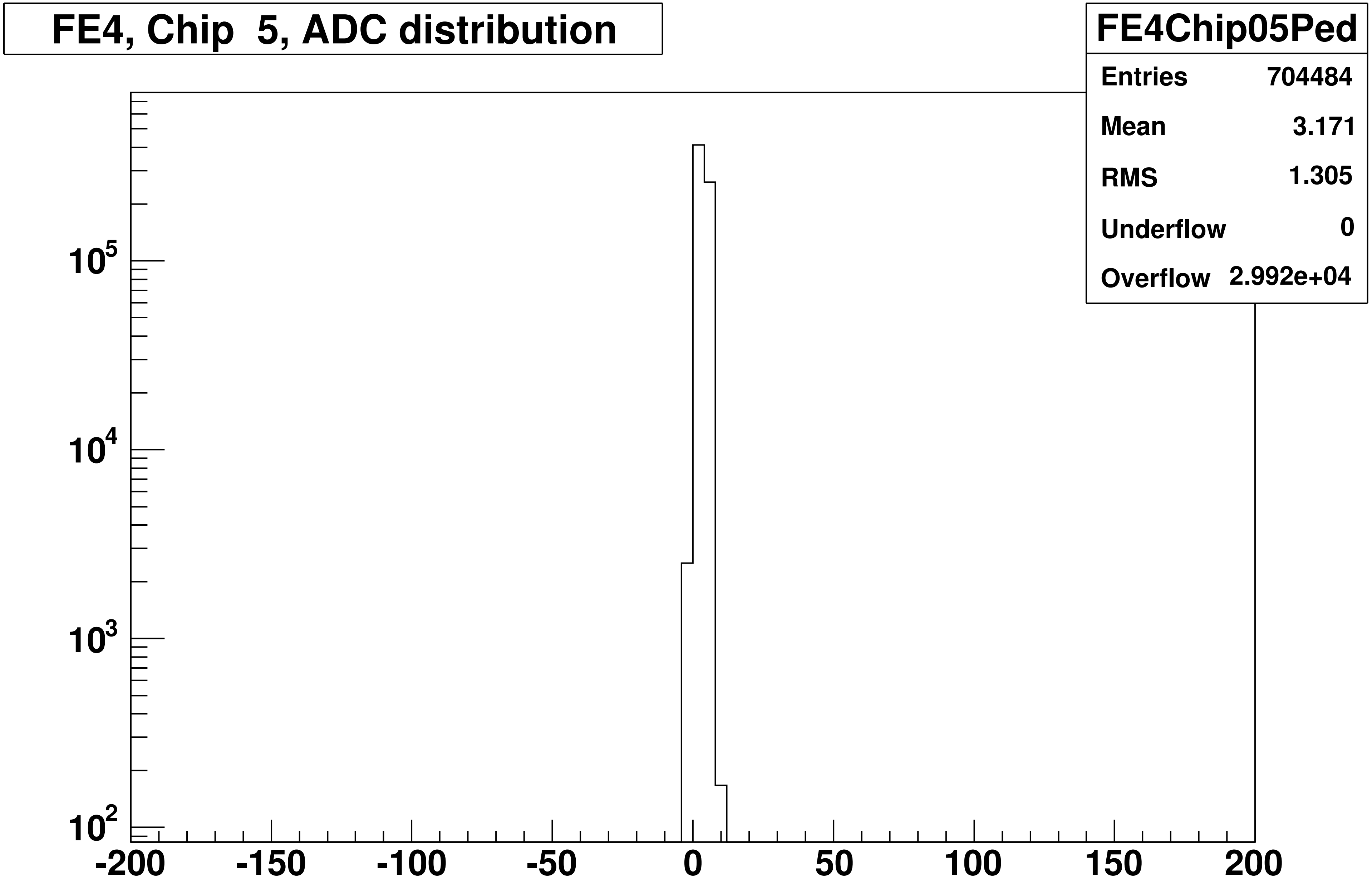

FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 704484
Mean 3.171
RMS 1.305
Underflow 0
Overflow 2.992e+04
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10^4
10^3
10^2
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-100
-50
0
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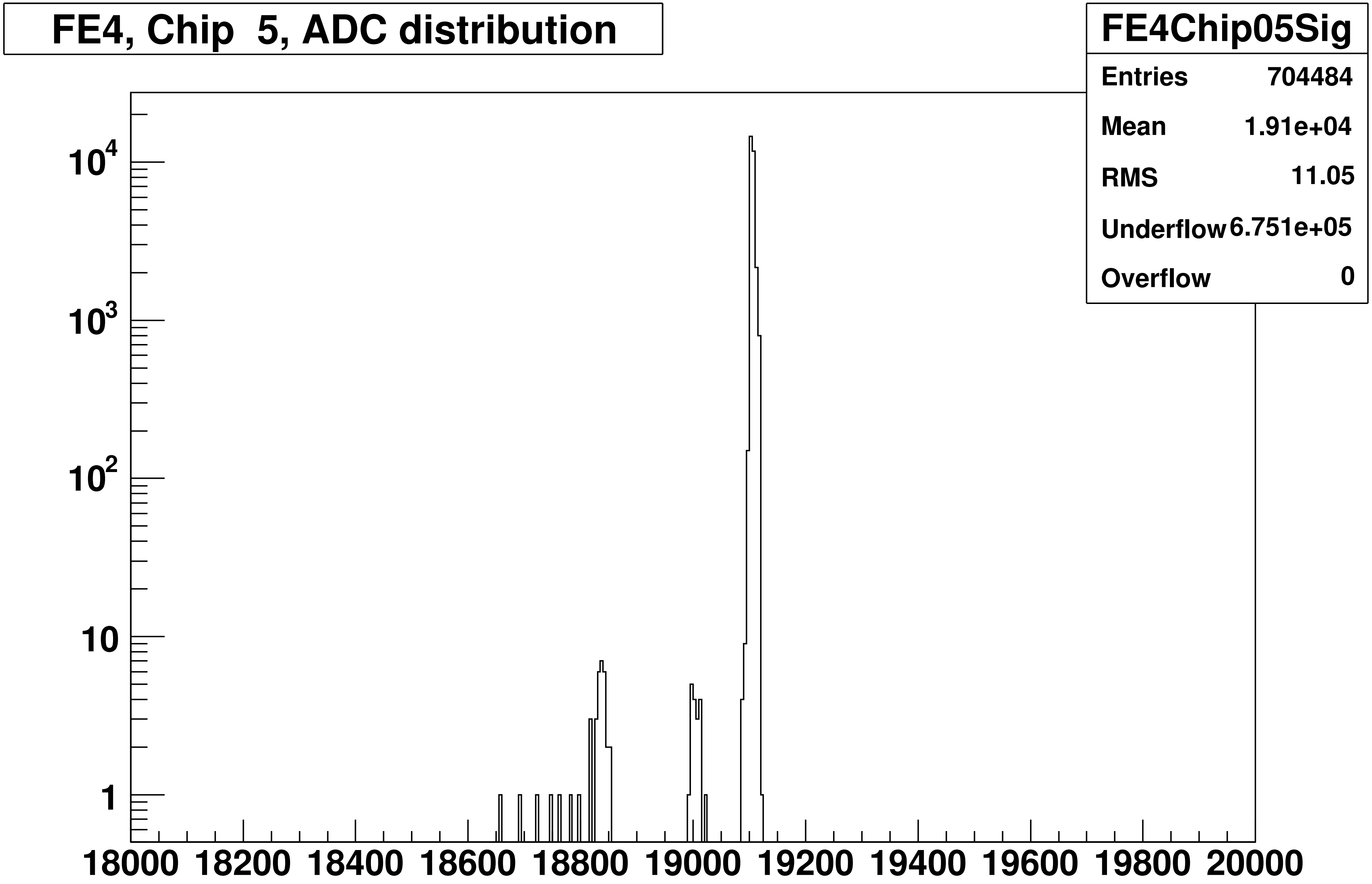
100
150
200


FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 704484
Mean 1.91e+04
RMS 11.05
Underflow 6.751e+05
Overflow 0
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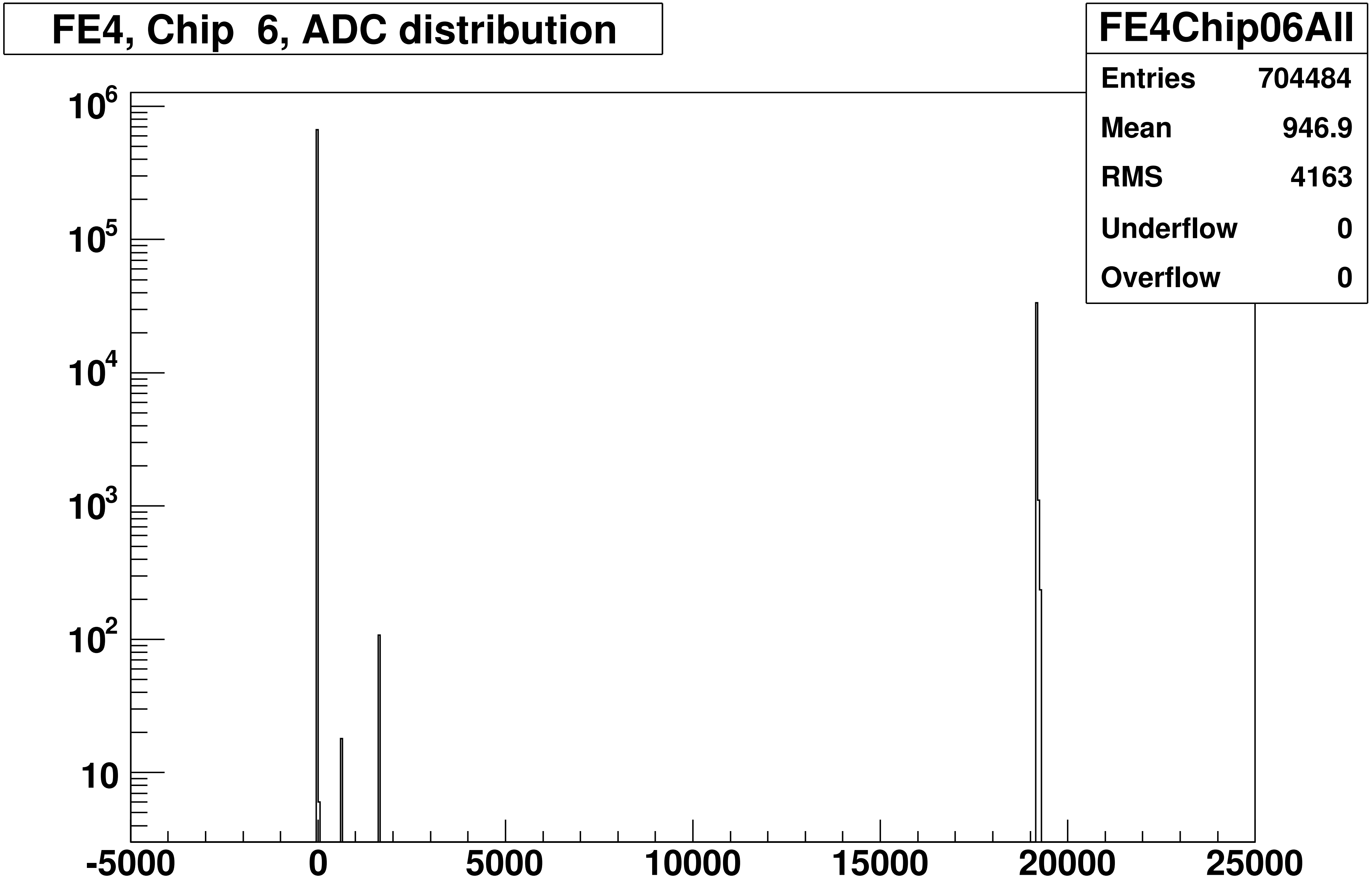
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 704484
Mean 946.9
RMS 4163
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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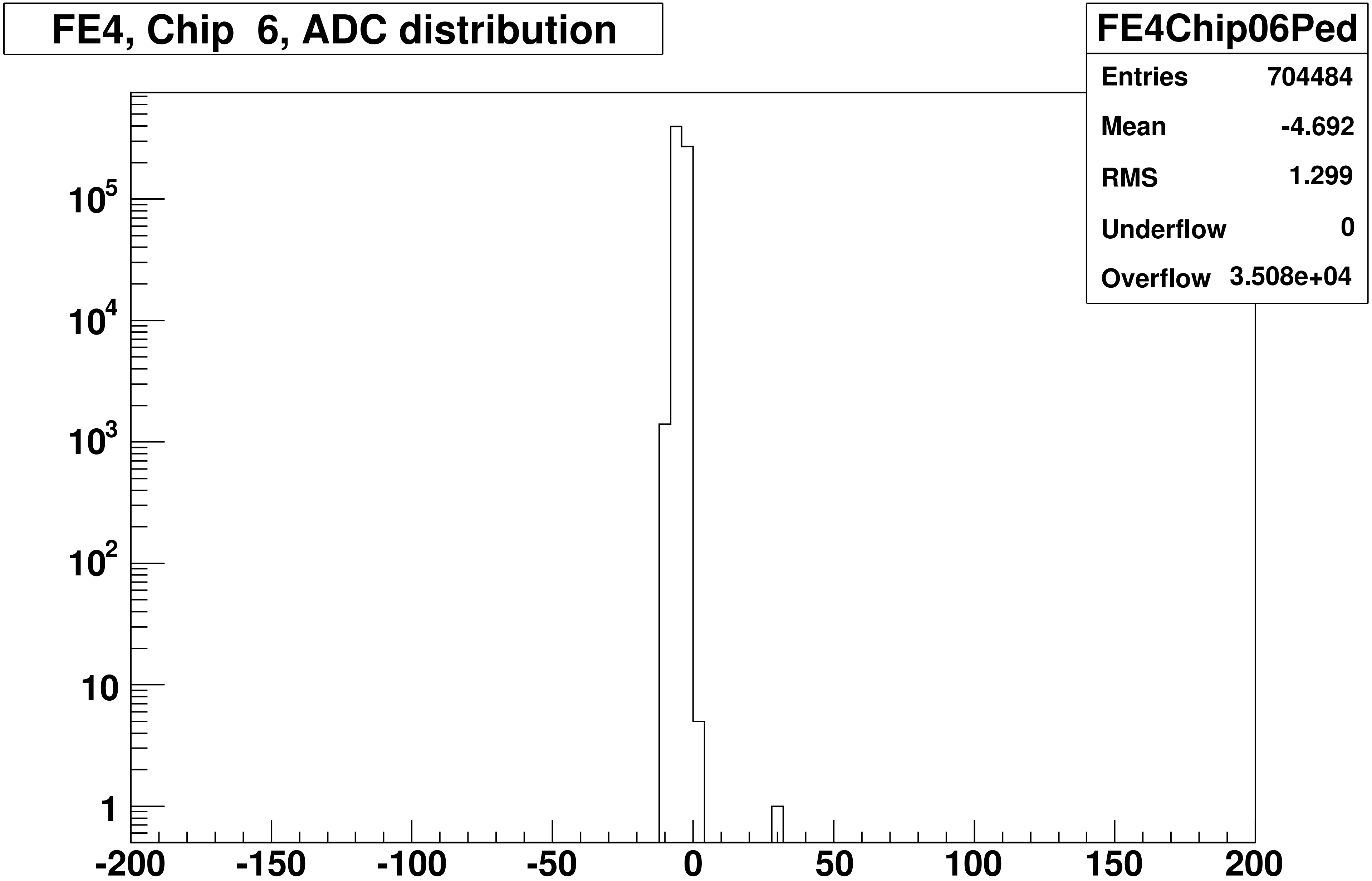
10000
15000
20000
25000


FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 704484
Mean -4.692
RMS 1.299
Underflow 0
Overflow 3.508e+04
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10^4
10^3
10^2
10
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-150
-100
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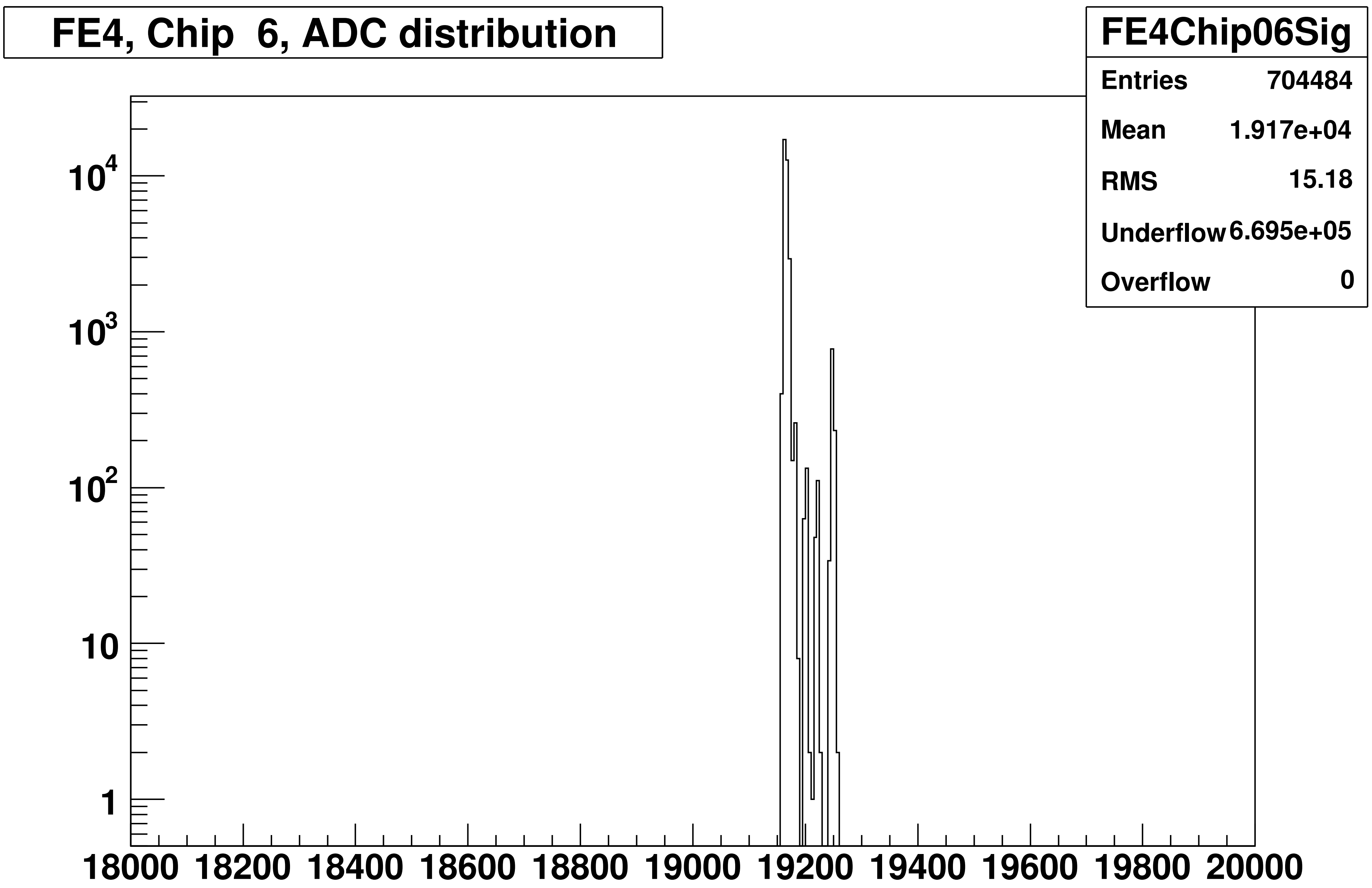
0
50
100
150
200


FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 704484
Mean 1.917e+04
RMS 15.18
Underflow 6.695e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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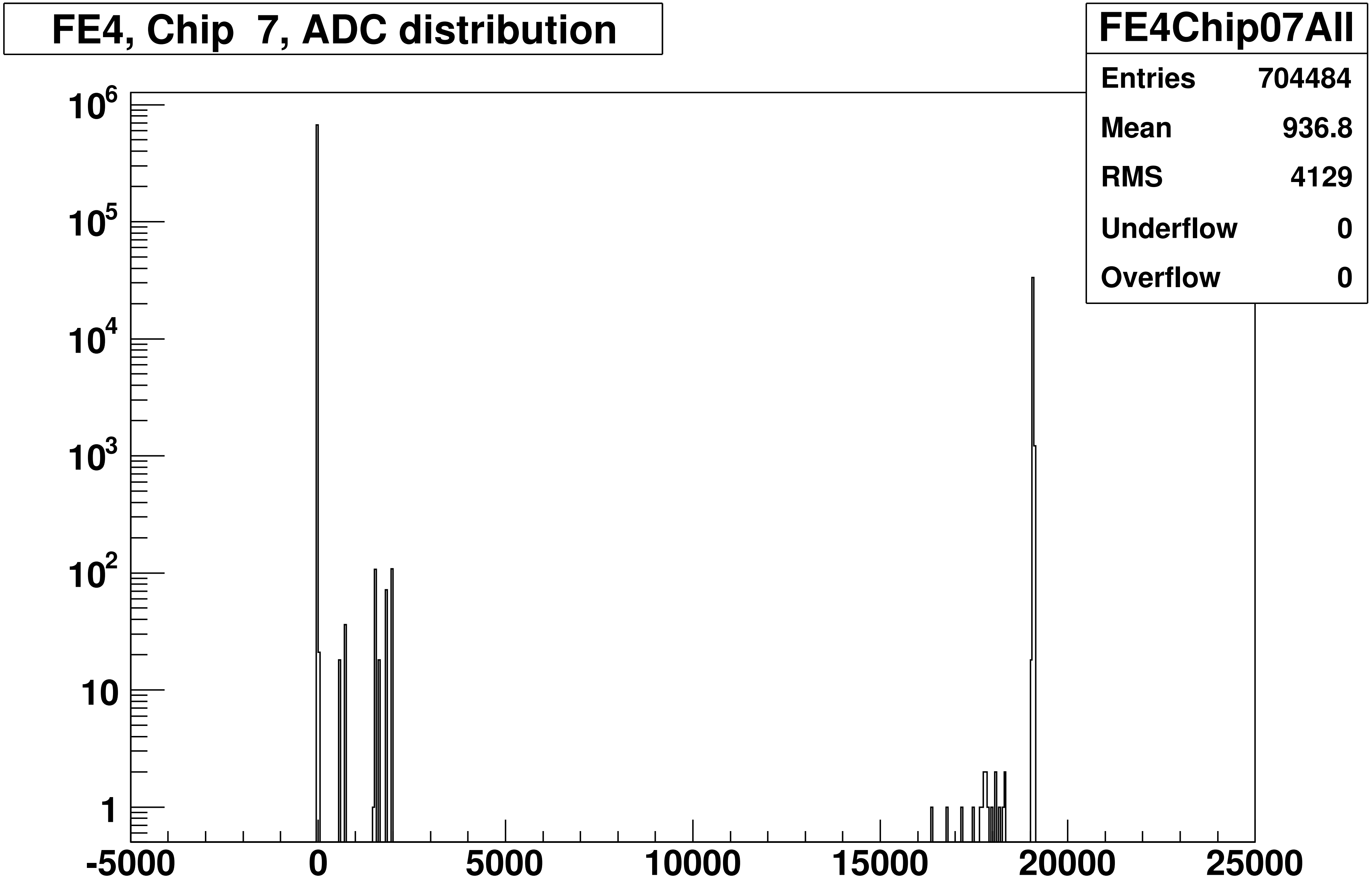
20000



FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 704484
Mean 936.8
RMS 4129
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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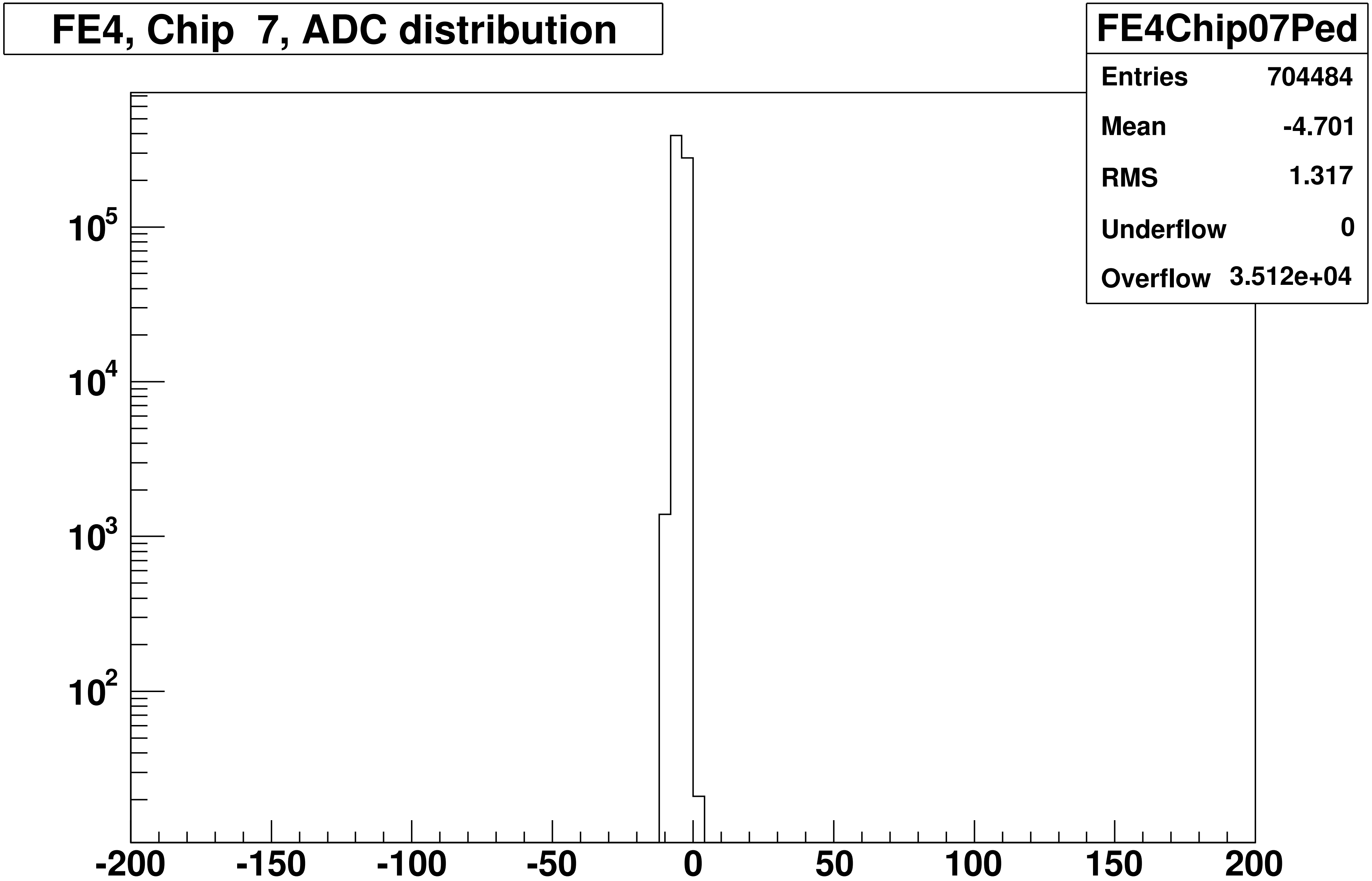
FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 704484
Mean -4.701
RMS 1.317
Underflow 0
Overflow 3.512e+04
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10^4
10^3
10^2
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-100
-50
0
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100
150
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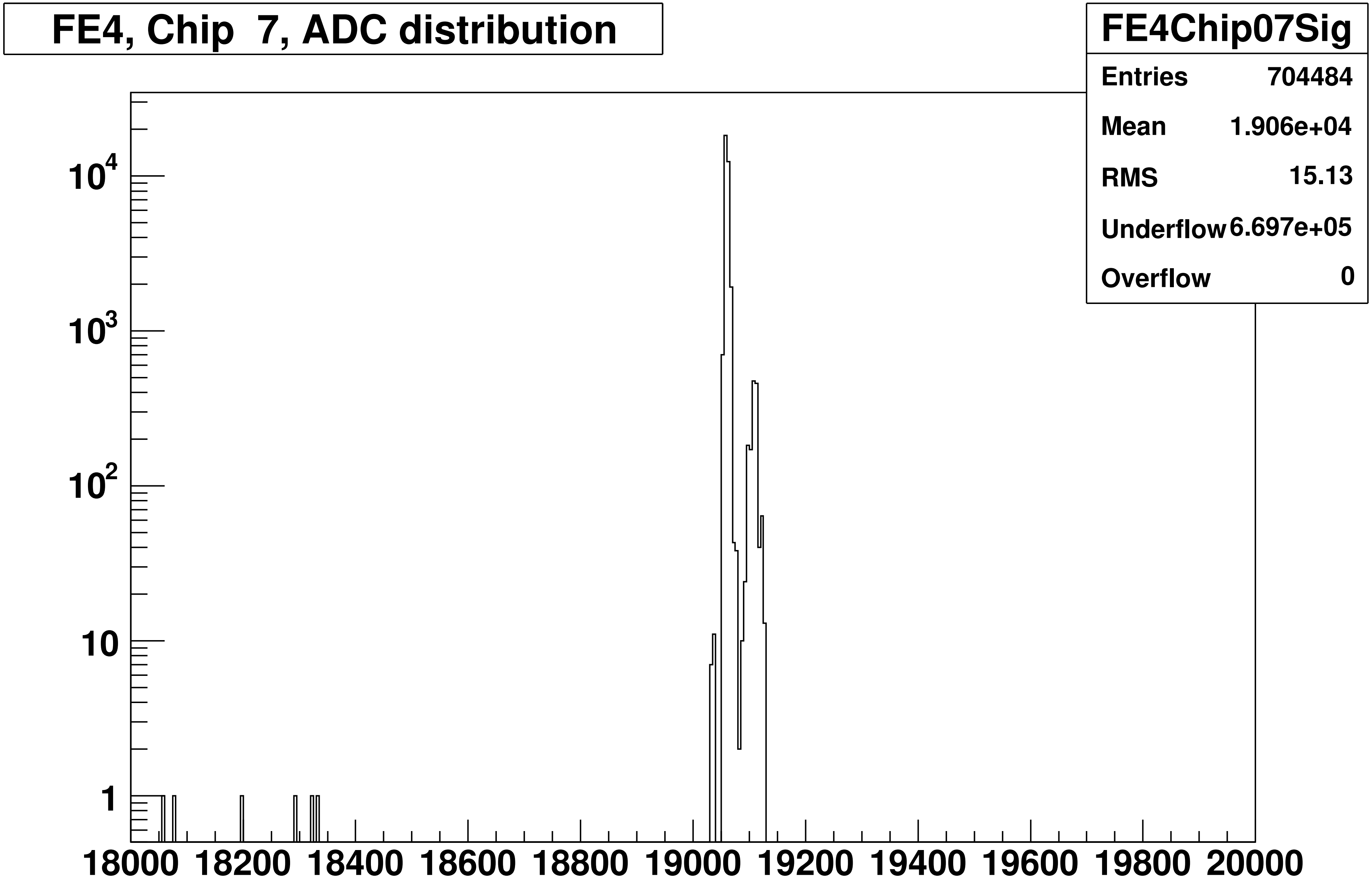
FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 704484
Mean 1.906e+04
RMS 15.13
Underflow 6.697e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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19000
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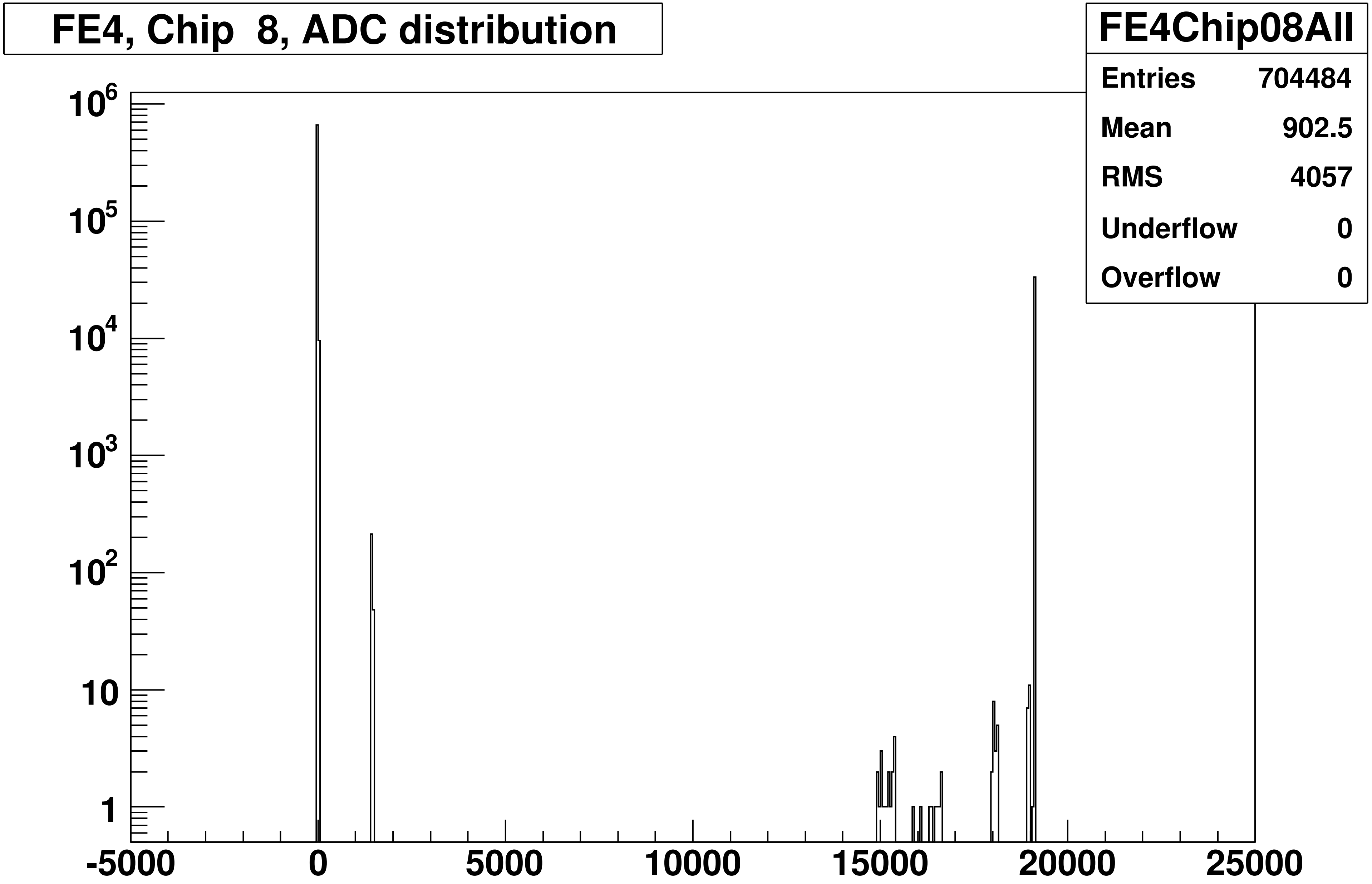
FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 704484
Mean 902.5
RMS 4057
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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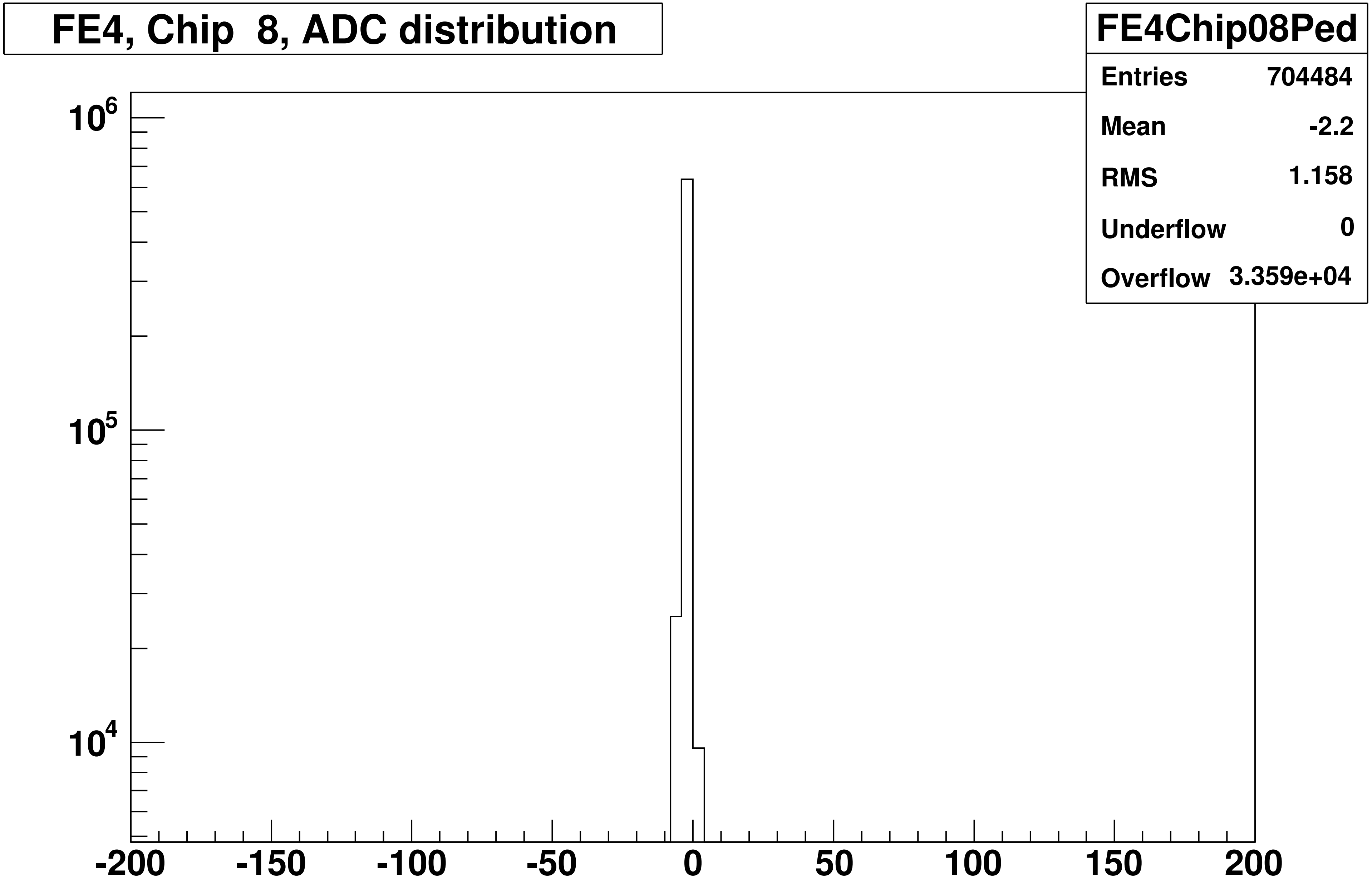

FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 704484
Mean -2.2
RMS 1.158
Underflow 0
Overflow 3.359e+04
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10^5
10^4
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-150
-100
-50
0
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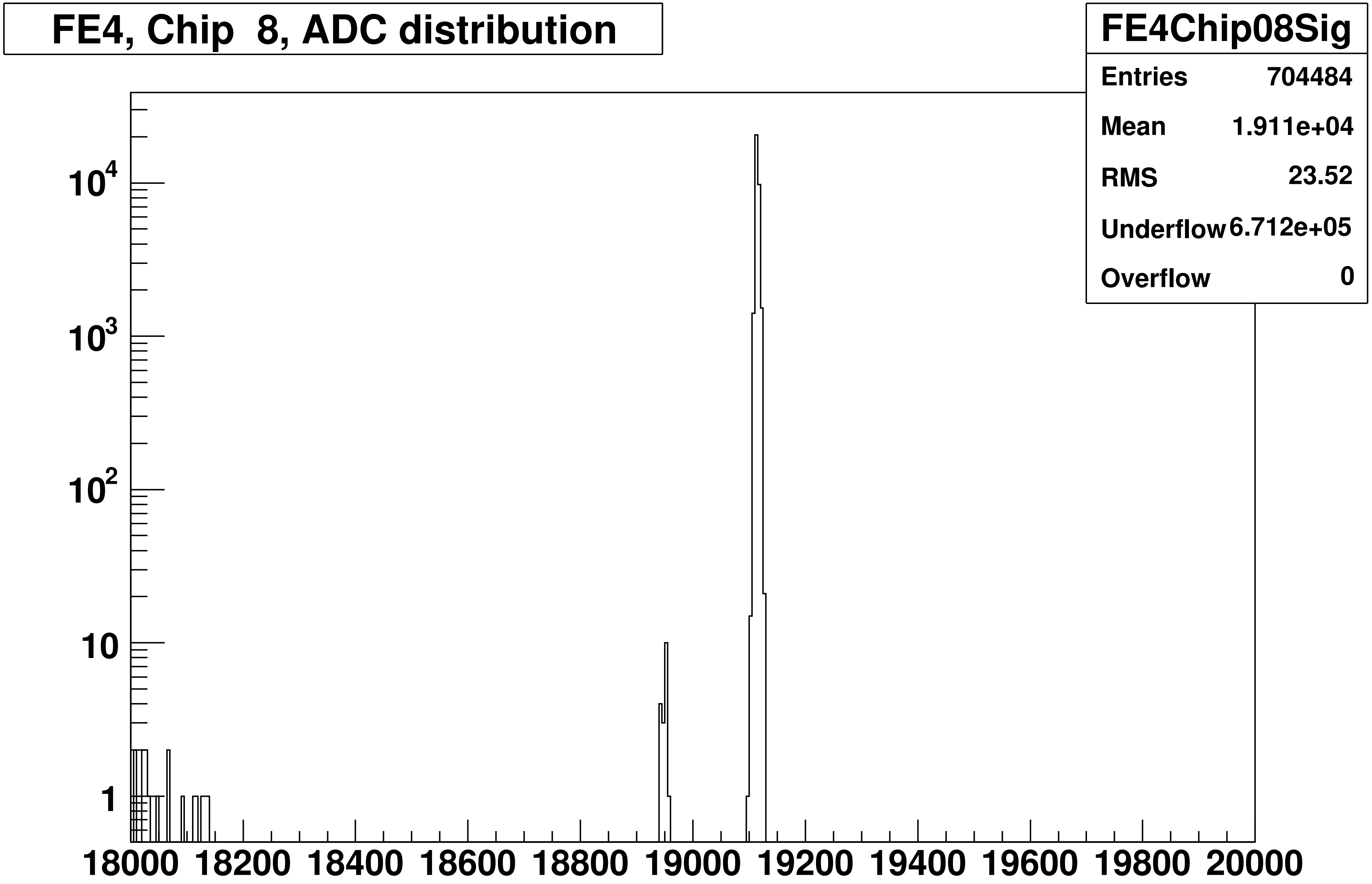
100
150
200


FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 704484
Mean 1.911e+04
RMS 23.52
Underflow 6.712e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
18400
18600
18800
19000
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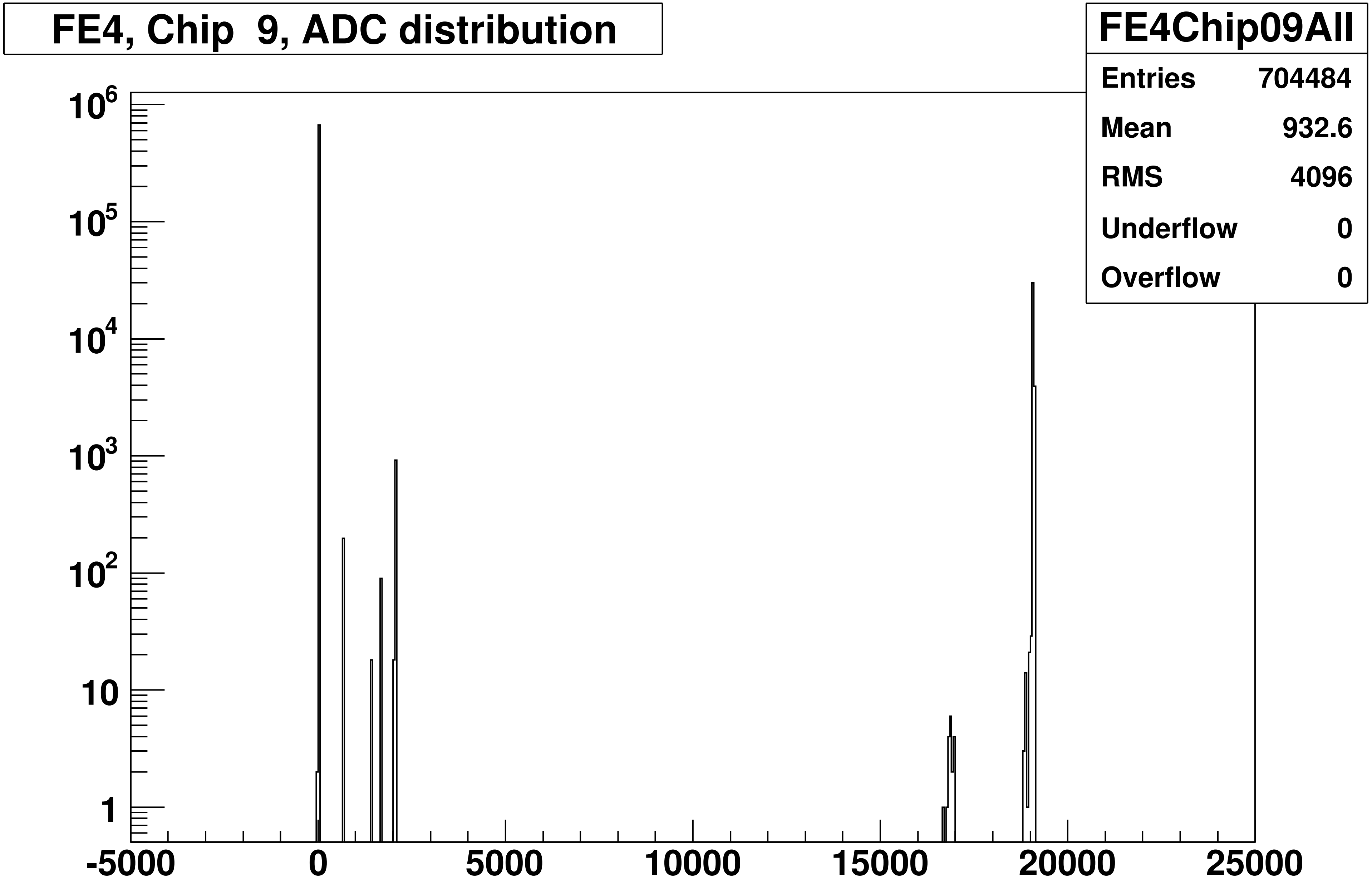
19400
19600
19800
20000



FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 704484
Mean 932.6
RMS 4096
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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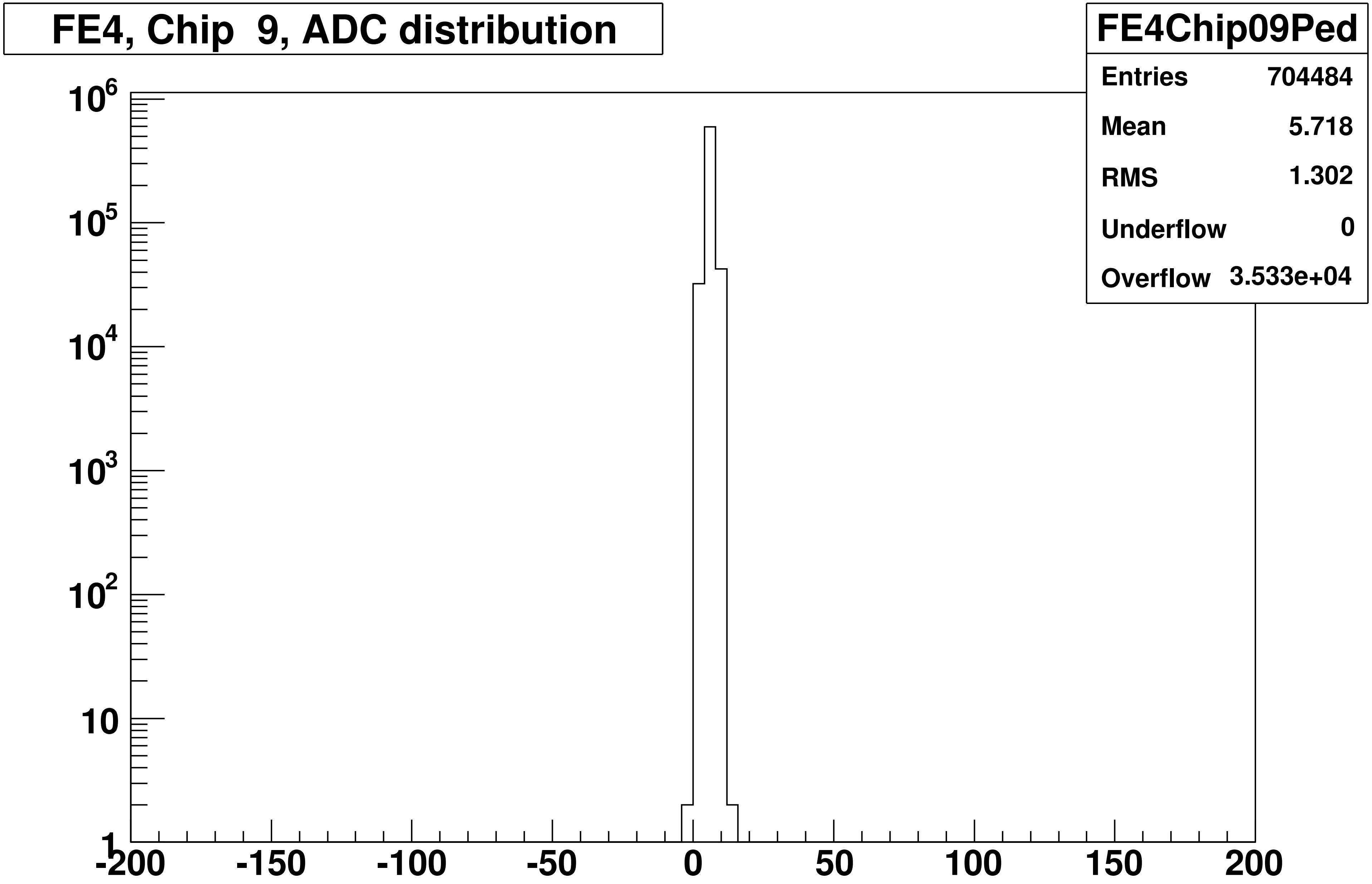
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 704484
Mean 5.718
RMS 1.302
Underflow 0
Overflow 3.533e+04
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10^5
10^4
10^3
10^2
10
1
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-150
-100
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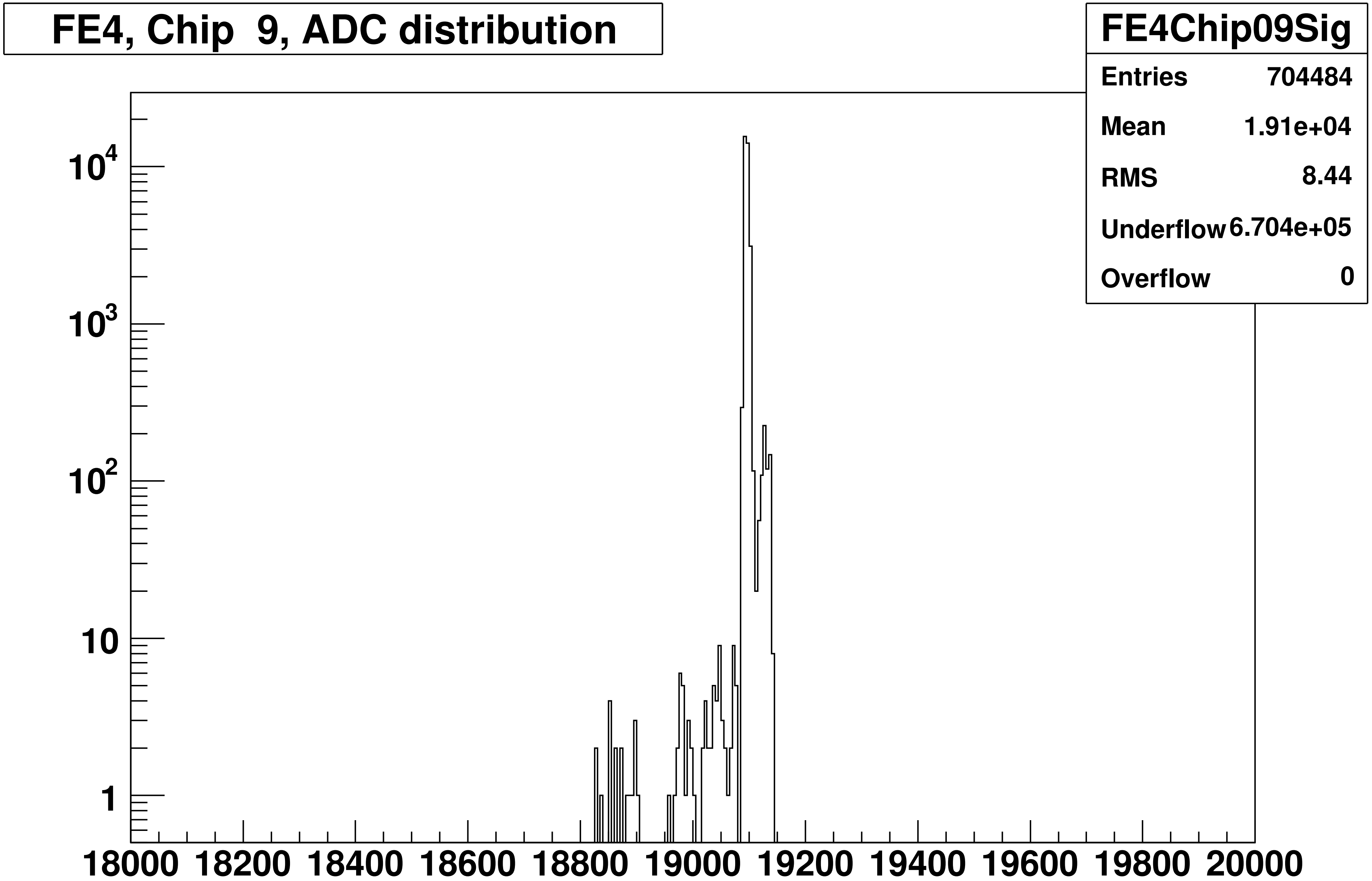
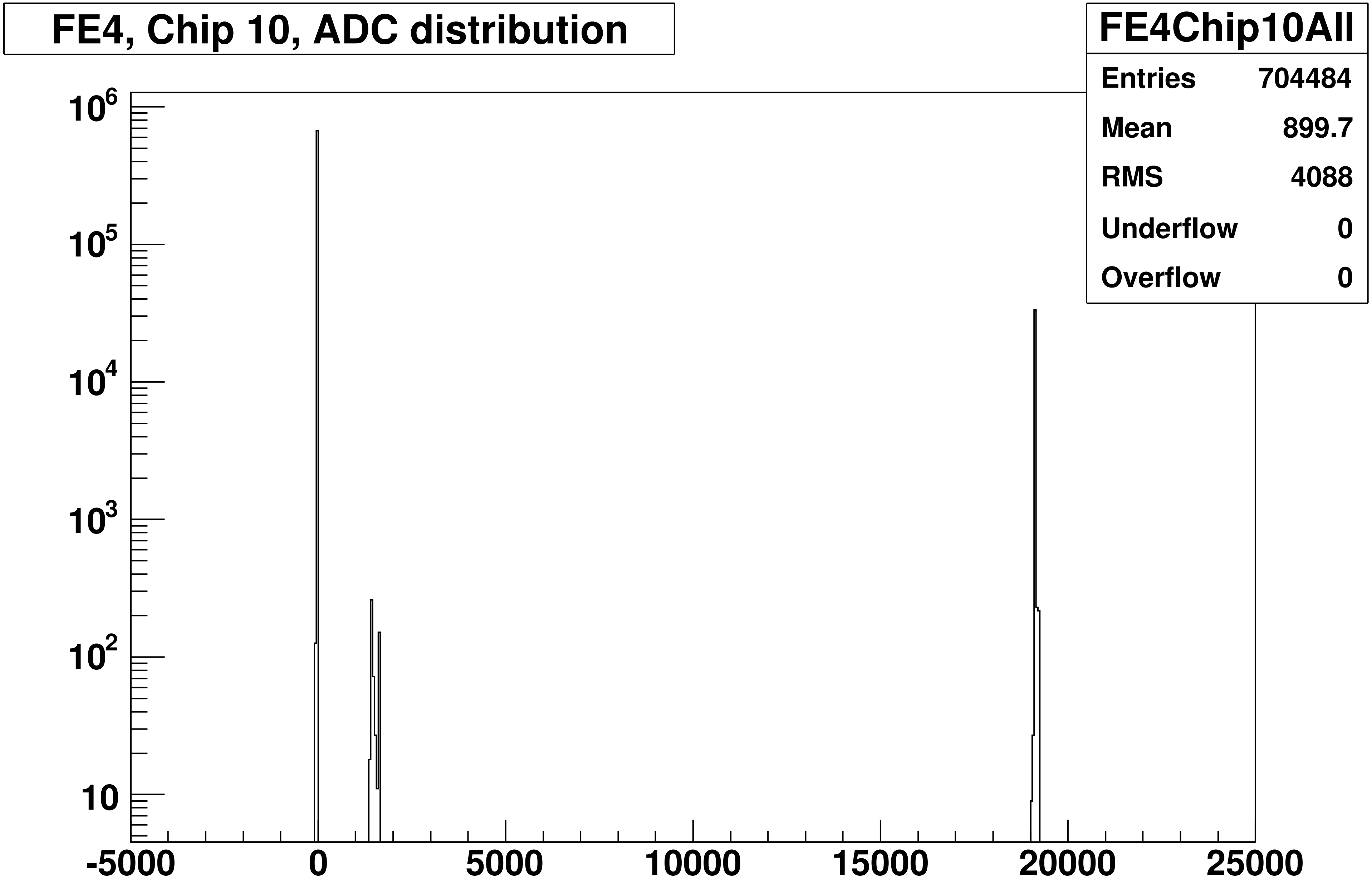
0
50
100
150
200


FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 704484
Mean 1.91e+04
RMS 8.44
Underflow 6.704e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 704484
Mean 899.7
RMS 4088
Underflow 0
Overflow 0
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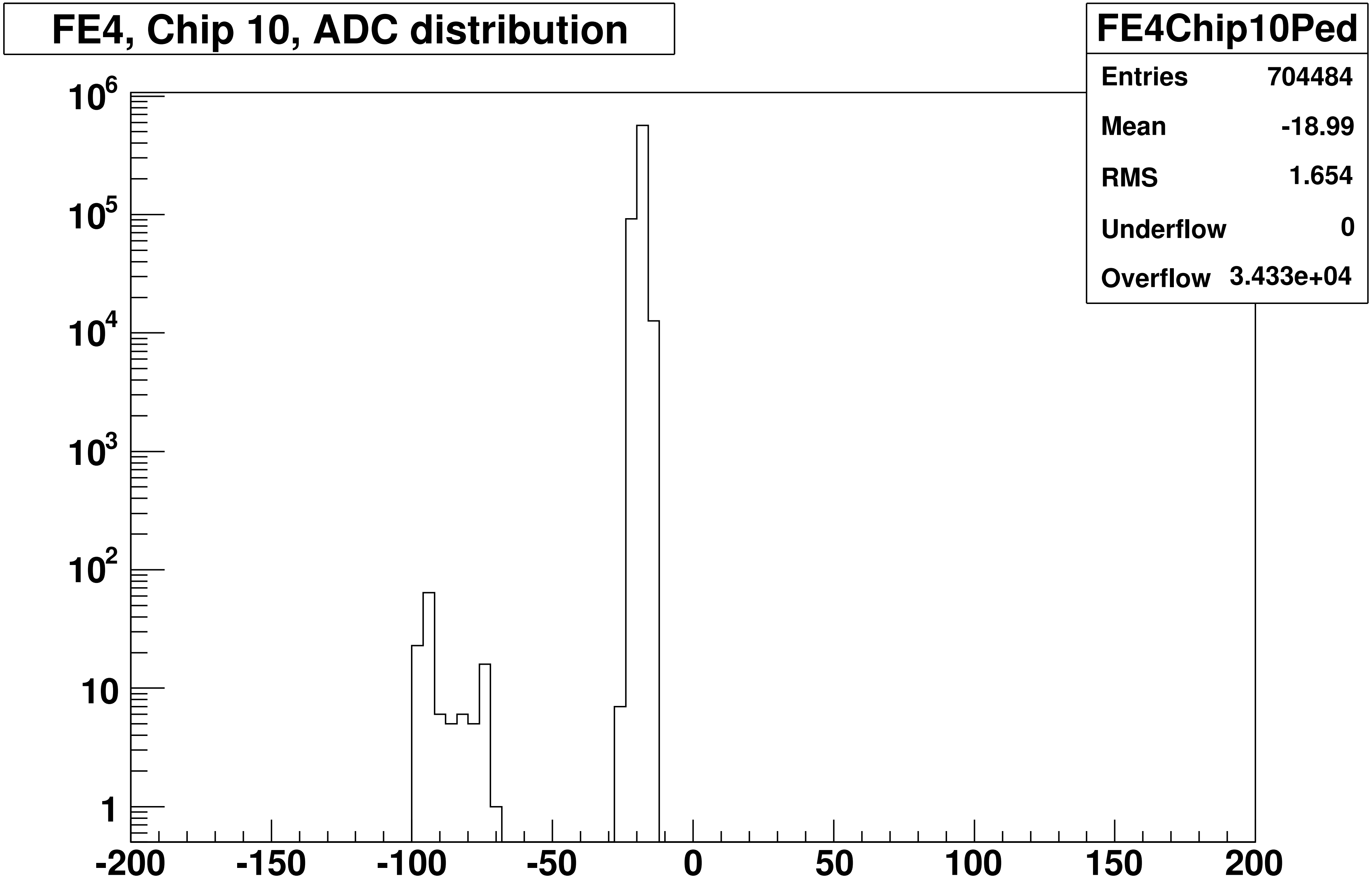
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 704484
Mean -18.99
RMS 1.654
Underflow 0
Overflow 3.433e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
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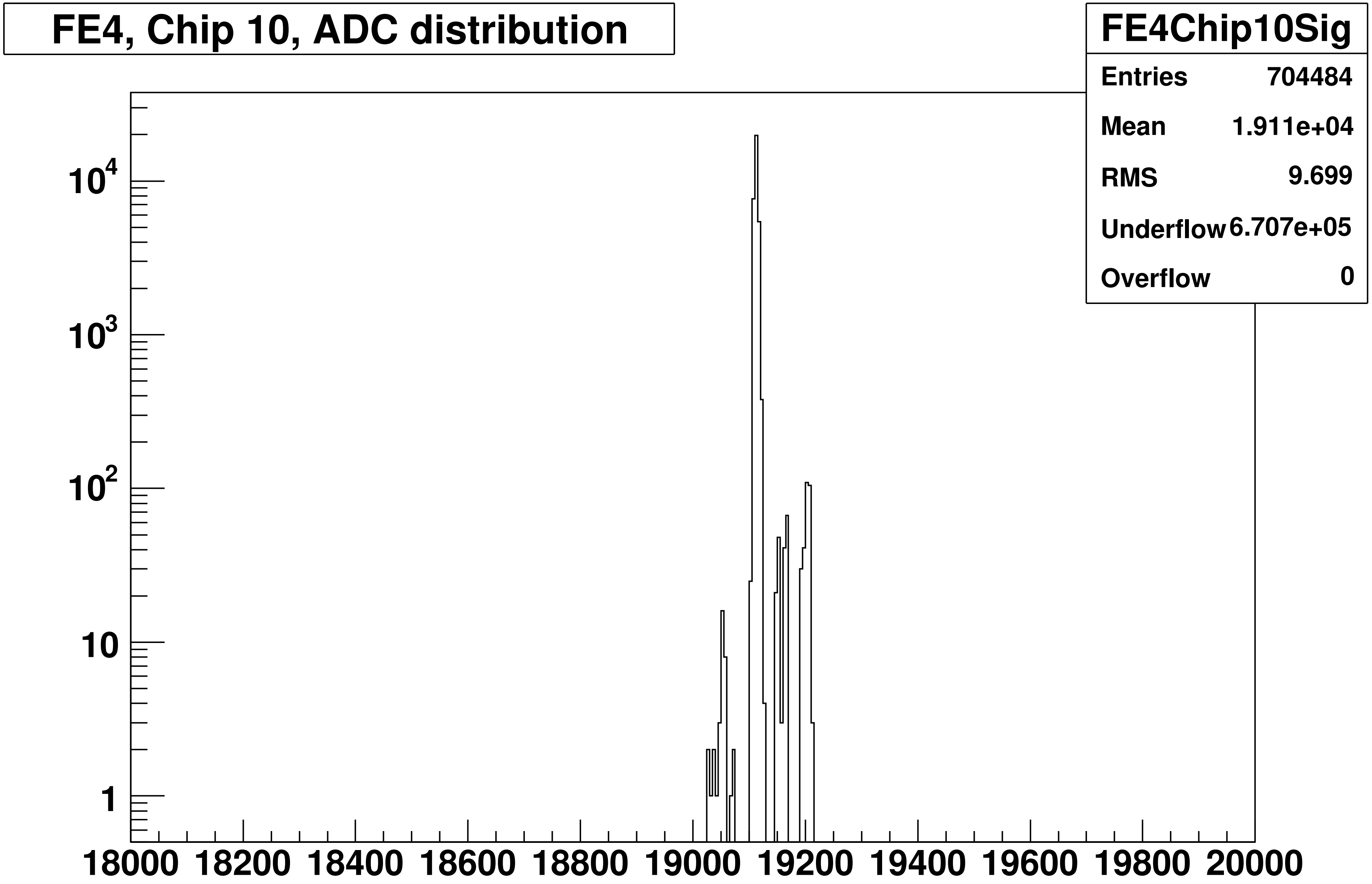
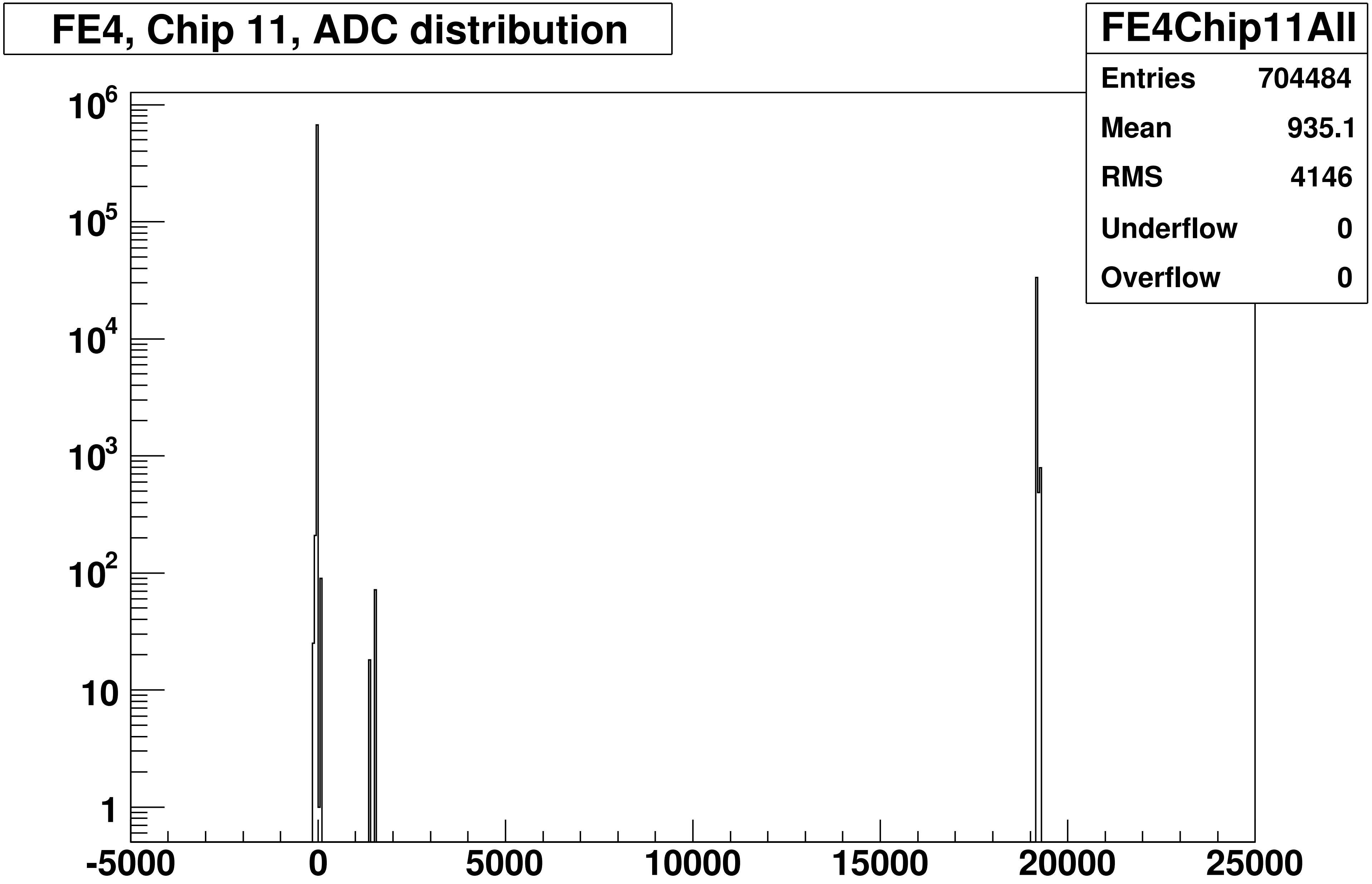
0
50
100
150
200



FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 704484
Mean 1.911e+04
RMS 9.699
Underflow 6.707e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 704484
Mean 935.1
RMS 4146
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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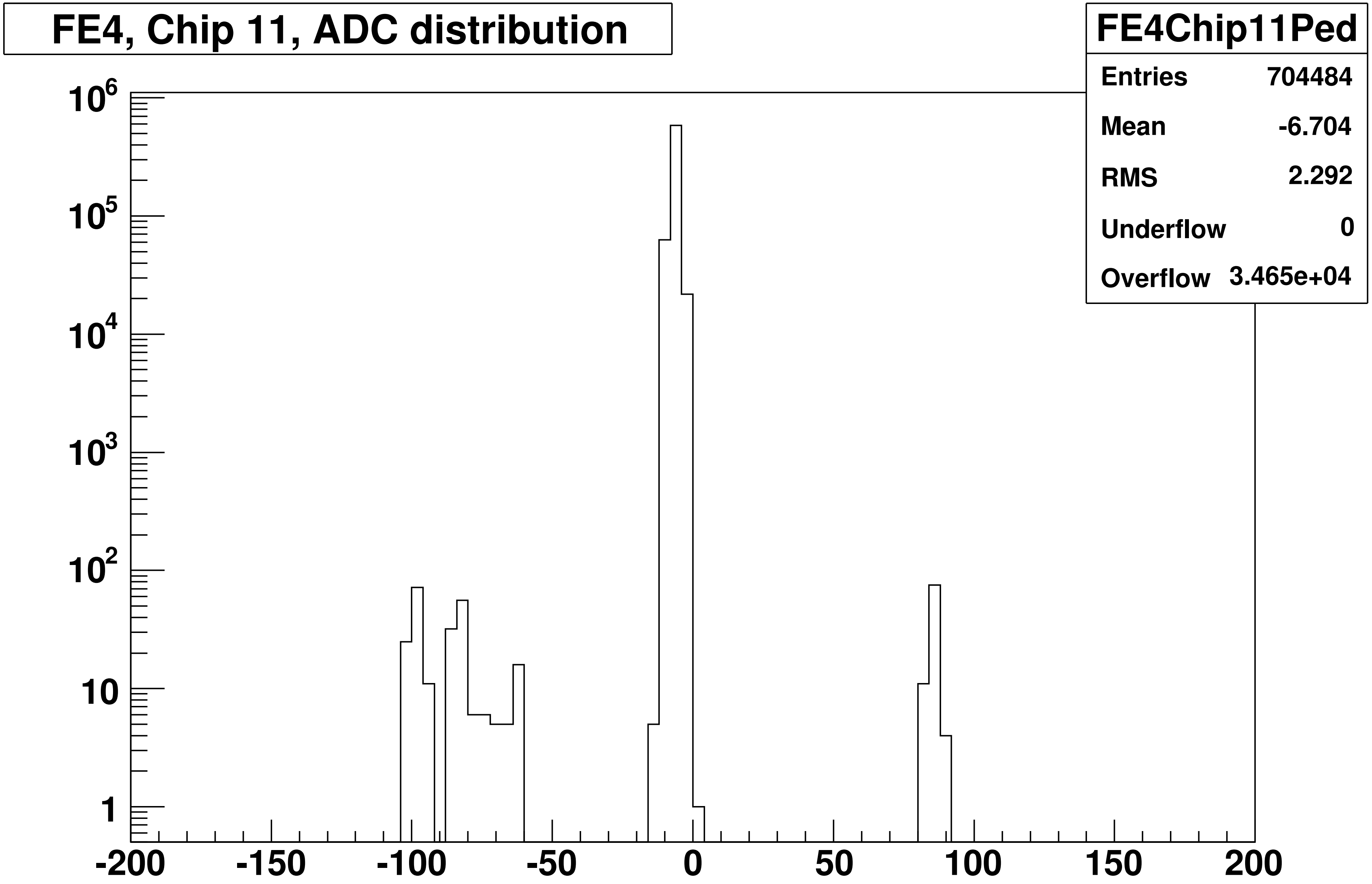
0
5000
10000
15000
20000
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 704484
Mean -6.704
RMS 2.292
Underflow 0
Overflow 3.465e+04
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10^5
10^4
10^3
10^2
10
1
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0
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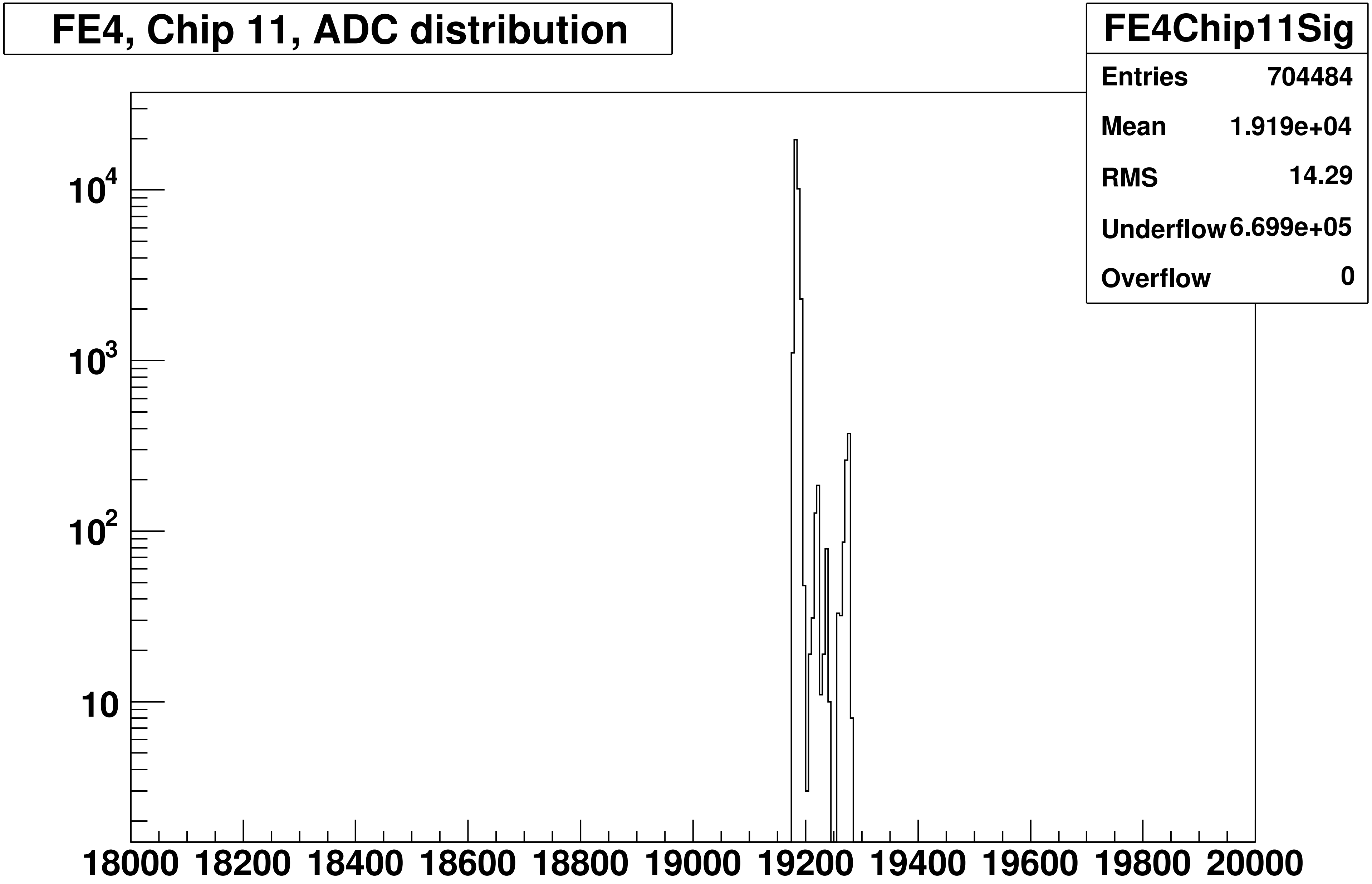
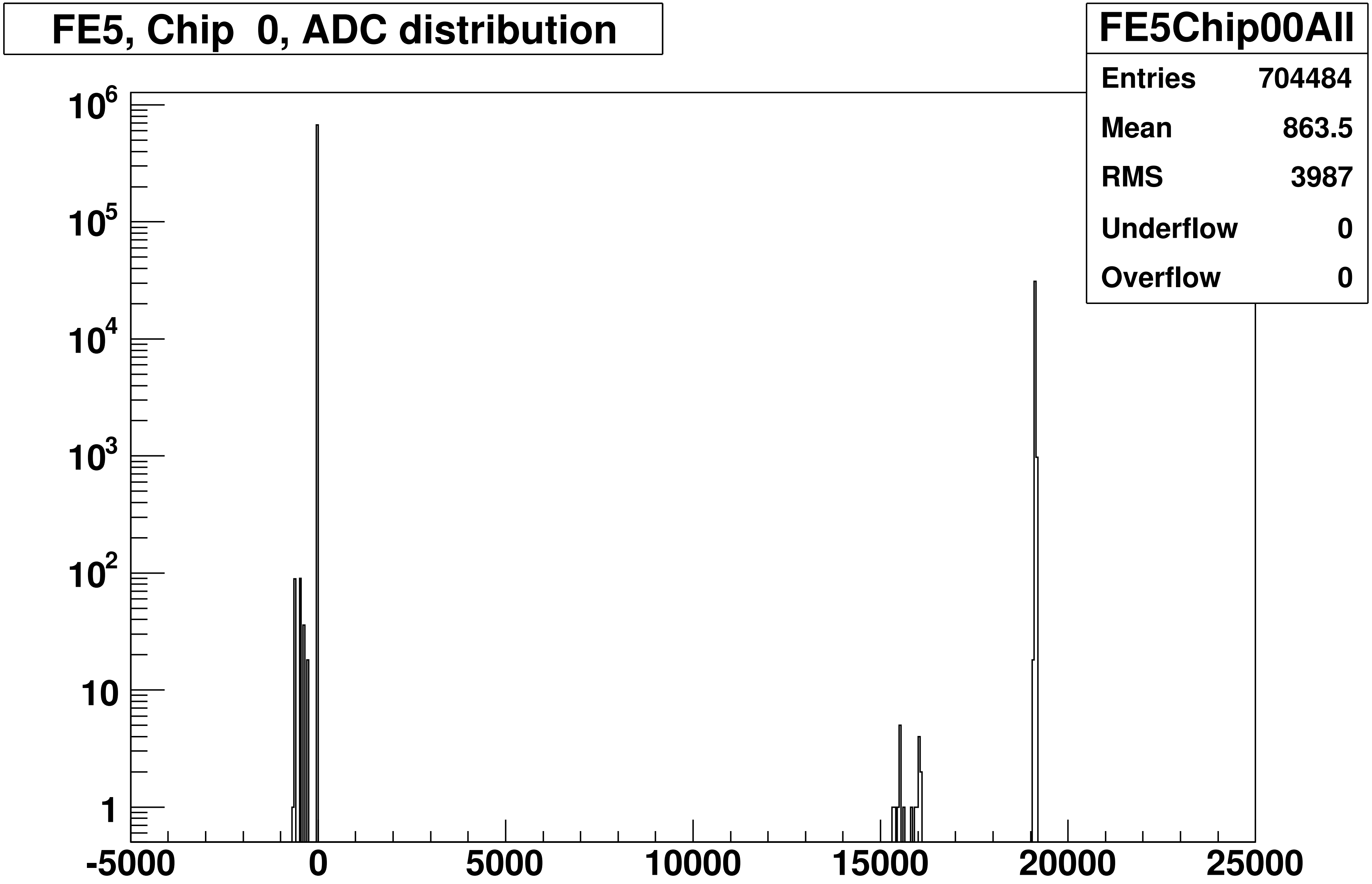
100
150
200



FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 704484
Mean 1.919e+04
RMS 14.29
Underflow 6.699e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 704484
Mean 863.5
RMS 3987
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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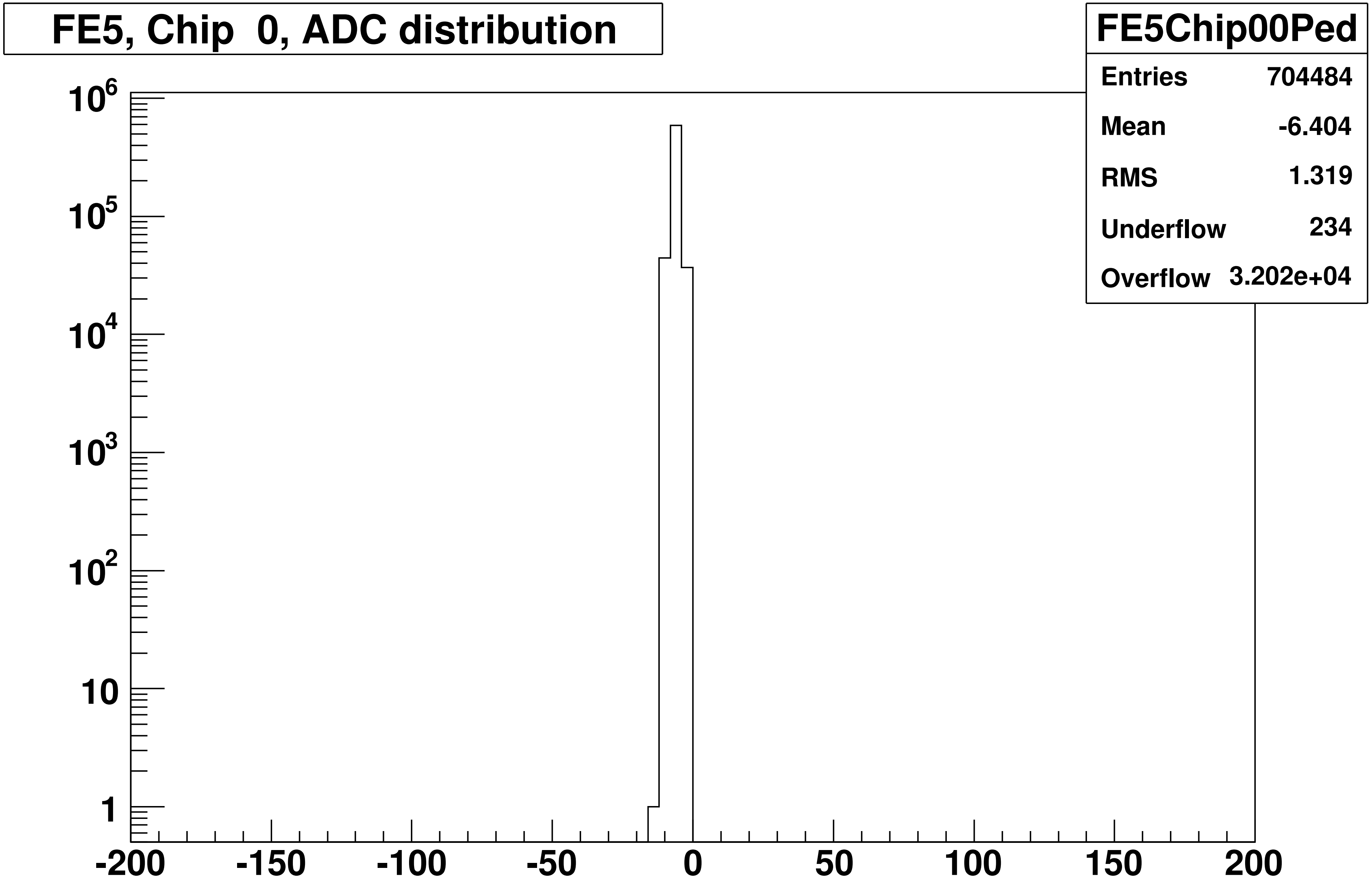
20000
25000


FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 704484
Mean -6.404
RMS 1.319
Underflow 234
Overflow 3.202e+04
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10^5
10^4
10^3
10^2
10
1
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-100
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0
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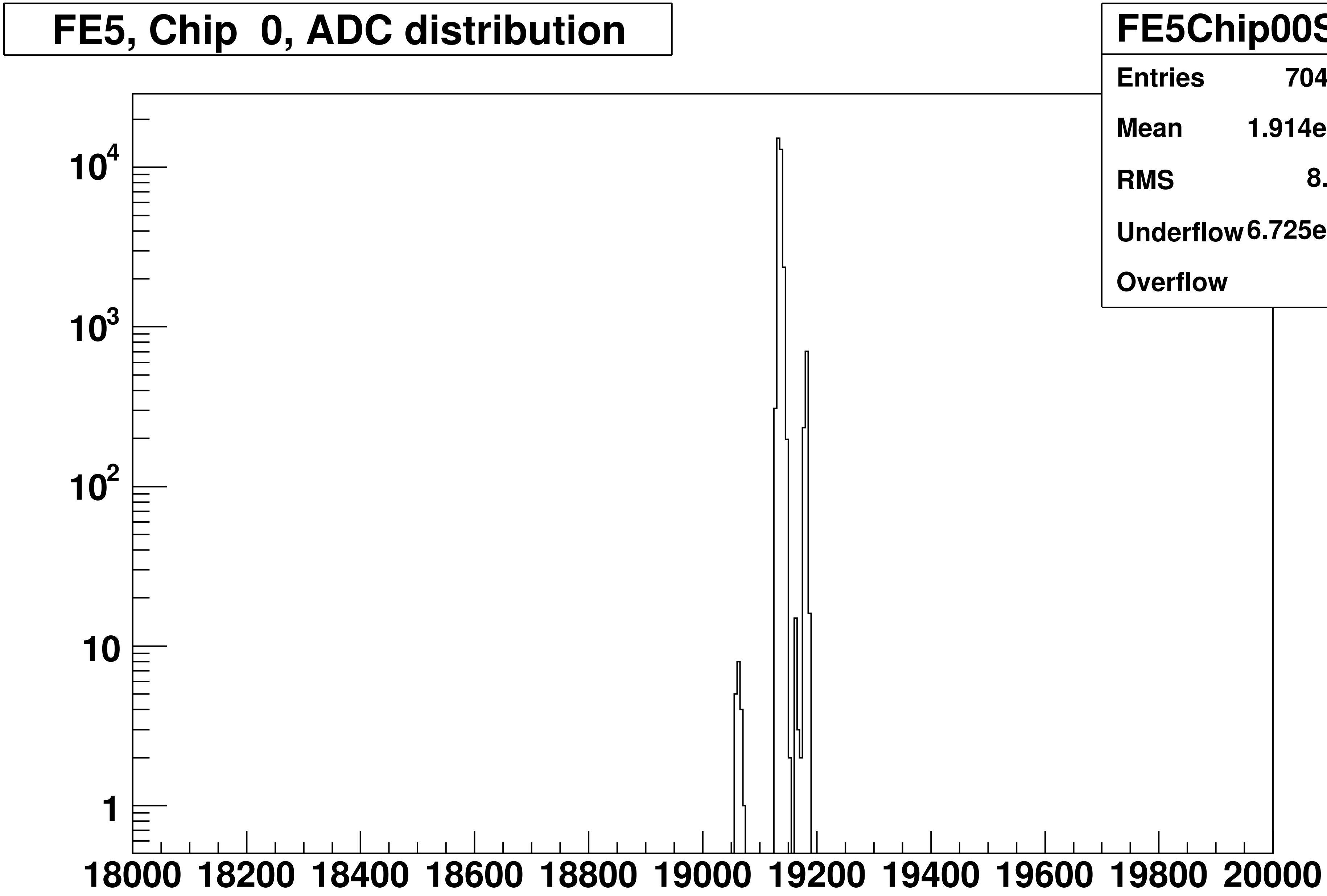
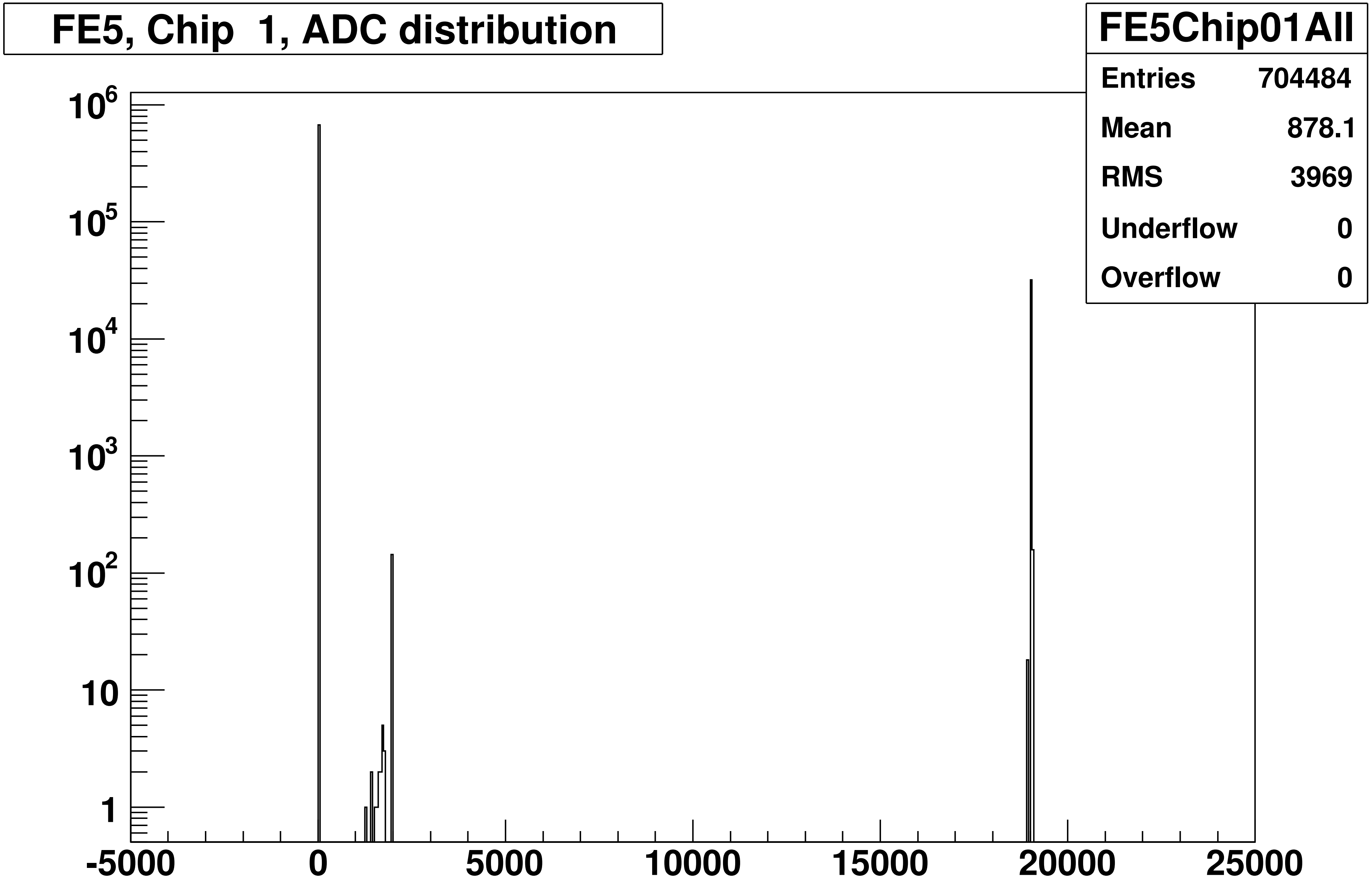
150
200


FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 704484
Mean 1.914e+04
RMS 8.622
Underflow 6.725e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 704484
Mean 878.1
RMS 3969
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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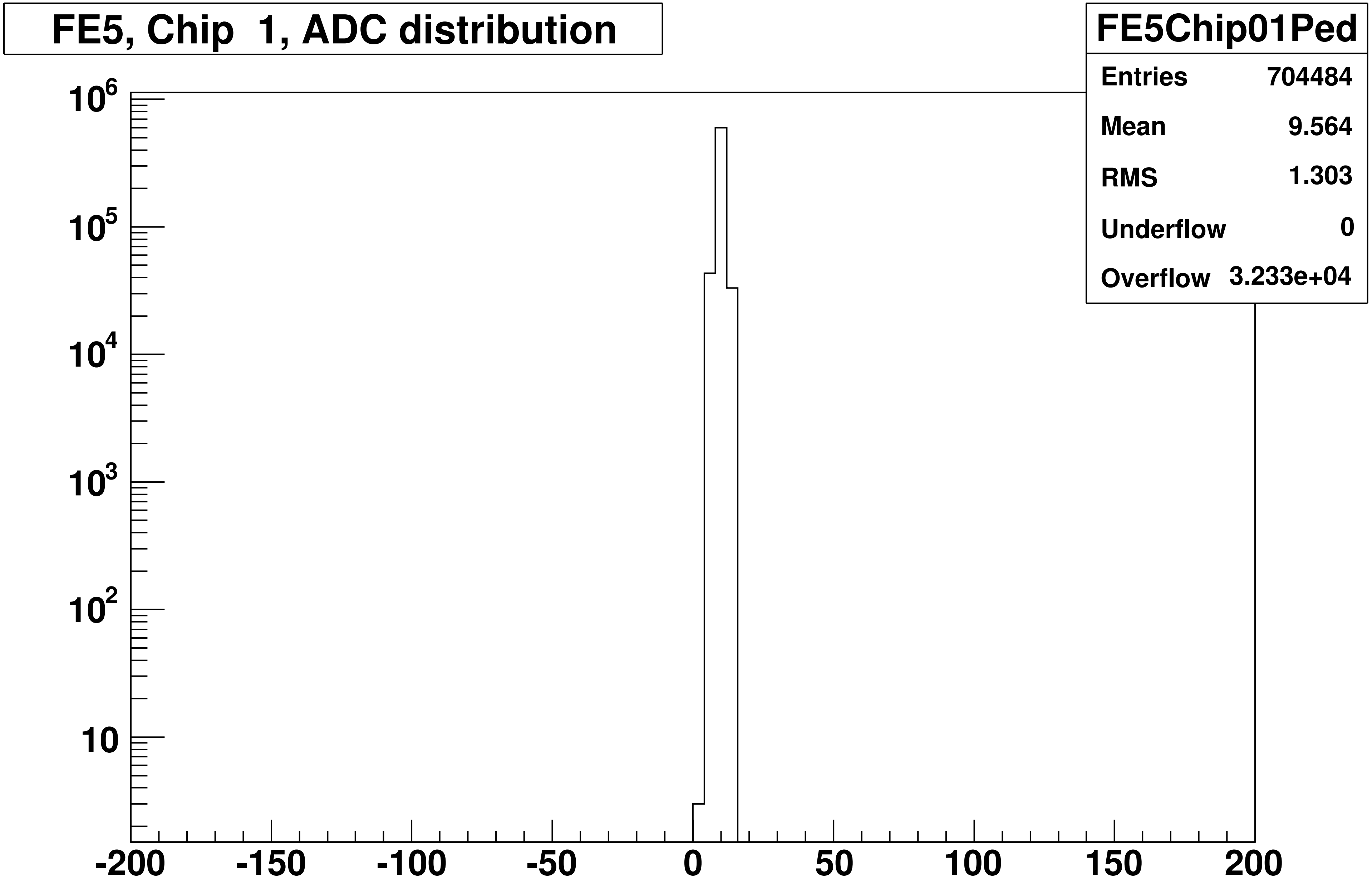
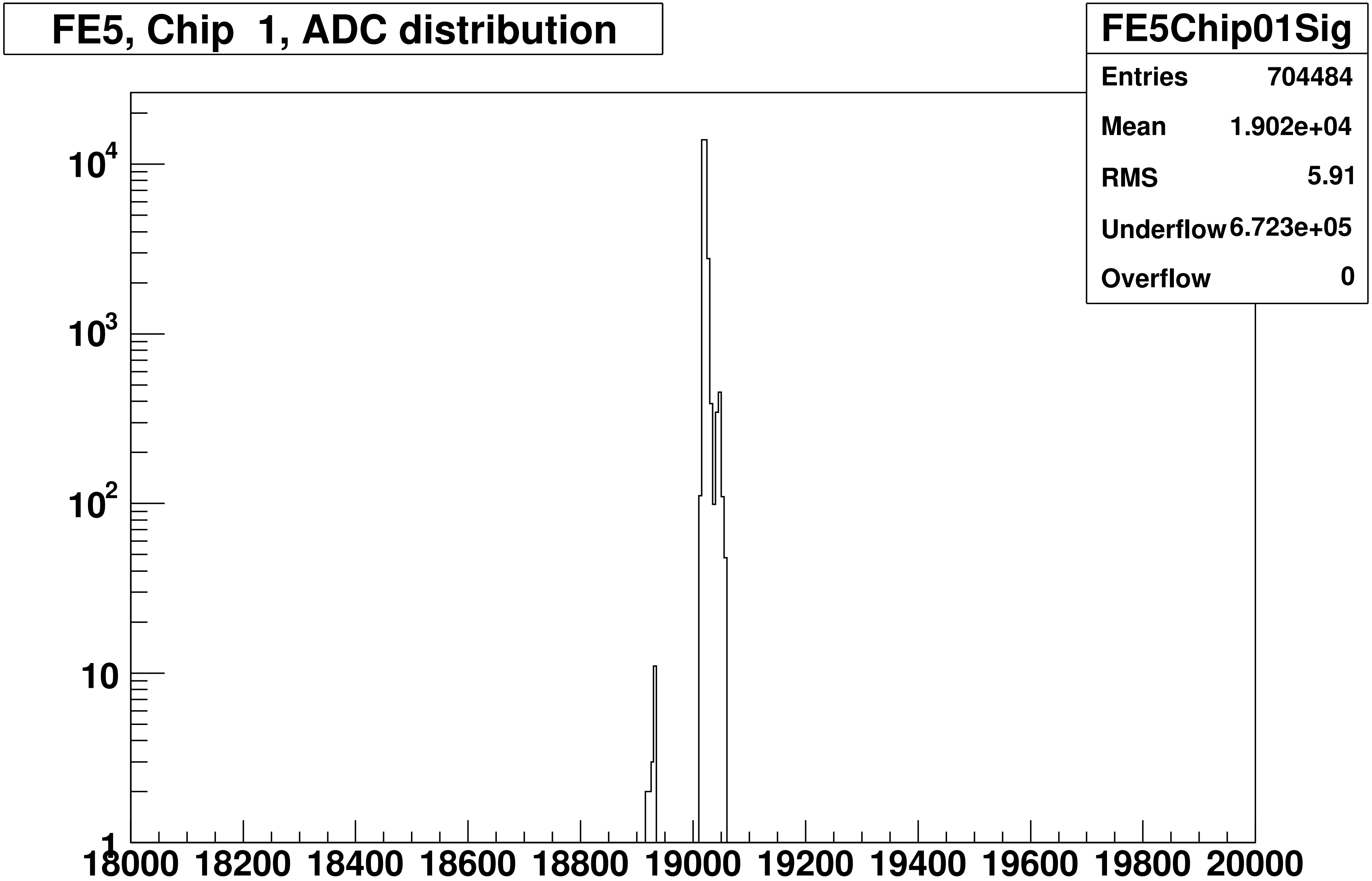
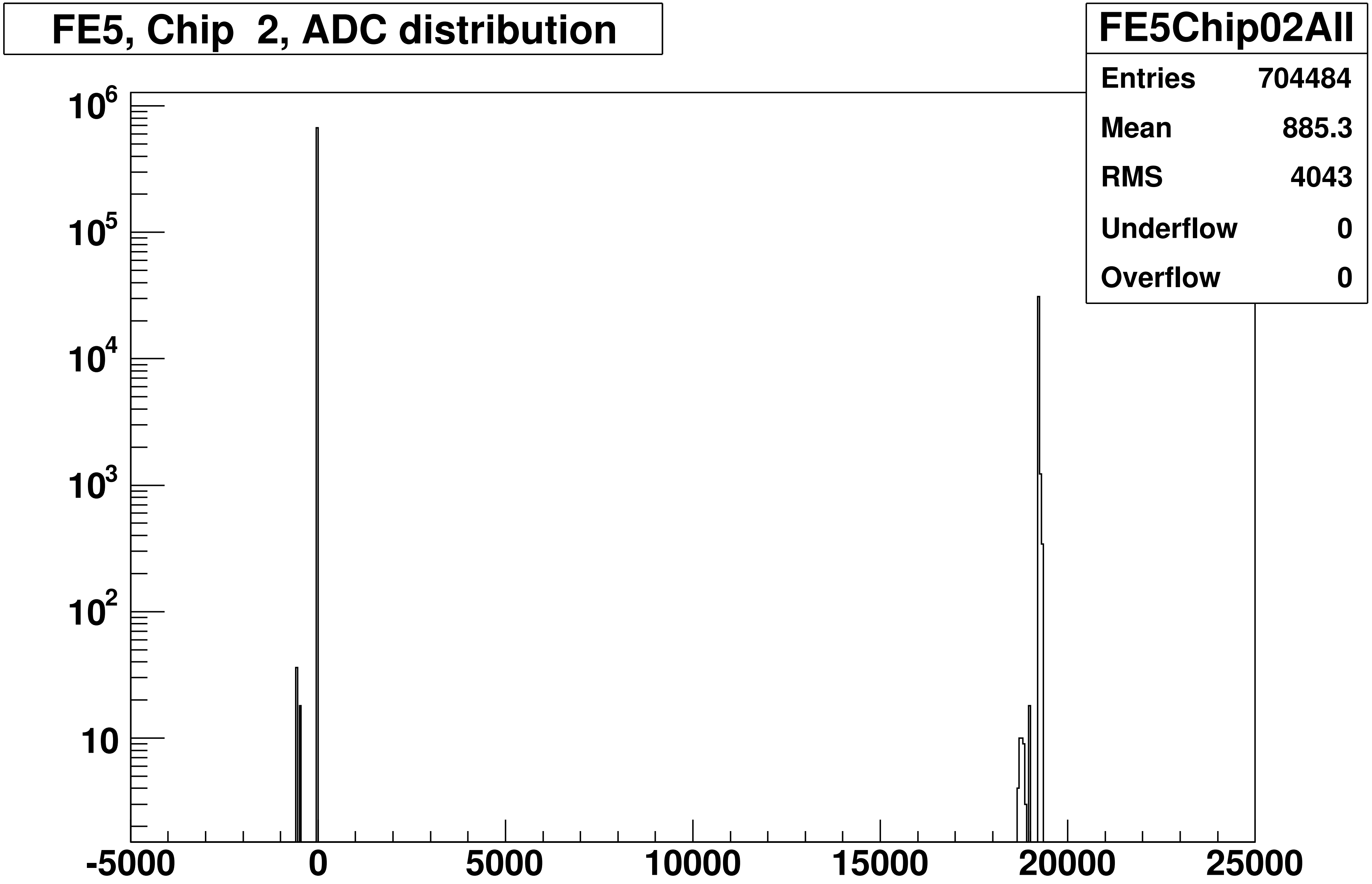


FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 704484
Mean 9.564
RMS 1.303
Underflow 0
Overflow 3.233e+04
10
10^2
10^3
10^4
10^5
10^6
-200
-150
-100
-50
0
50
100
150
200



FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 704484
Mean 1.902e+04
RMS 5.91
Underflow 6.723e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 704484
Mean 885.3
RMS 4043
Underflow 0
Overflow 0
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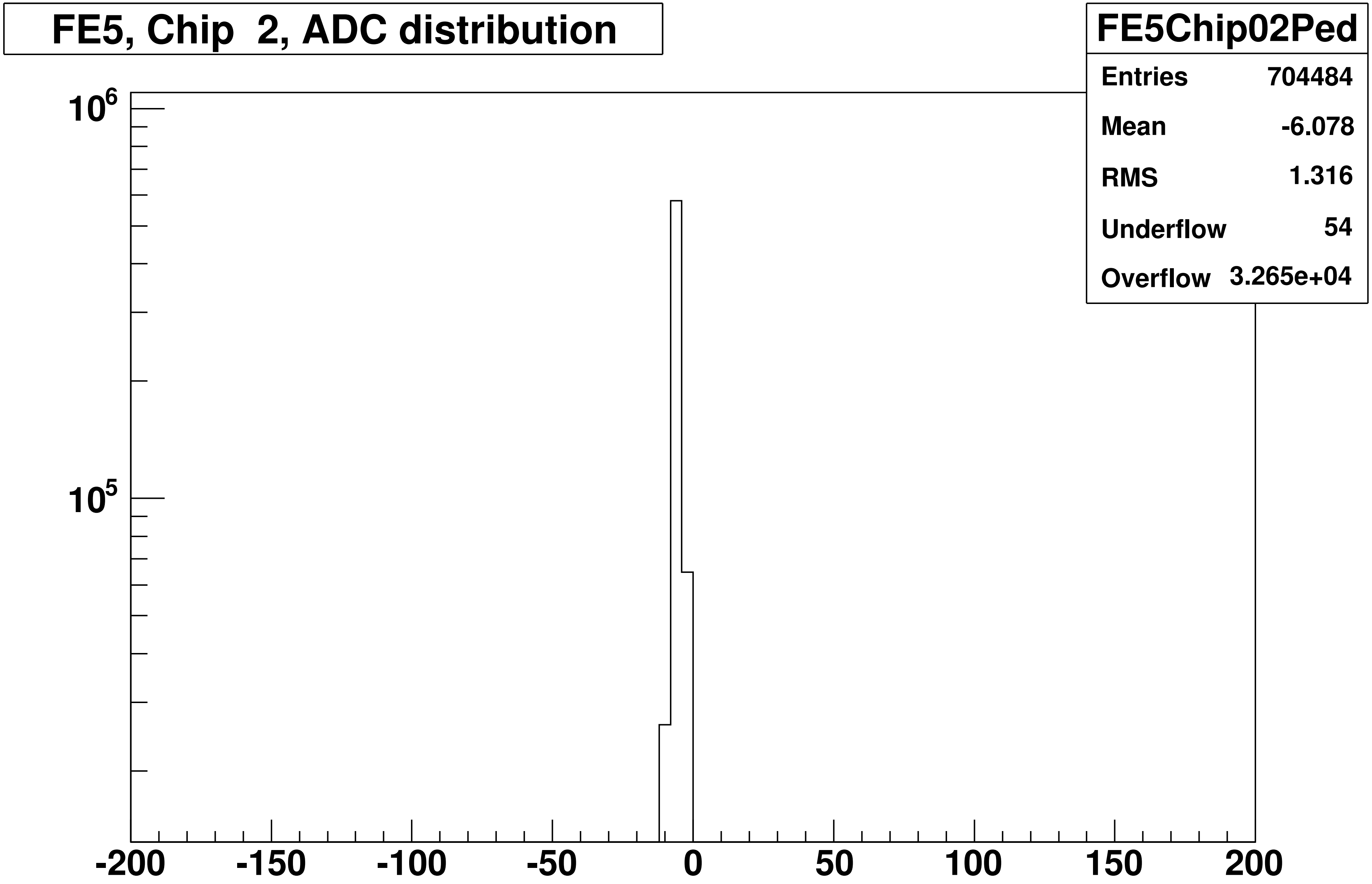
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 704484
Mean -6.078
RMS 1.316
Underflow 54
Overflow 3.265e+04
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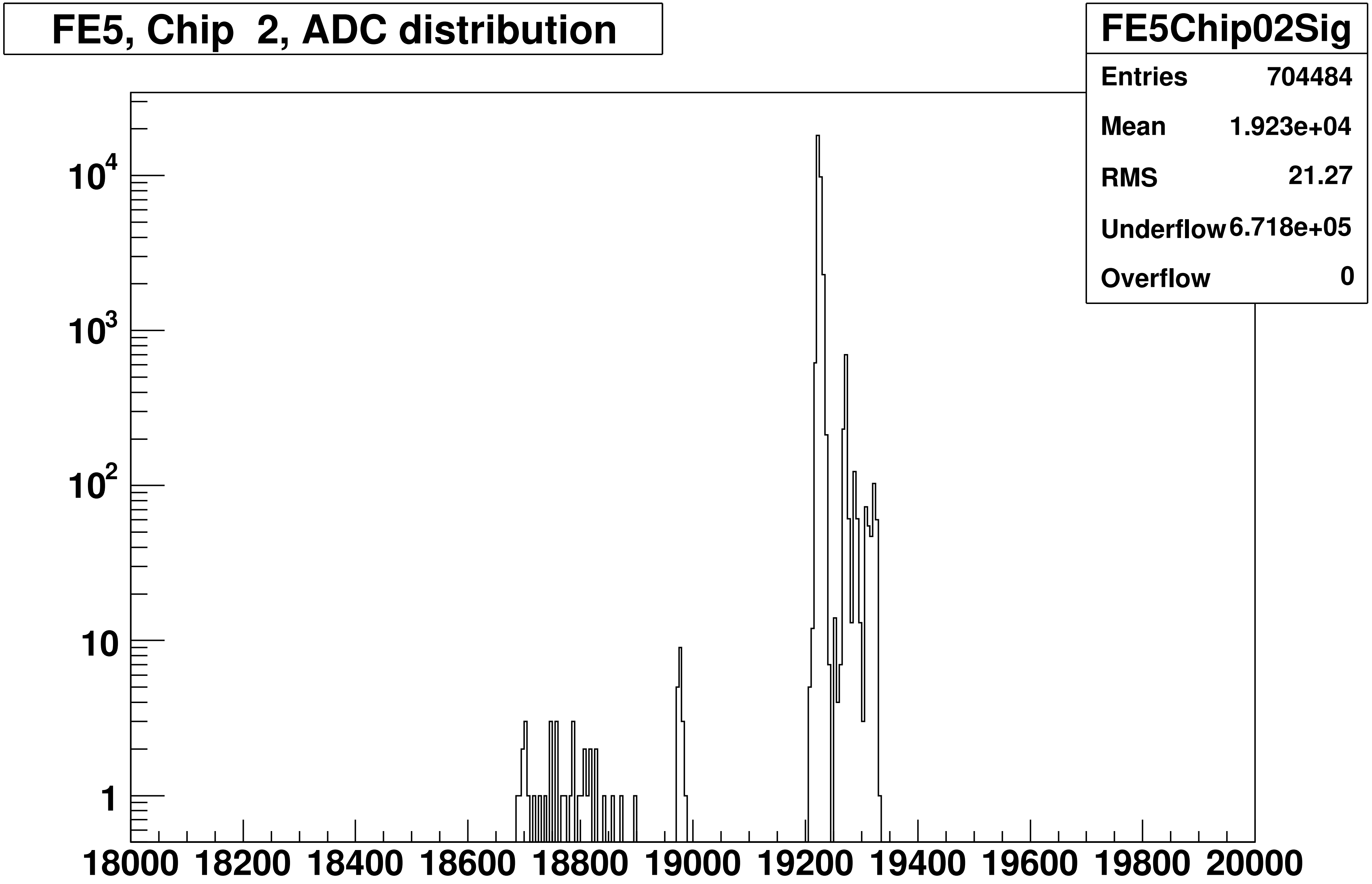
10^5
-200
-150
-100
-50
0
50
100
150
200



FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 704484
Mean 1.923e+04
RMS 21.27
Underflow 6.718e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
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19000
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19400
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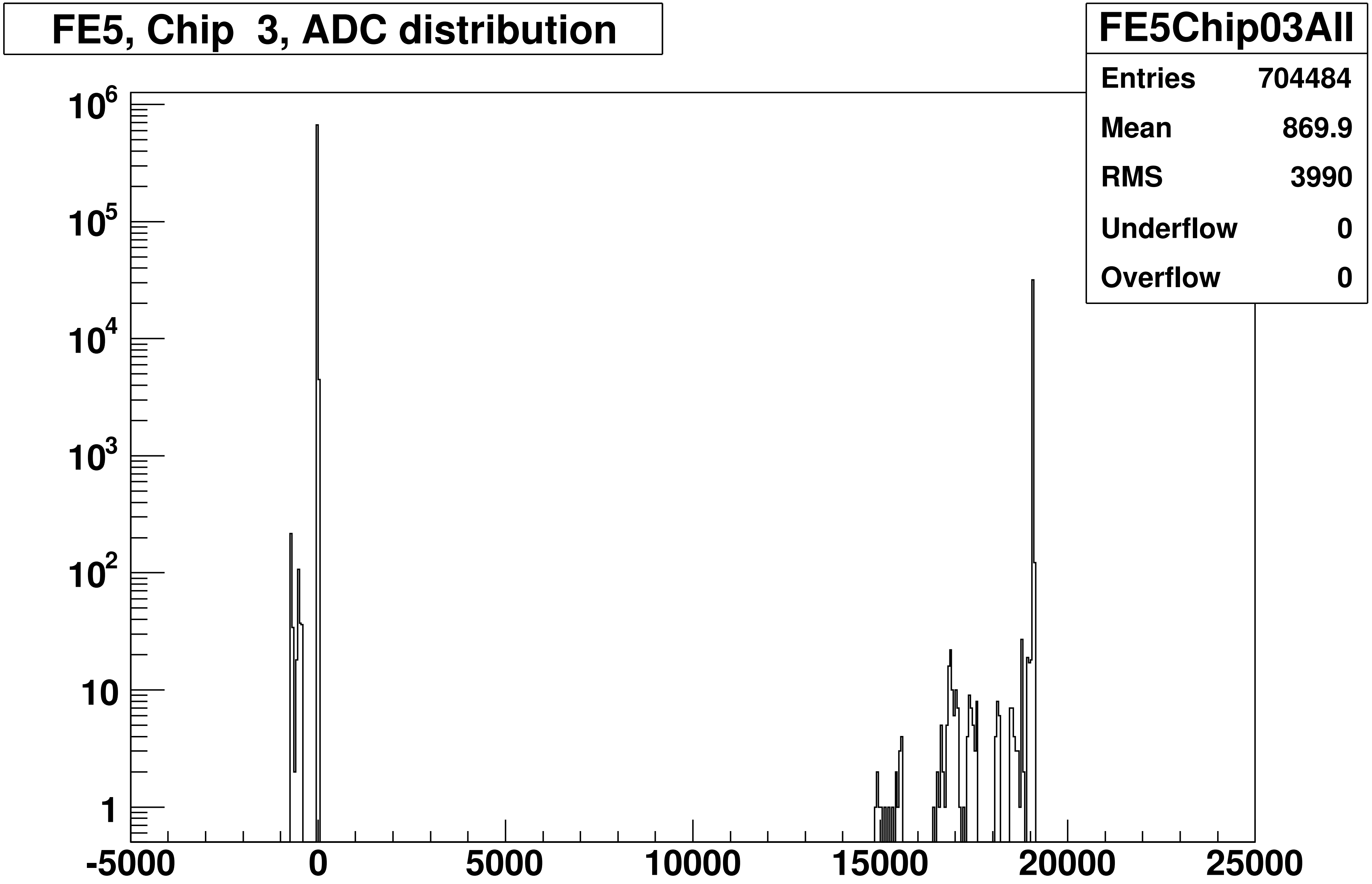
20000


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 704484
Mean 869.9
RMS 3990
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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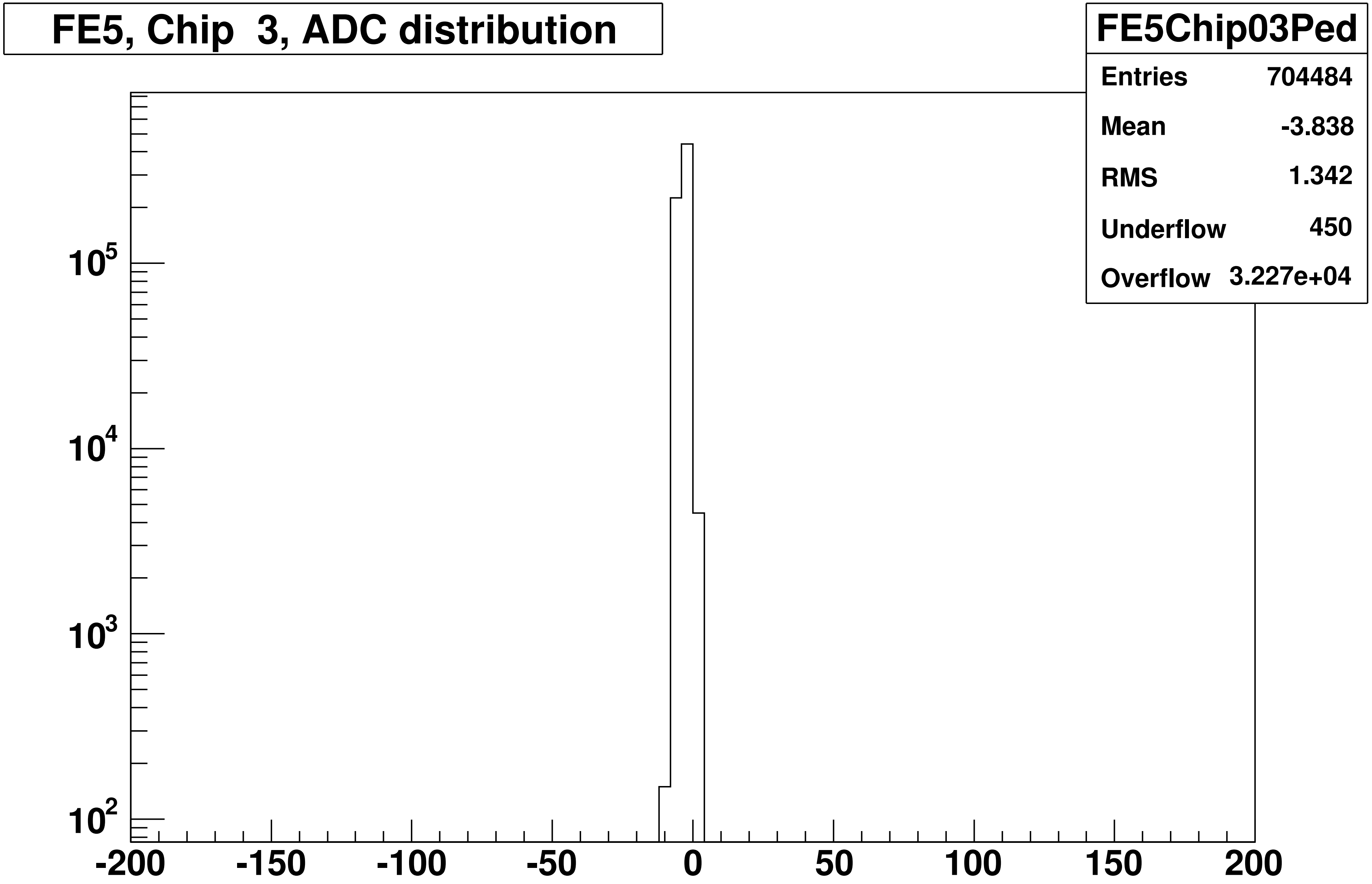
FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 704484
Mean -3.838
RMS 1.342
Underflow 450
Overflow 3.227e+04
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10^4
10^3
10^2
-200
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-100
-50
0
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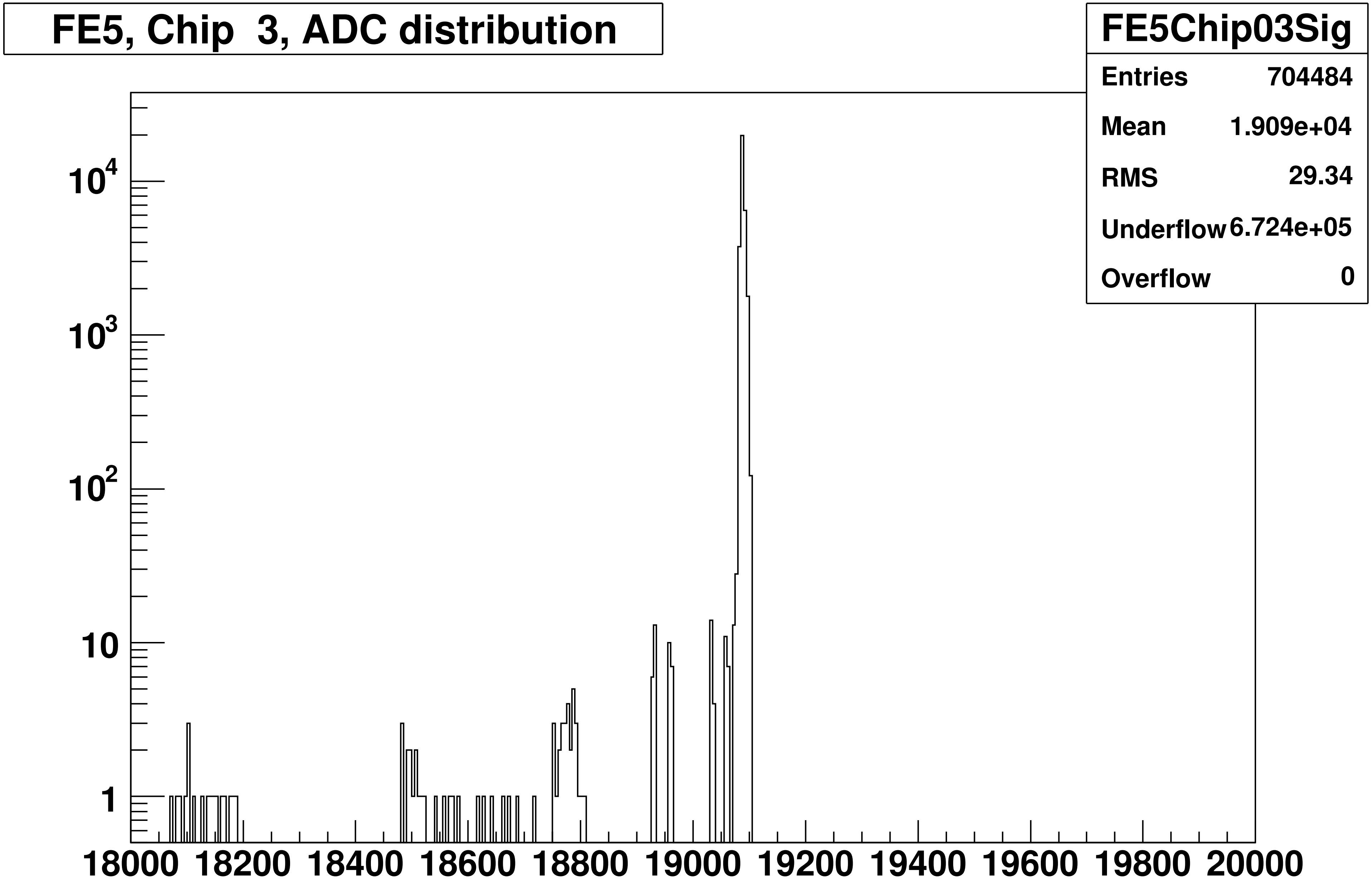
100
150
200



FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 704484
Mean 1.909e+04
RMS 29.34
Underflow 6.724e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
18400
18600
18800
19000
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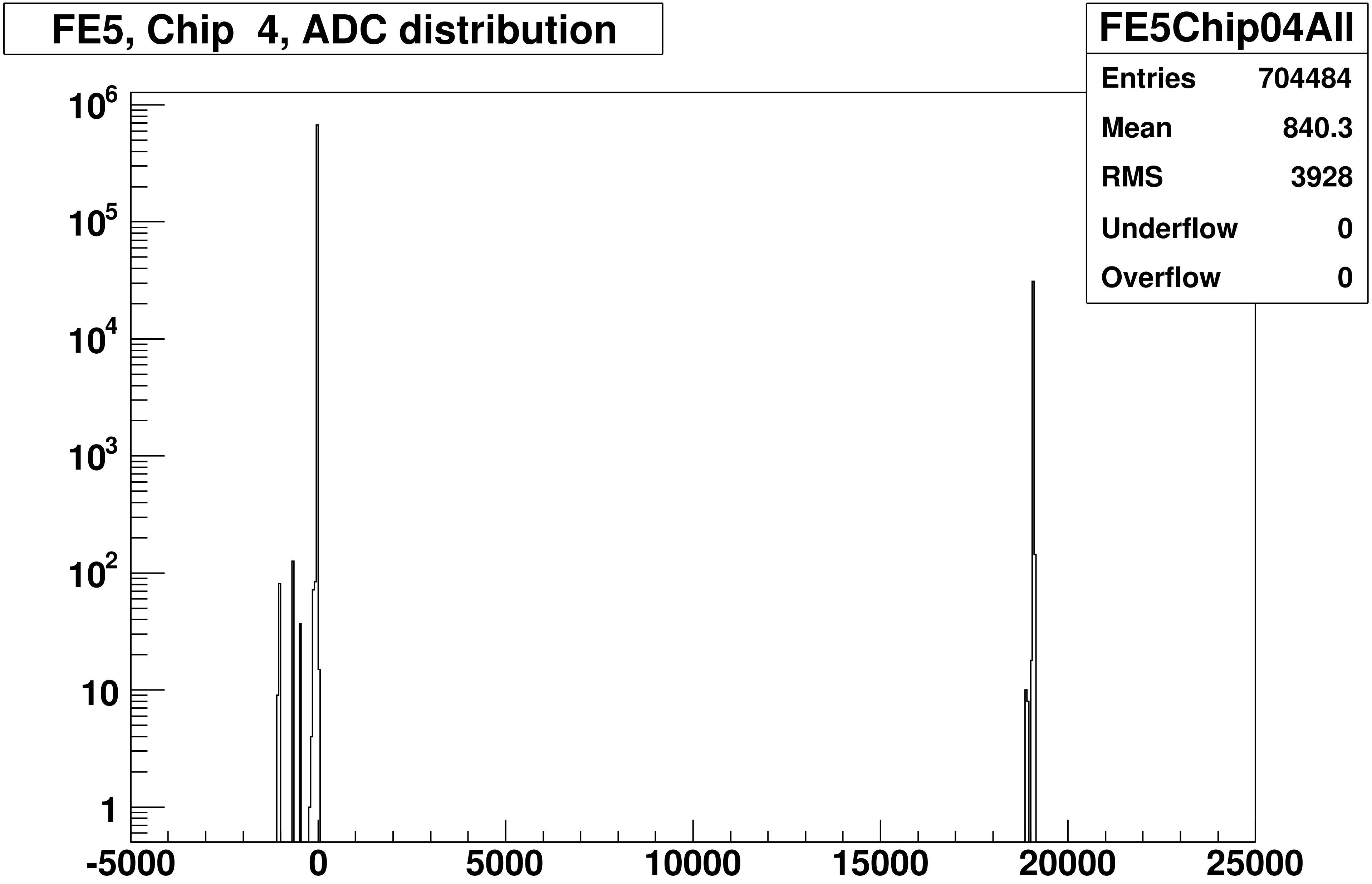
20000



FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 704484
Mean 840.3
RMS 3928
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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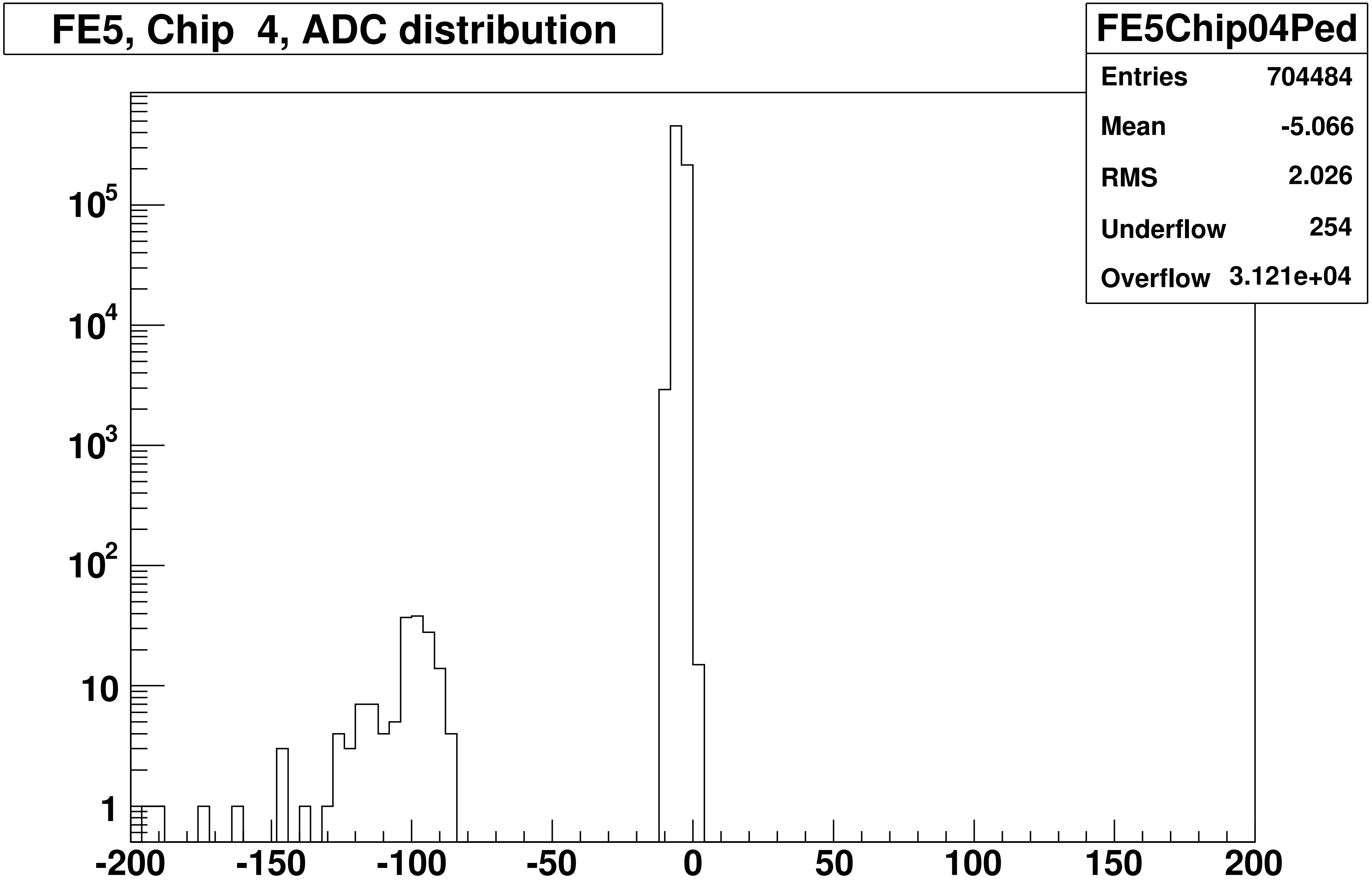
FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 704484
Mean -5.066
RMS 2.026
Underflow 254
Overflow 3.121e+04
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10^4
10^3
10^2
10
1
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-100
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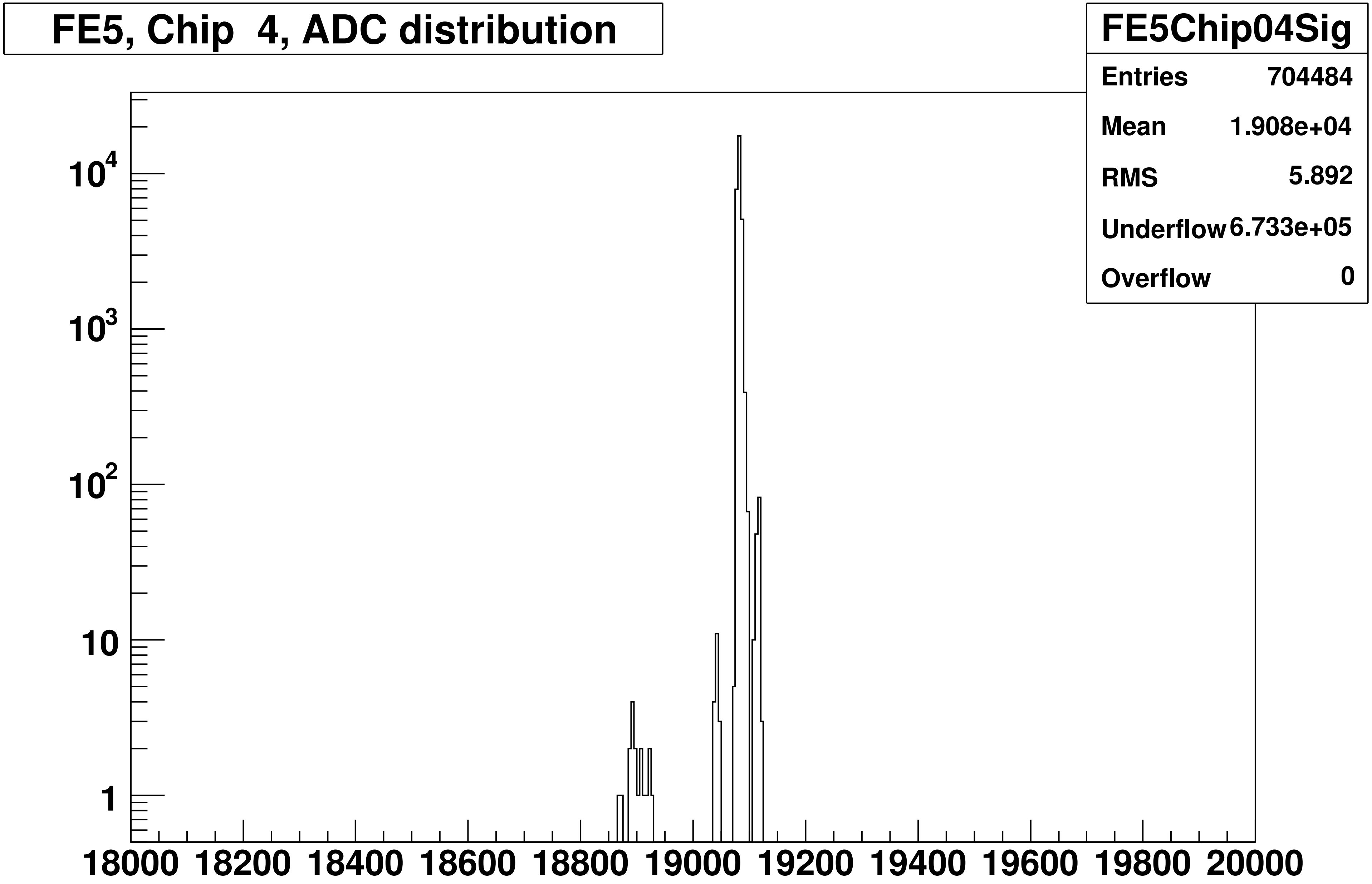
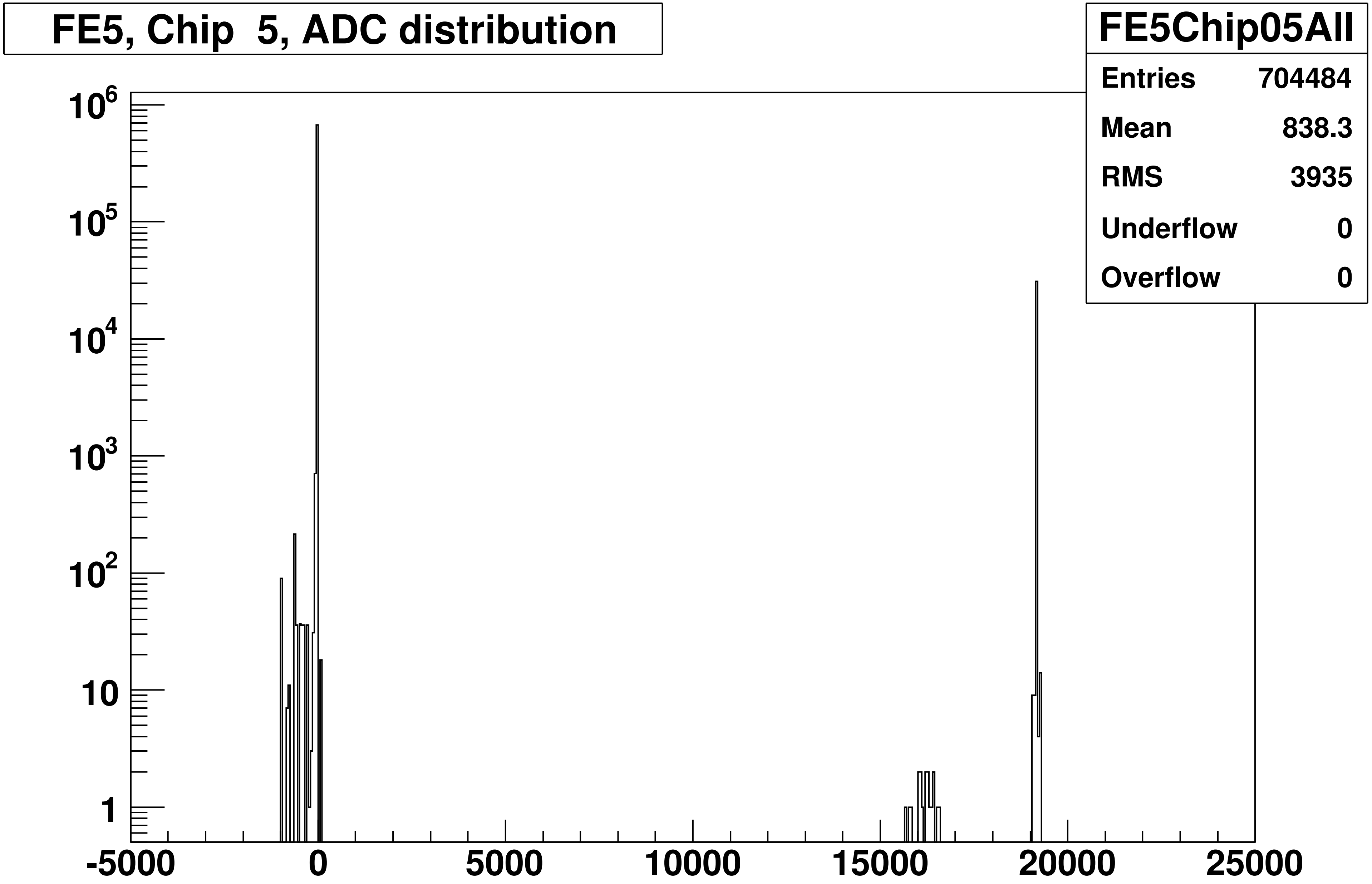
0
50
100
150
200


FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 704484
Mean 1.908e+04
RMS 5.892
Underflow 6.733e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 704484
Mean 838.3
RMS 3935
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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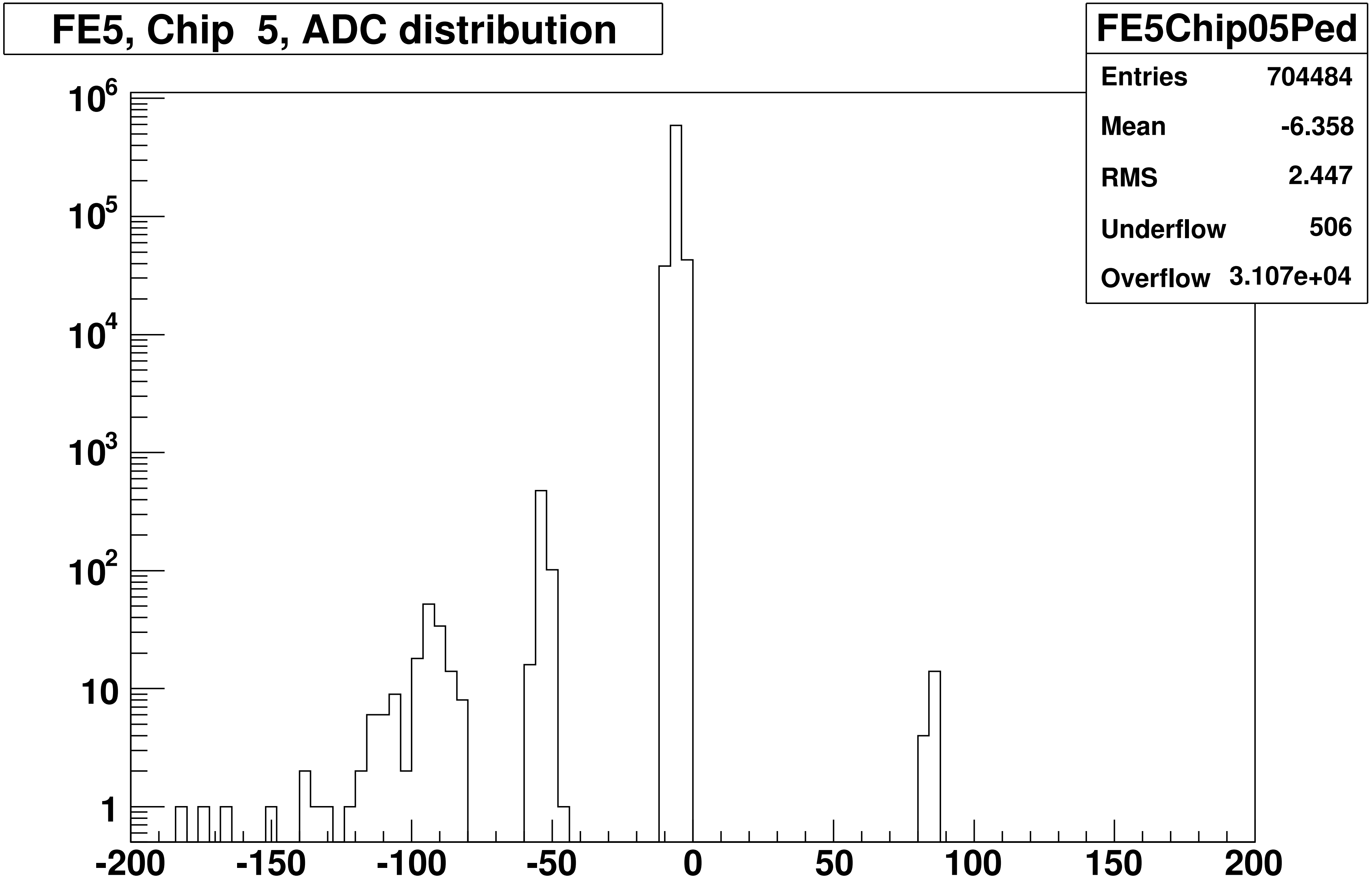

FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 704484
Mean -6.358
RMS 2.447
Underflow 506
Overflow 3.107e+04
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10^5
10^4
10^3
10^2
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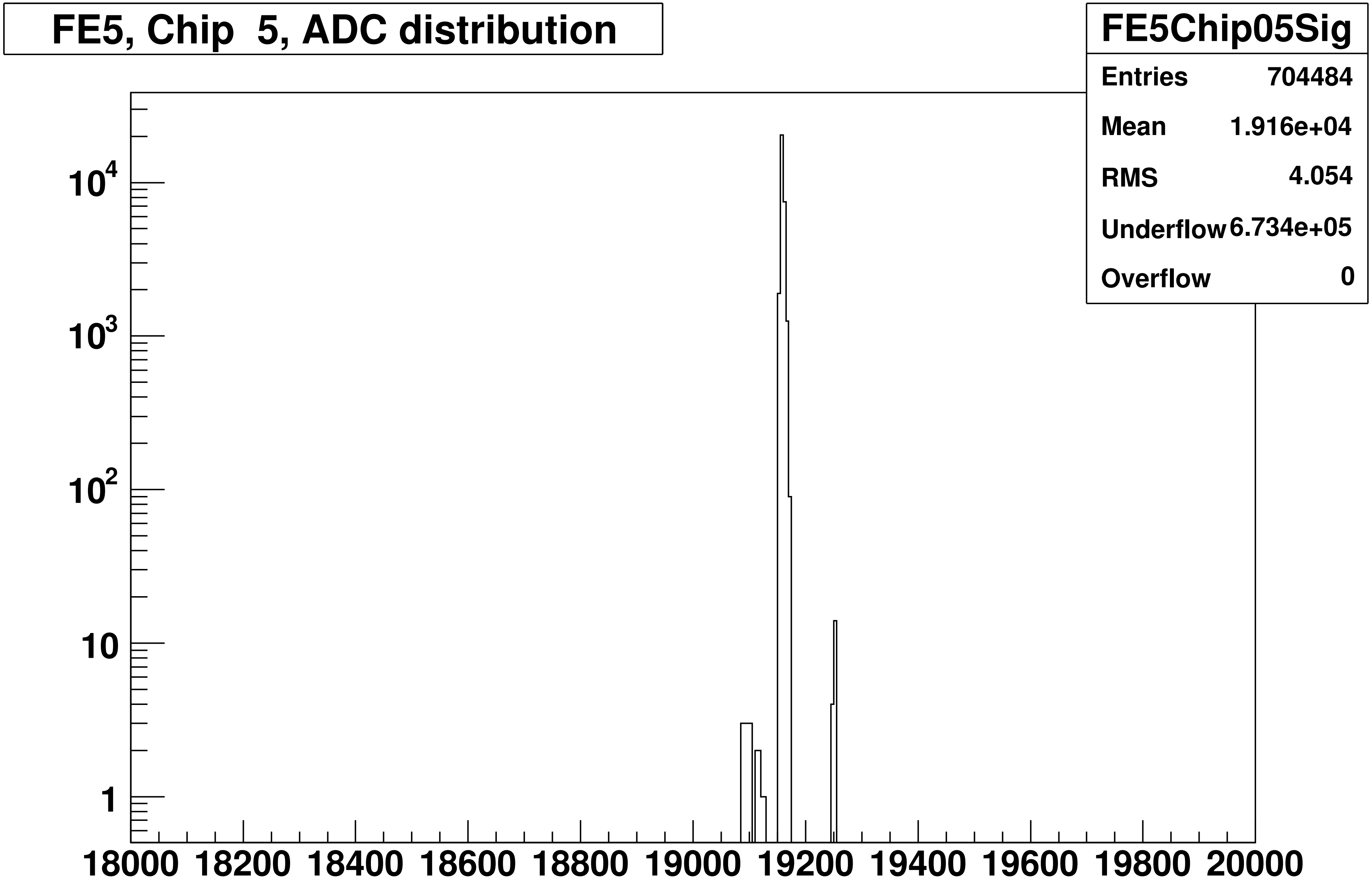
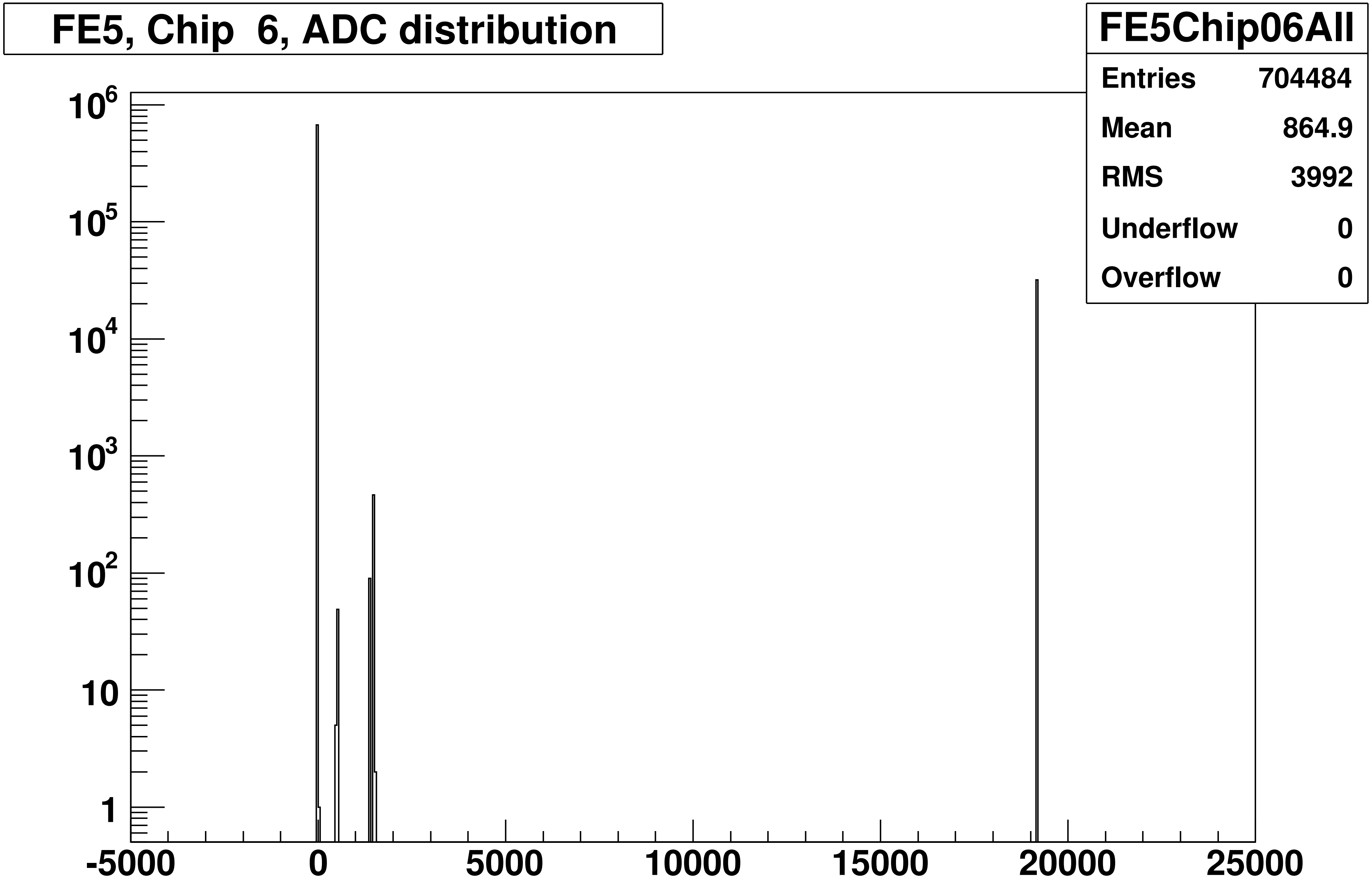
150
200


FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 704484
Mean 1.916e+04
RMS 4.054
Underflow 6.734e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 704484
Mean 864.9
RMS 3992
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
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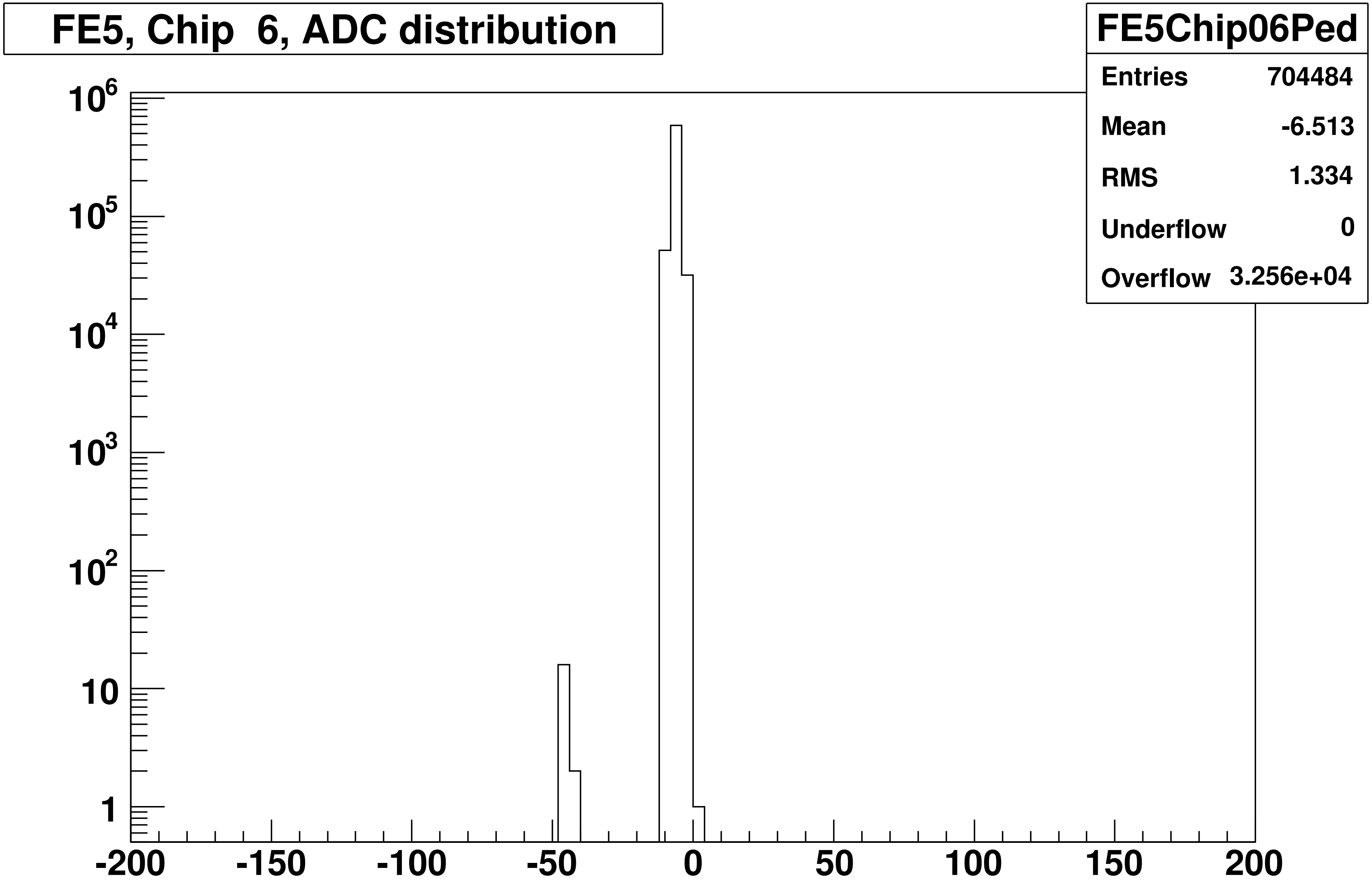
25000


FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 704484
Mean -6.513
RMS 1.334
Underflow 0
Overflow 3.256e+04
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10^5
10^4
10^3
10^2
10
1
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150
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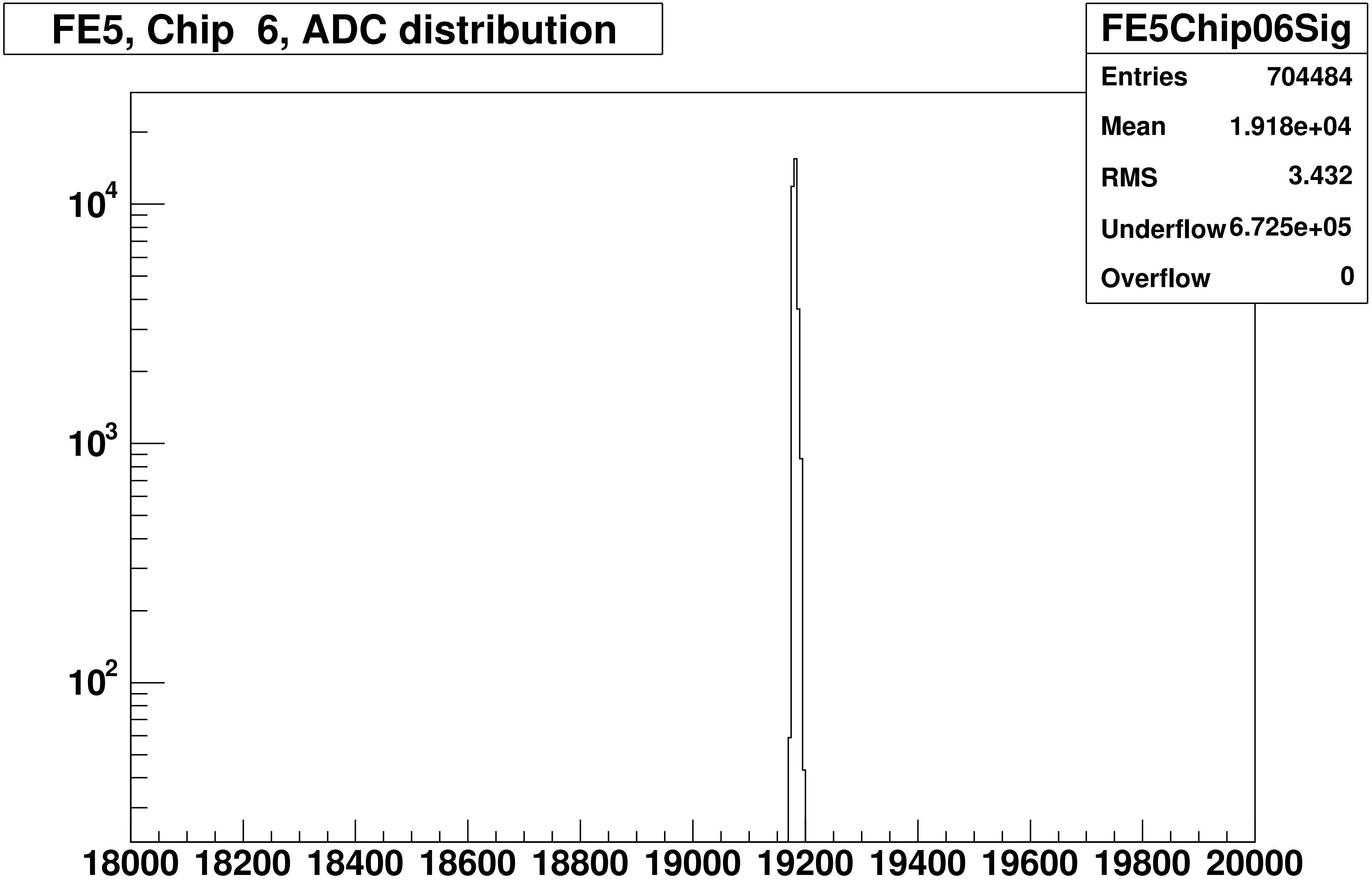

FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 704484
Mean 1.918e+04
RMS 3.432
Underflow 6.725e+05
Overflow 0
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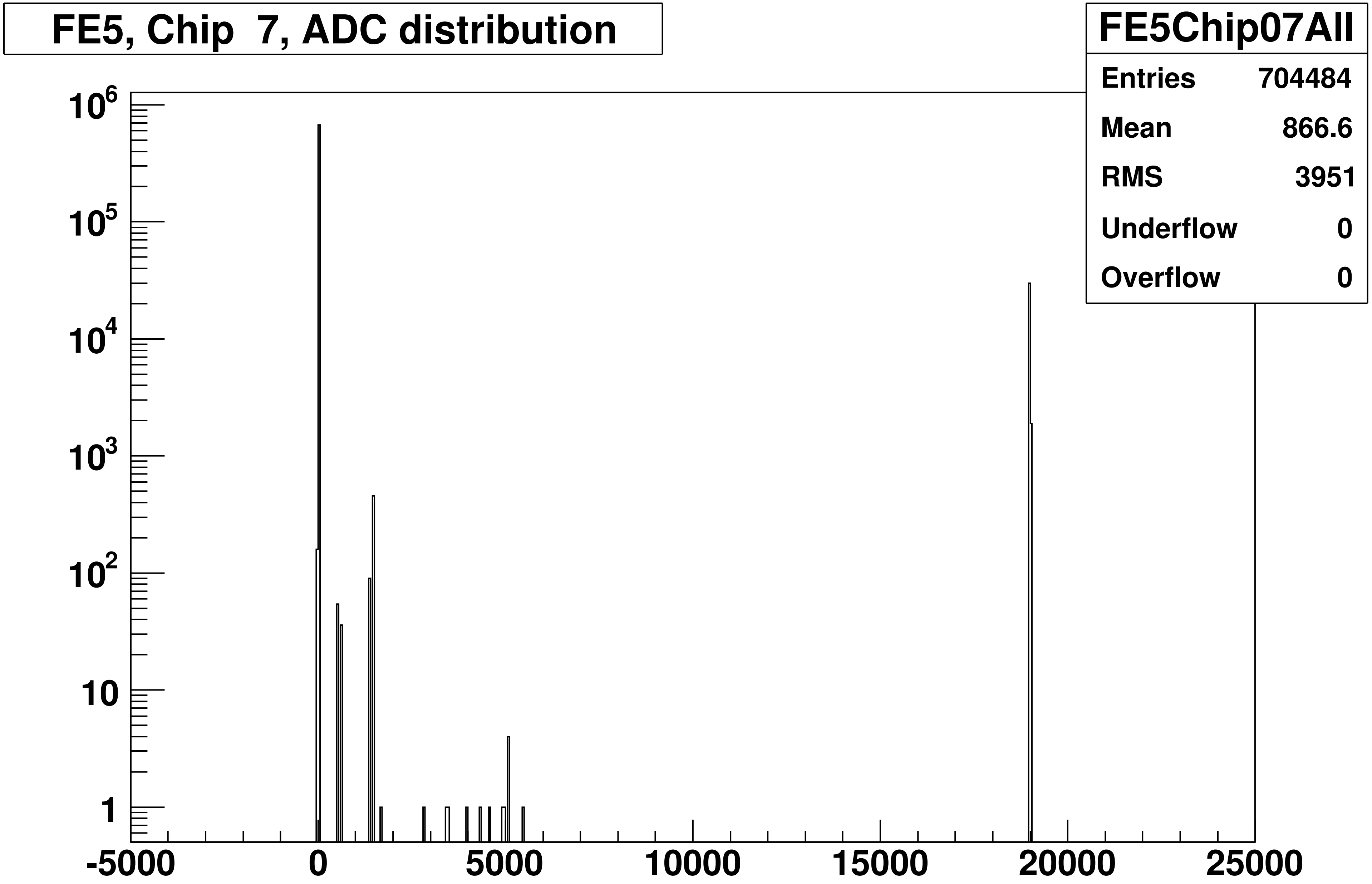
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 704484
Mean 866.6
RMS 3951
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
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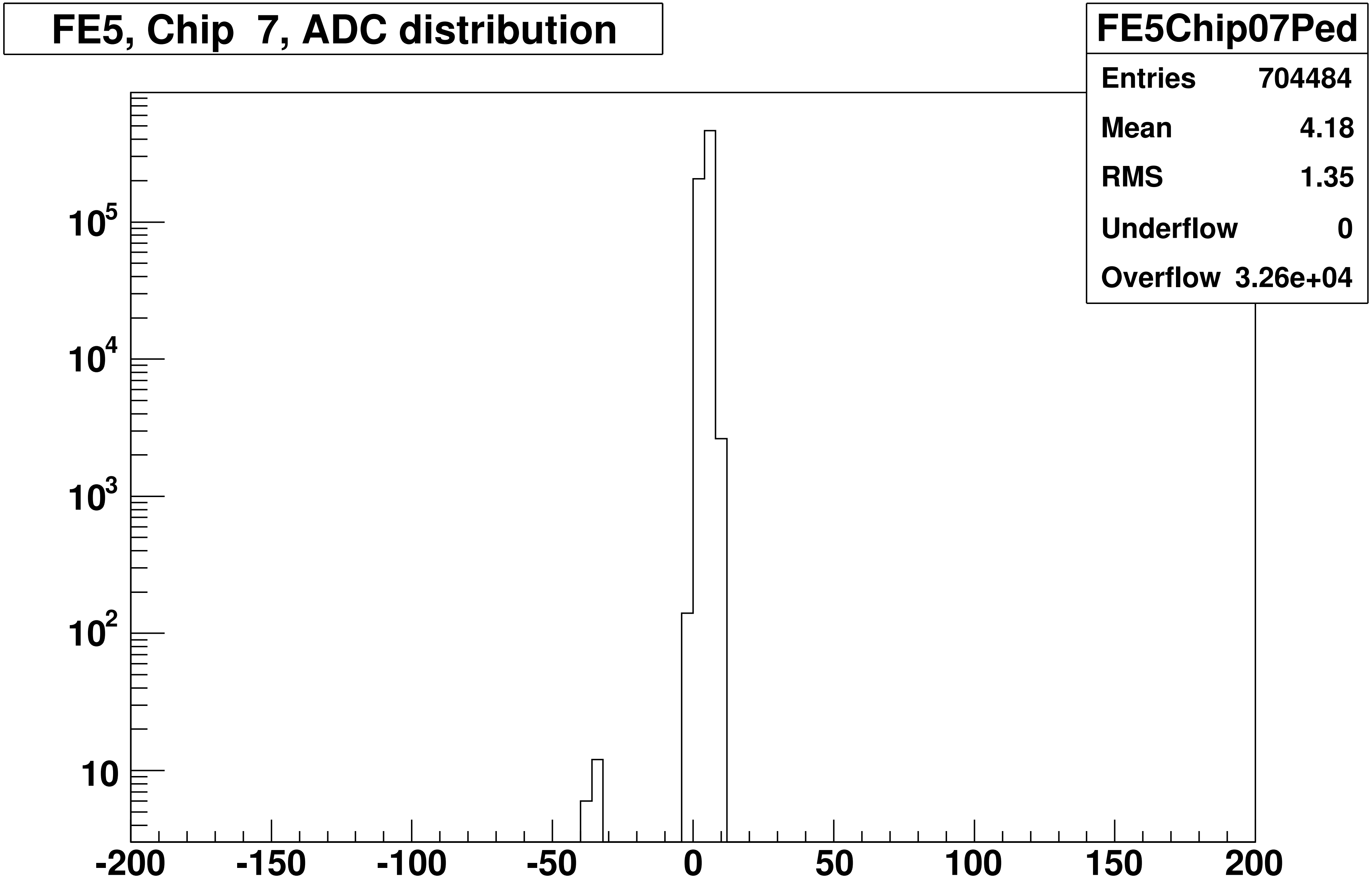
10000
15000
20000
25000



FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 704484
Mean 4.18
RMS 1.35
Underflow 0
Overflow 3.26e+04
10
10^2
10^3
10^4
10^5
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-150
-100
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0
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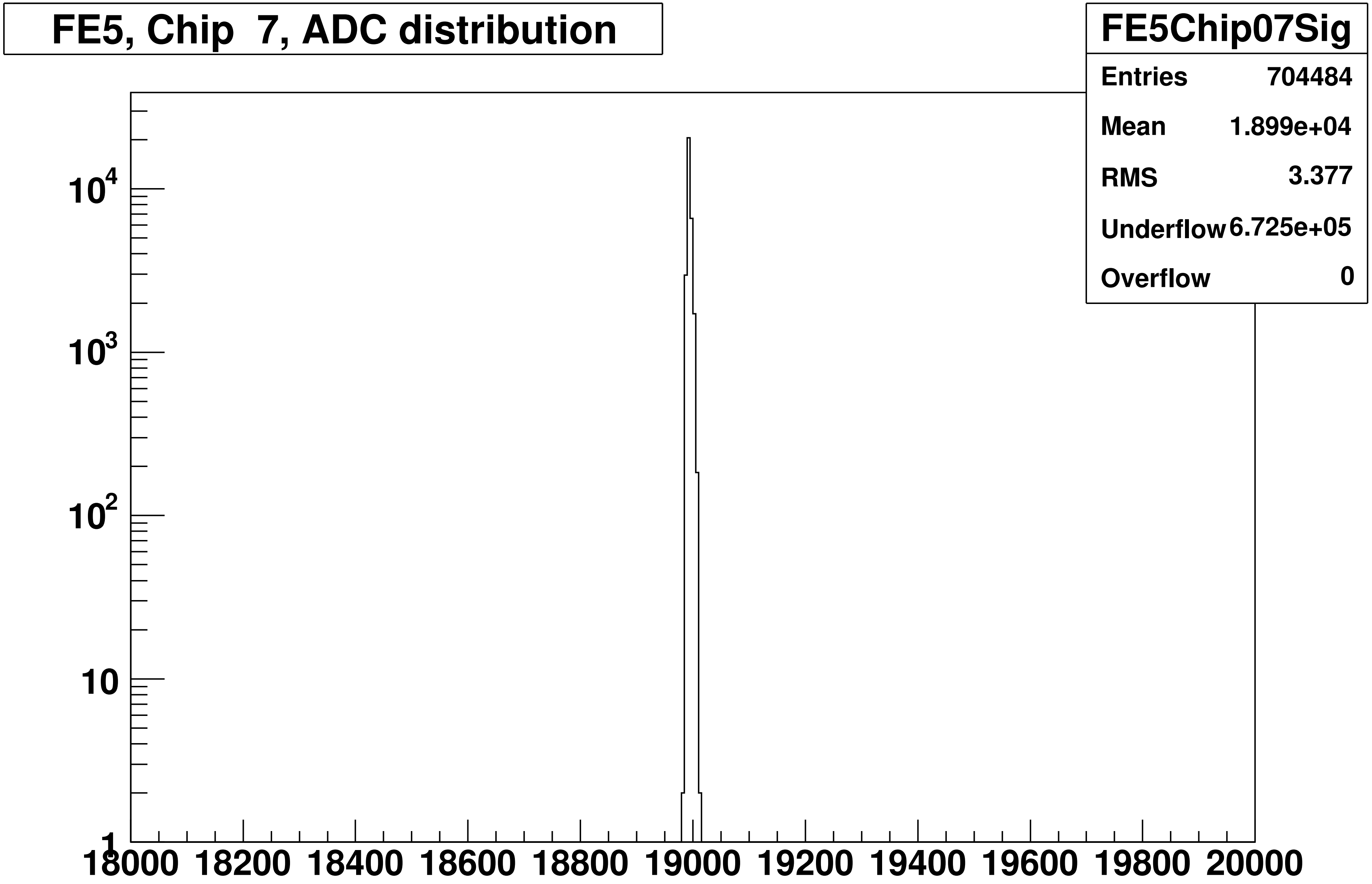
100
150
200


FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 704484
Mean 1.899e+04
RMS 3.377
Underflow 6.725e+05
Overflow 0
1
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10^2
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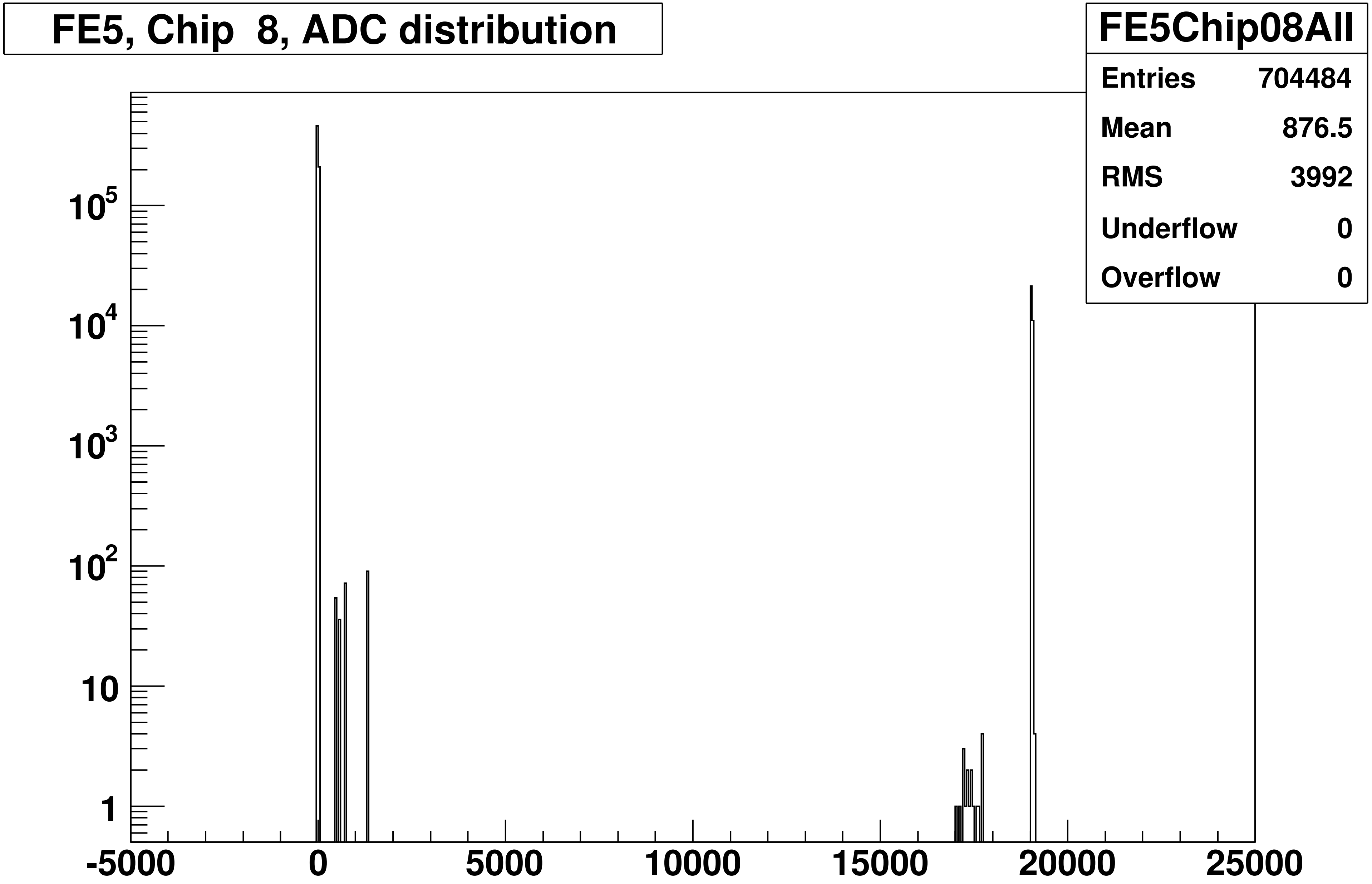
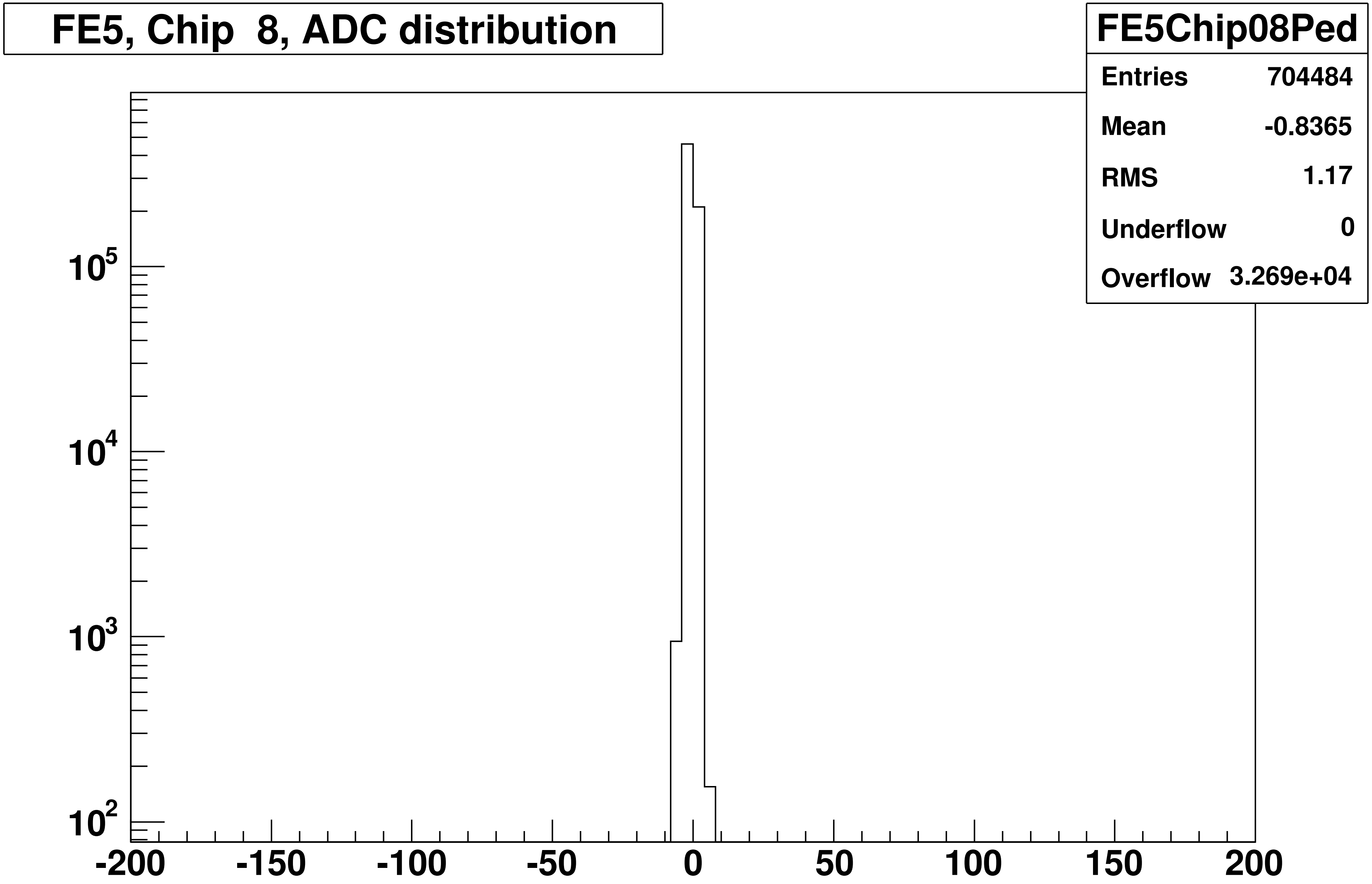


FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 704484
Mean 876.5
RMS 3992
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 704484
Mean -0.8365
RMS 1.17
Underflow 0
Overflow 3.269e+04
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10^4
10^3
10^2
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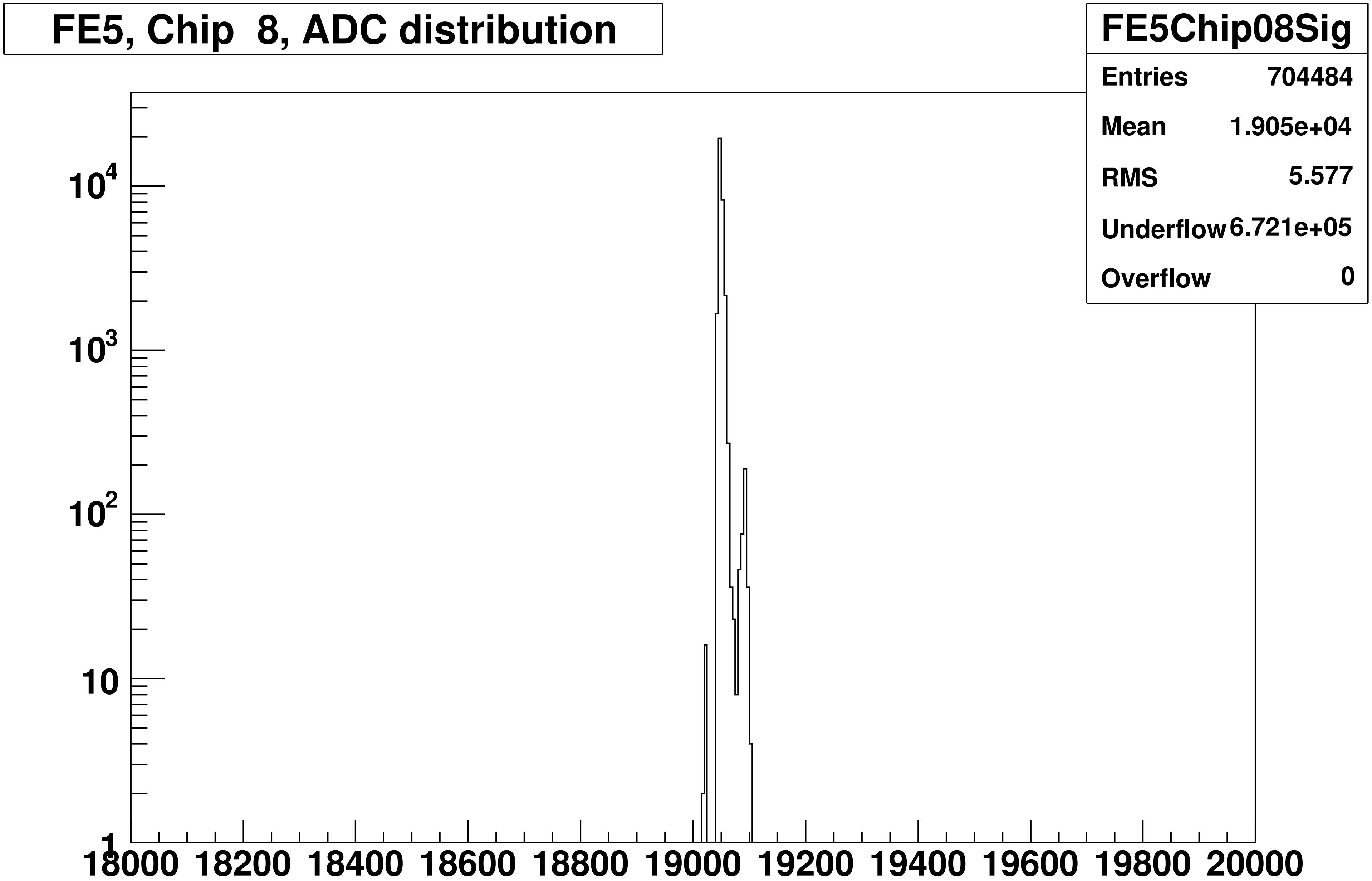
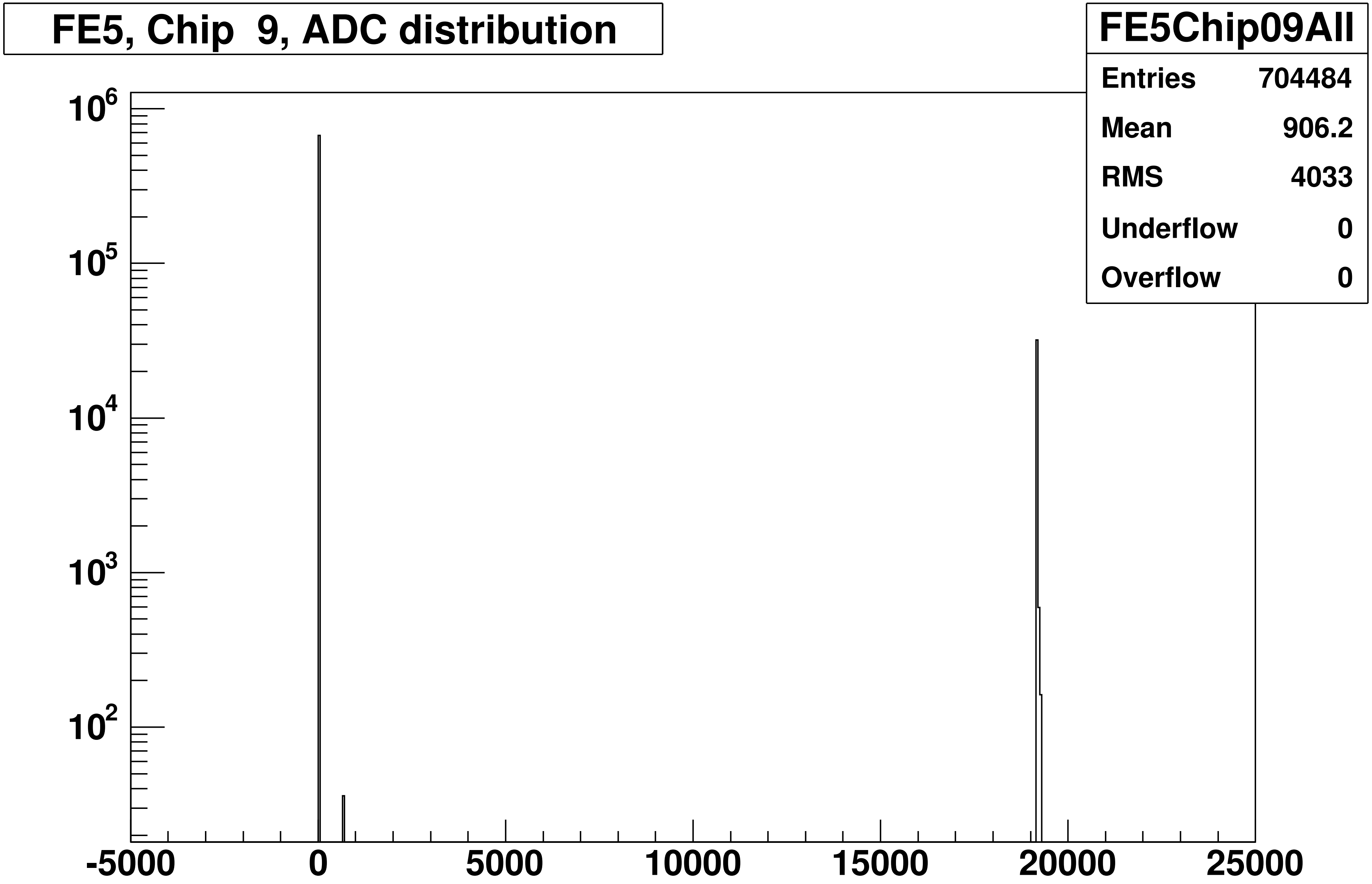
100
150
200


FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 704484
Mean 1.905e+04
RMS 5.577
Underflow 6.721e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 704484
Mean 906.2
RMS 4033
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
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0
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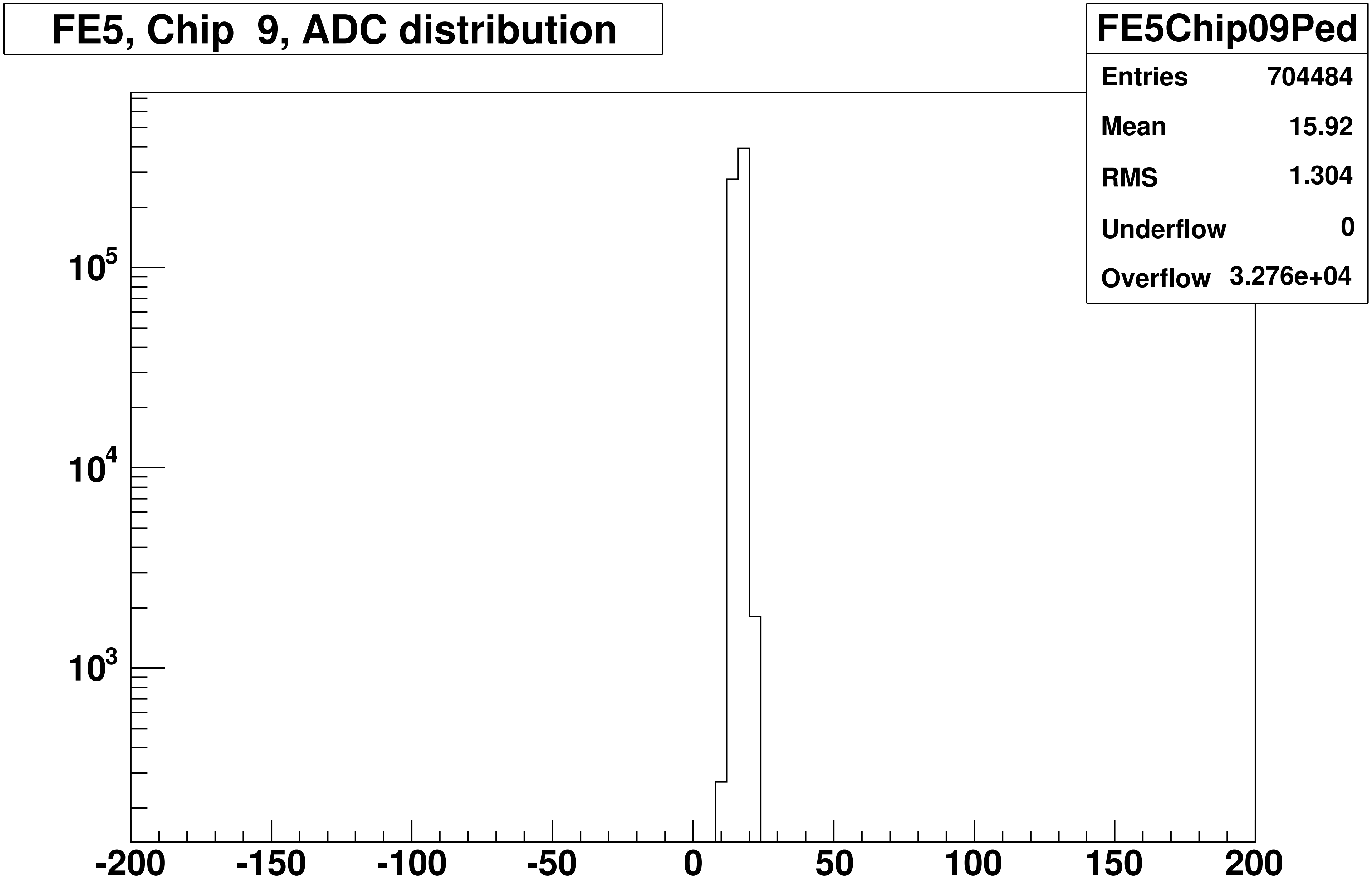
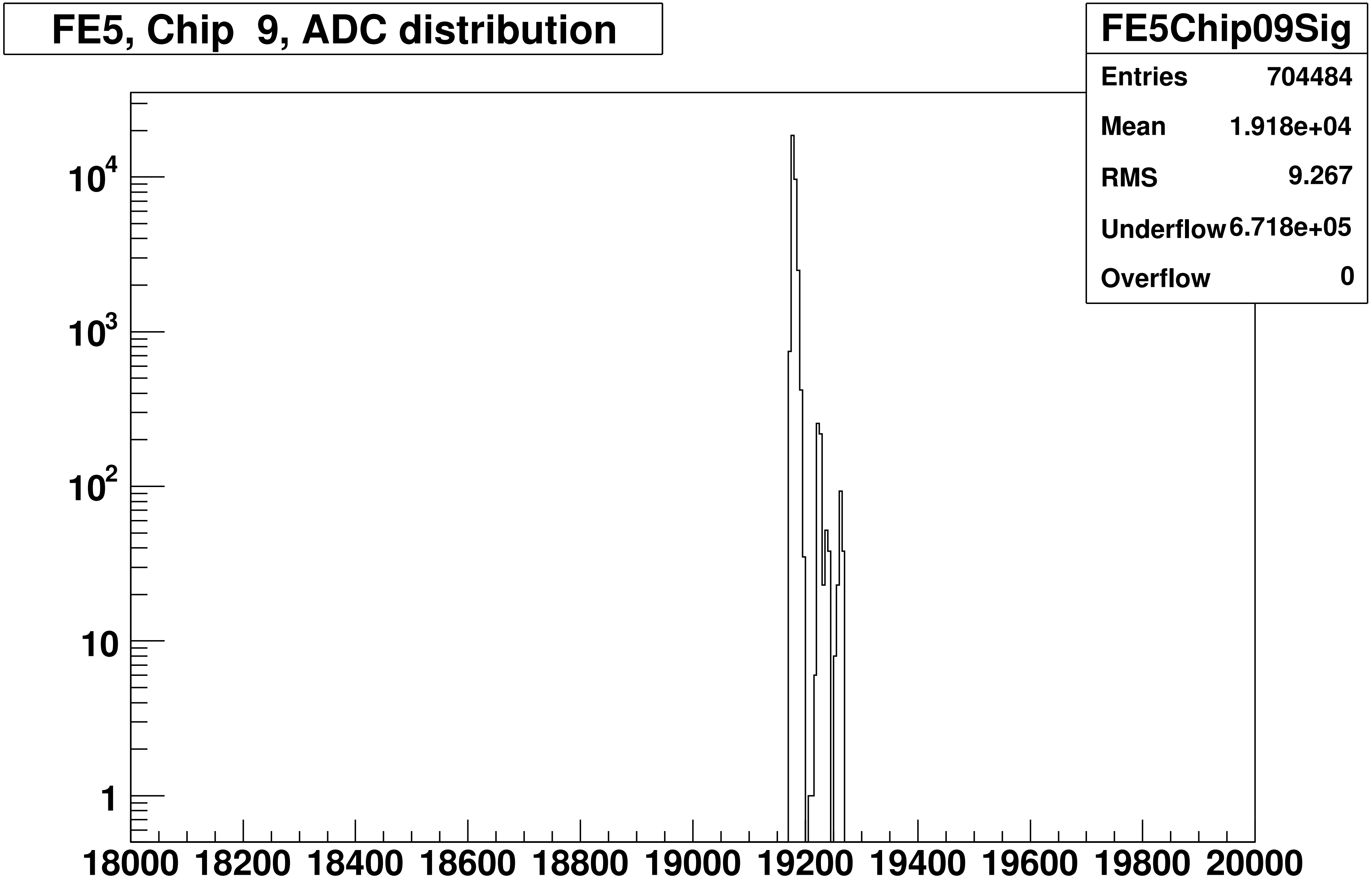
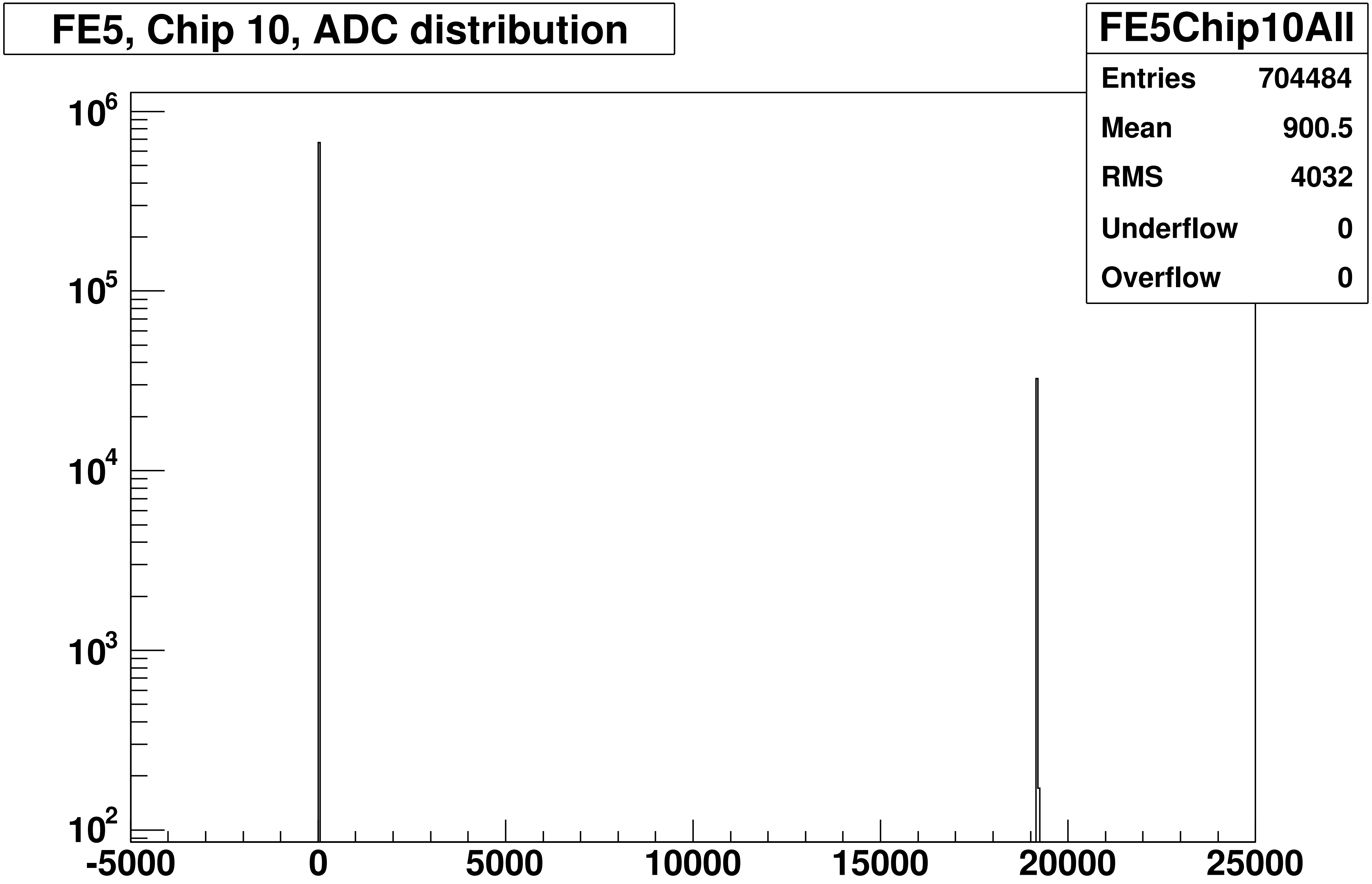
15000
20000
25000


FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 704484
Mean 15.92
RMS 1.304
Underflow 0
Overflow 3.276e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200



FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 704484
Mean 1.918e+04
RMS 9.267
Underflow 6.718e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 704484
Mean 900.5
RMS 4032
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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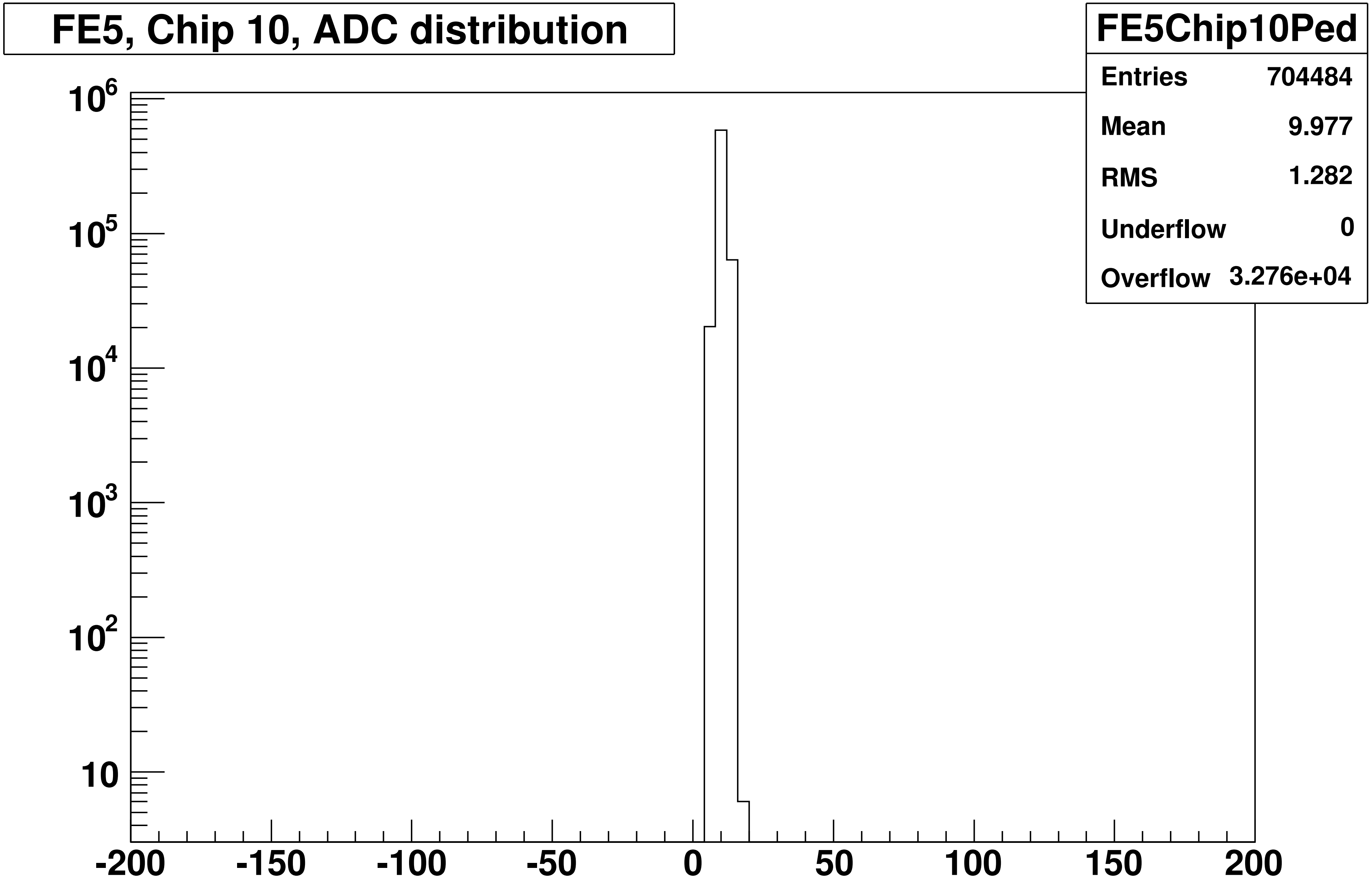
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 704484
Mean 9.977
RMS 1.282
Underflow 0
Overflow 3.276e+04
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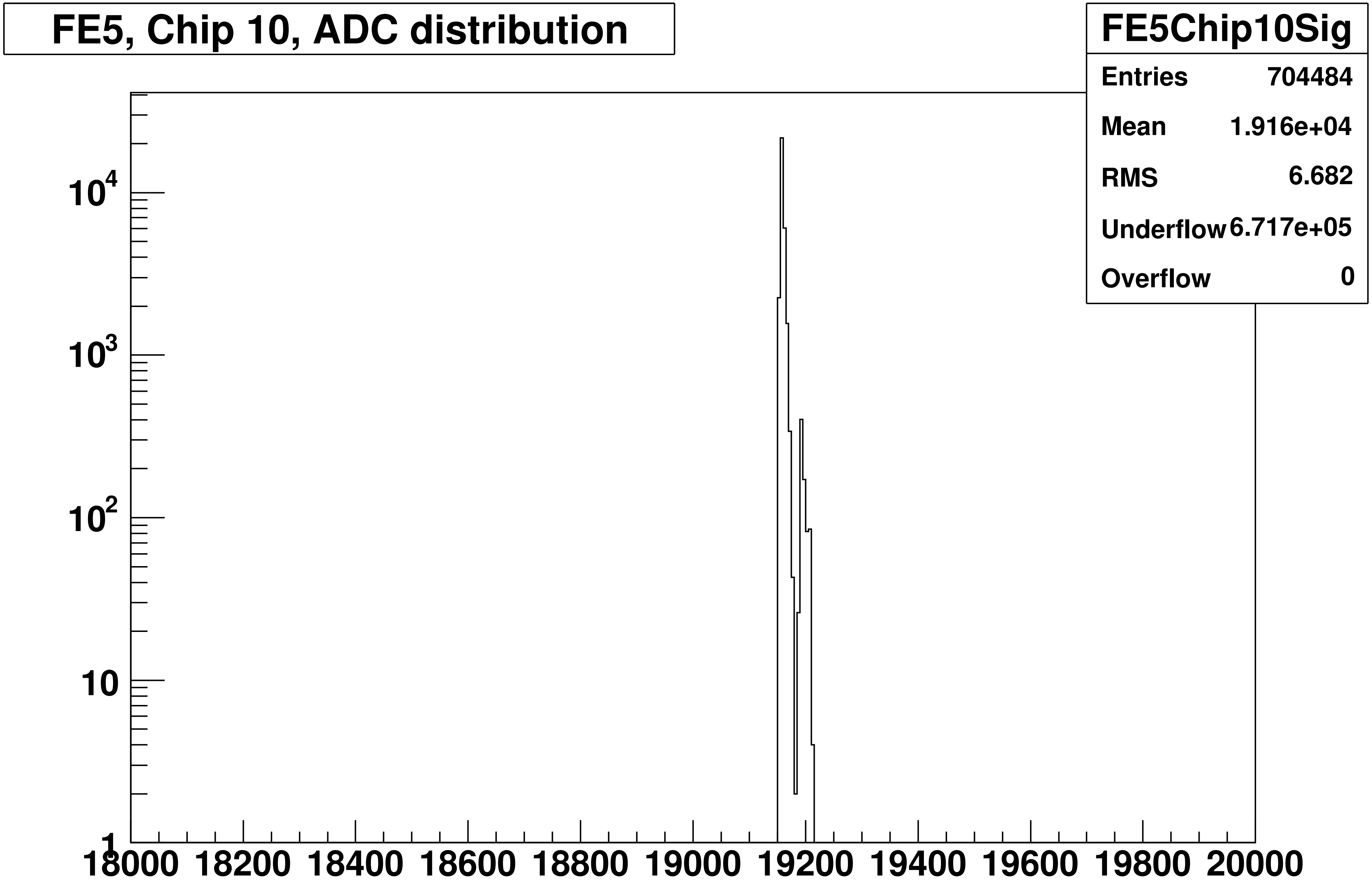
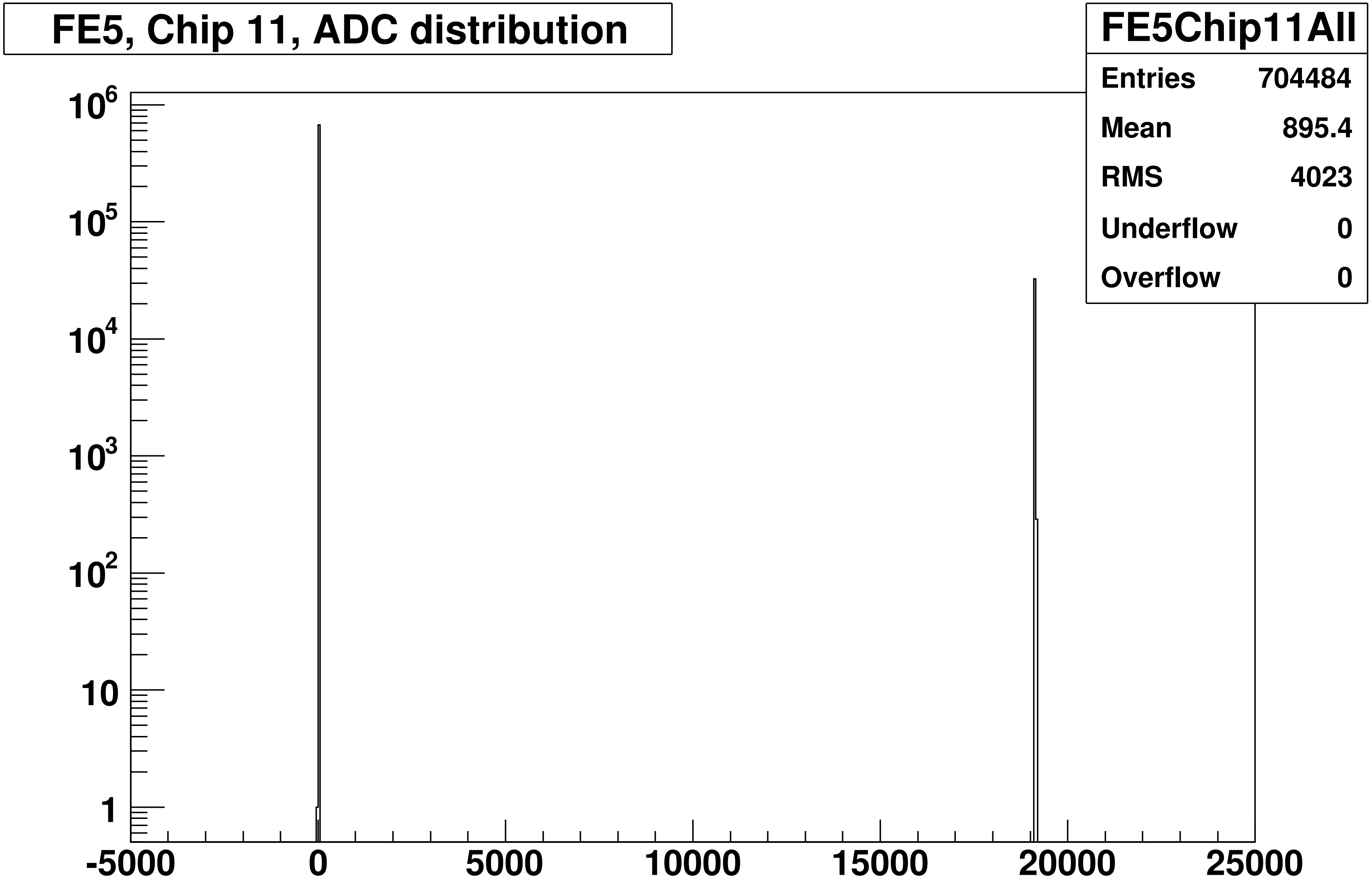
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 704484
Mean 1.916e+04
RMS 6.682
Underflow 6.717e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 704484
Mean 895.4
RMS 4023
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
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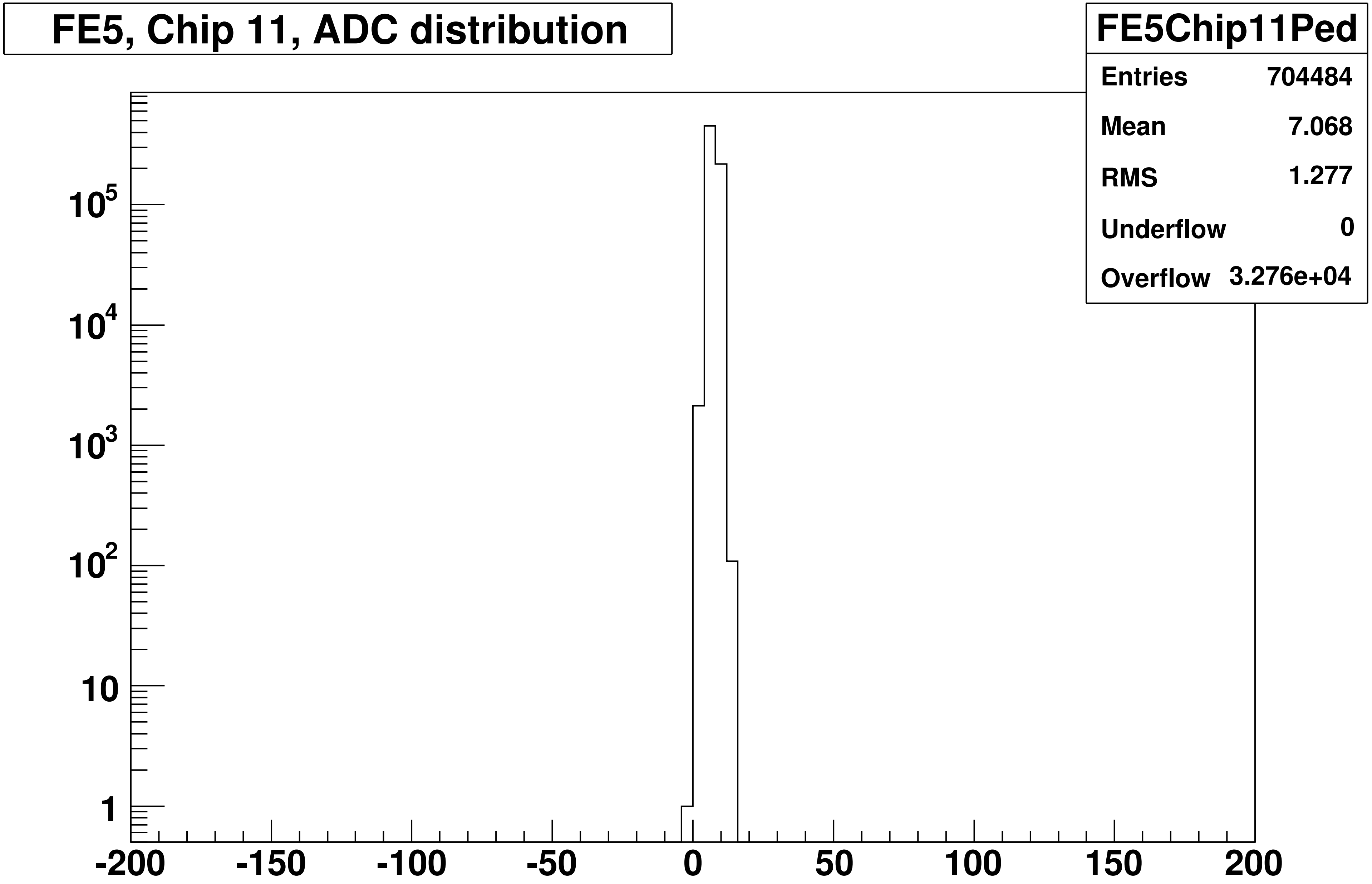
0
5000
10000
15000
20000
25000



FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 704484
Mean 7.068
RMS 1.277
Underflow 0
Overflow 3.276e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
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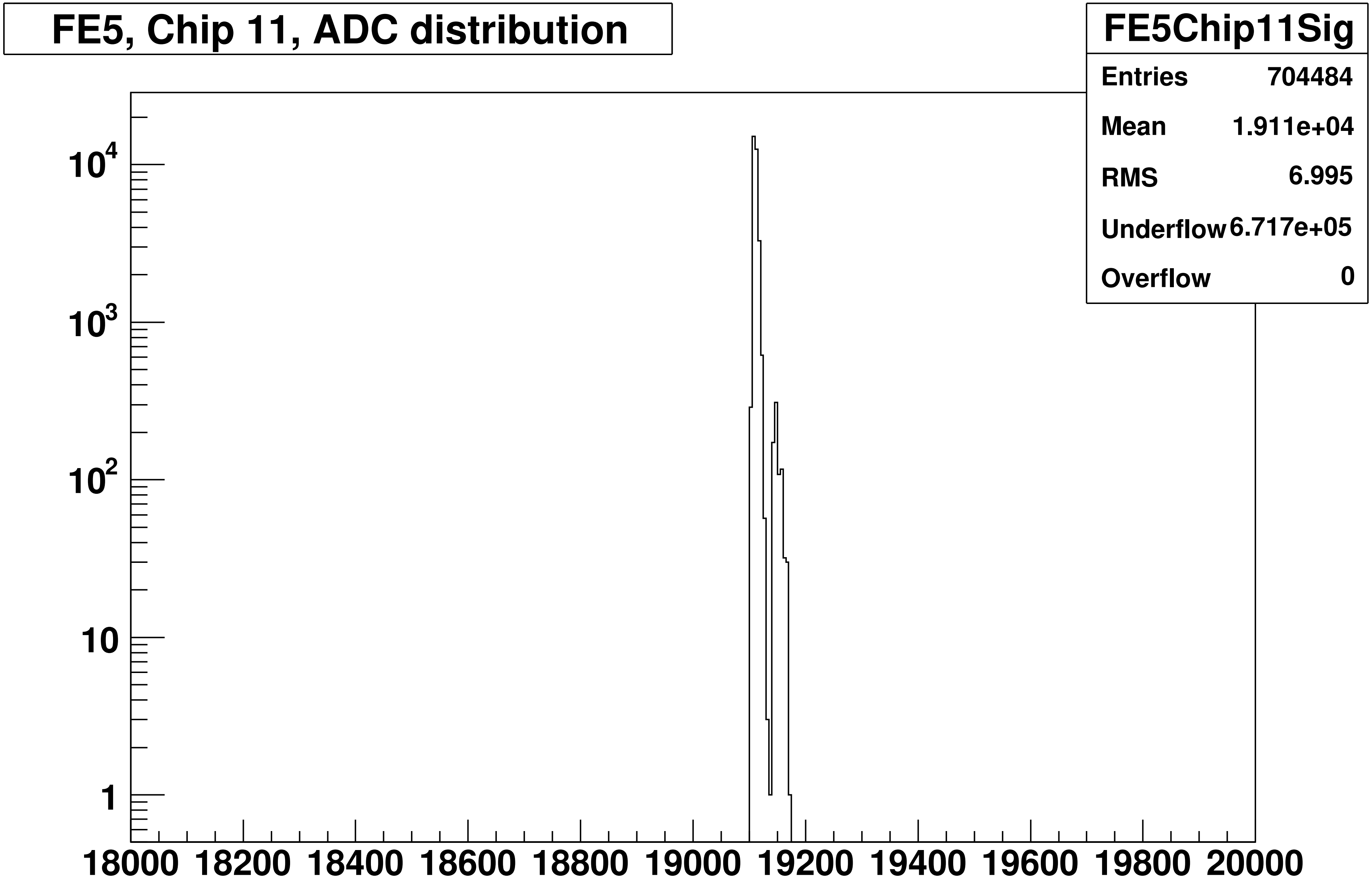
100
150
200


FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 704484
Mean 1.911e+04
RMS 6.995
Underflow 6.717e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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19000
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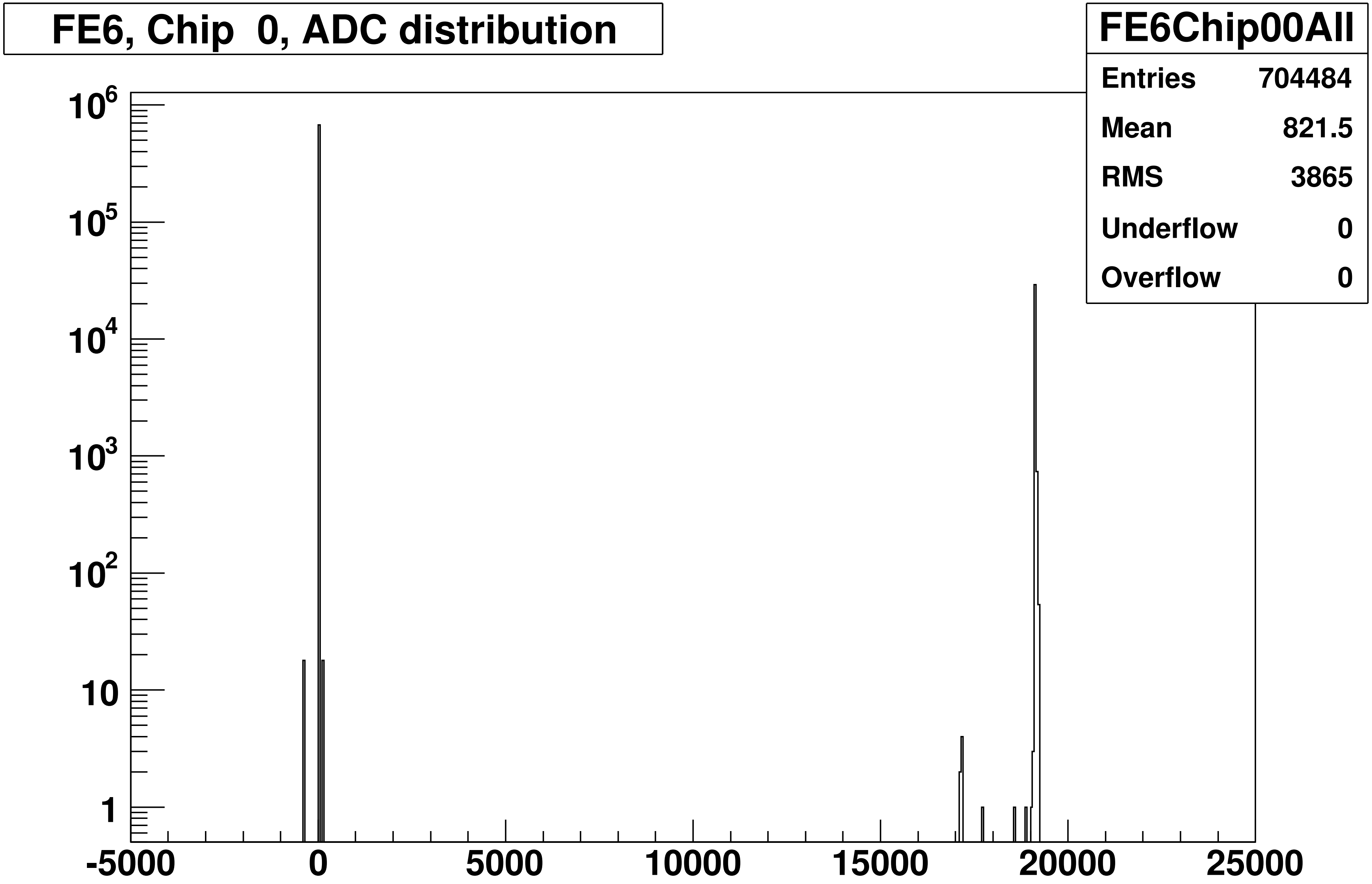
19800
20000



FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 704484
Mean 821.5
RMS 3865
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
5000
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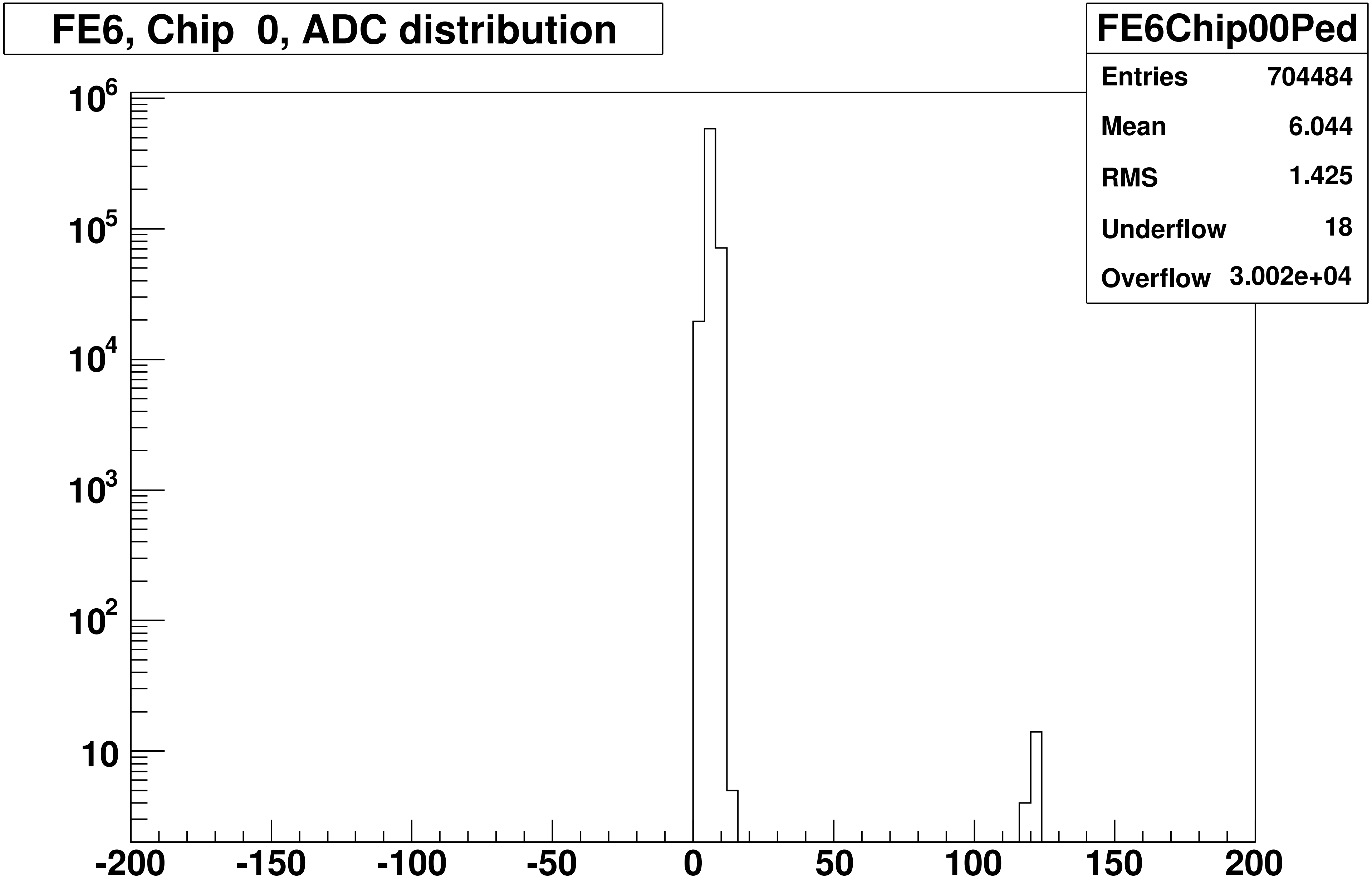
20000
25000



FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 704484
Mean 6.044
RMS 1.425
Underflow 18
Overflow 3.002e+04
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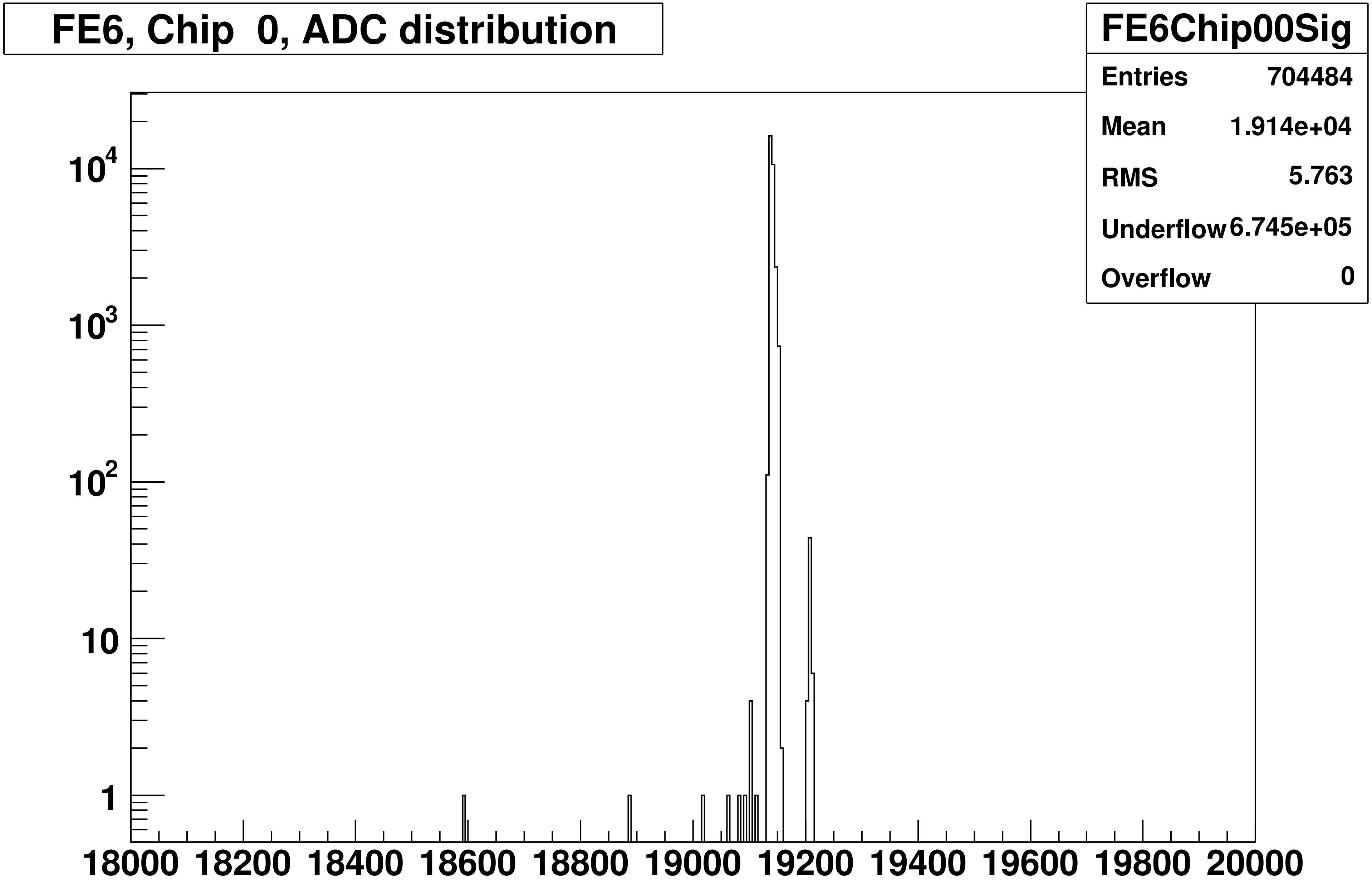
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 704484
Mean 1.914e+04
RMS 5.763
Underflow 6.745e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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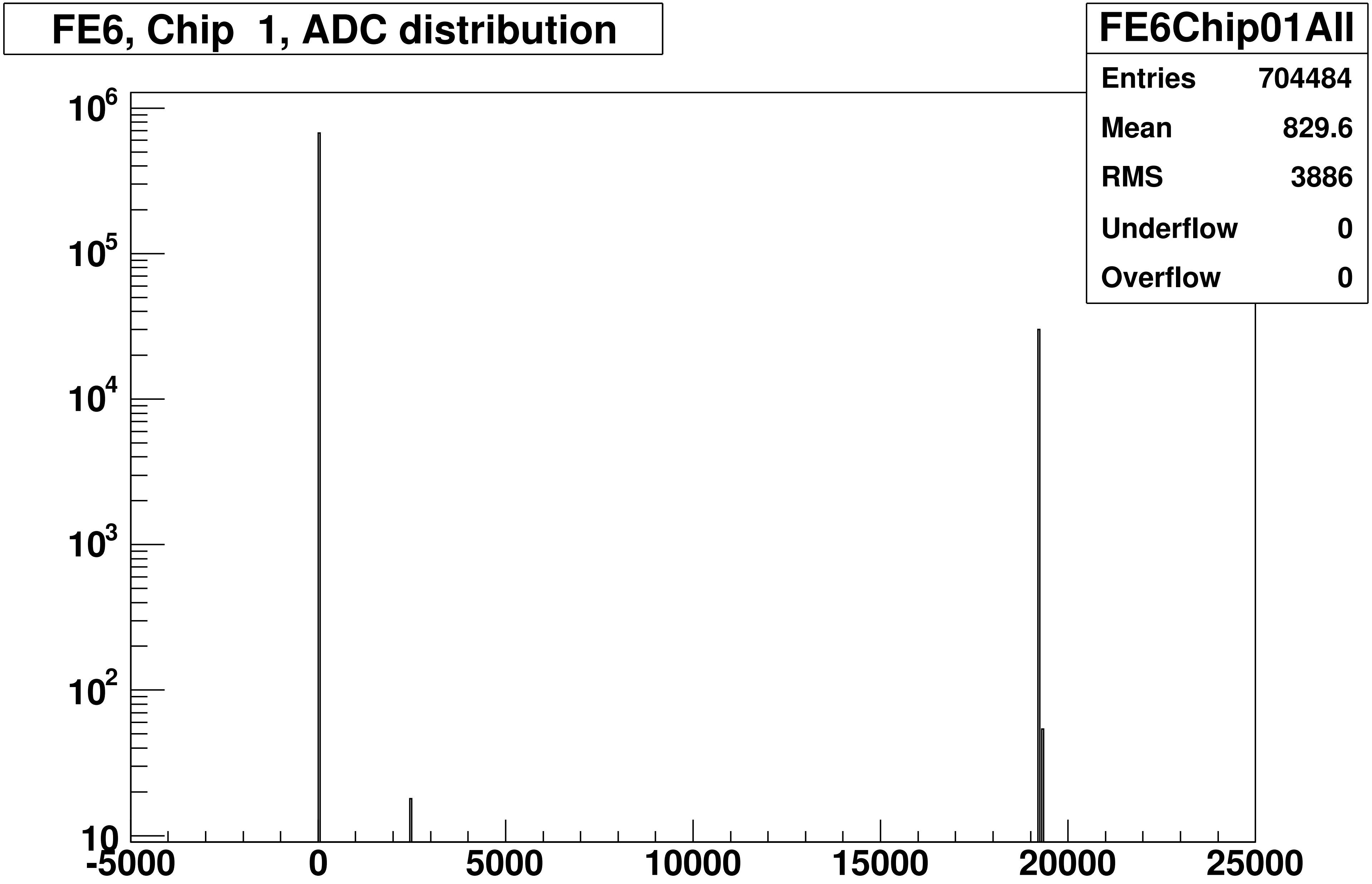
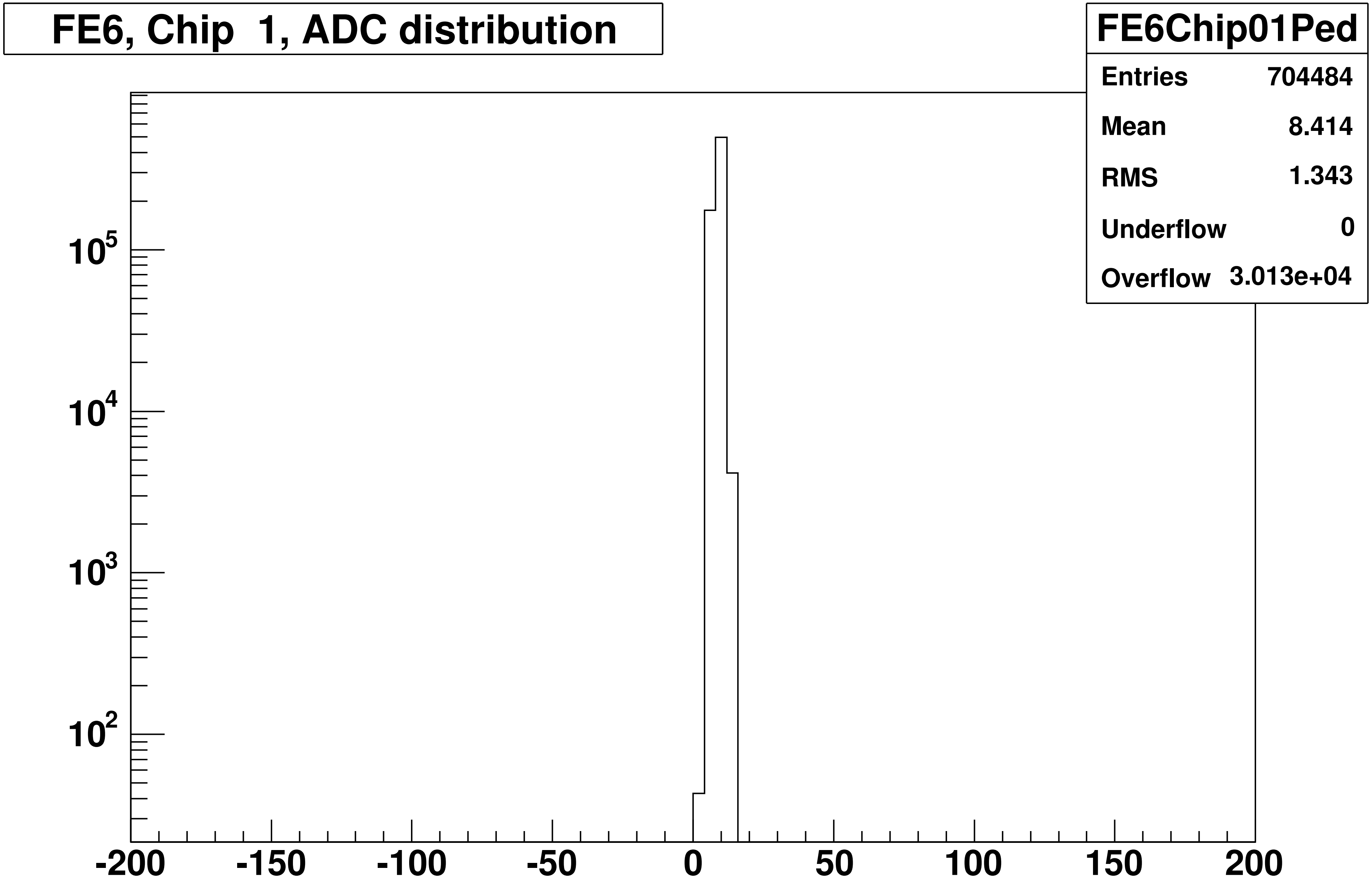
19600
19800
20000


FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 704484
Mean 829.6
RMS 3886
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 704484
Mean 8.414
RMS 1.343
Underflow 0
Overflow 3.013e+04
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10^4
10^3
10^2
-200
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-100
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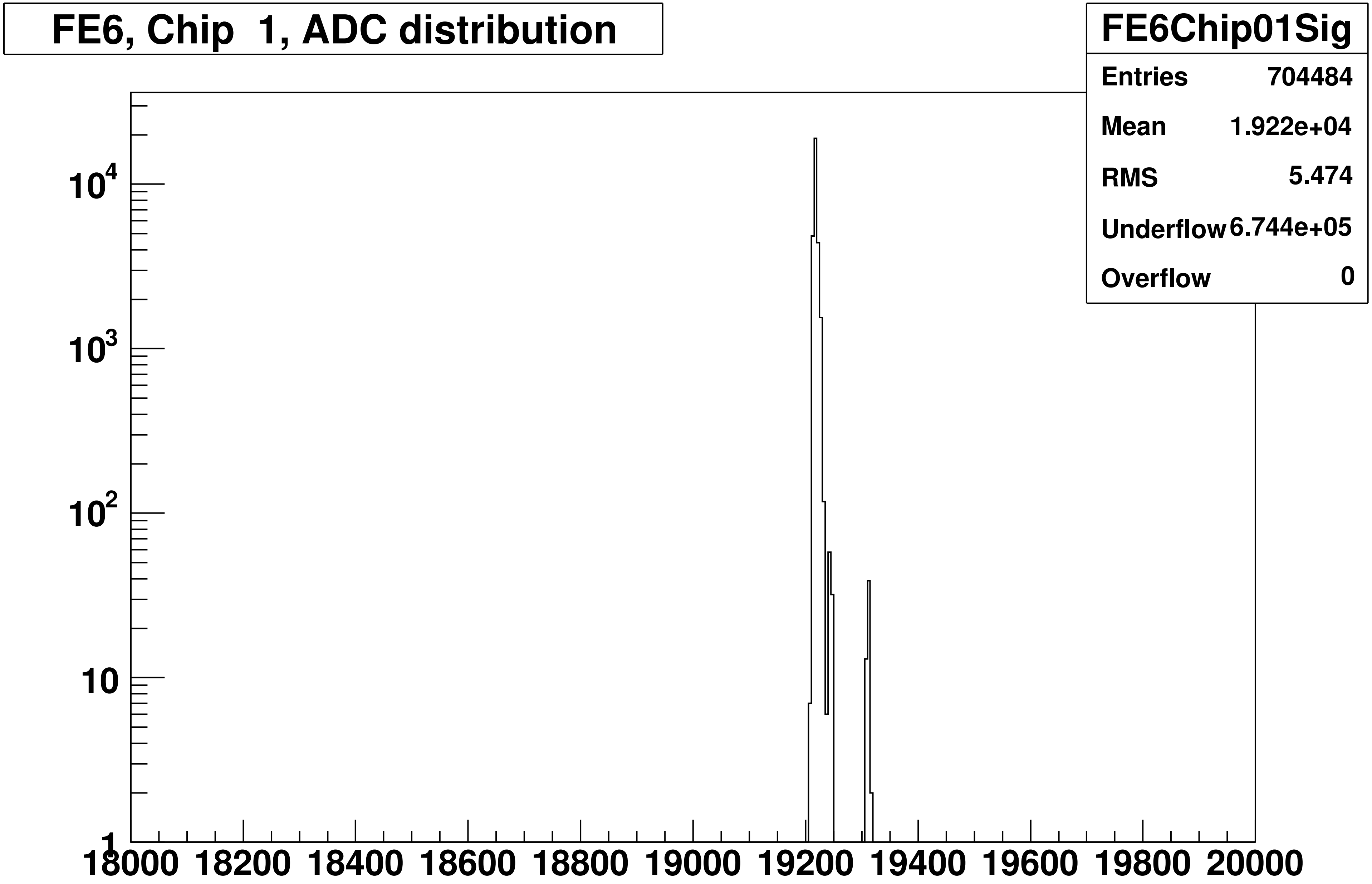
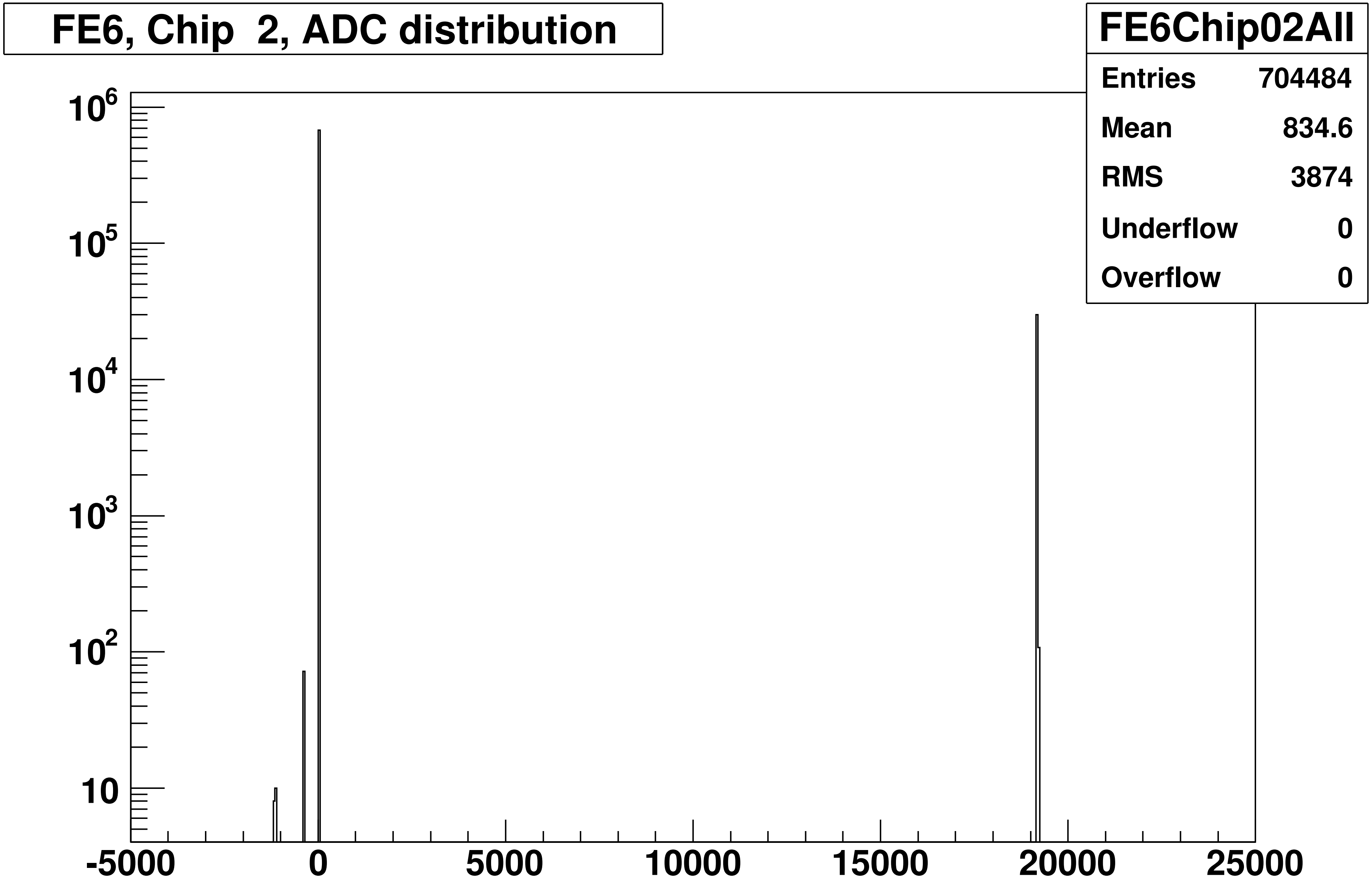
0
50
100
150
200


FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 704484
Mean 1.922e+04
RMS 5.474
Underflow 6.744e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 704484
Mean 834.6
RMS 3874
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
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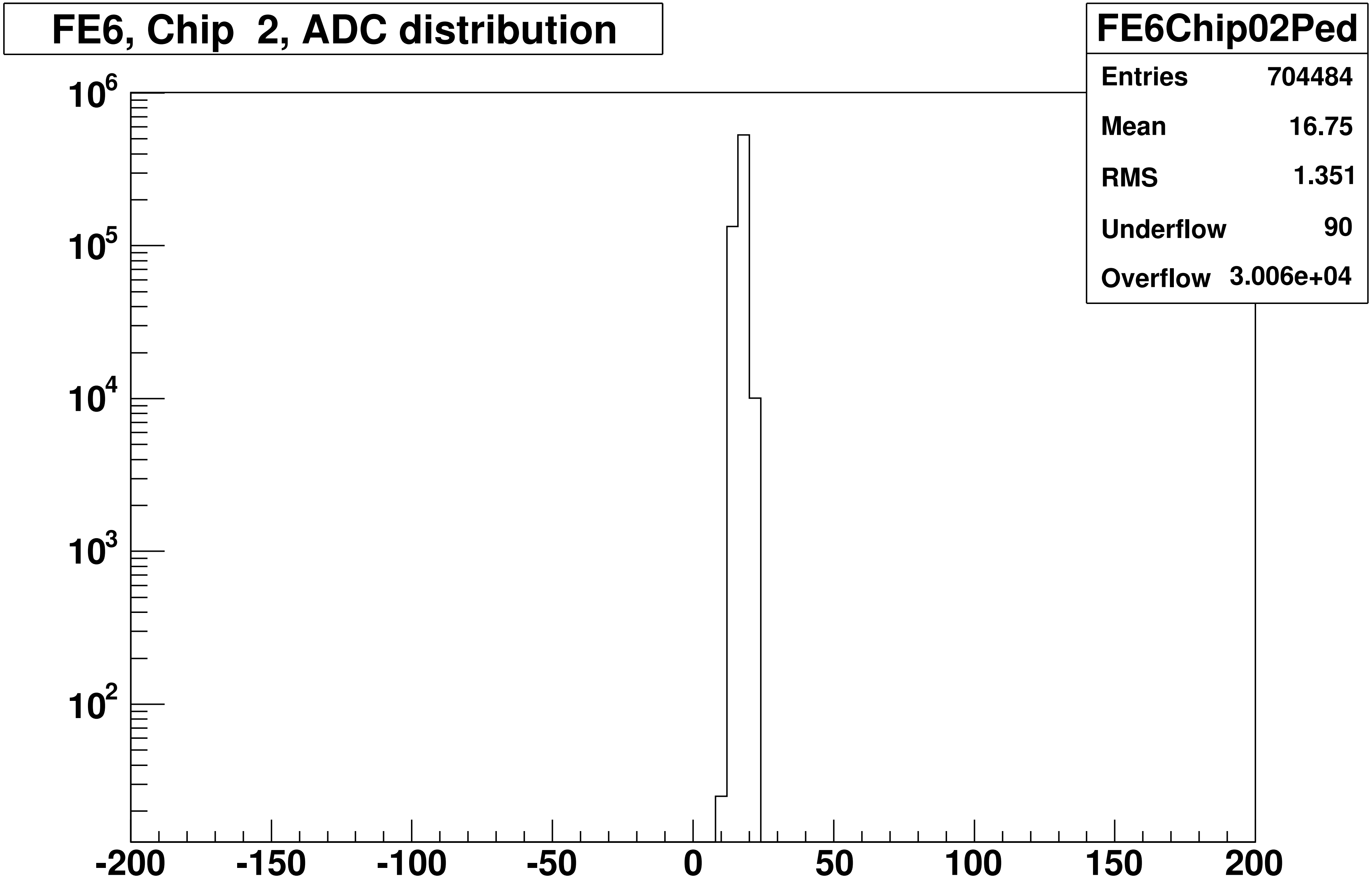
15000
20000
25000


FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 704484
Mean 16.75
RMS 1.351
Underflow 90
Overflow 3.006e+04
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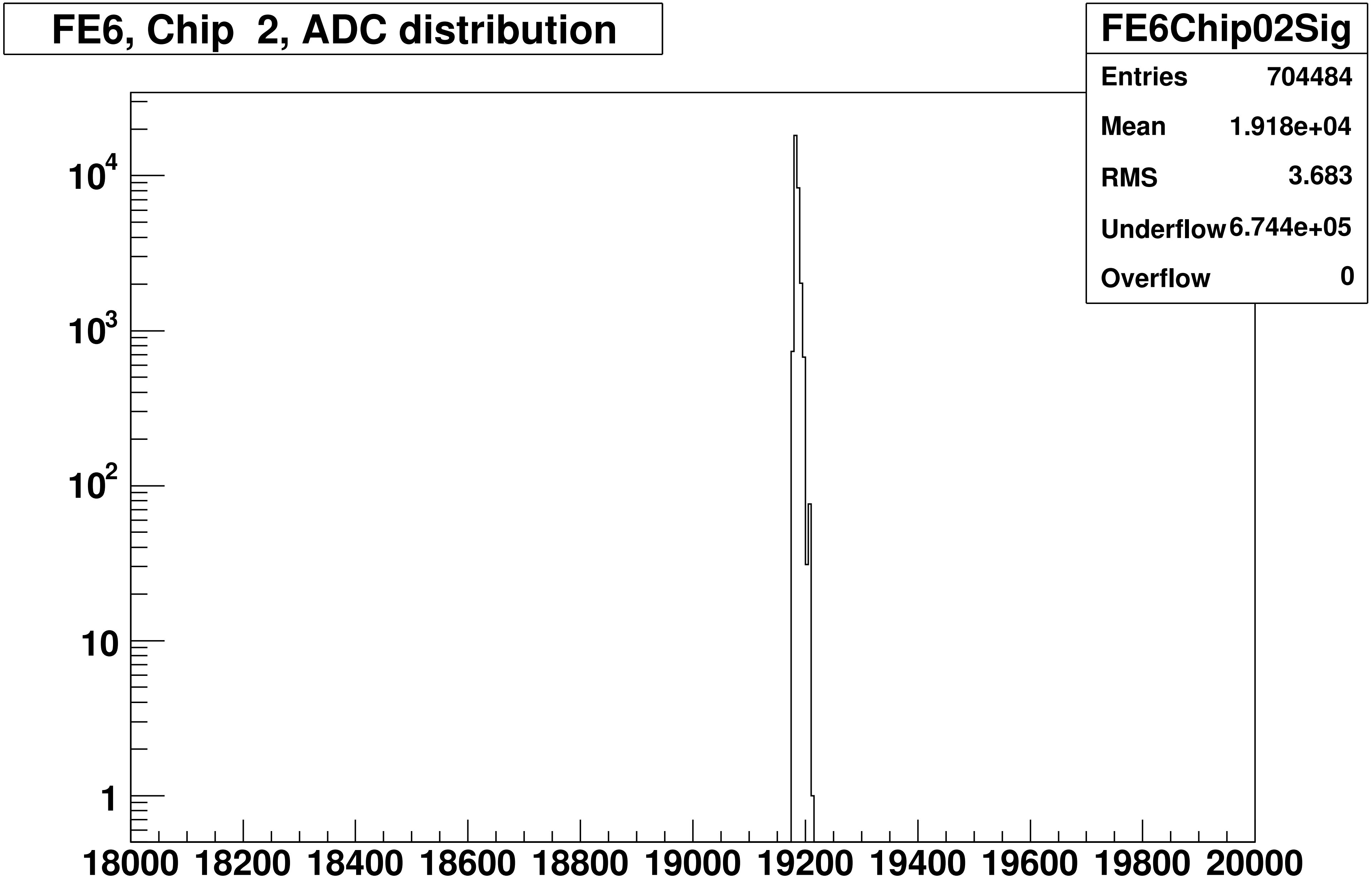
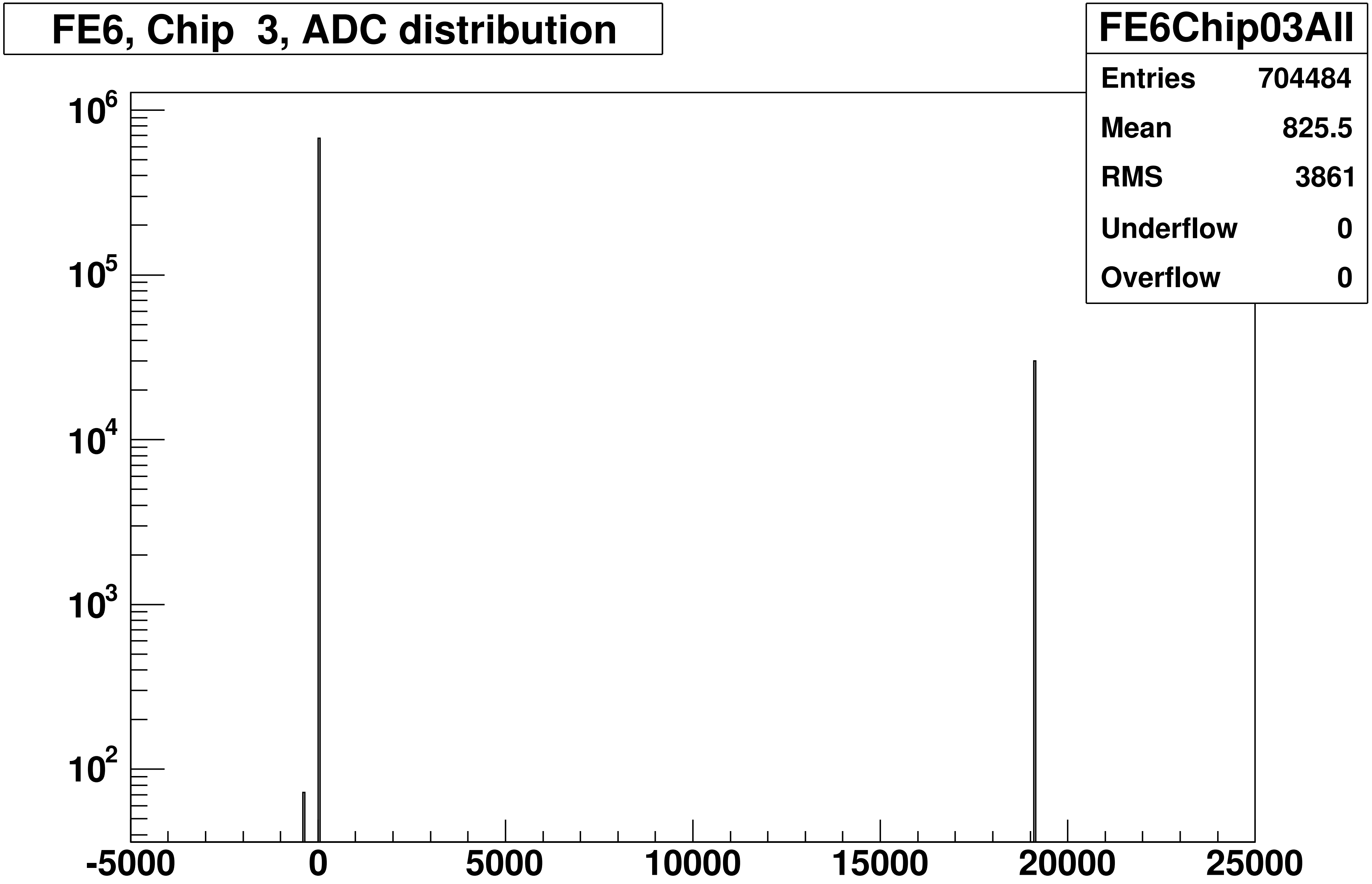
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 704484
Mean 1.918e+04
RMS 3.683
Underflow 6.744e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 704484
Mean 825.5
RMS 3861
Underflow 0
Overflow 0
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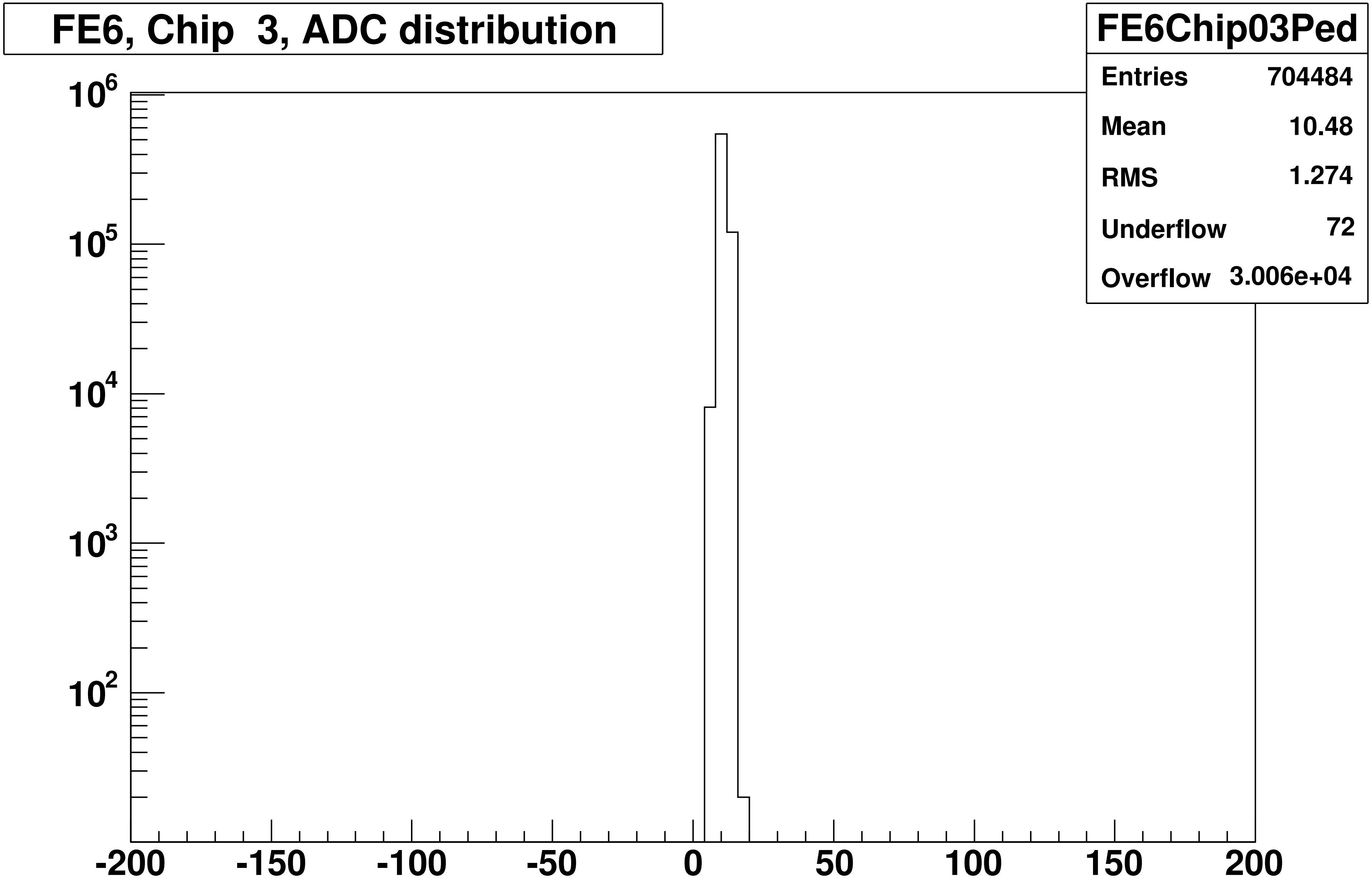
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 704484
Mean 10.48
RMS 1.274
Underflow 72
Overflow 3.006e+04
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10^5
10^4
10^3
10^2
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-100
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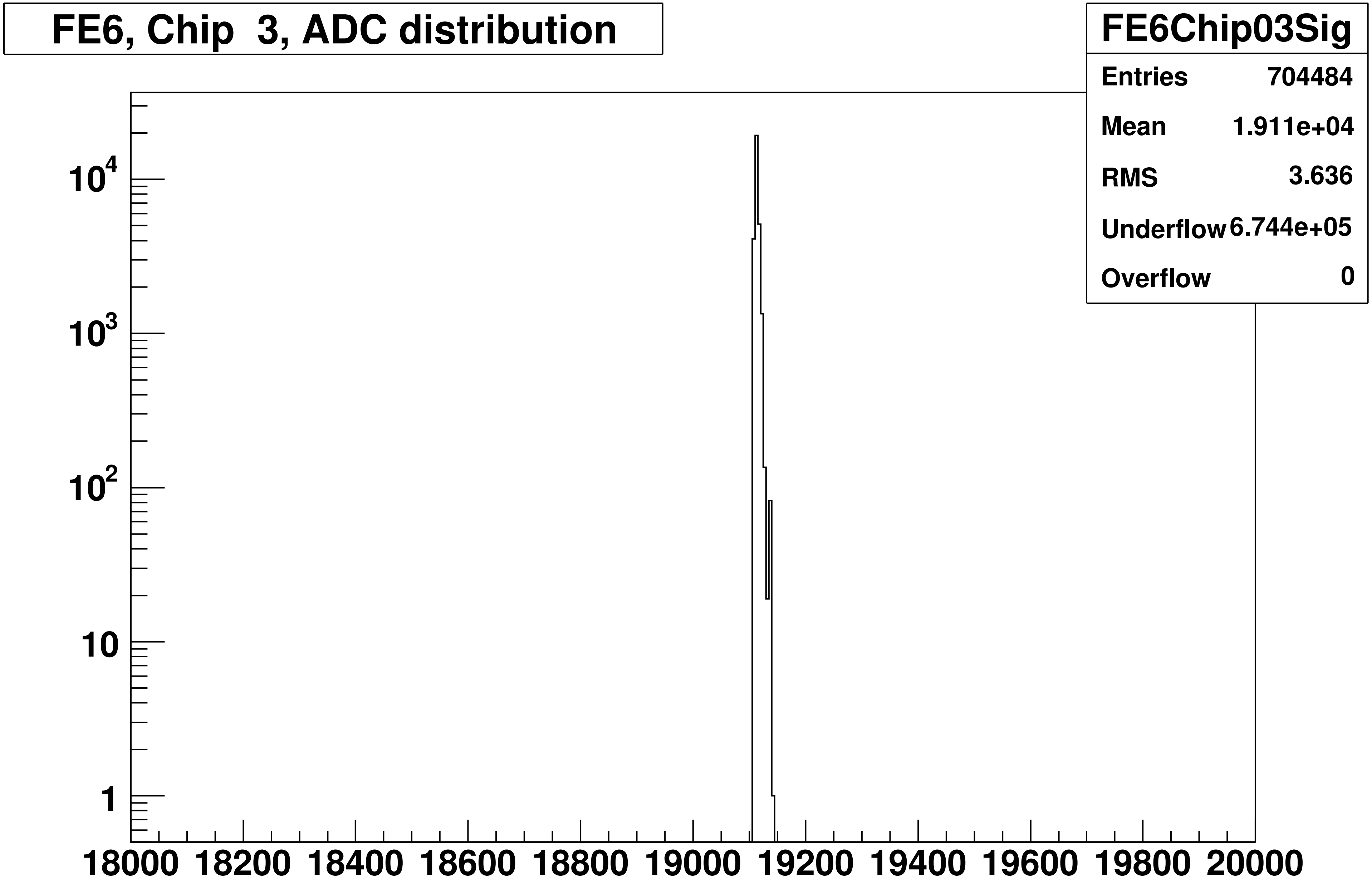
100
150
200



FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 704484
Mean 1.911e+04
RMS 3.636
Underflow 6.744e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
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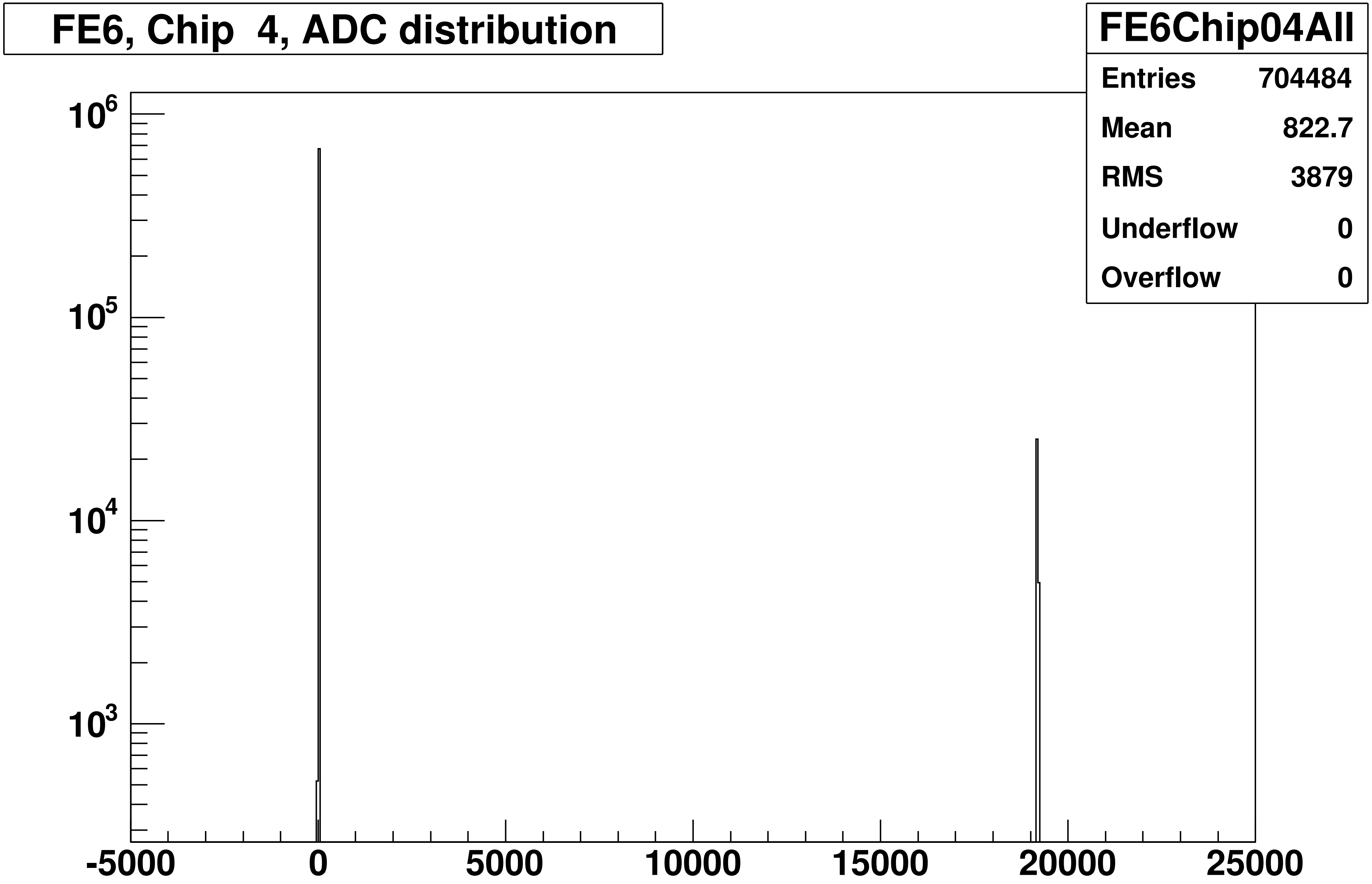
19400
19600
19800
20000



FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 704484
Mean 822.7
RMS 3879
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
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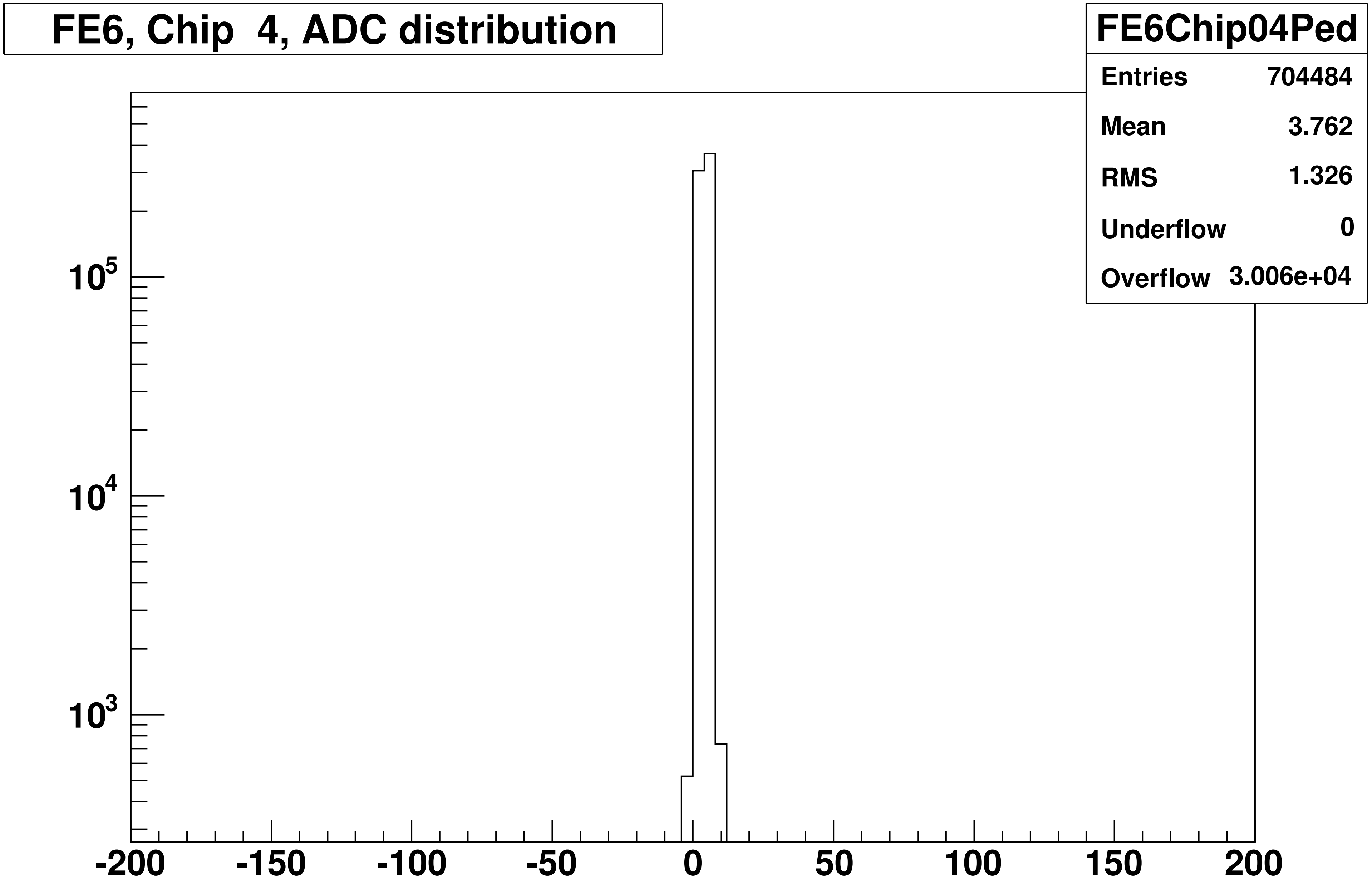
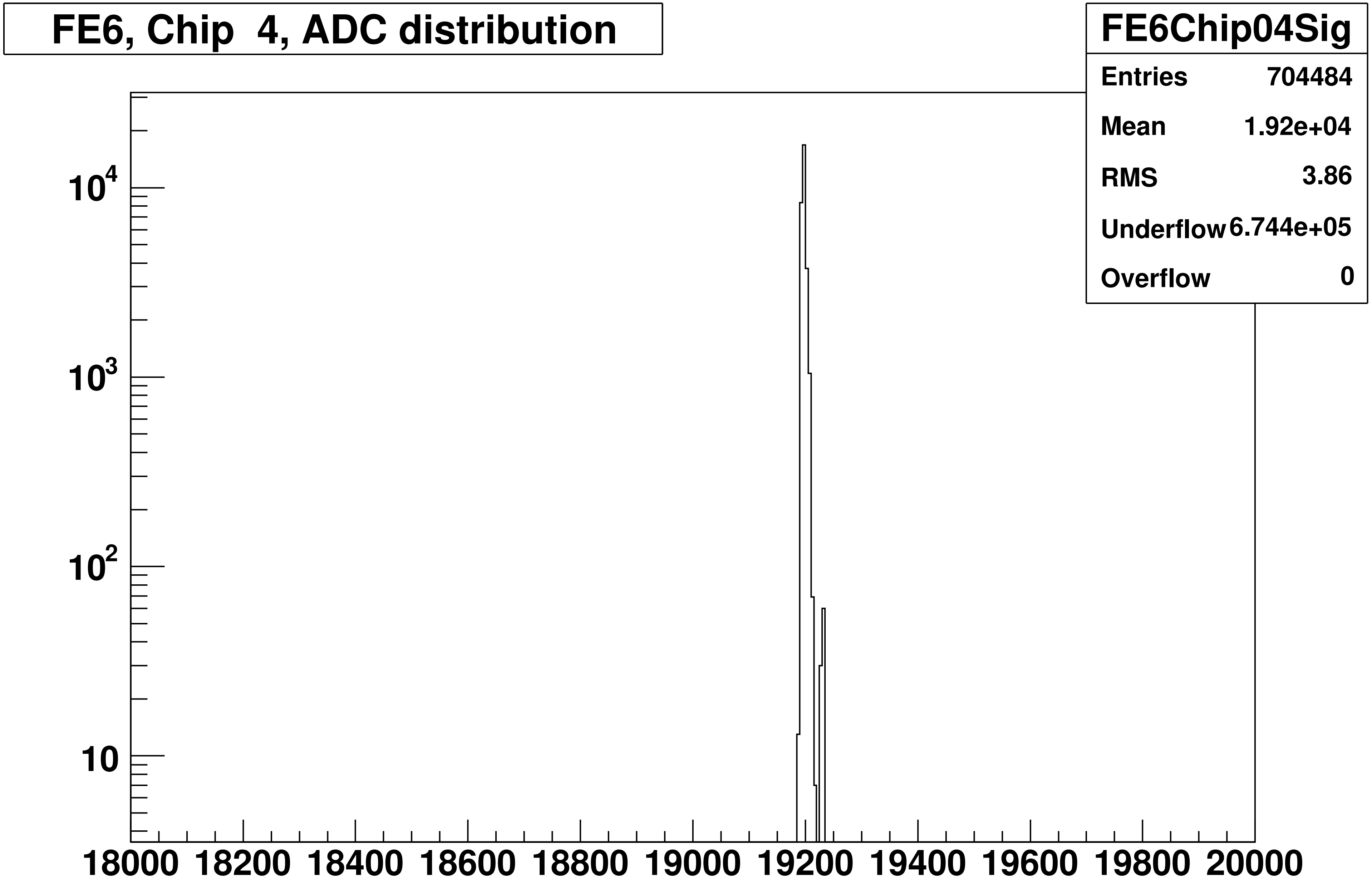
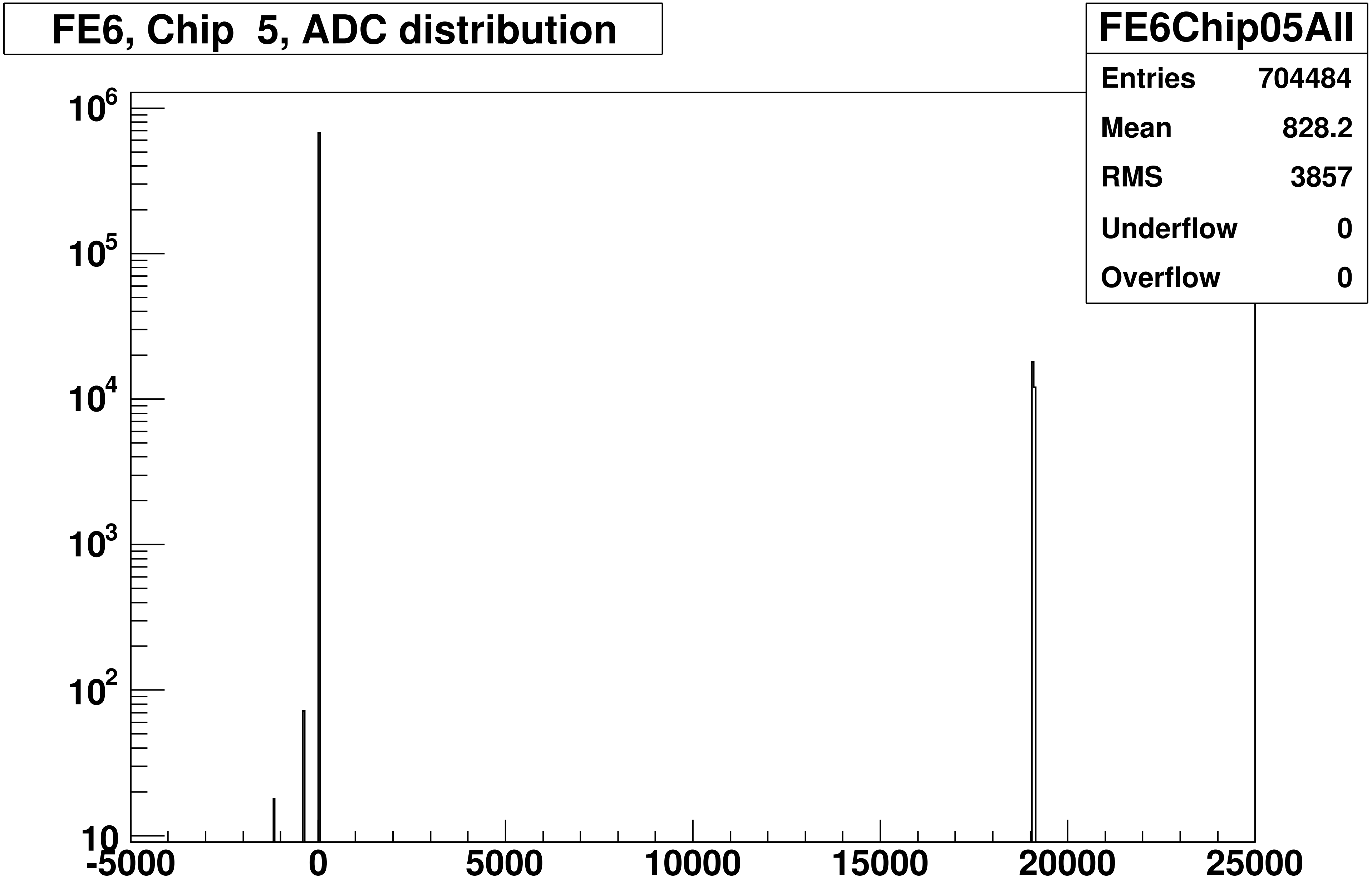
0
5000
10000
15000
20000
25000



FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 704484
Mean 3.762
RMS 1.326
Underflow 0
Overflow 3.006e+04
10^5
10^4
10^3
-200 -150 -100 -50 0 50 100 150 200


FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 704484
Mean 1.92e+04
RMS 3.86
Underflow 6.744e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 704484
Mean 828.2
RMS 3857
Underflow 0
Overflow 0
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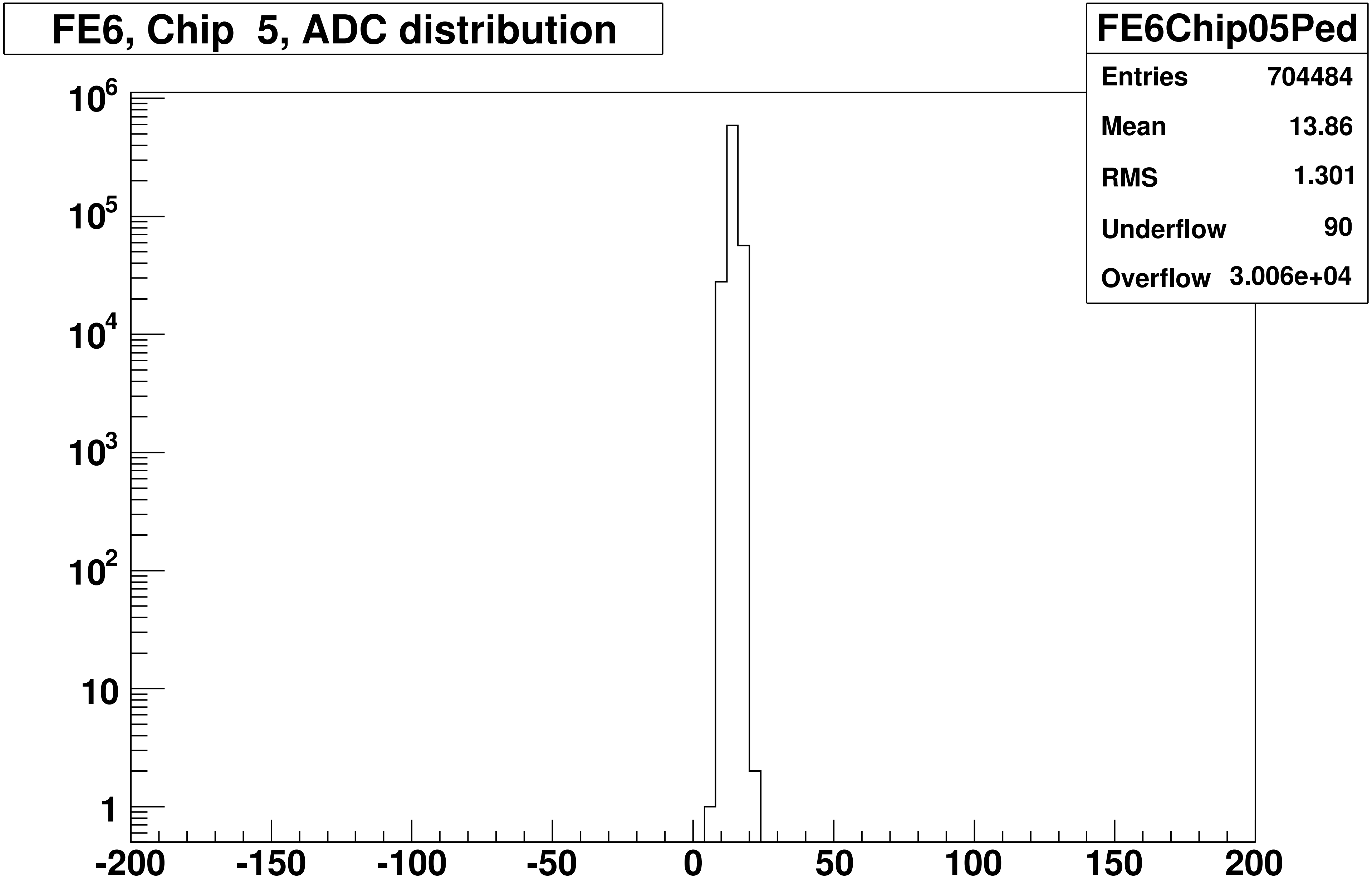
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 704484
Mean 13.86
RMS 1.301
Underflow 90
Overflow 3.006e+04
10^6
10^5
10^4
10^3
10^2
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1
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-100
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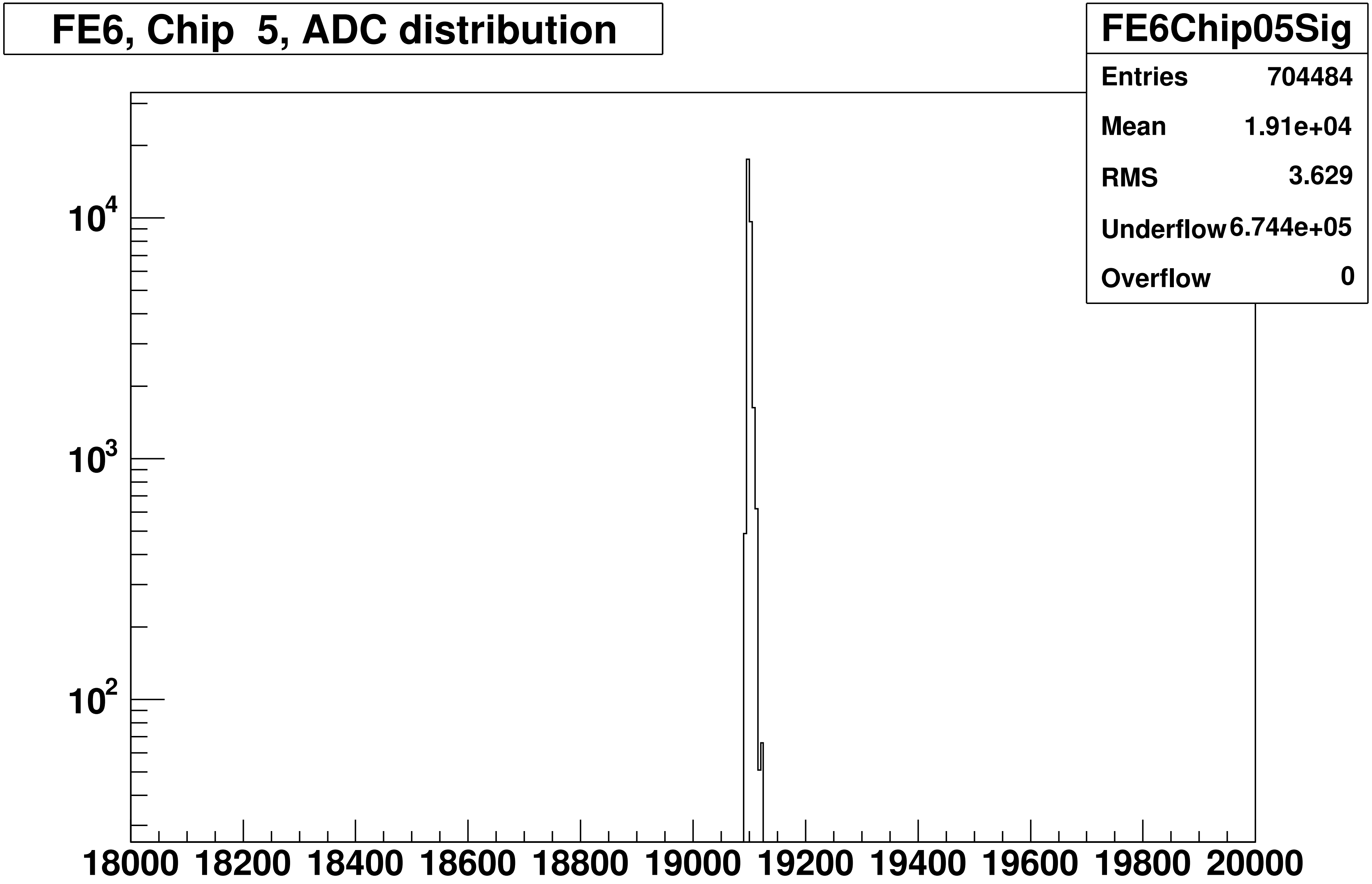
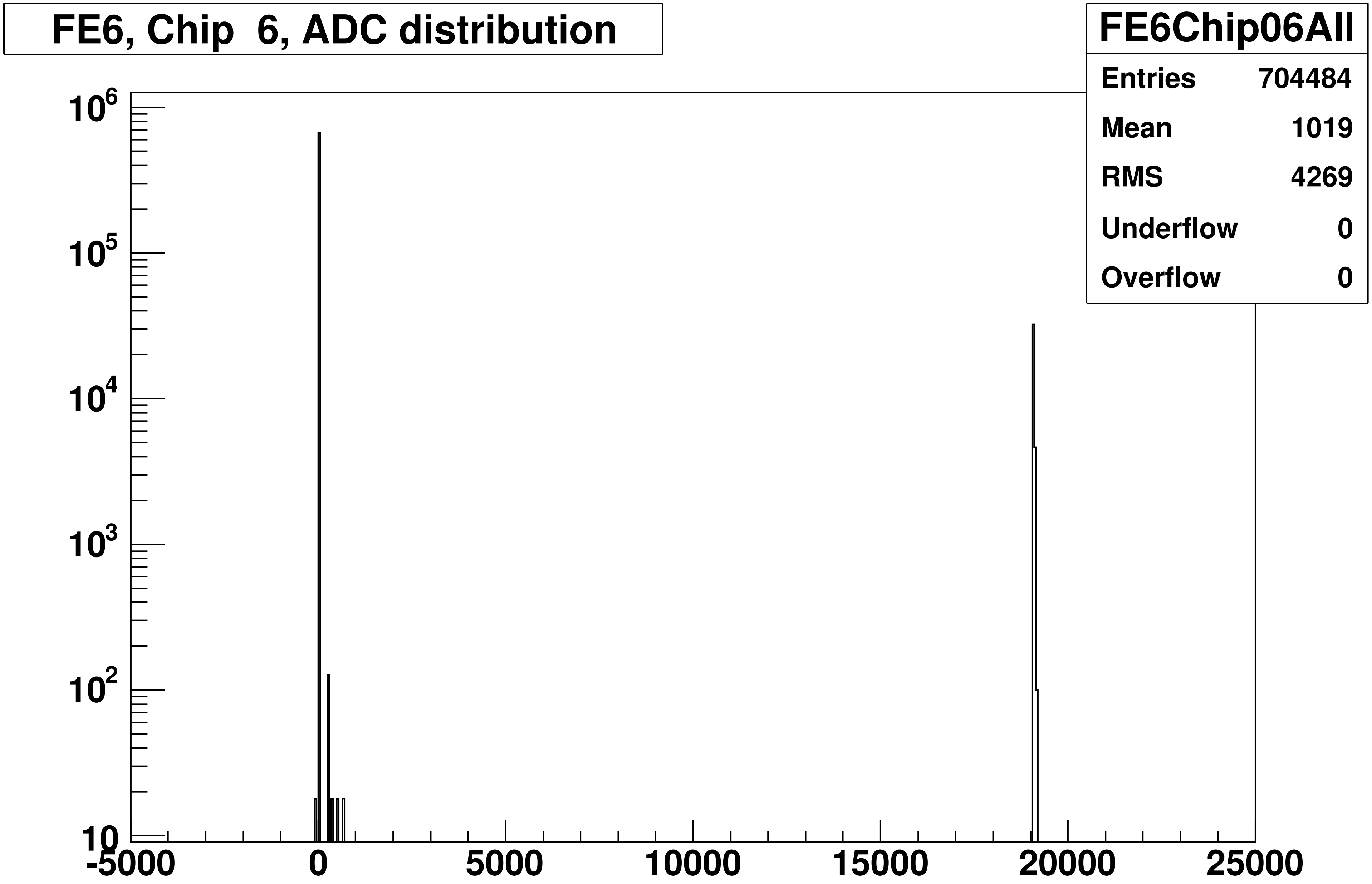
100
150
200



FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 704484
Mean 1.91e+04
RMS 3.629
Underflow 6.744e+05
Overflow 0
10^4
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 704484
Mean 1019
RMS 4269
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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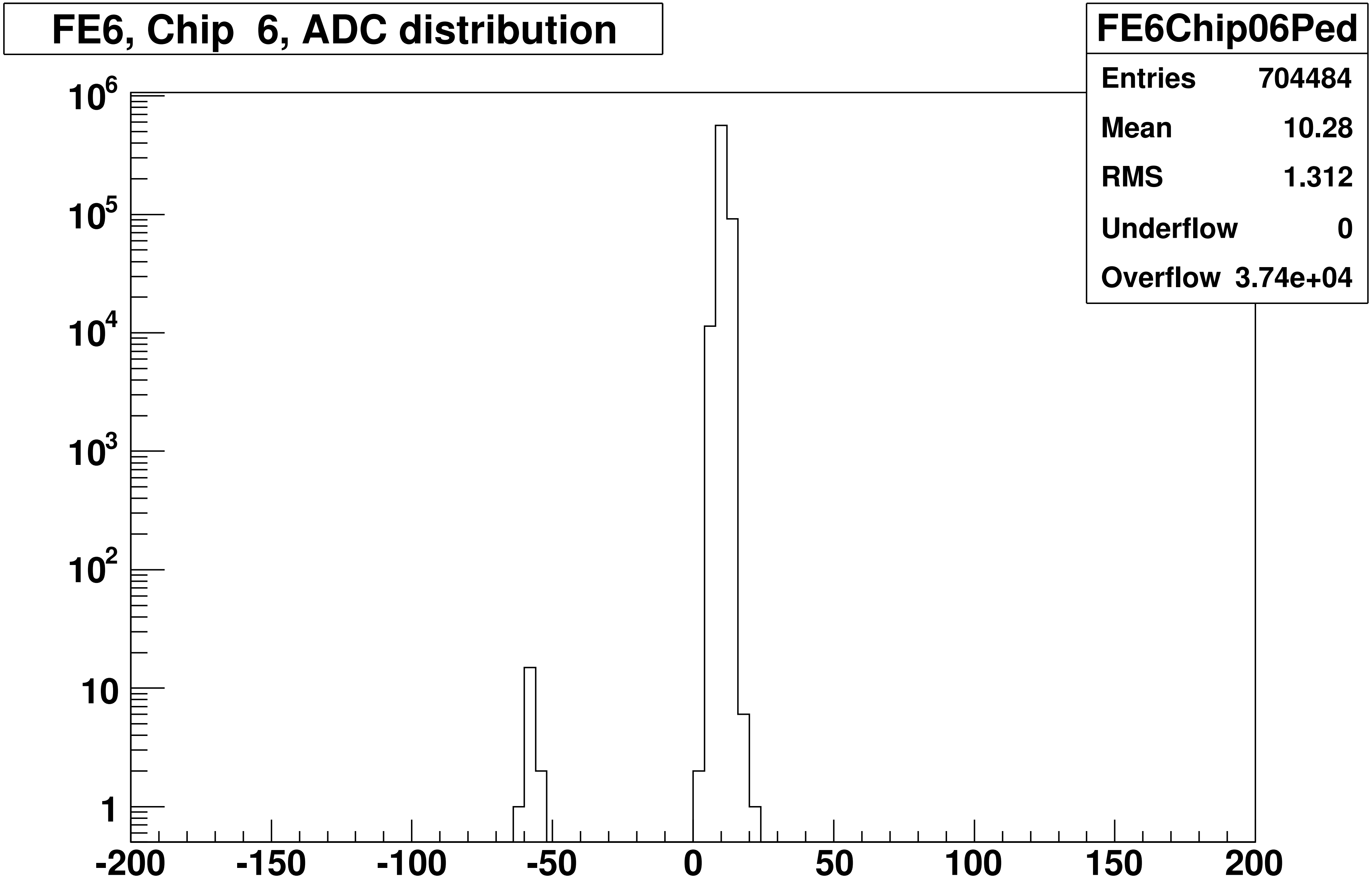
25000


FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 704484
Mean 10.28
RMS 1.312
Underflow 0
Overflow 3.74e+04
10^6
10^5
10^4
10^3
10^2
10
1
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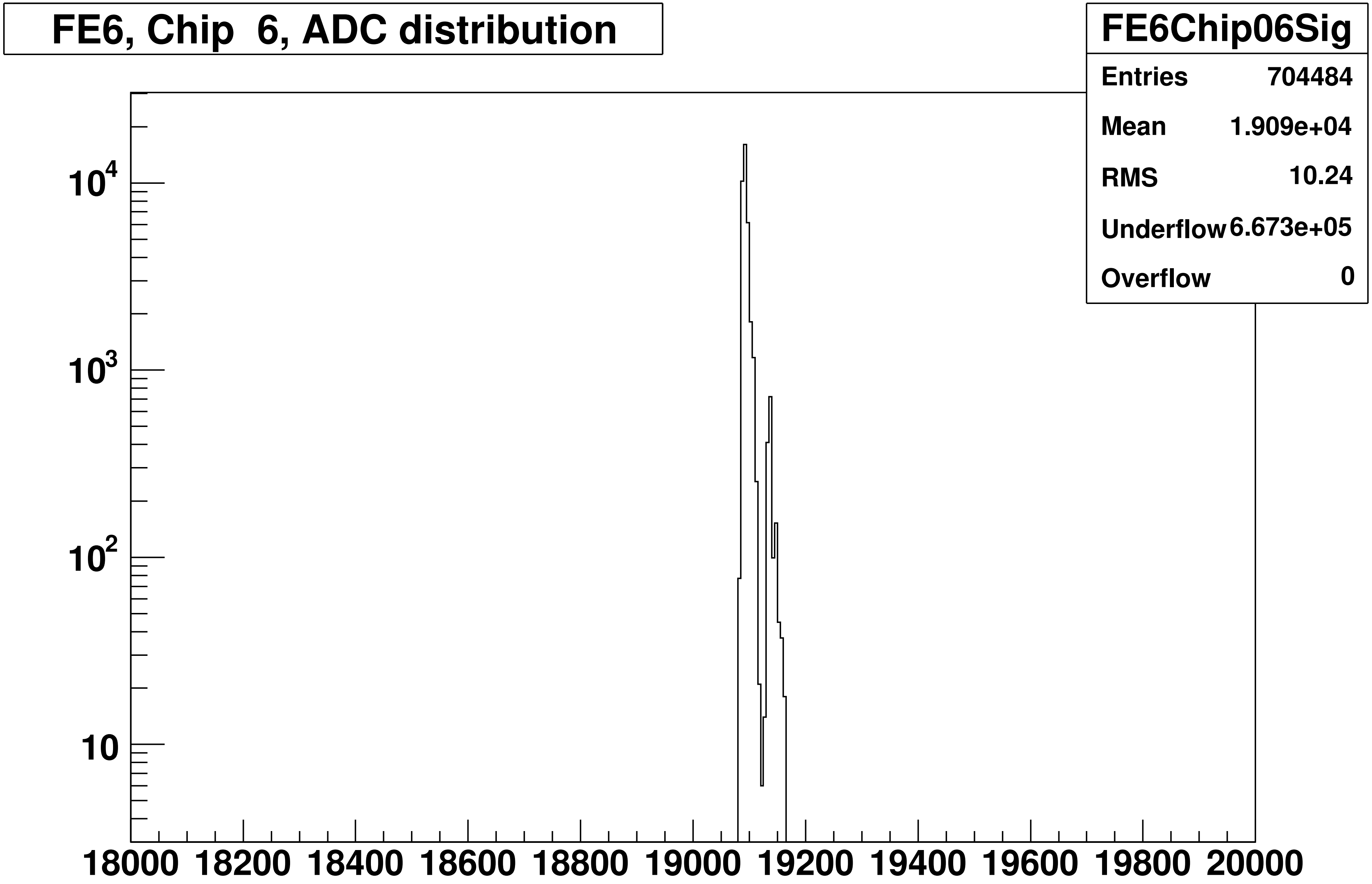
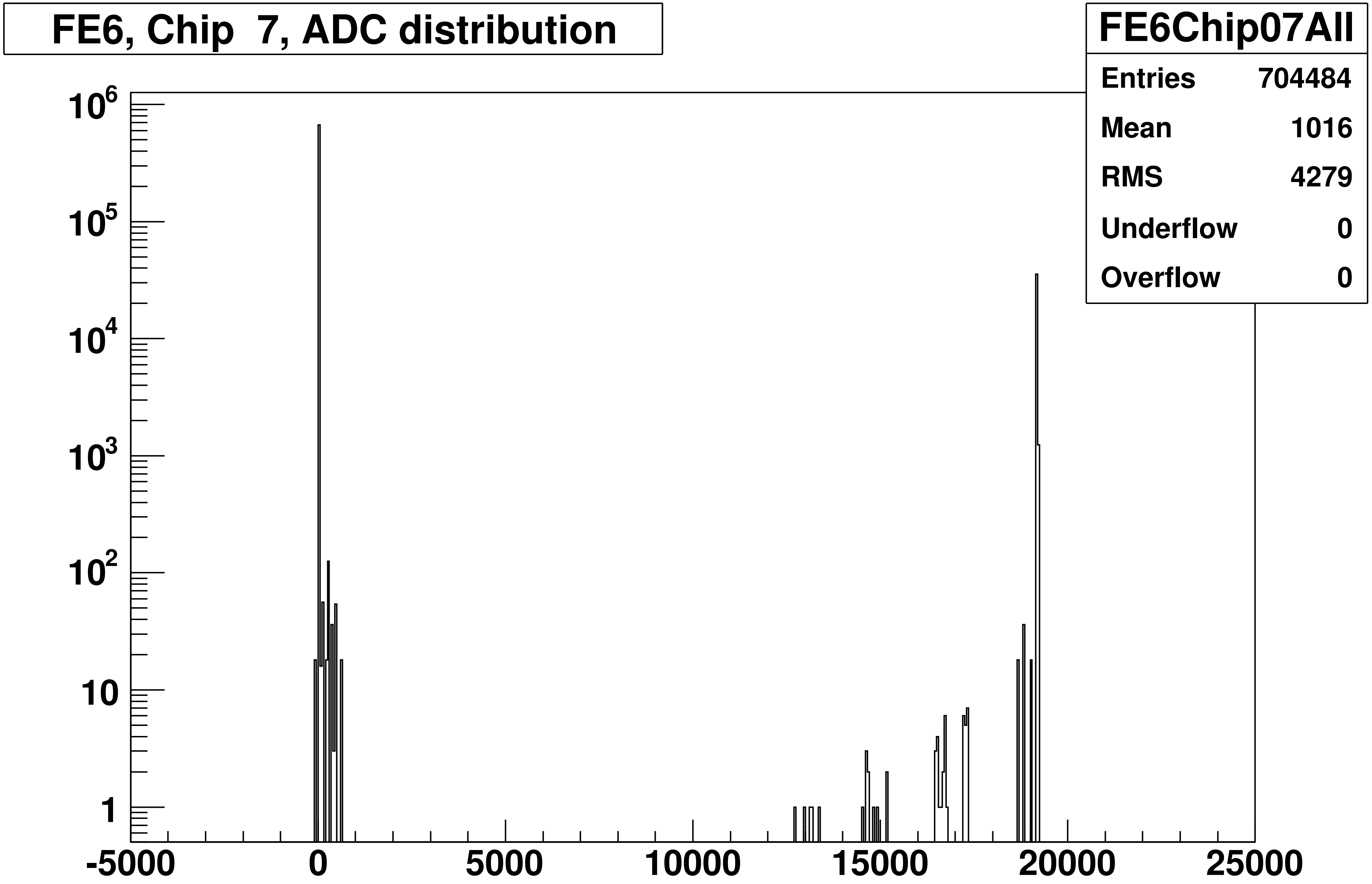
-100
-50
0
50
100
150
200



FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 704484
Mean 1.909e+04
RMS 10.24
Underflow 6.673e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 704484
Mean 1016
RMS 4279
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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1
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0
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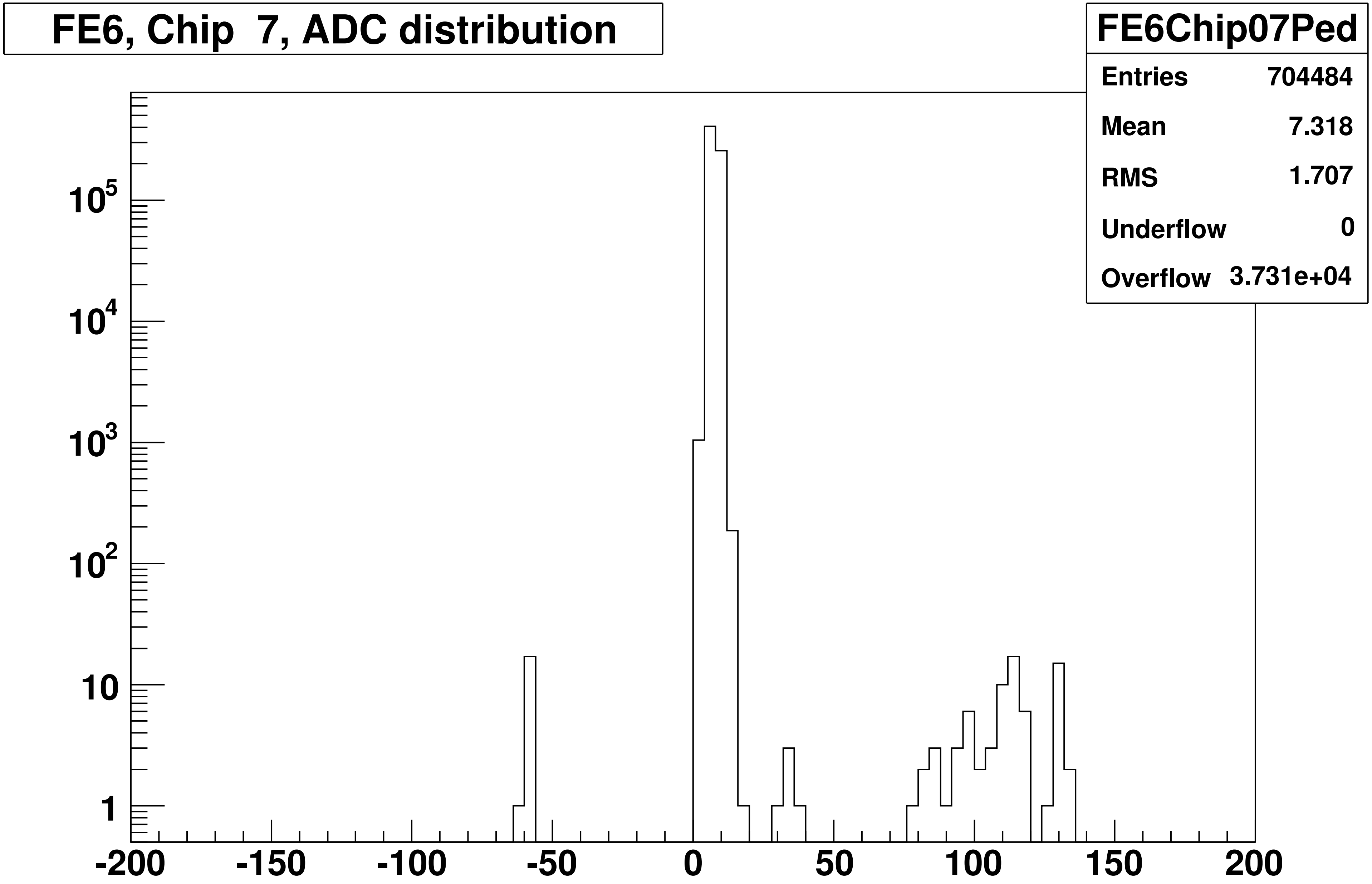

FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 704484
Mean 7.318
RMS 1.707
Underflow 0
Overflow 3.731e+04
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10^4
10^3
10^2
10
1
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150
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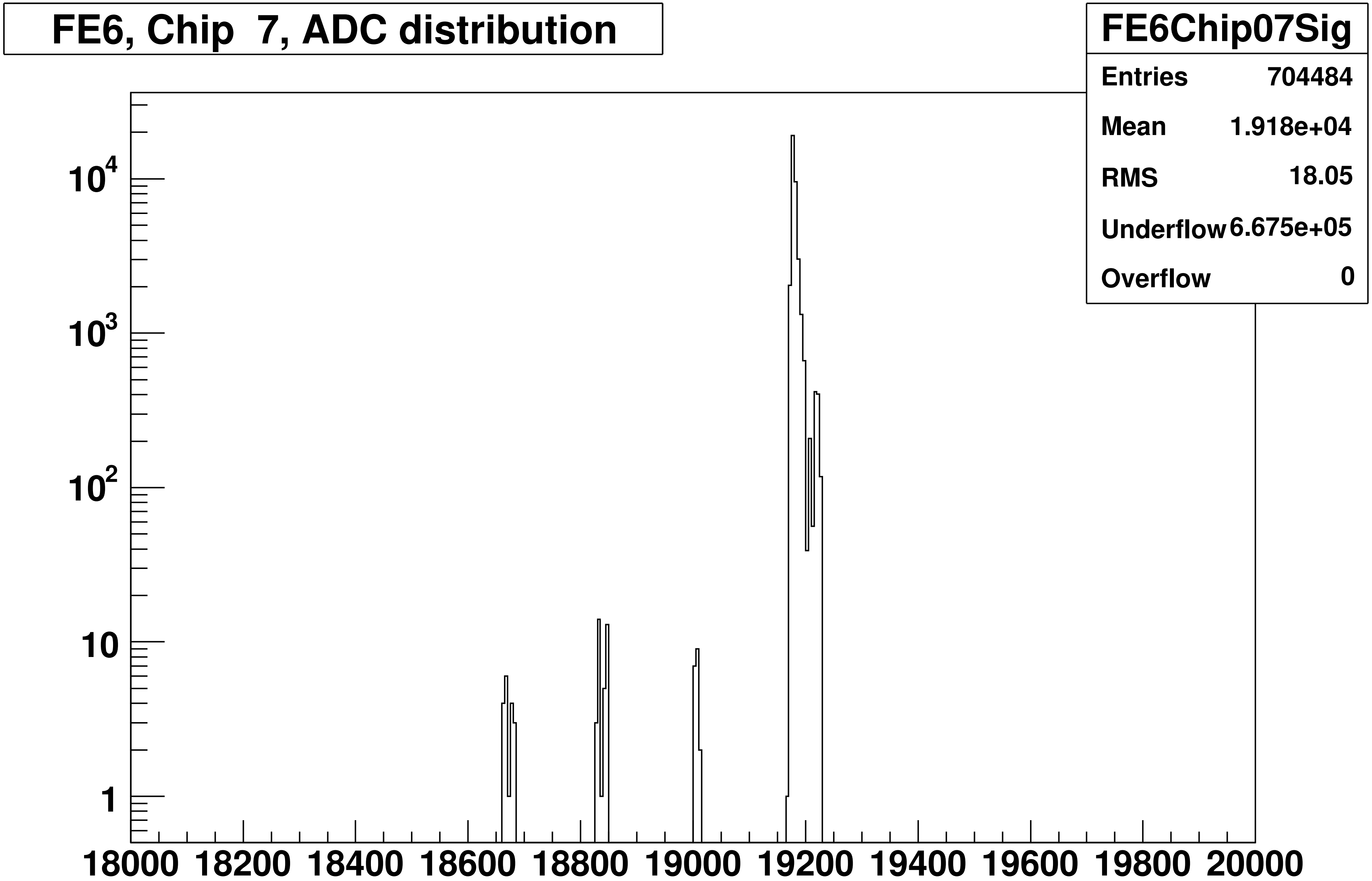
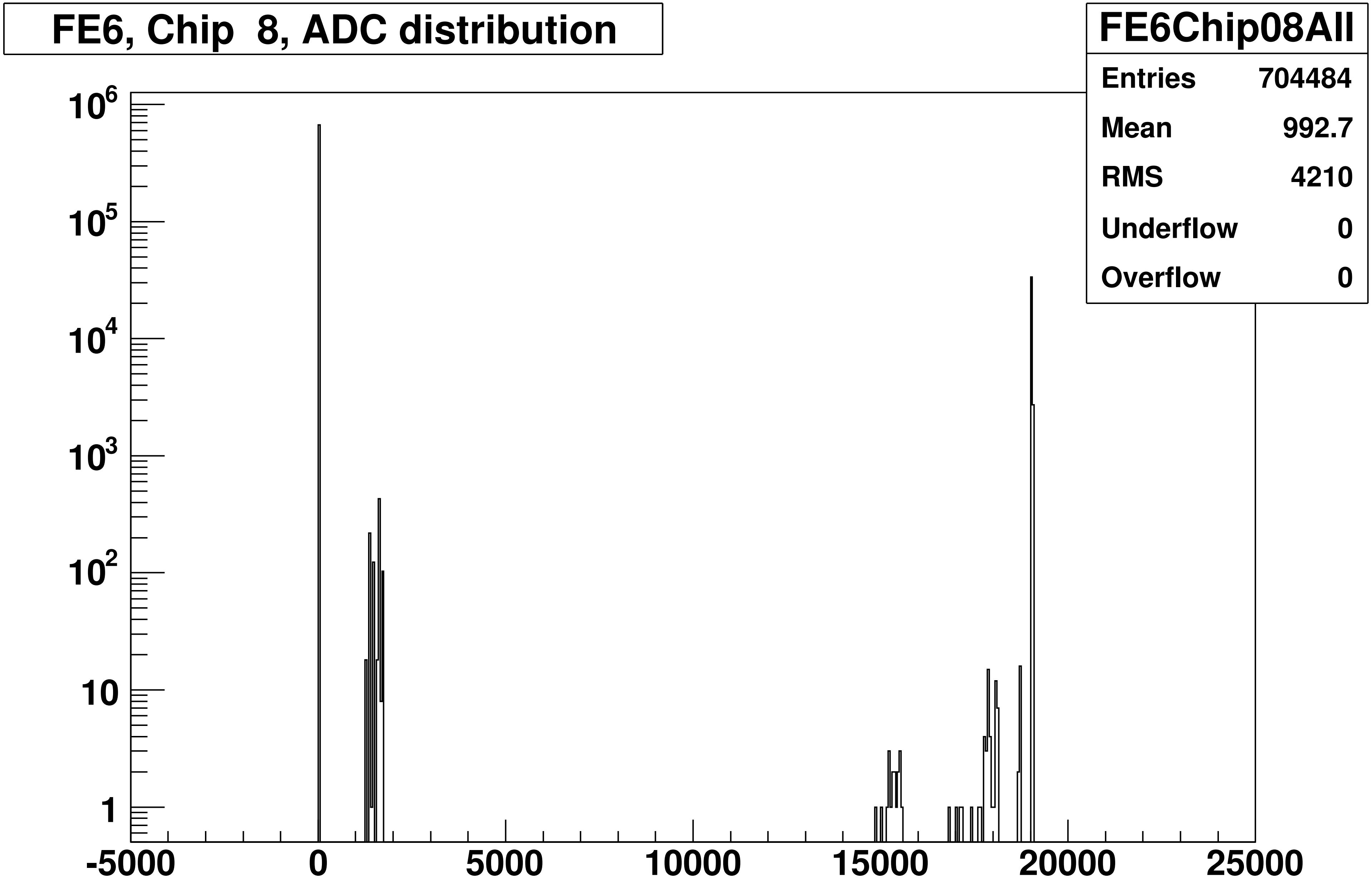

FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 704484
Mean 1.918e+04
RMS 18.05
Underflow 6.675e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 704484
Mean 992.7
RMS 4210
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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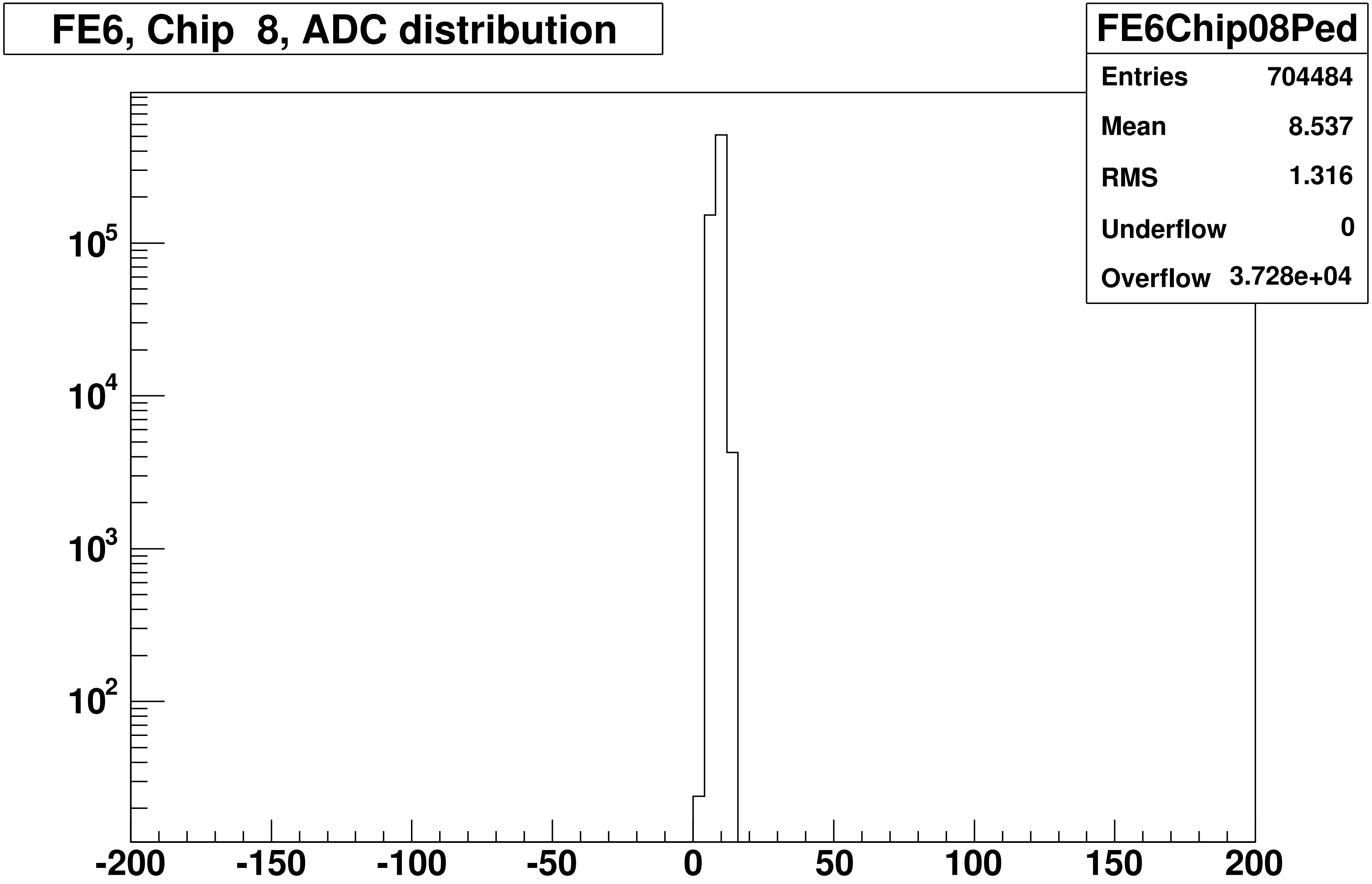
FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 704484
Mean 8.537
RMS 1.316
Underflow 0
Overflow 3.728e+04
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10^4
10^3
10^2
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150
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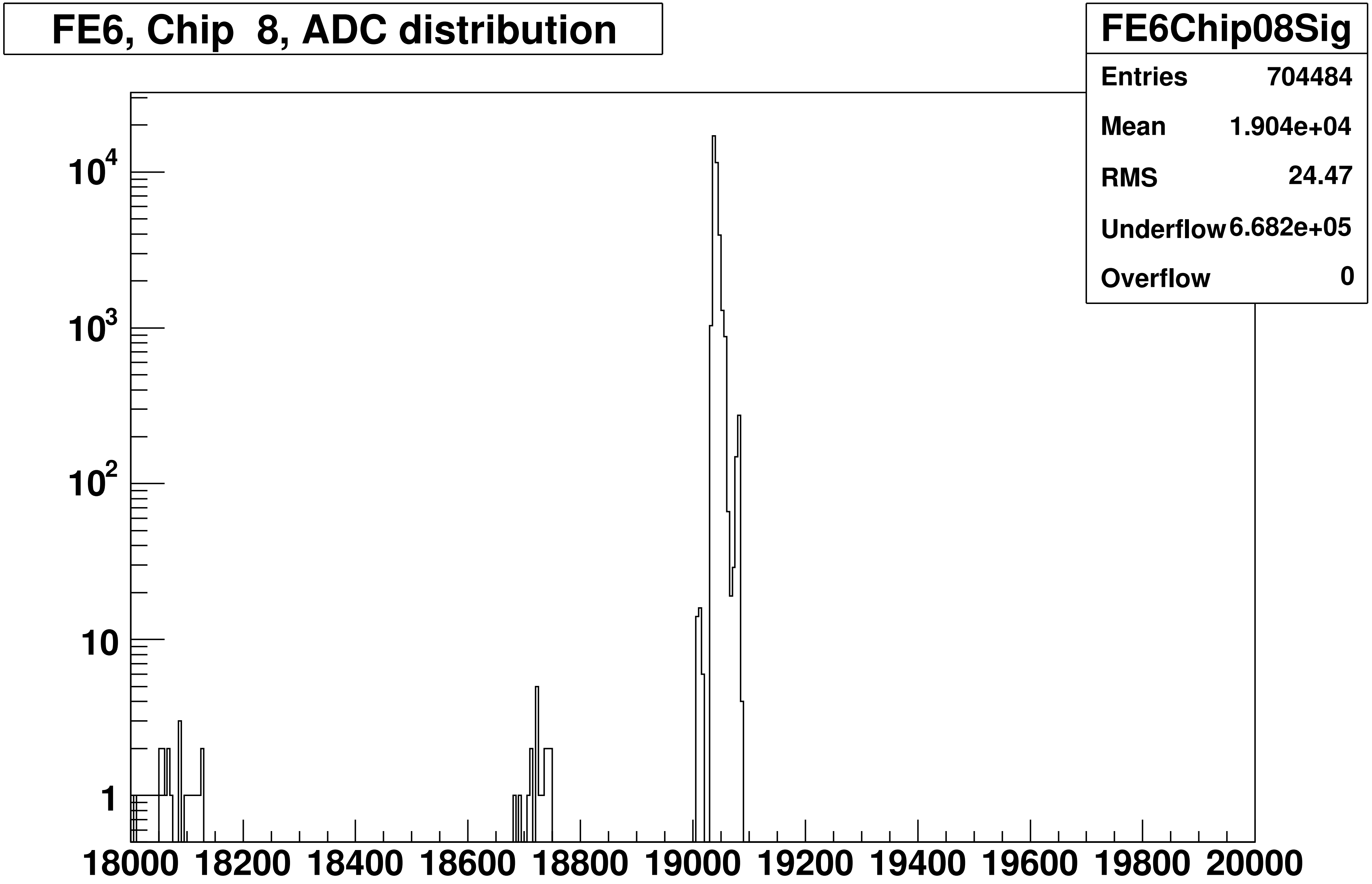

FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 704484
Mean 1.904e+04
RMS 24.47
Underflow 6.682e+05
Overflow 0
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10^3
10^2
10
1
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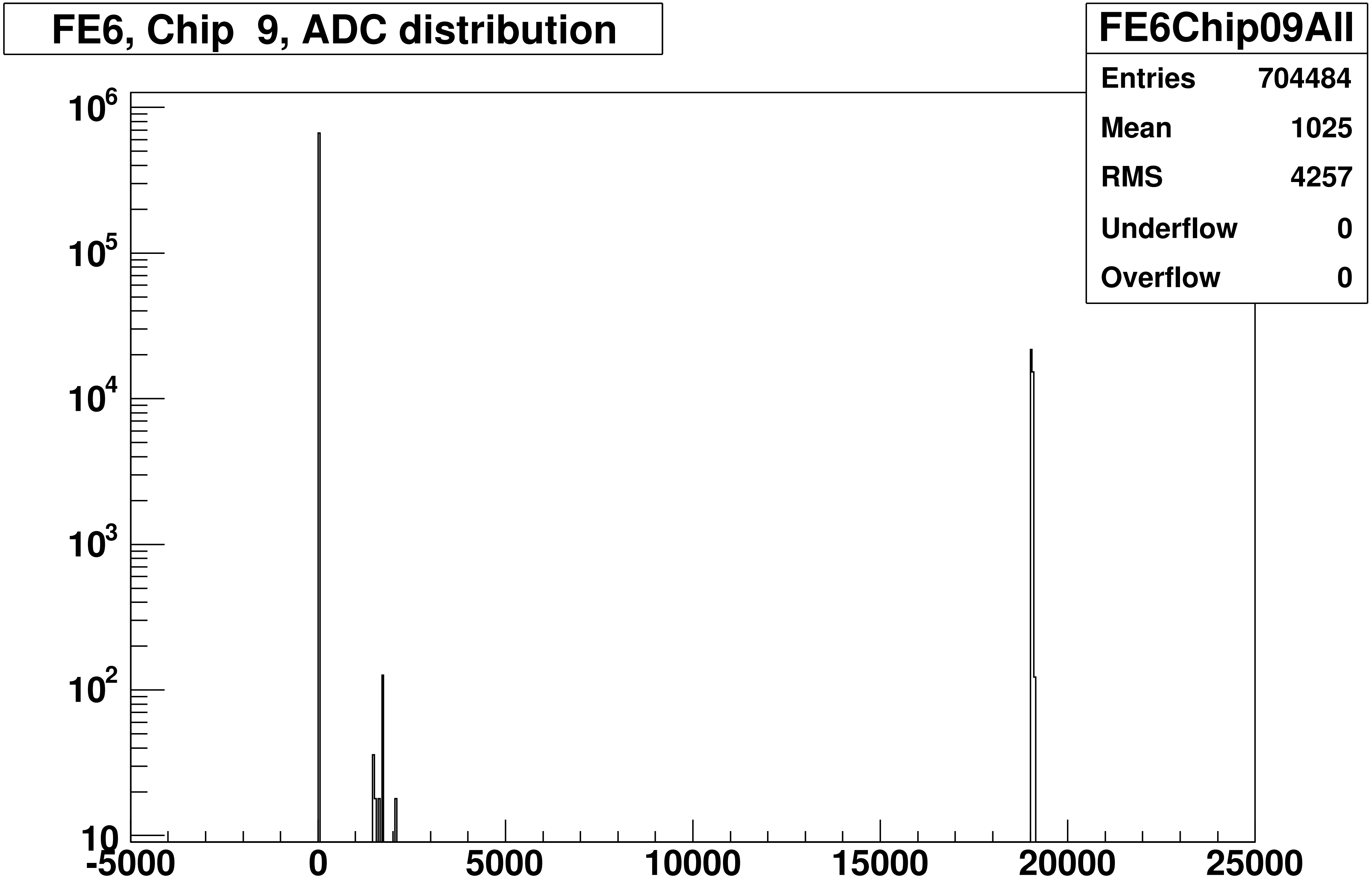

FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 704484
Mean 1025
RMS 4257
Underflow 0
Overflow 0
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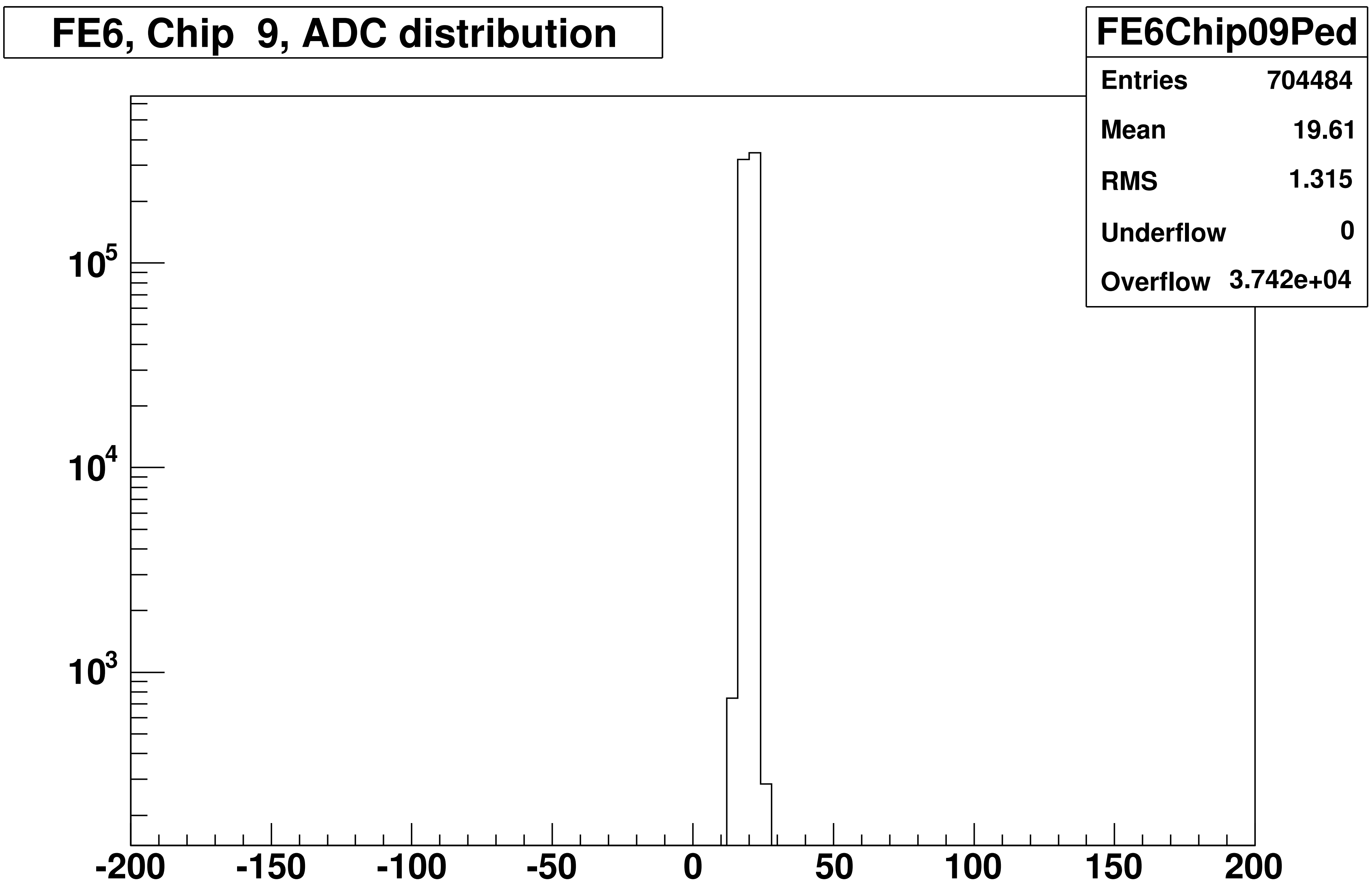
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 704484
Mean 19.61
RMS 1.315
Underflow 0
Overflow 3.742e+04
10^5
10^4
10^3
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-150
-100
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0
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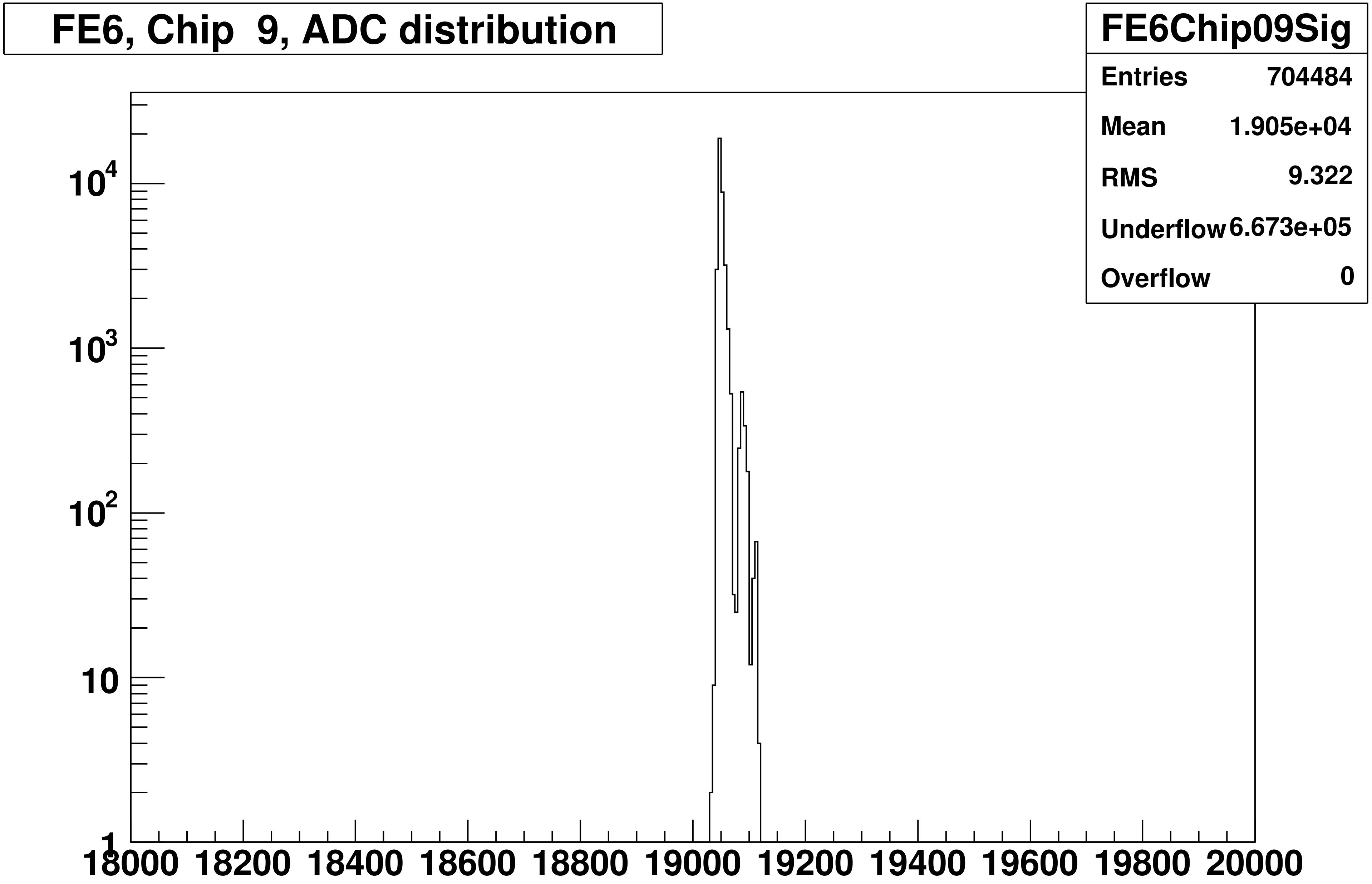
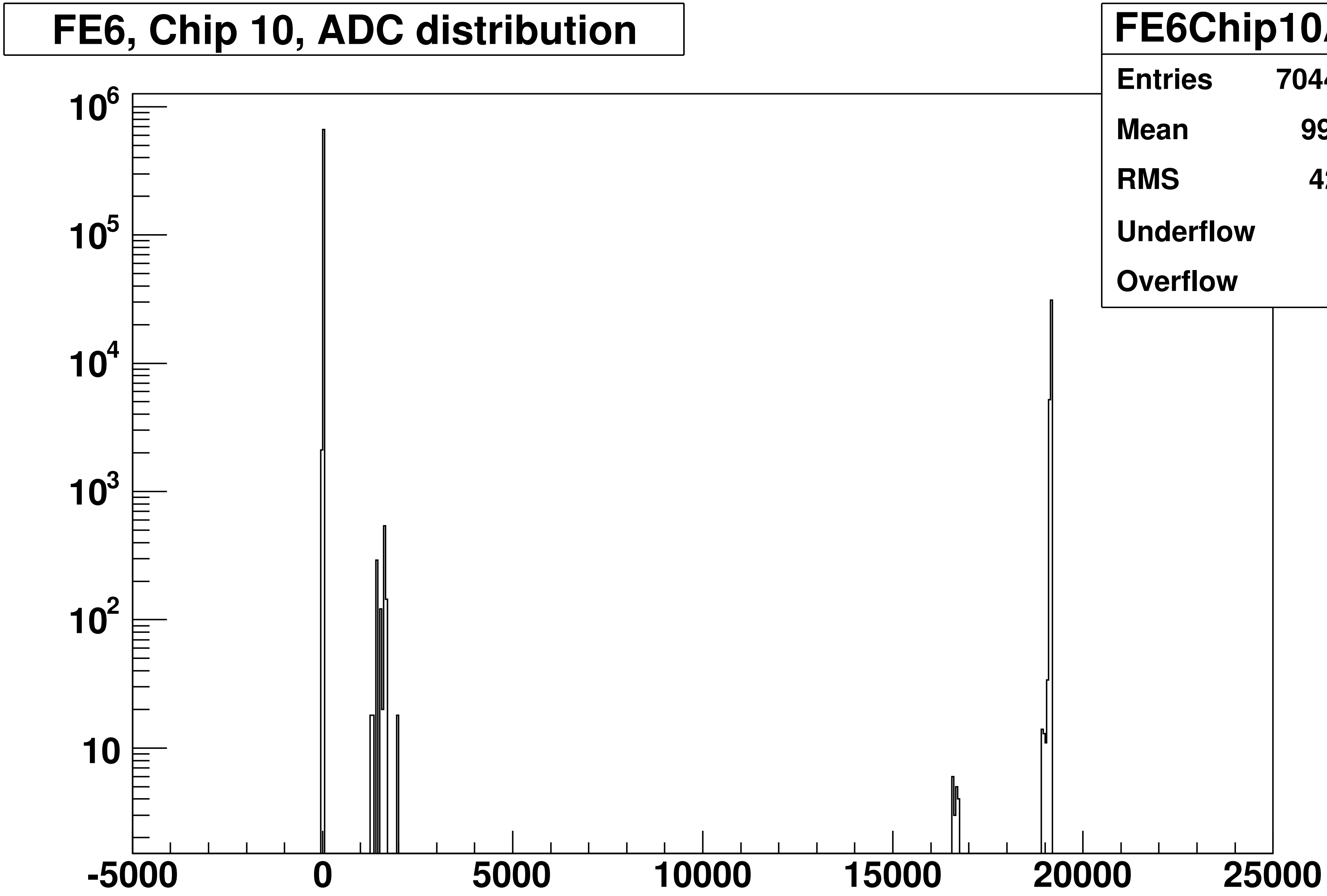
150
200



FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 704484
Mean 1.905e+04
RMS 9.322
Underflow 6.673e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 704484
Mean 991.6
RMS 4232
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
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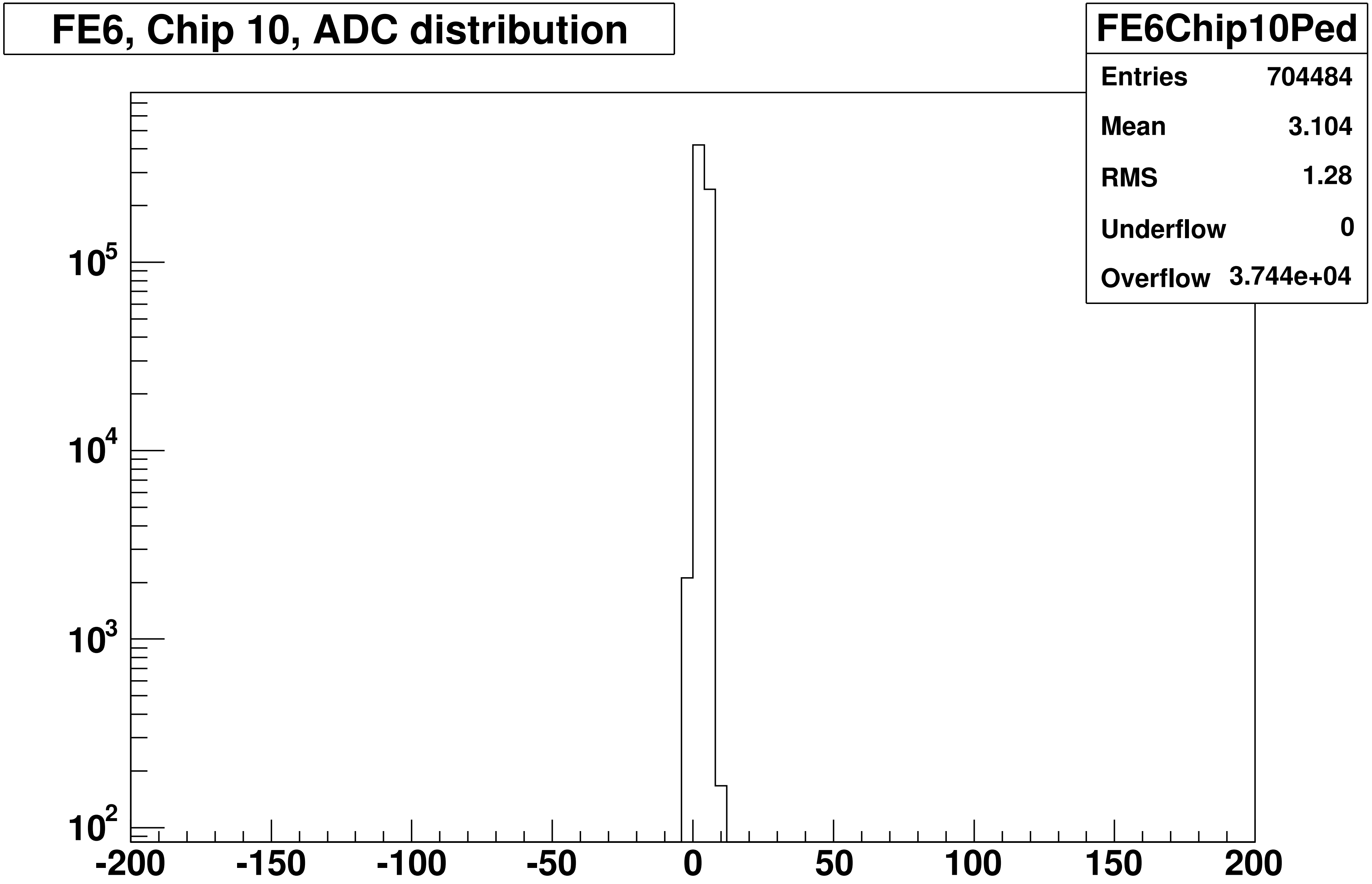
20000
25000


FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 704484
Mean 3.104
RMS 1.28
Underflow 0
Overflow 3.744e+04
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10^4
10^3
10^2
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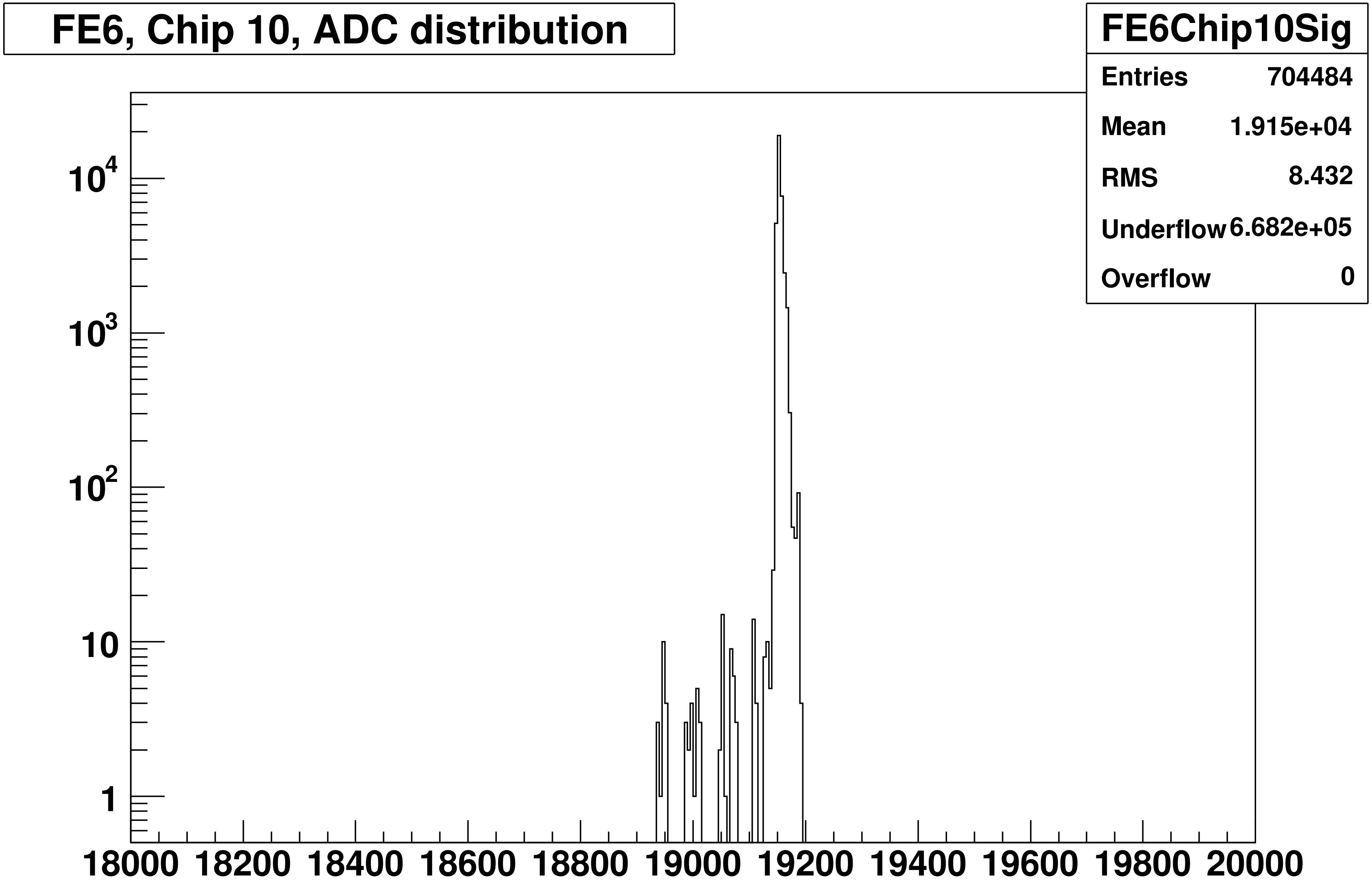
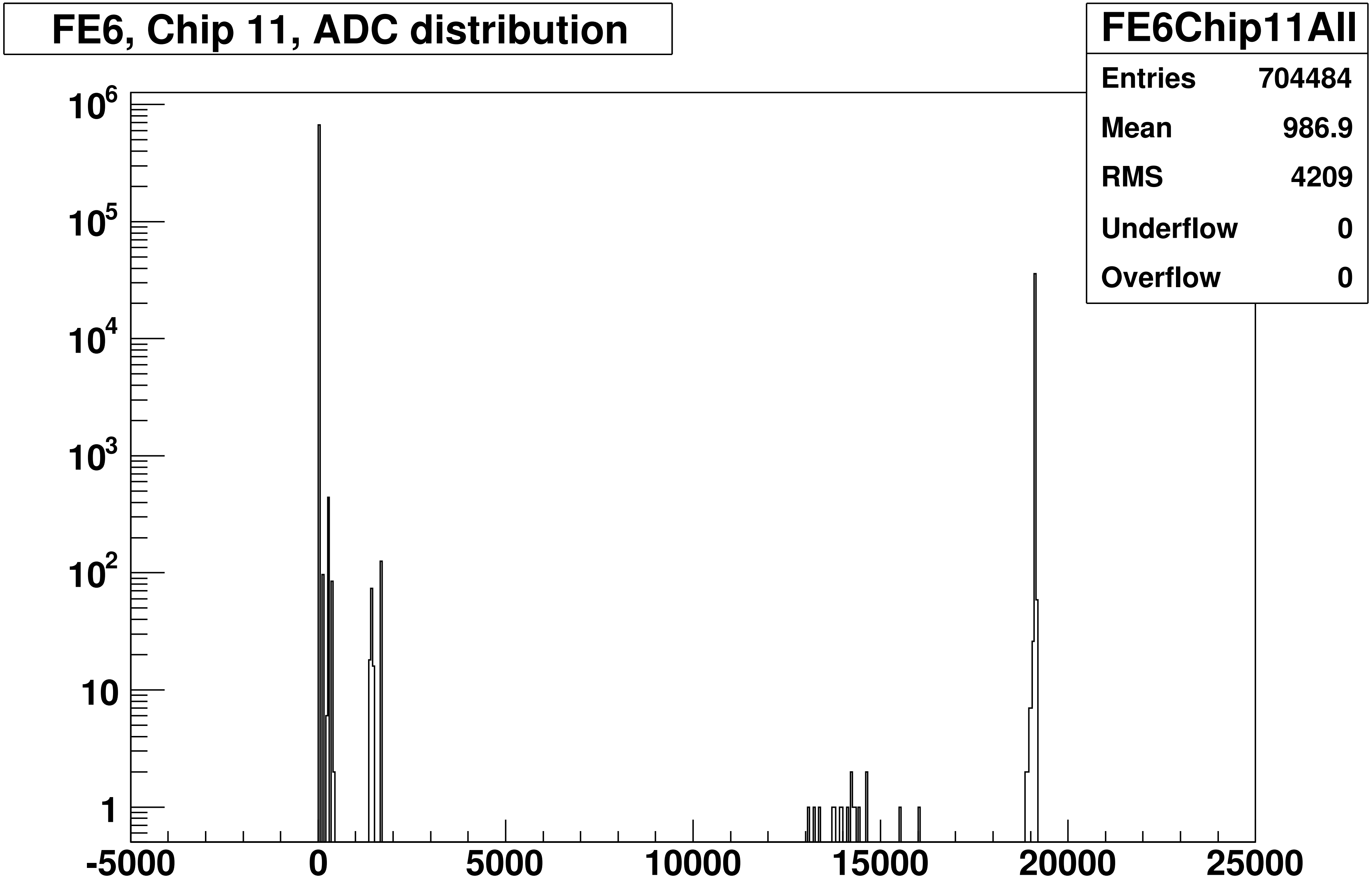
0
50
100
150
200


FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 704484
Mean 1.915e+04
RMS 8.432
Underflow 6.682e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 704484
Mean 986.9
RMS 4209
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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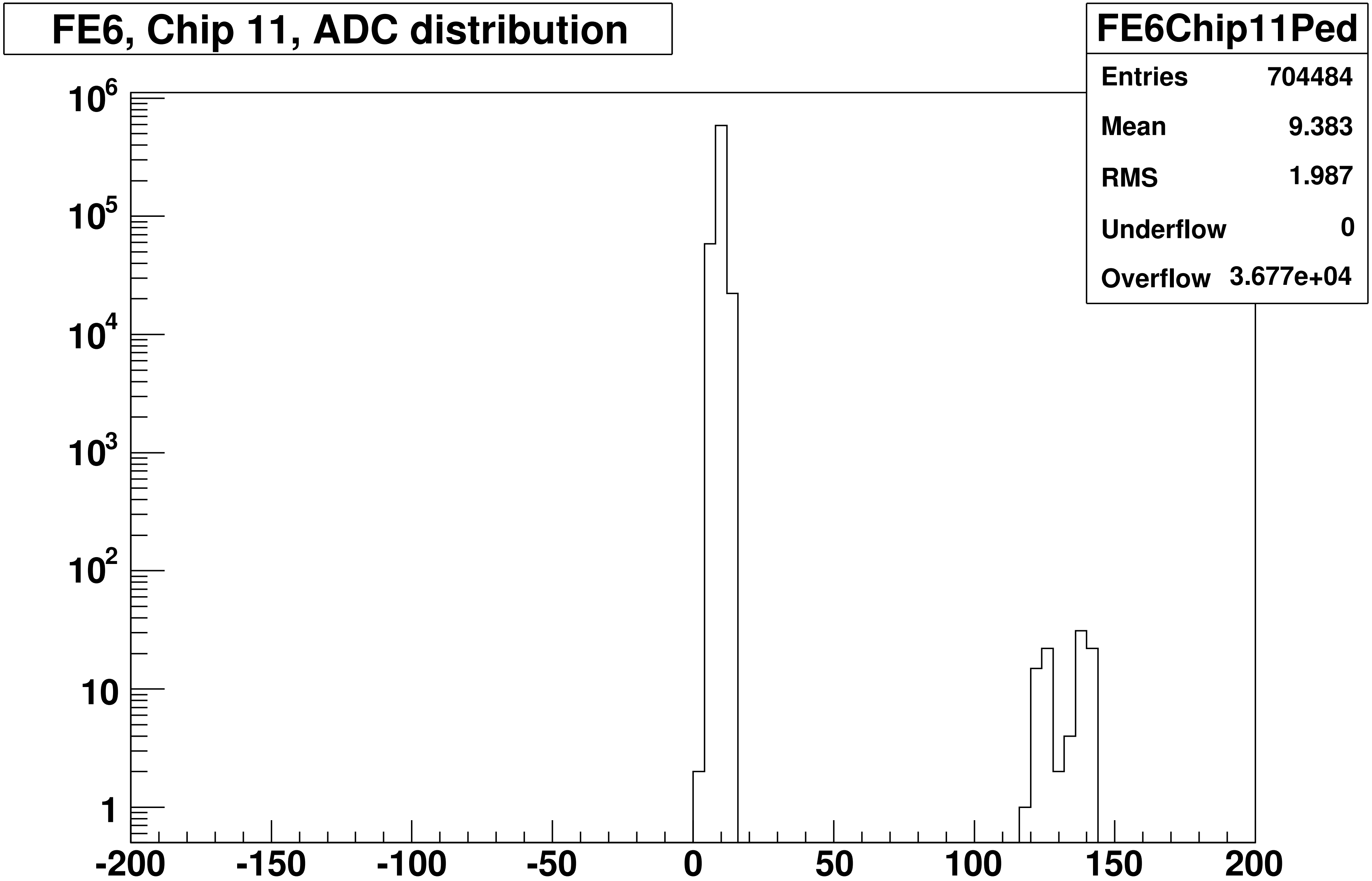
10000
15000
20000
25000


FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 704484
Mean 9.383
RMS 1.987
Underflow 0
Overflow 3.677e+04
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10^5
10^4
10^3
10^2
10
1
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-100
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0
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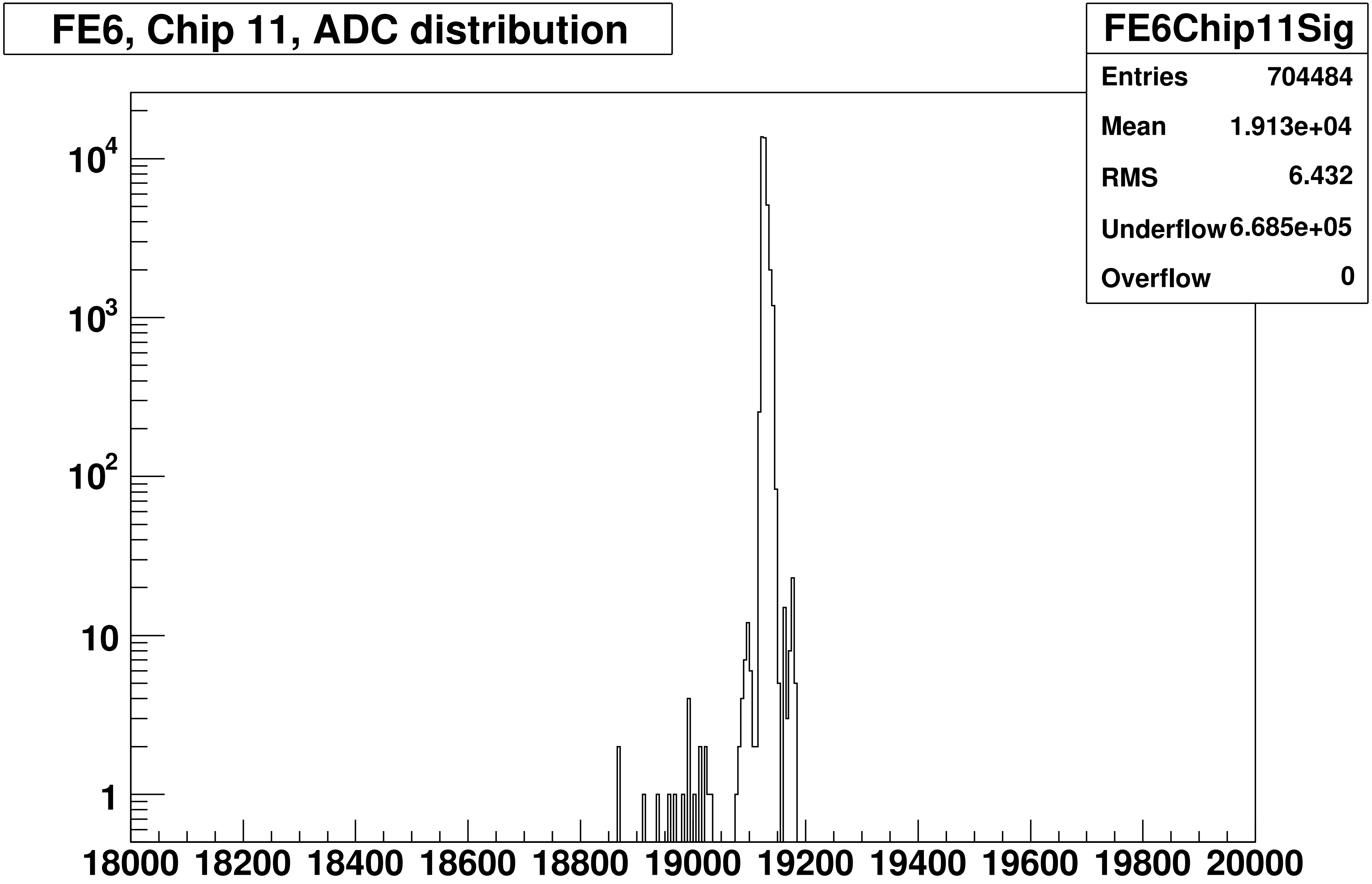
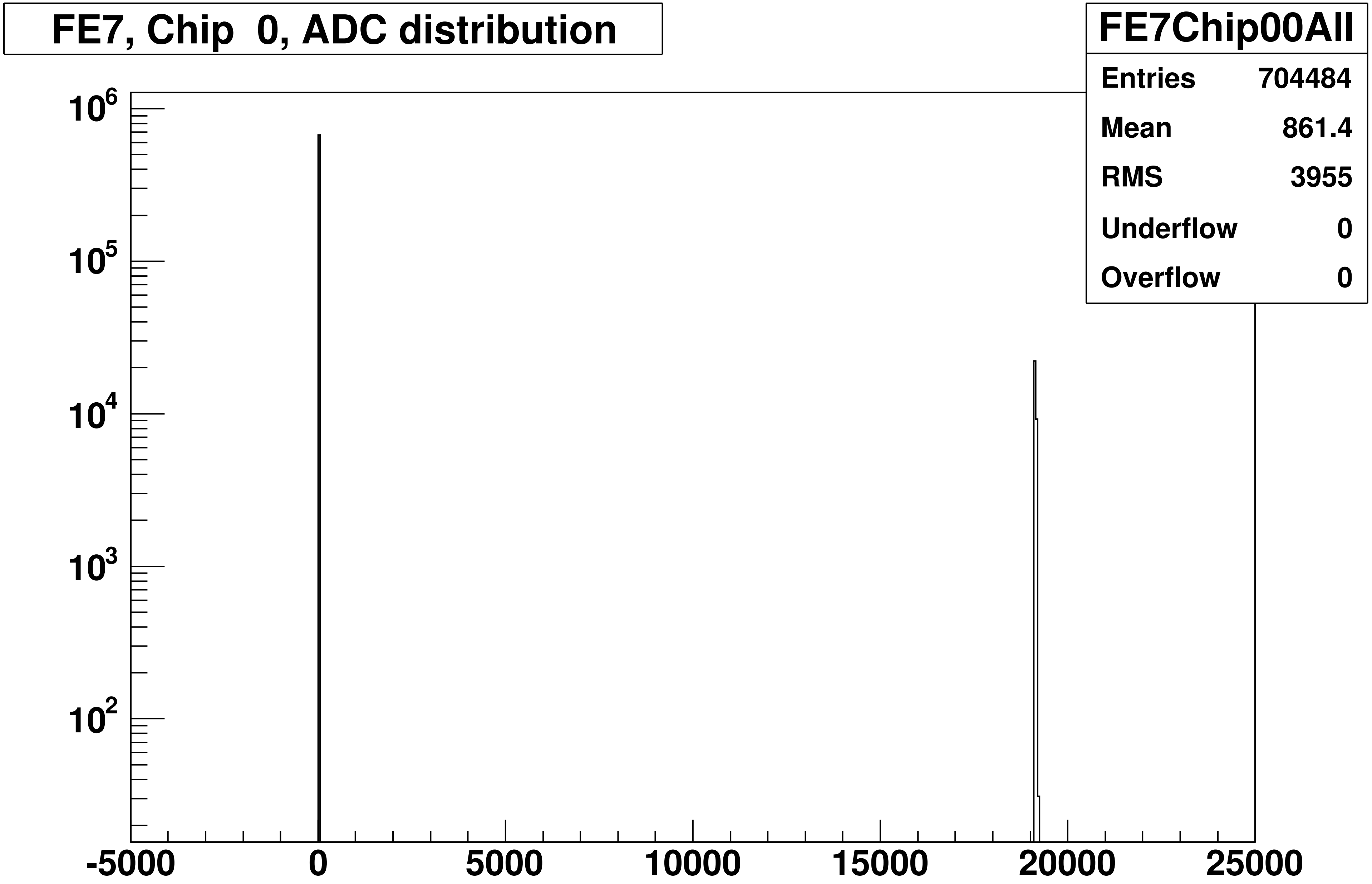
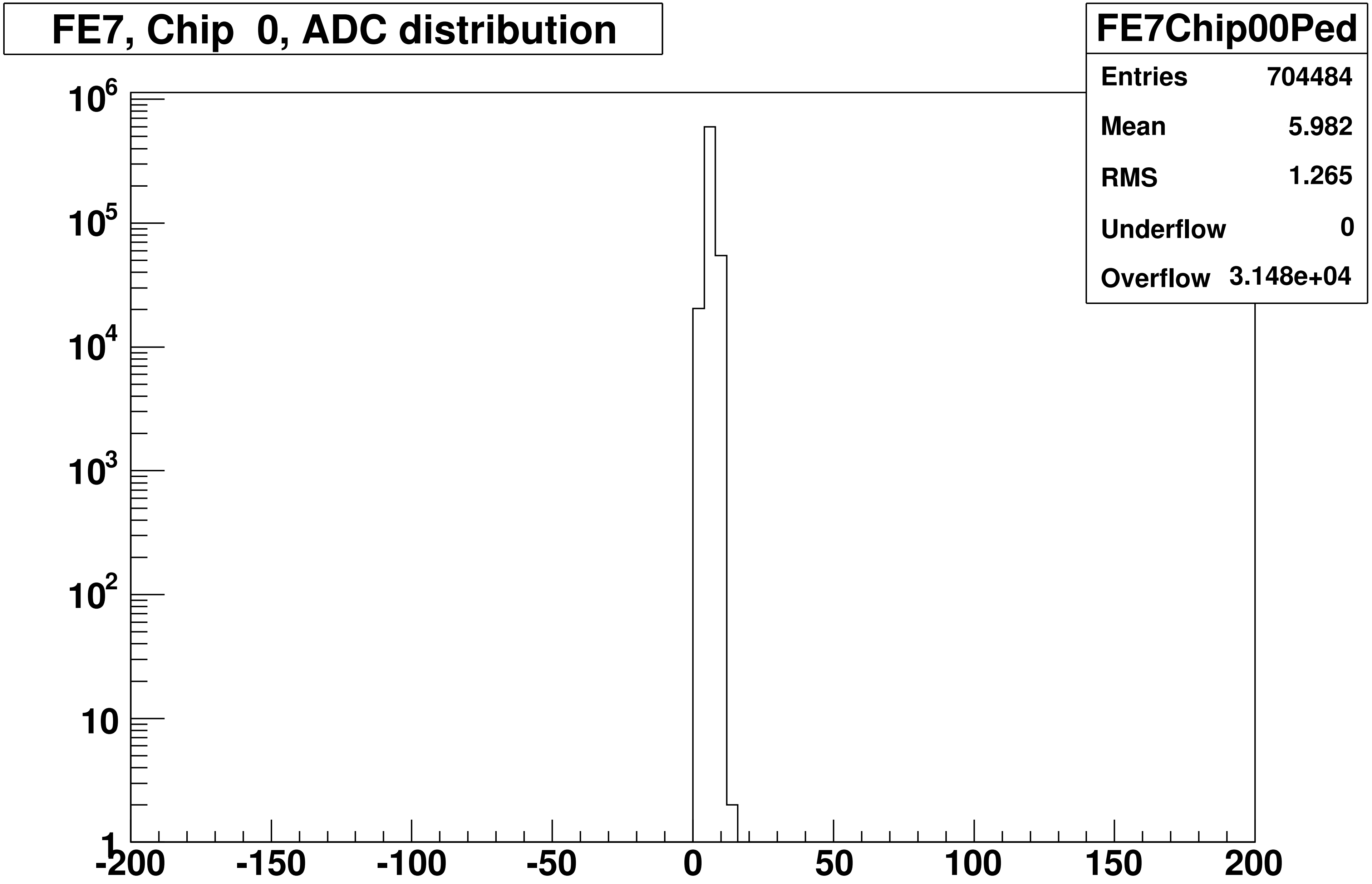
100
150
200


FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 704484
Mean 1.913e+04
RMS 6.432
Underflow 6.685e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 704484
Mean 861.4
RMS 3955
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 704484
Mean 5.982
RMS 1.265
Underflow 0
Overflow 3.148e+04
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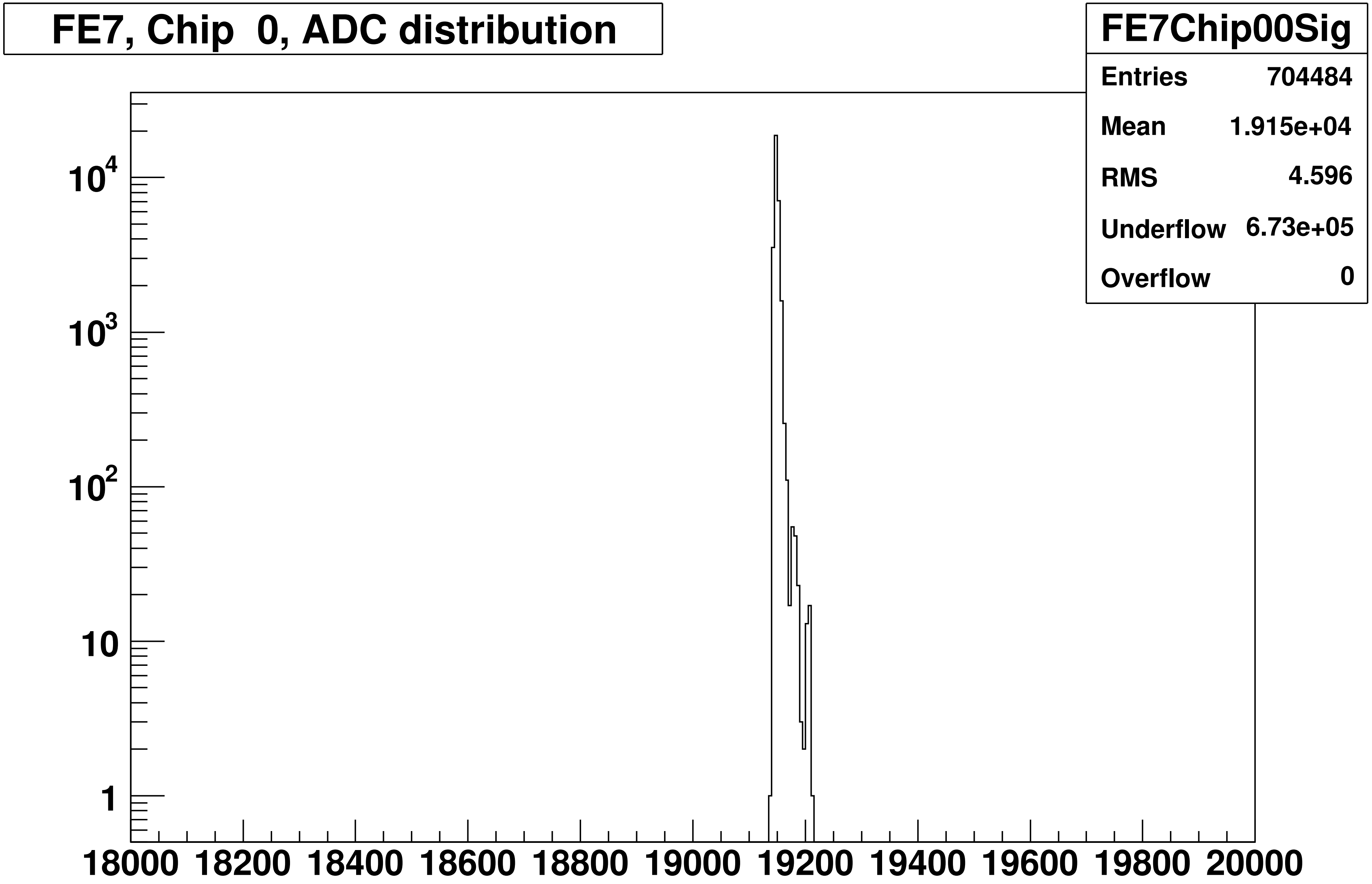
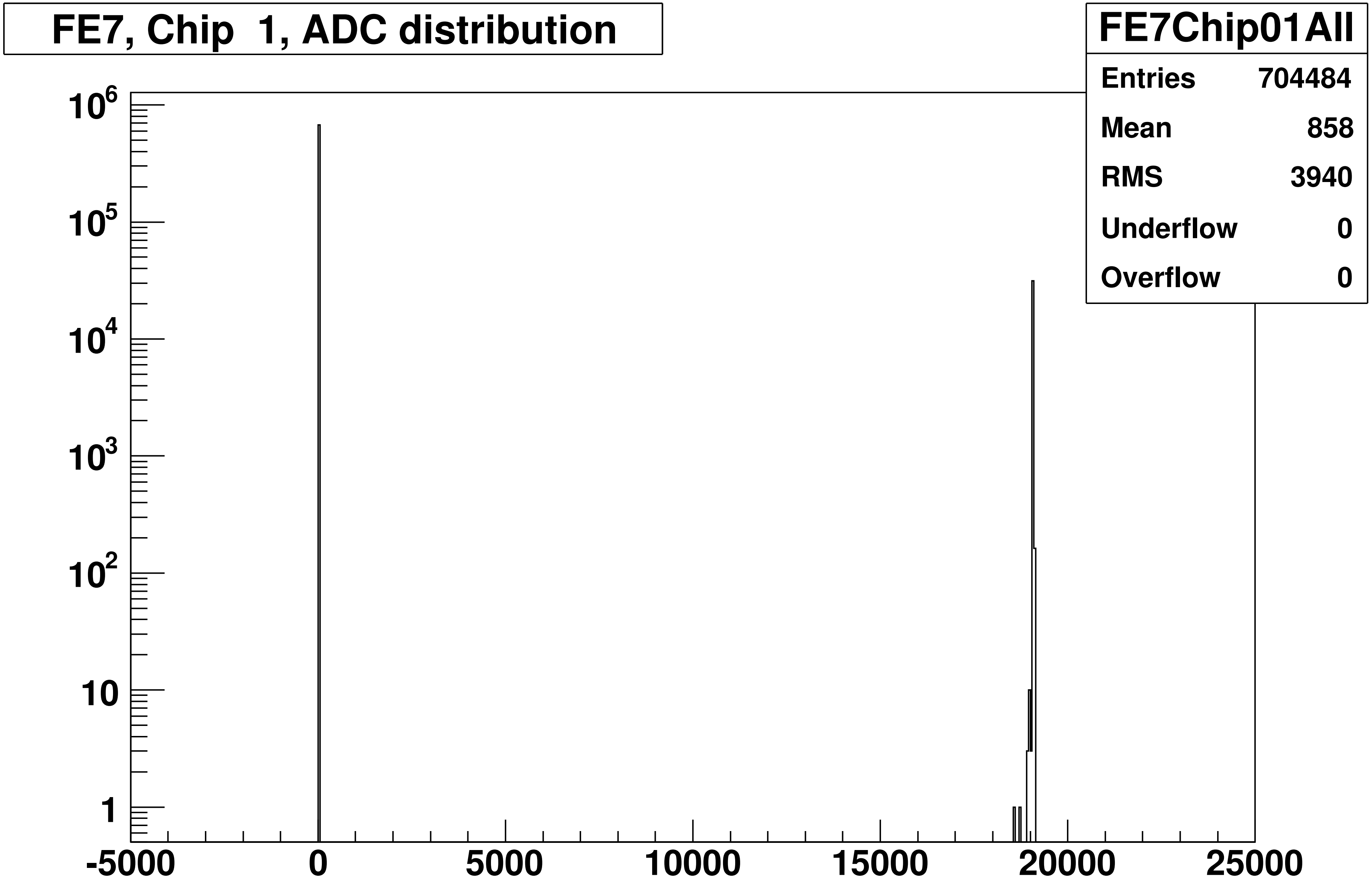
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 704484
Mean 1.915e+04
RMS 4.596
Underflow 6.73e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 704484
Mean 858
RMS 3940
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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10000
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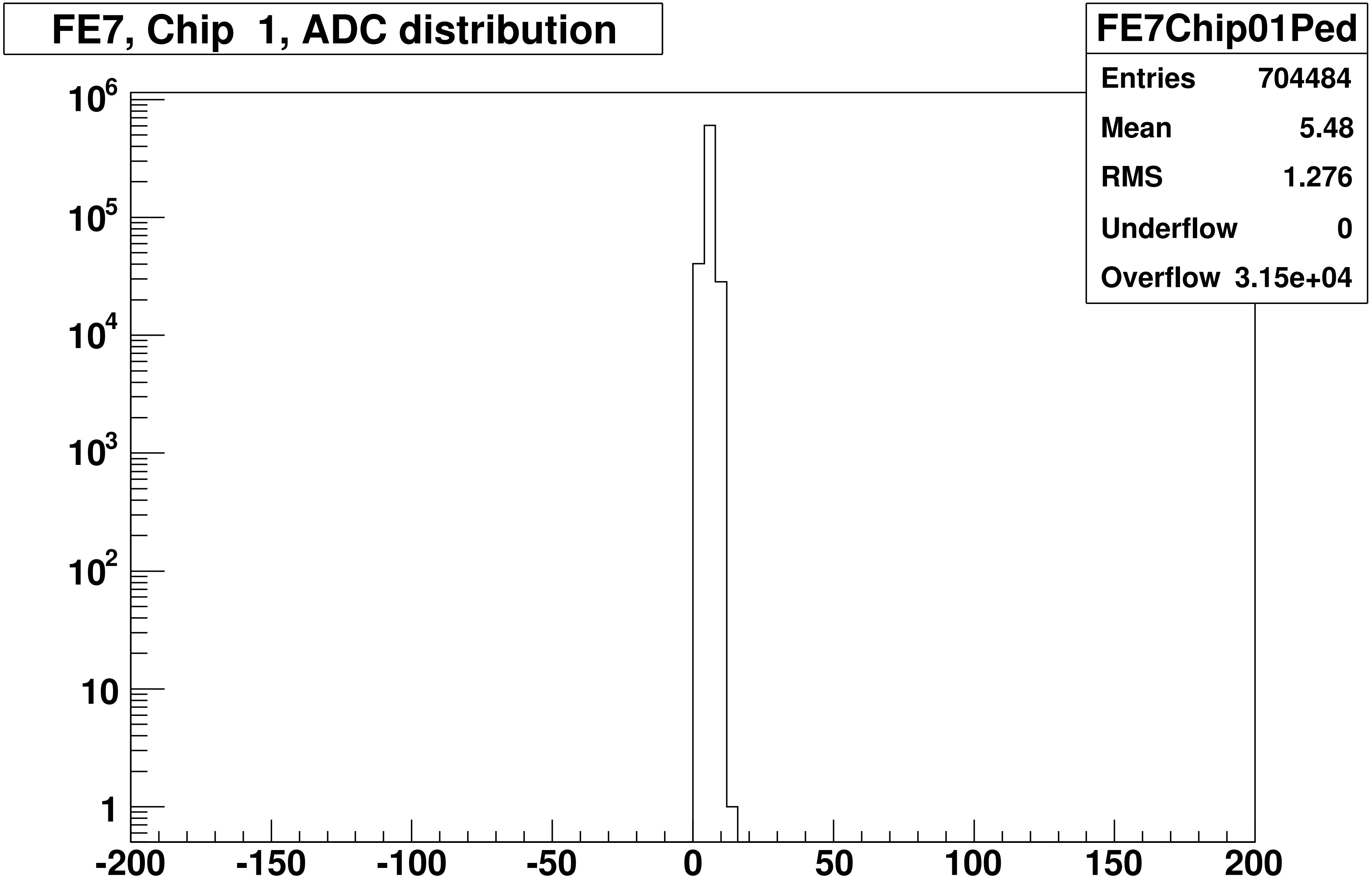
20000
25000


FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 704484
Mean 5.48
RMS 1.276
Underflow 0
Overflow 3.15e+04
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10^5
10^4
10^3
10^2
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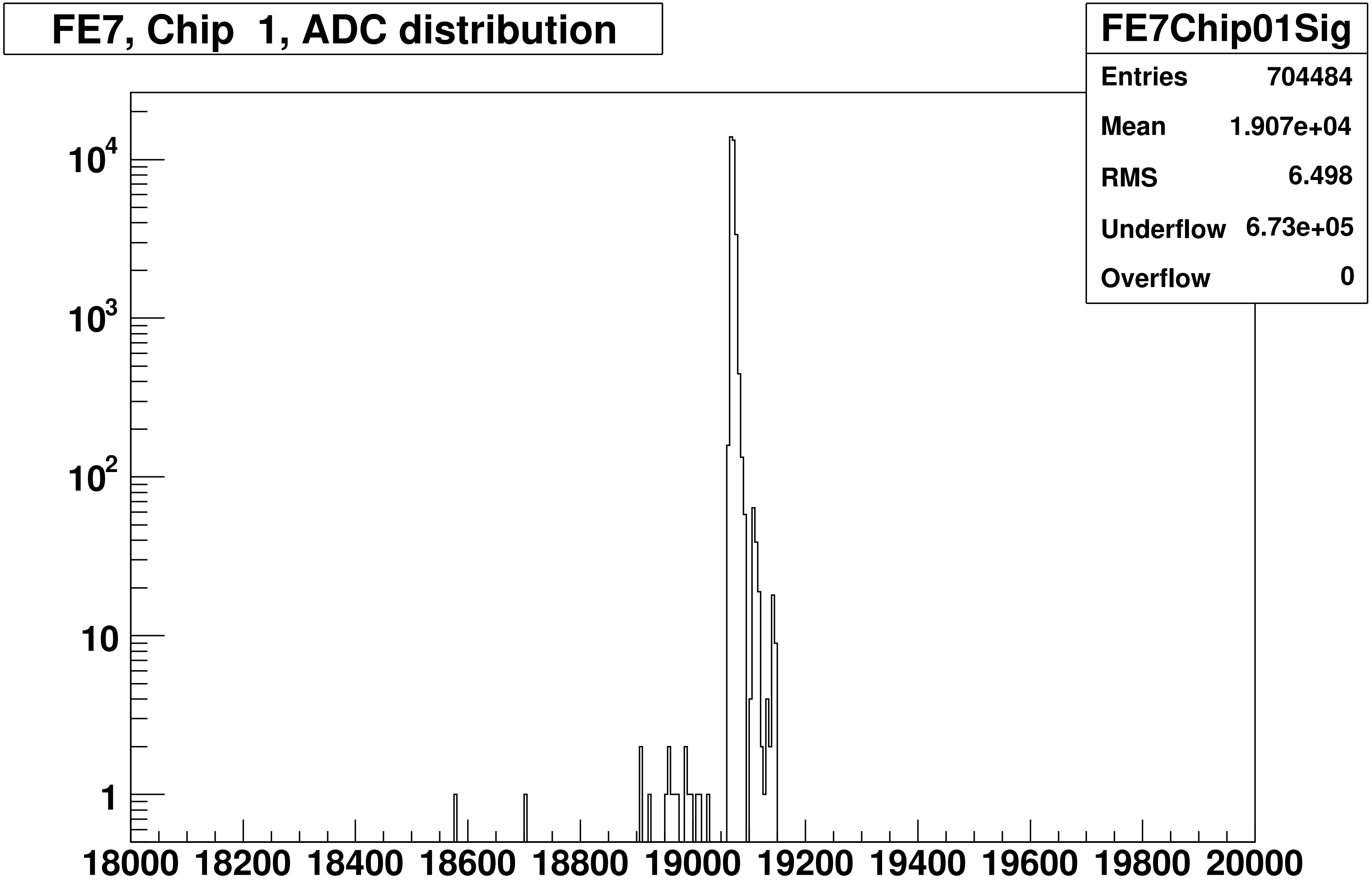
150
200



FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 704484
Mean 1.907e+04
RMS 6.498
Underflow 6.73e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
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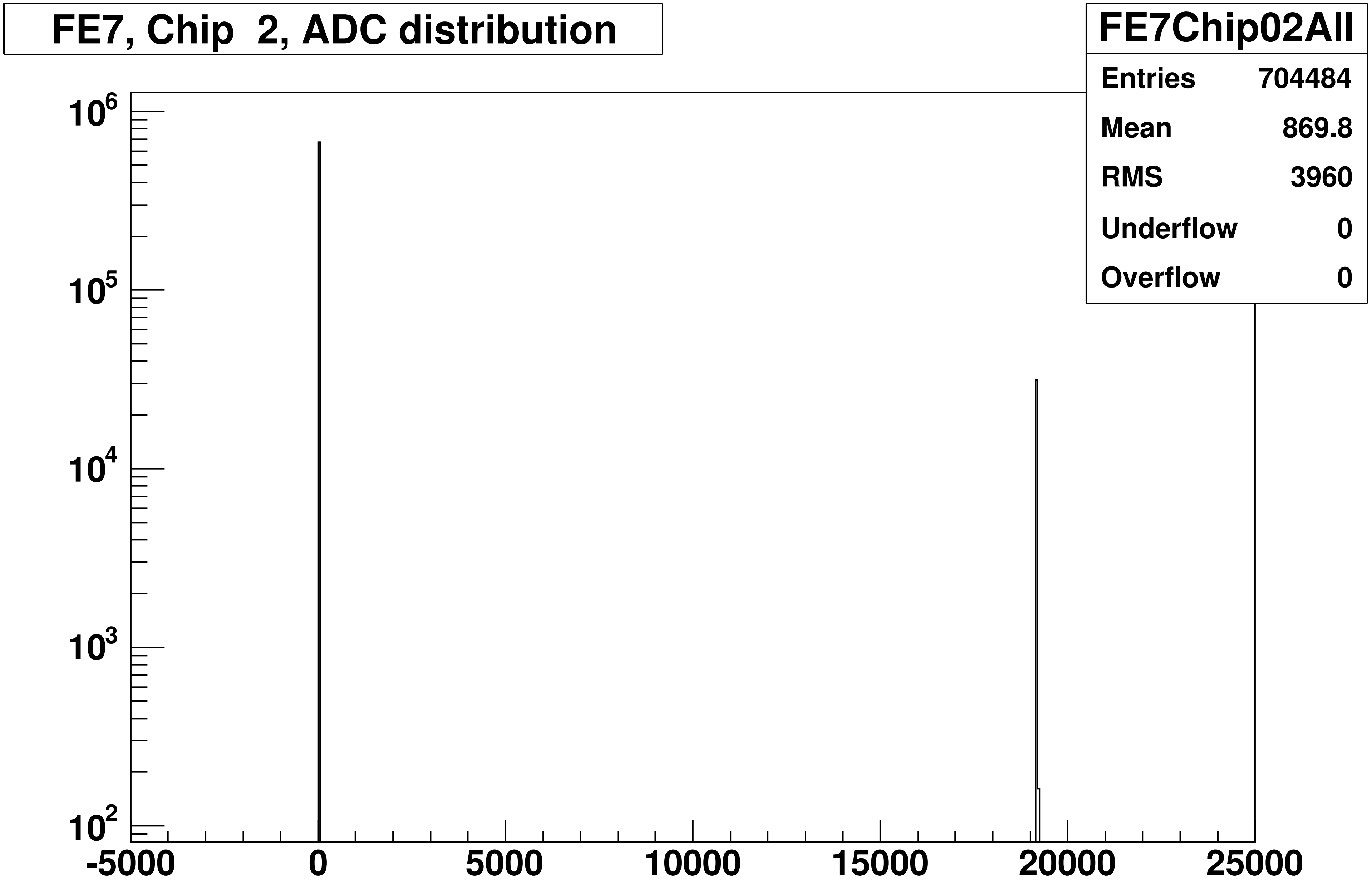
19800
20000


FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 704484
Mean 869.8
RMS 3960
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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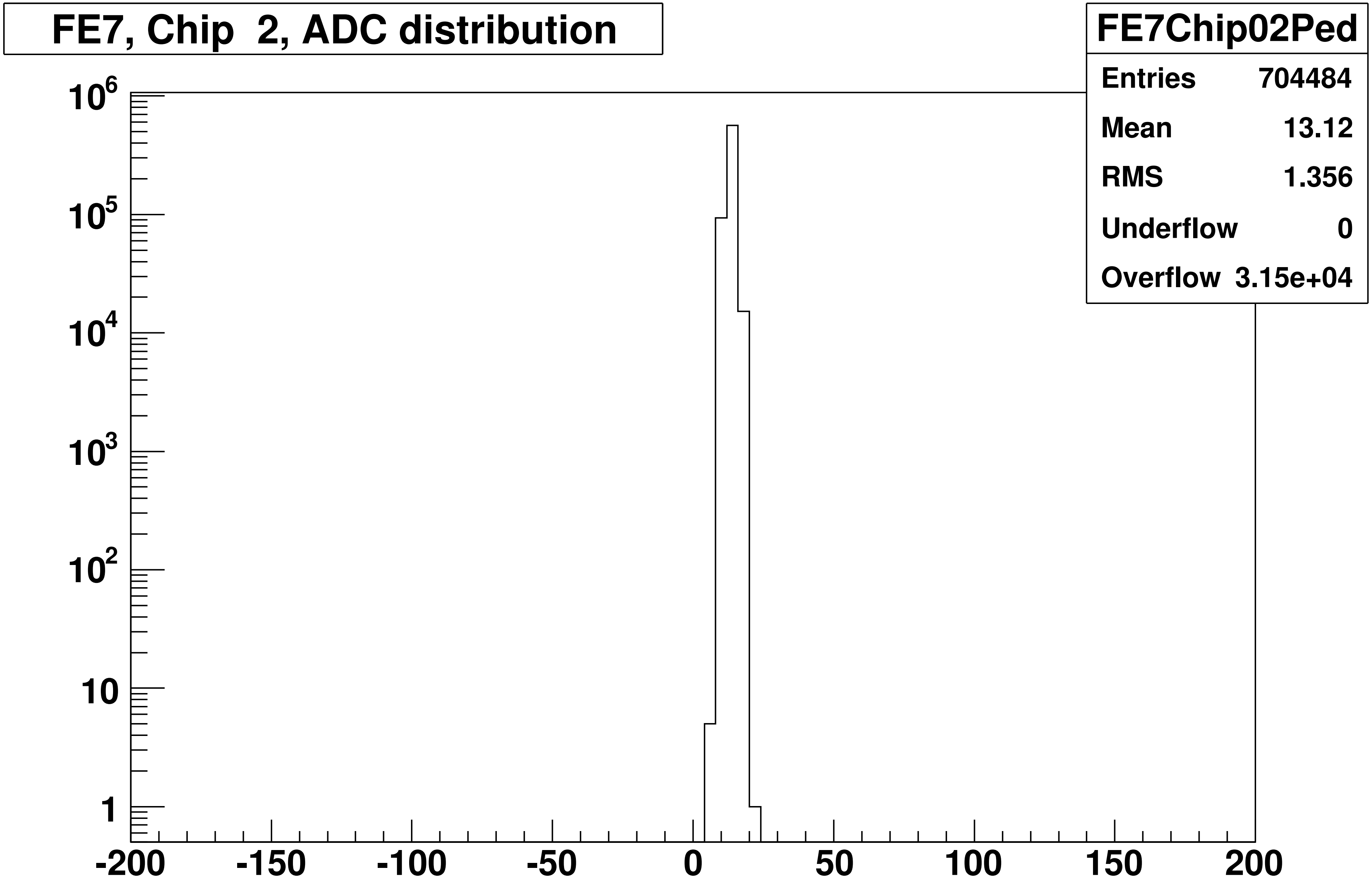
10000
15000
20000
25000


FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 704484
Mean 13.12
RMS 1.356
Underflow 0
Overflow 3.15e+04
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10^5
10^4
10^3
10^2
10
1
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-150
-100
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0
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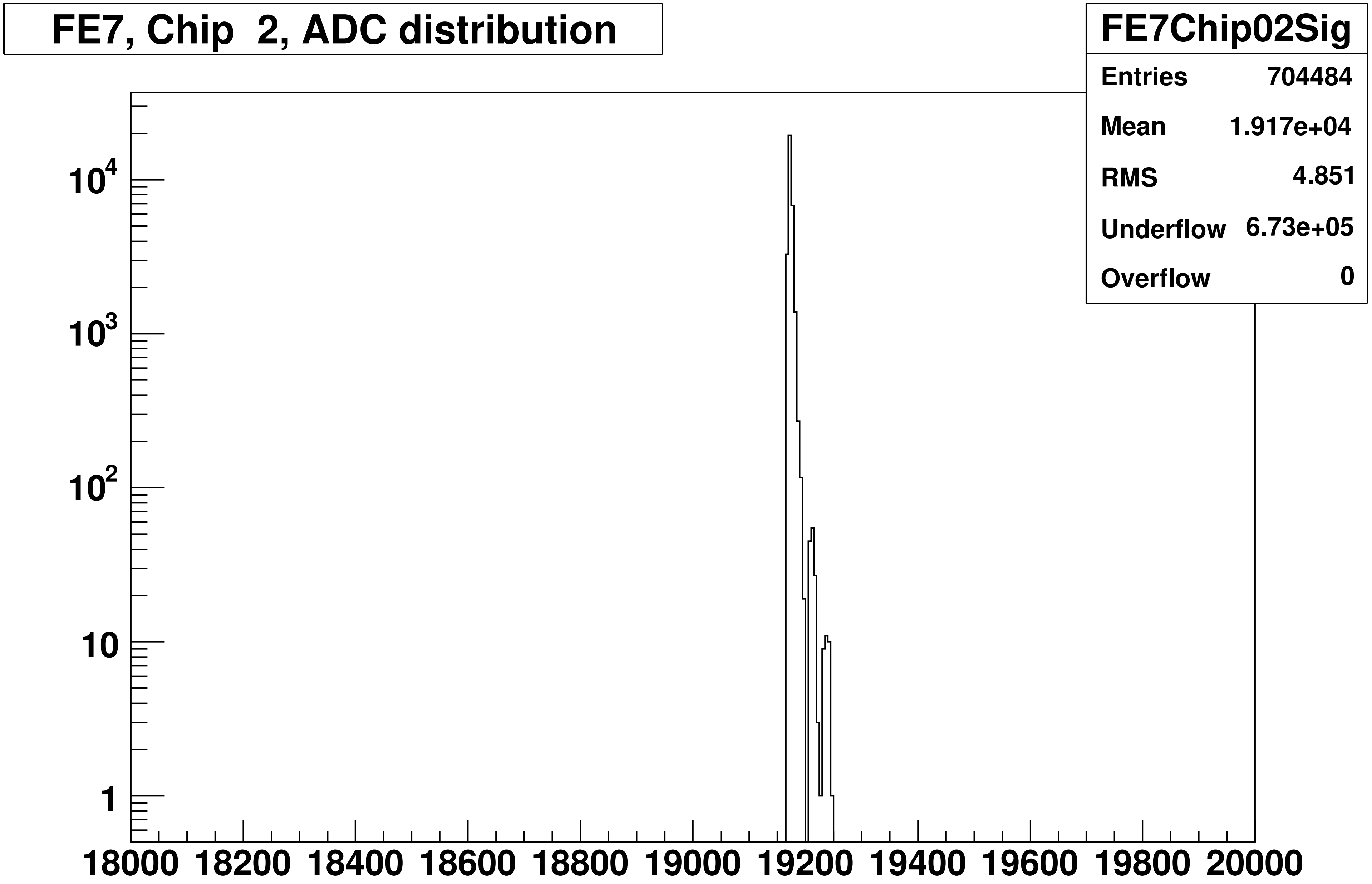
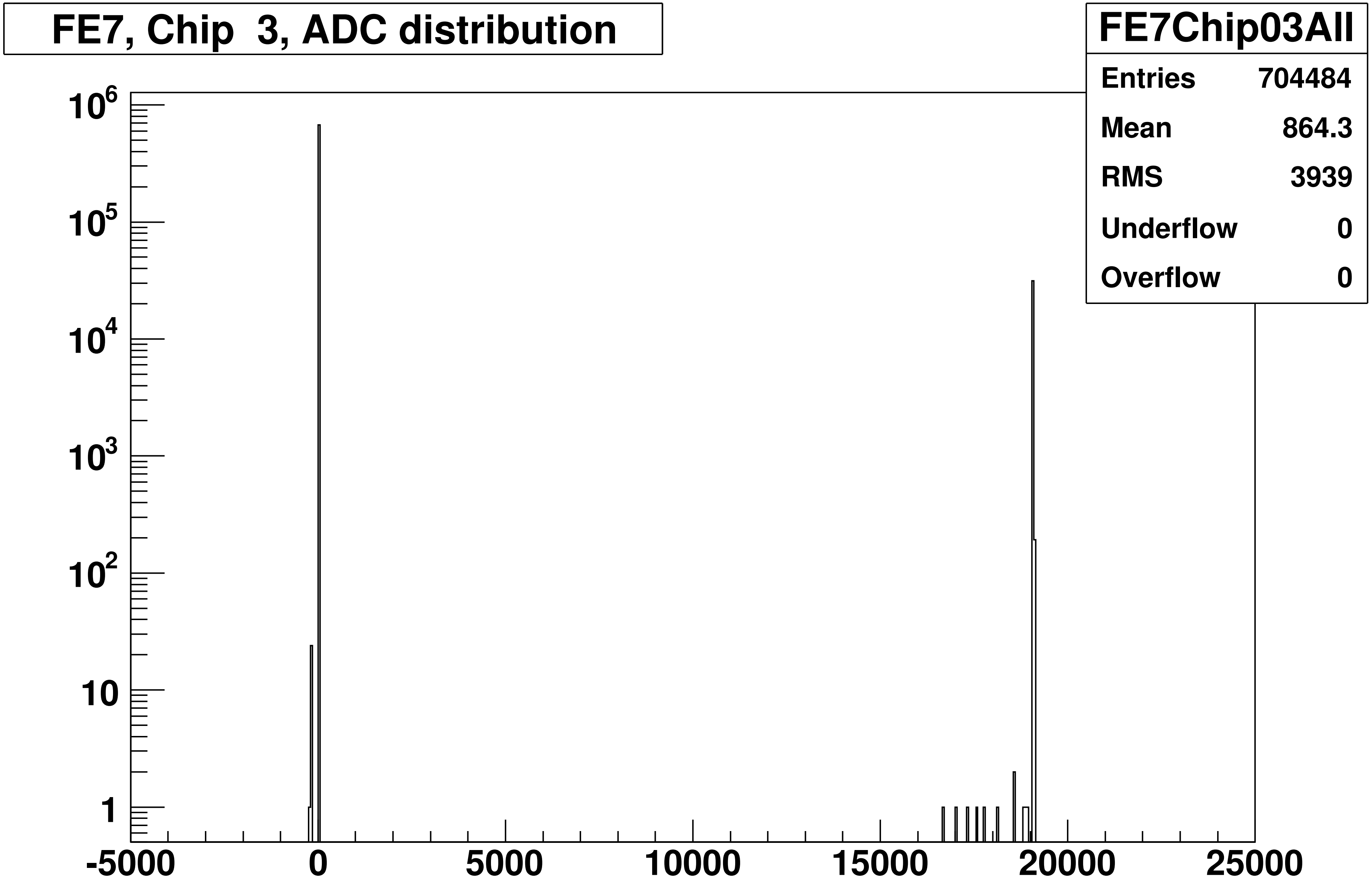
100
150
200


FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 704484
Mean 1.917e+04
RMS 4.851
Underflow 6.73e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 704484
Mean 864.3
RMS 3939
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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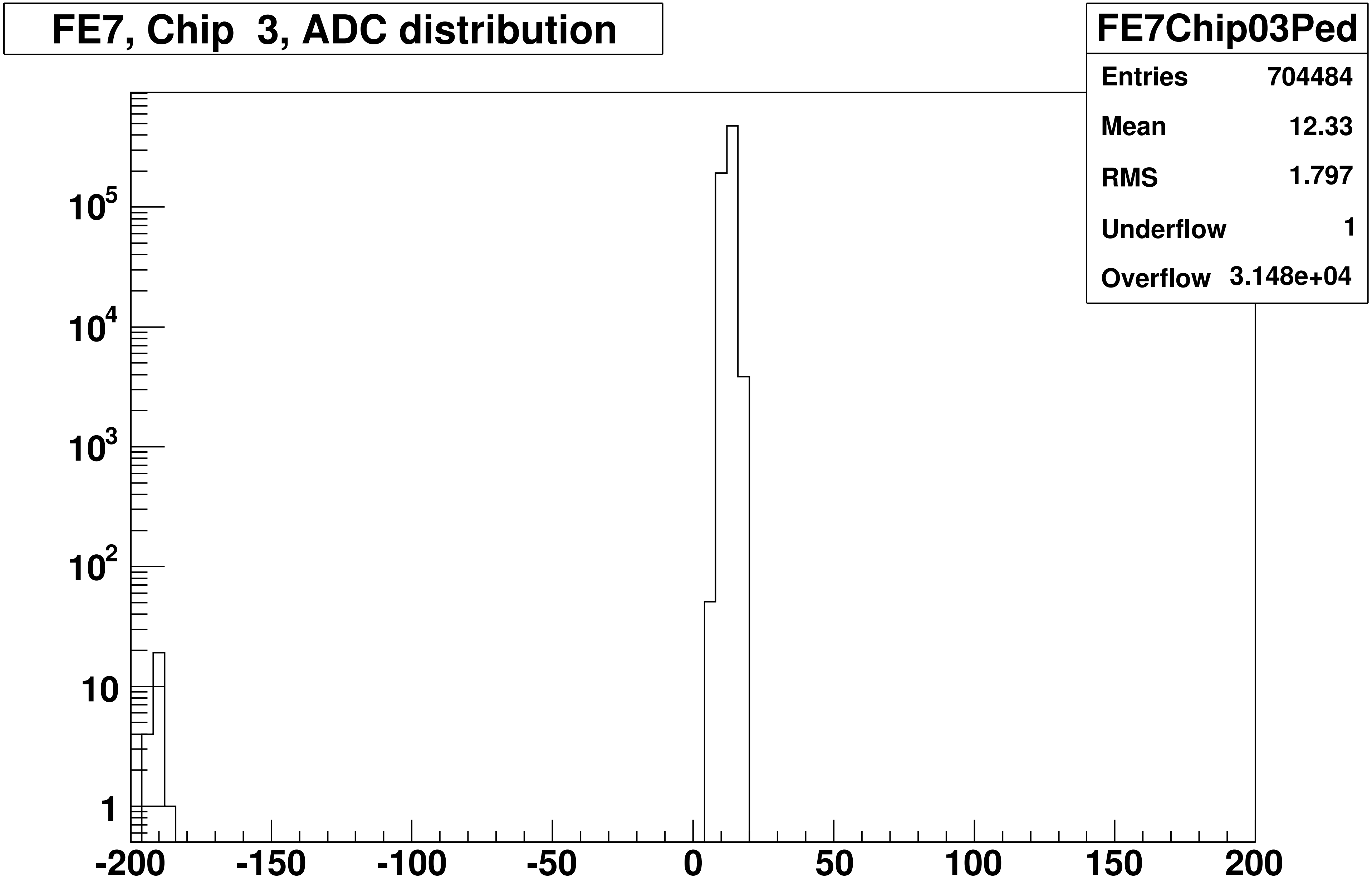
25000



FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 704484
Mean 12.33
RMS 1.797
Underflow 1
Overflow 3.148e+04
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10^4
10^3
10^2
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150
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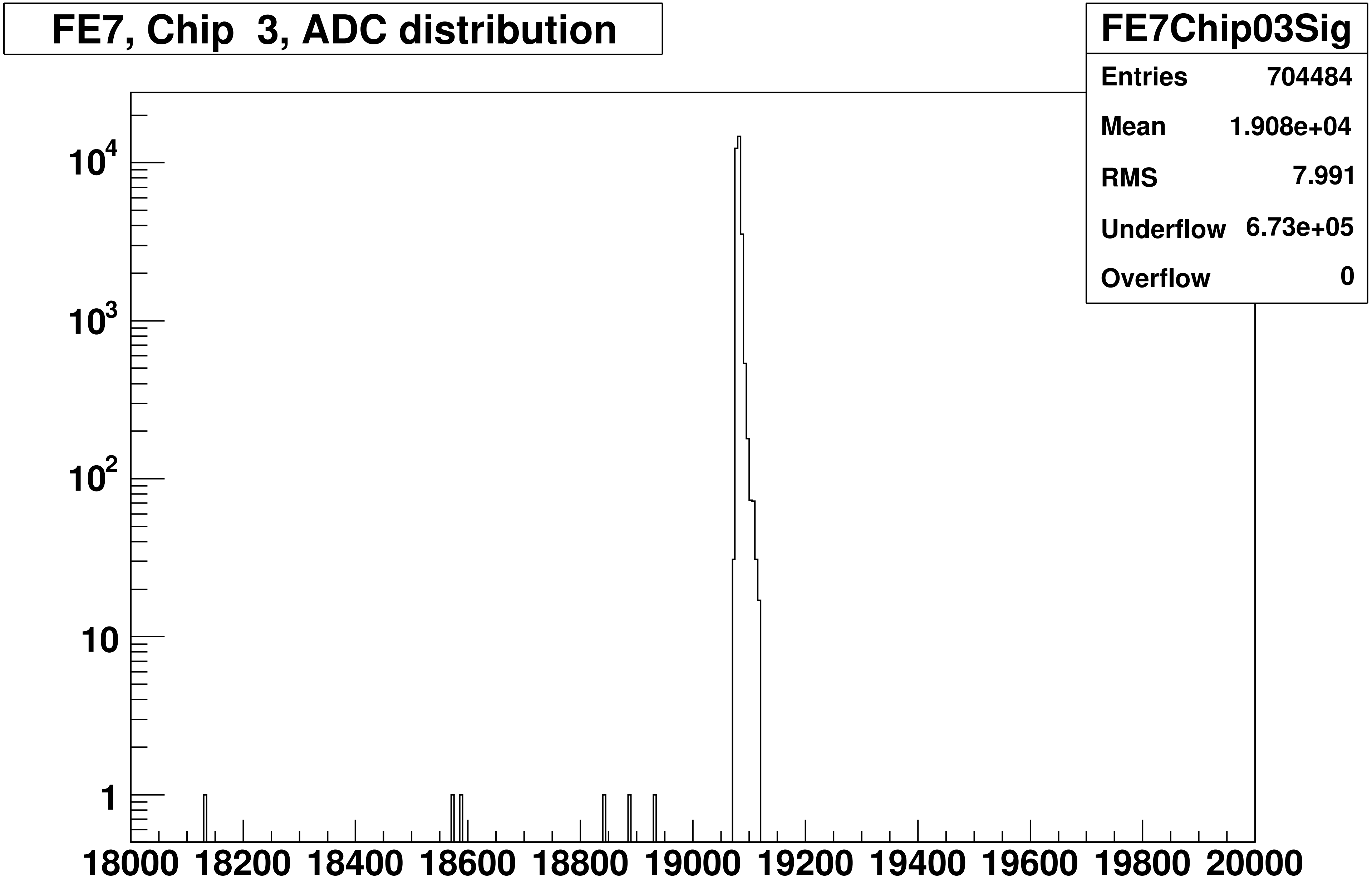
FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 704484
Mean 1.908e+04
RMS 7.991
Underflow 6.73e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
18400
18600
18800
19000
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19400
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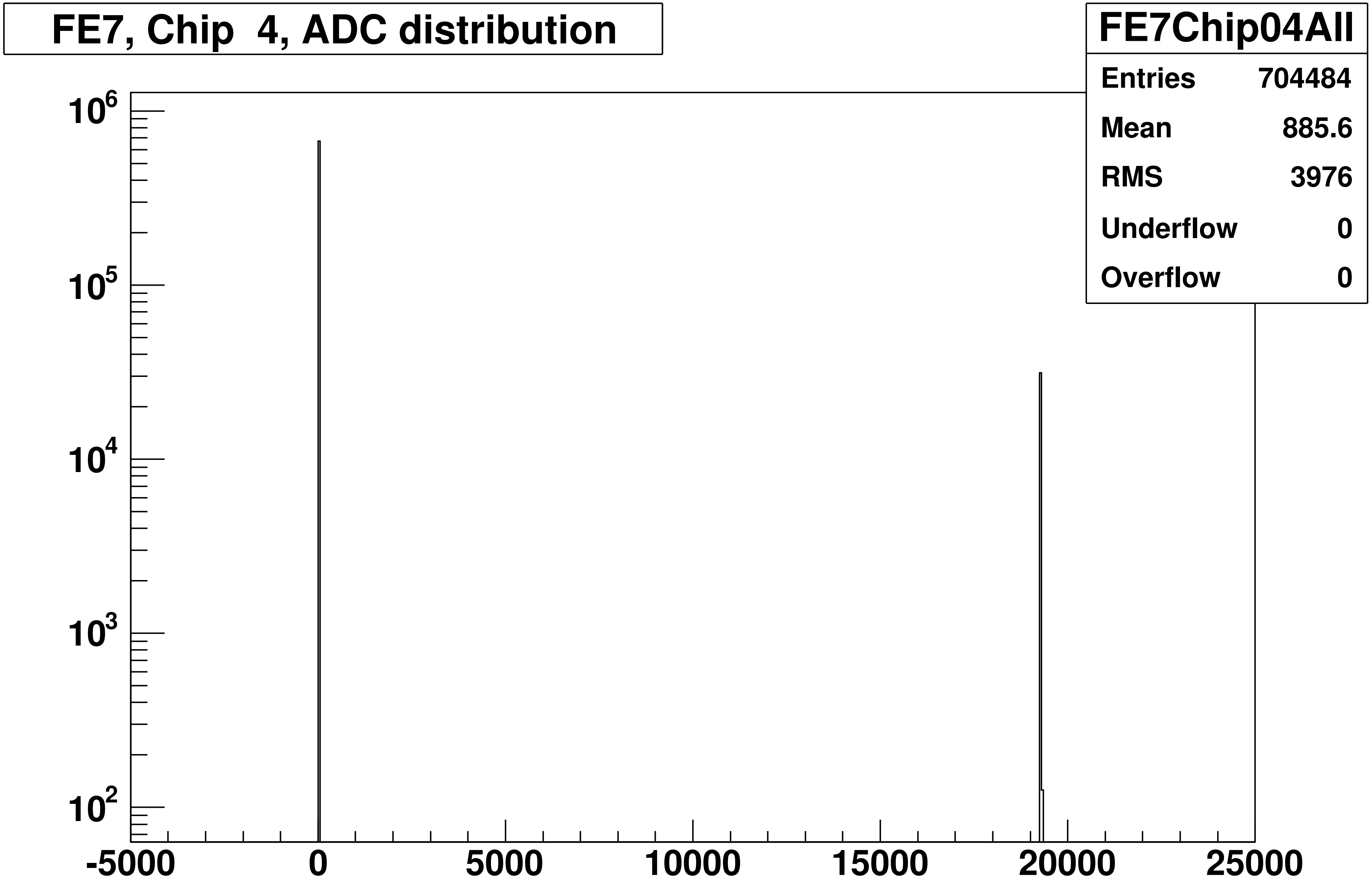
19800
20000



FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 704484
Mean 885.6
RMS 3976
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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10000
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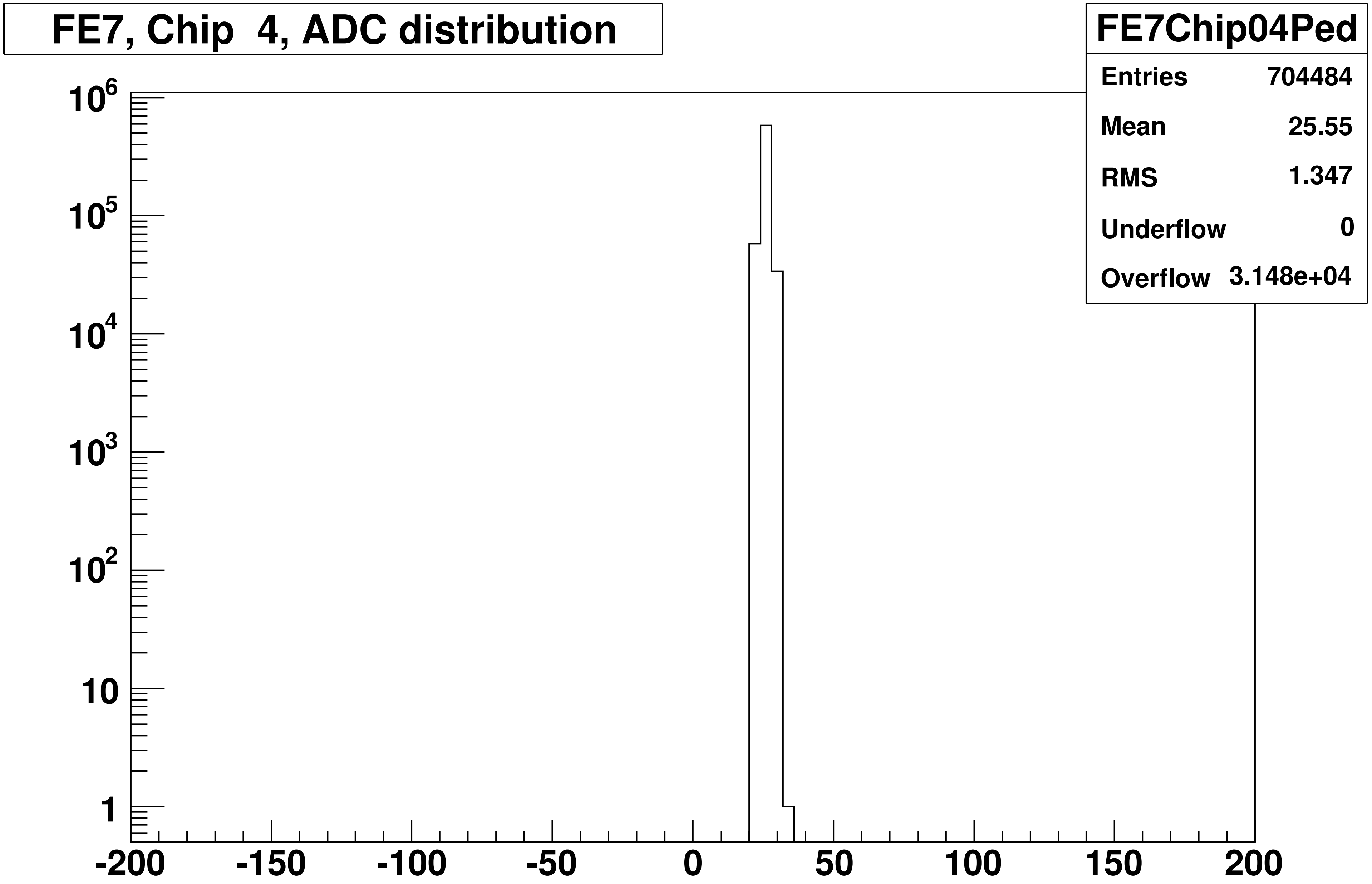
20000
25000


FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 704484
Mean 25.55
RMS 1.347
Underflow 0
Overflow 3.148e+04
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10^5
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10^3
10^2
10
1
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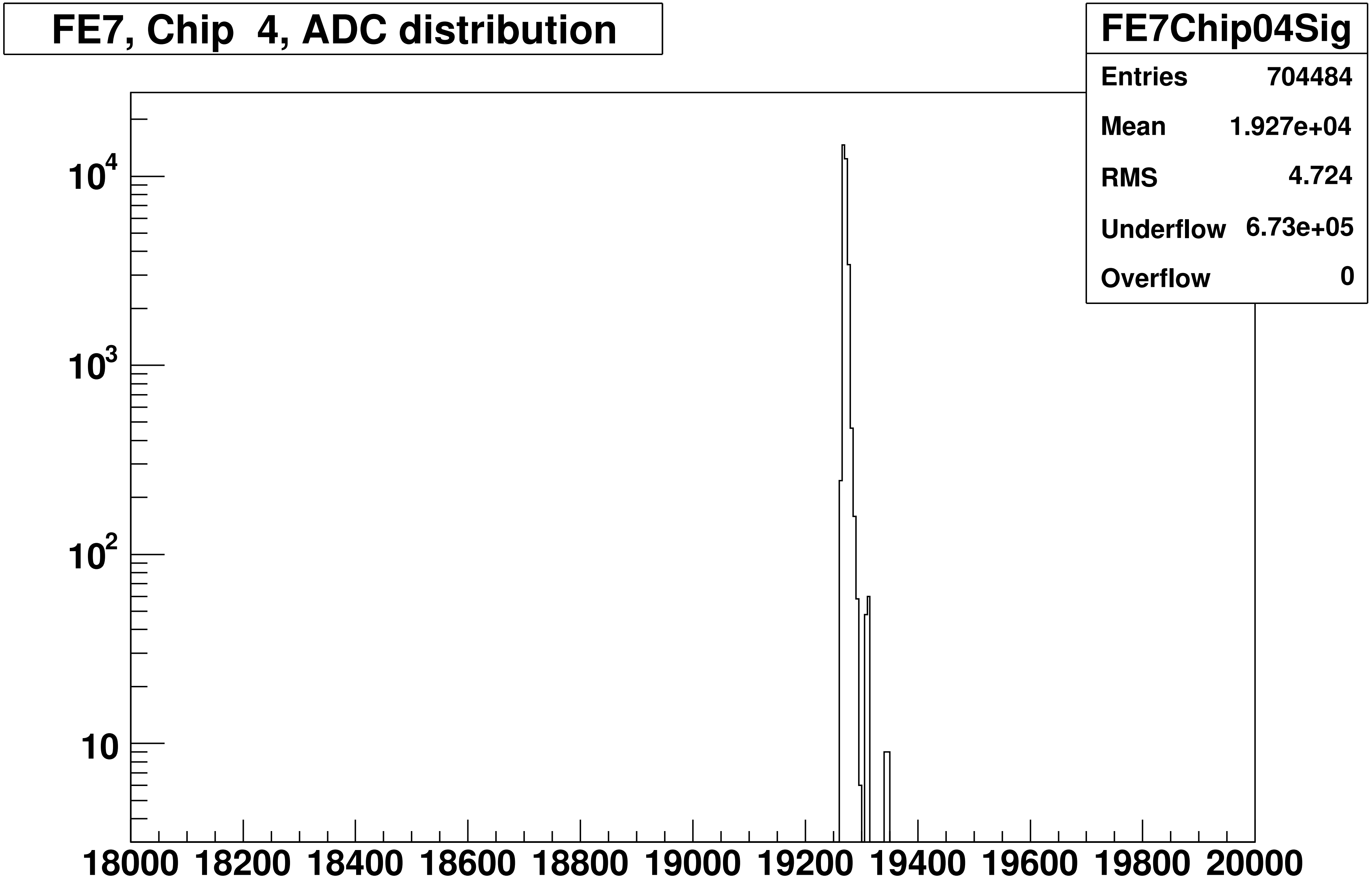
150
200


FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 704484
Mean 1.927e+04
RMS 4.724
Underflow 6.73e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
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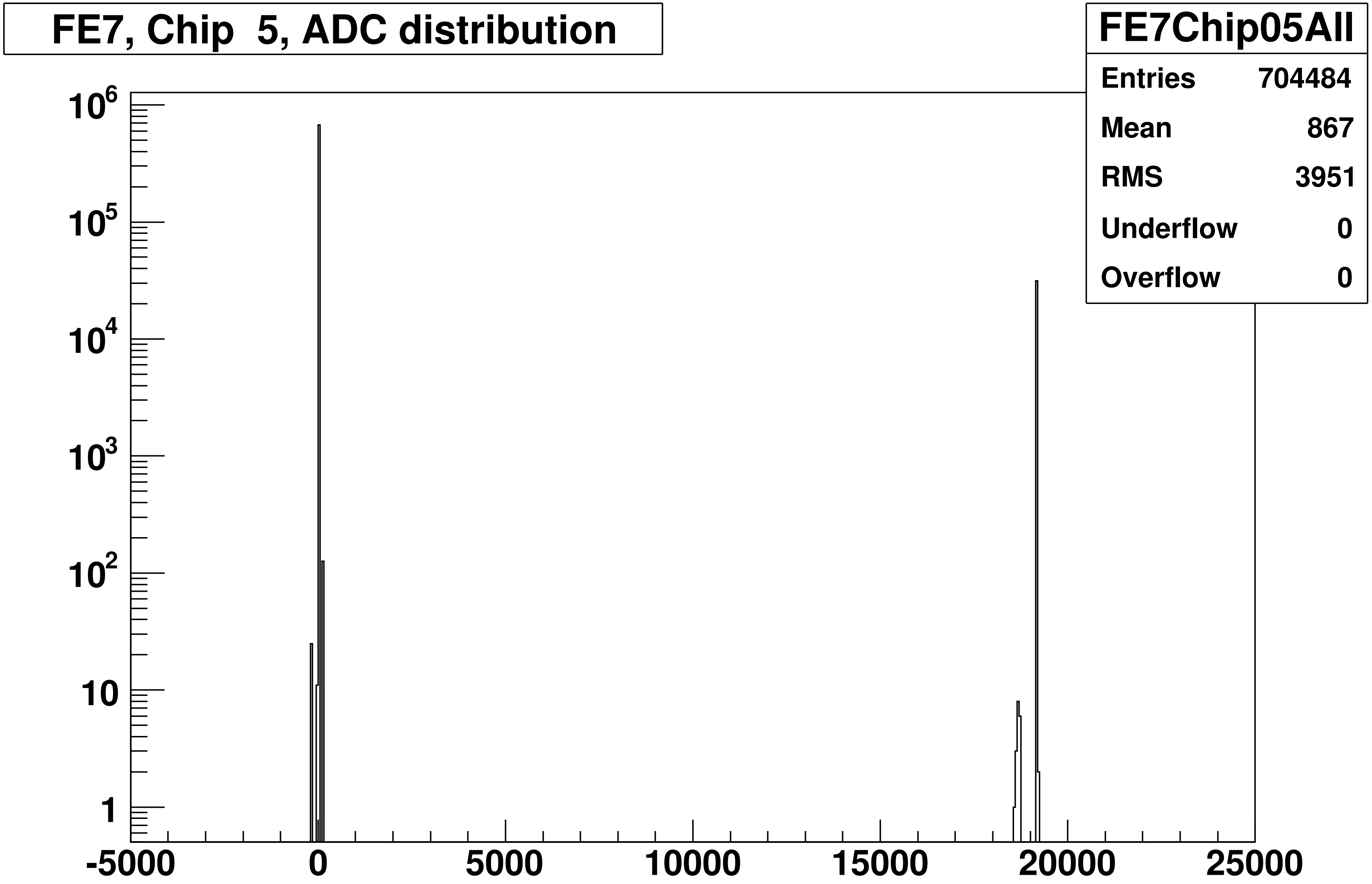
FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 704484
Mean 867
RMS 3951
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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0
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10000
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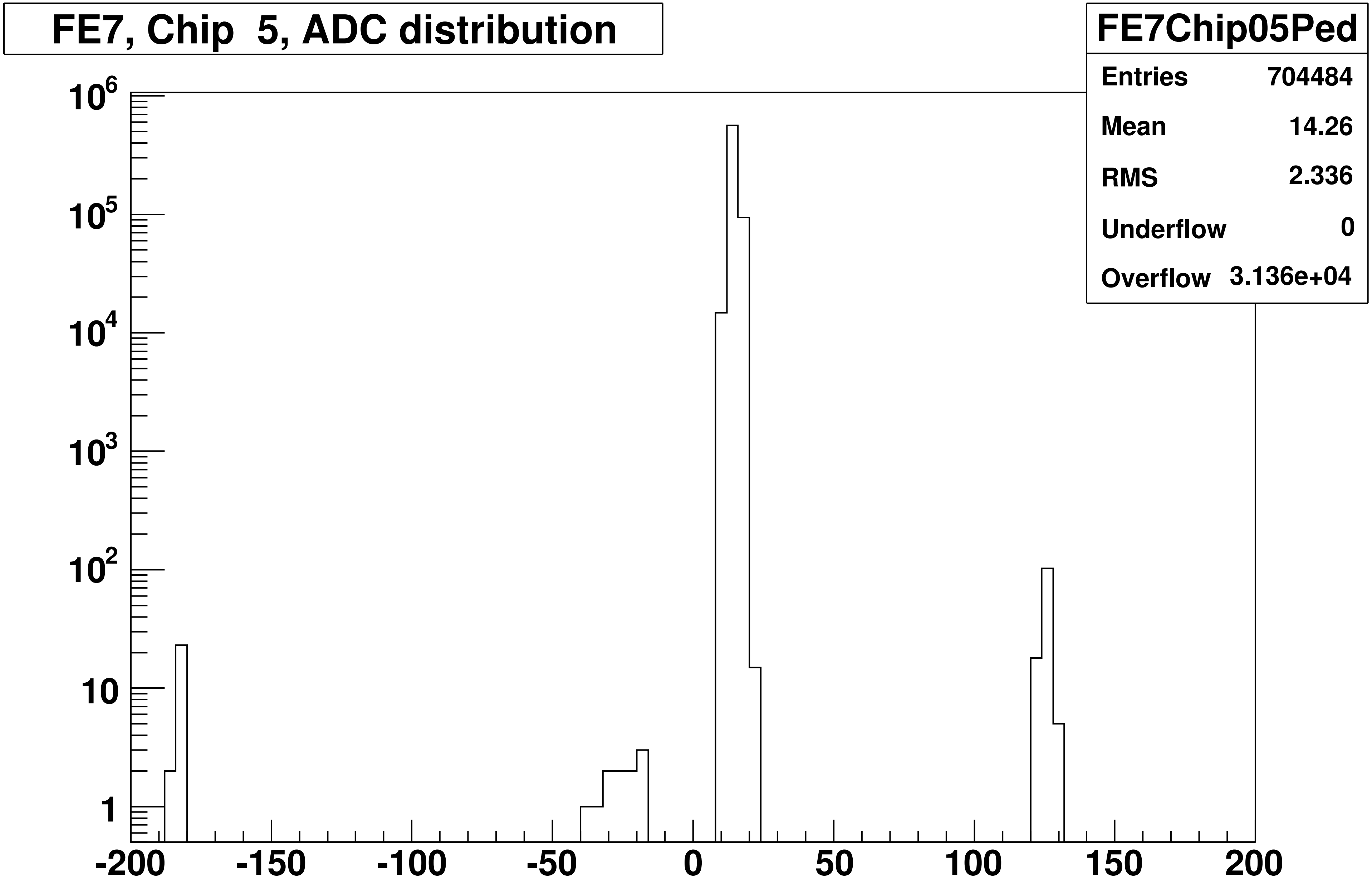
20000
25000


FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 704484
Mean 14.26
RMS 2.336
Underflow 0
Overflow 3.136e+04
10^6
10^5
10^4
10^3
10^2
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1
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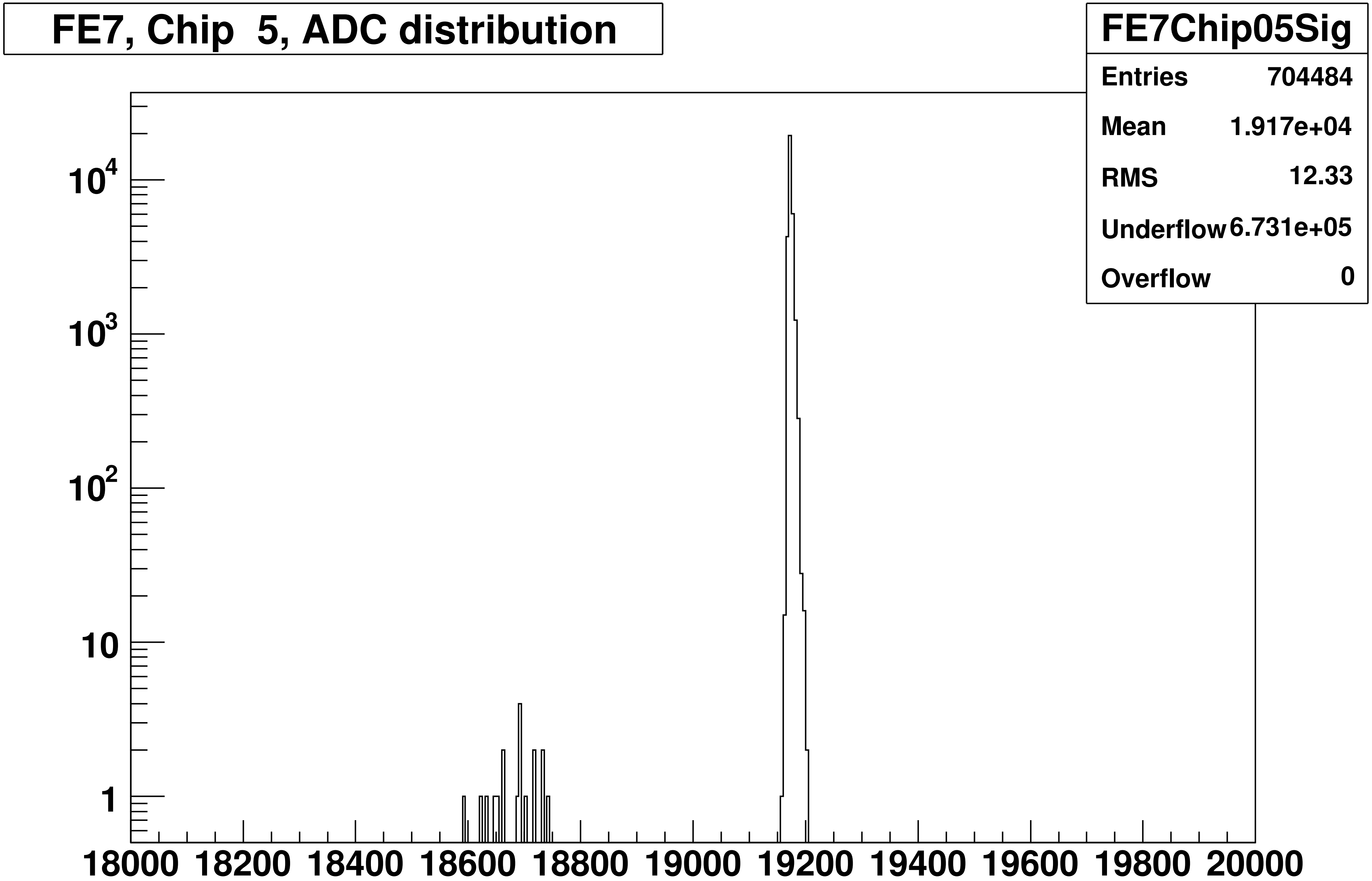
100
150
200


FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 704484
Mean 1.917e+04
RMS 12.33
Underflow 6.731e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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19400
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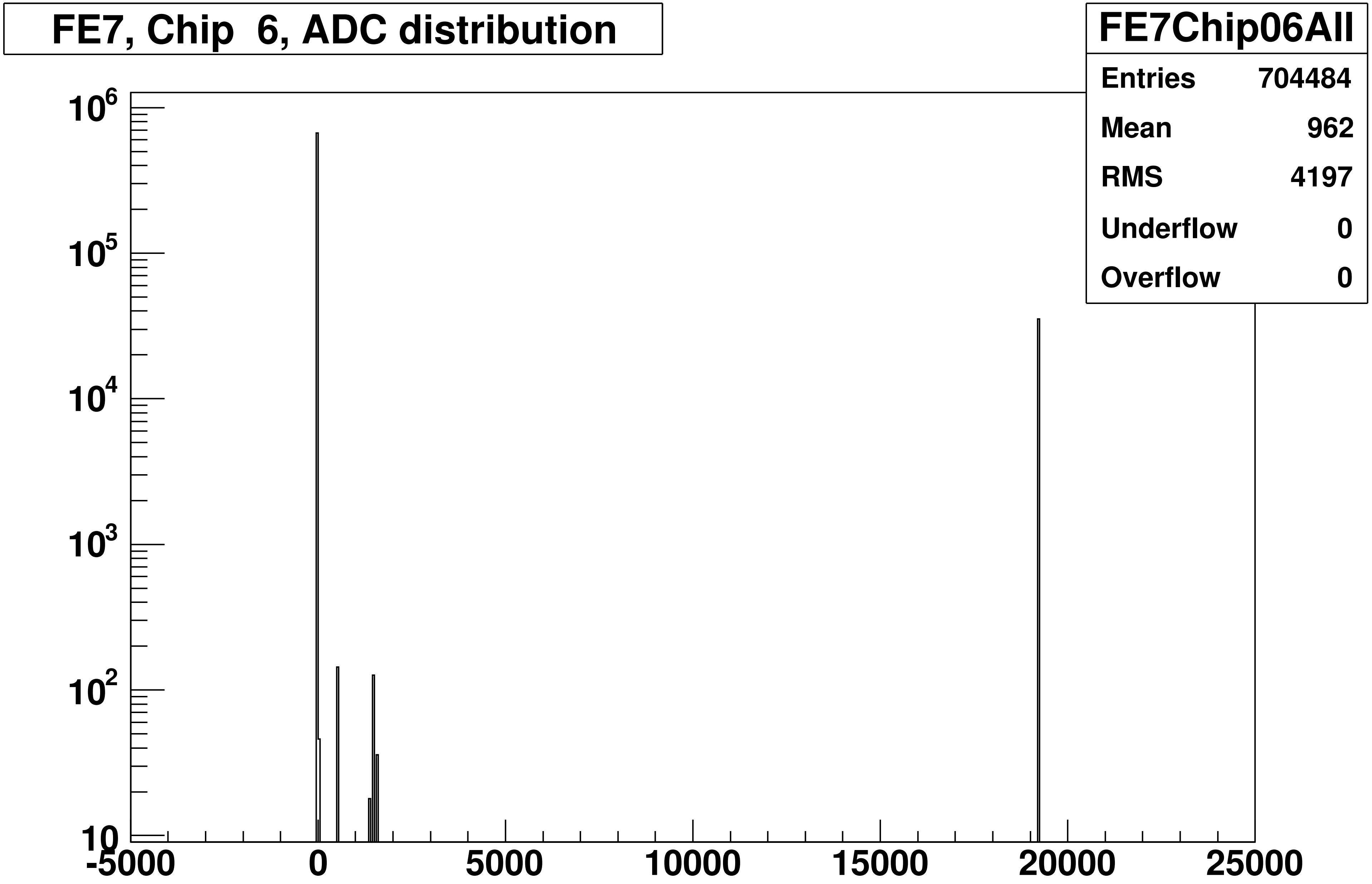
19800
20000


FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 704484
Mean 962
RMS 4197
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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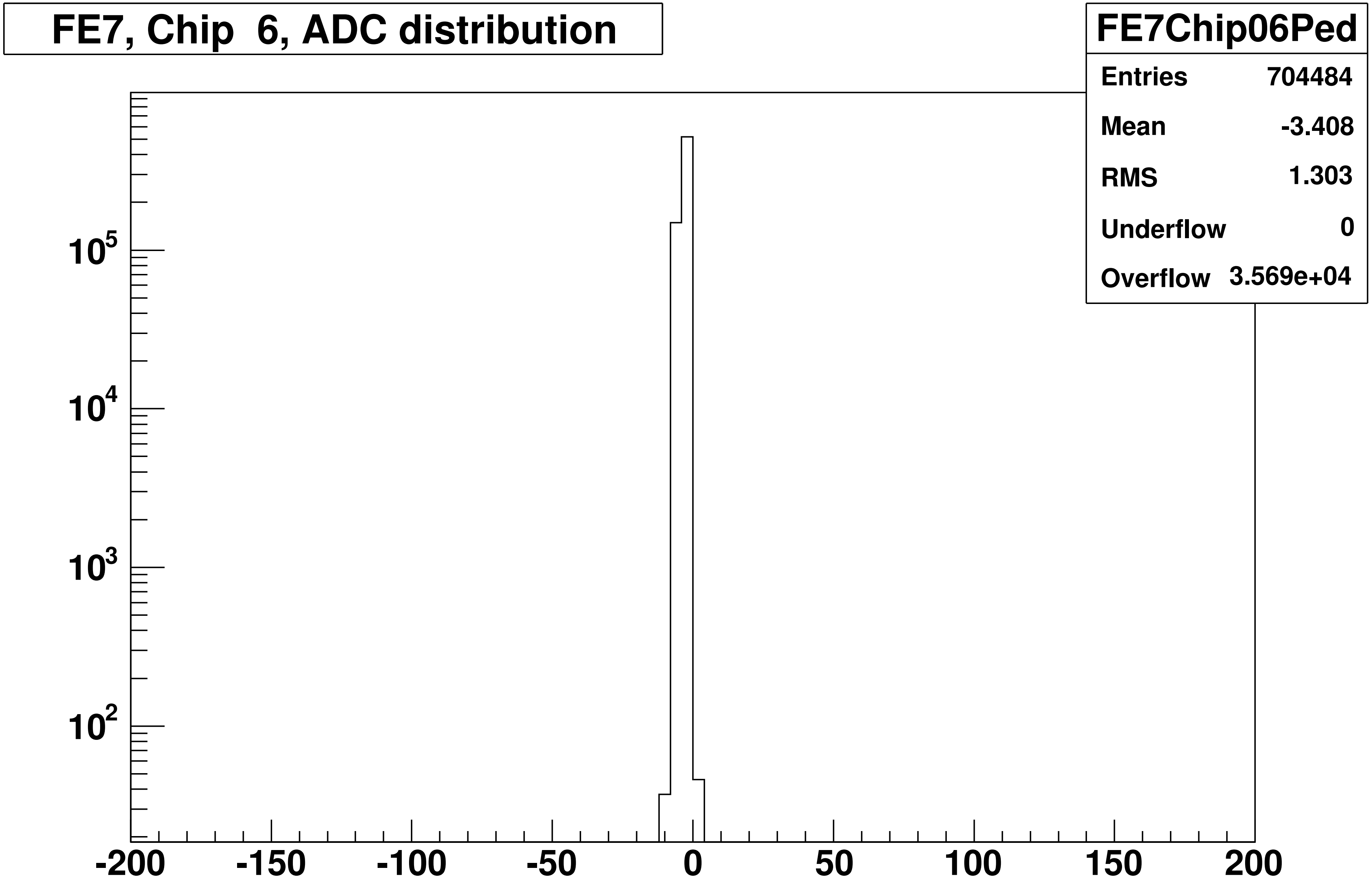
FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 704484
Mean -3.408
RMS 1.303
Underflow 0
Overflow 3.569e+04
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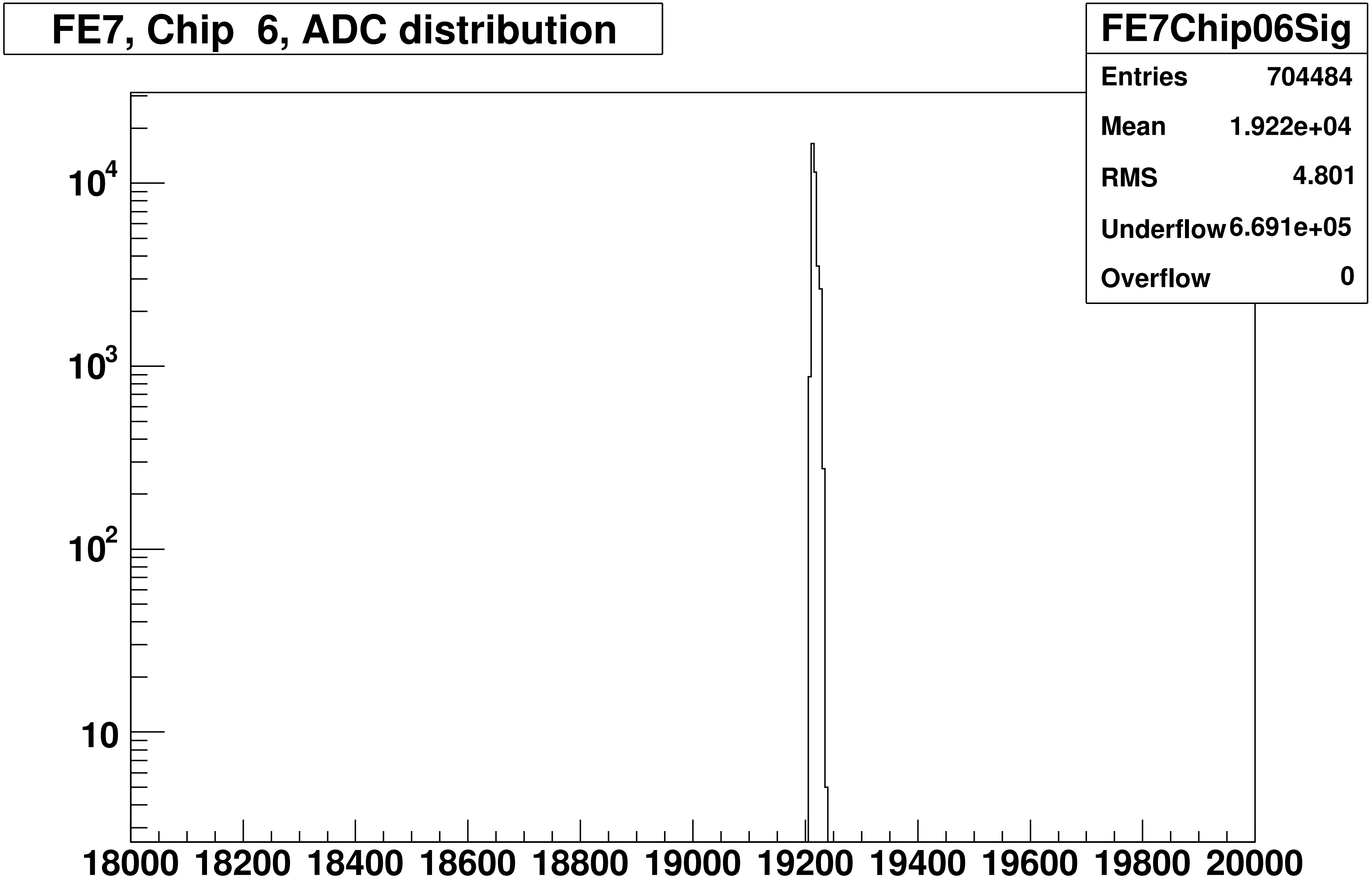
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200


FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 704484
Mean 1.922e+04
RMS 4.801
Underflow 6.691e+05
Overflow 0
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10^3
10^2
10
18000
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18400
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19000
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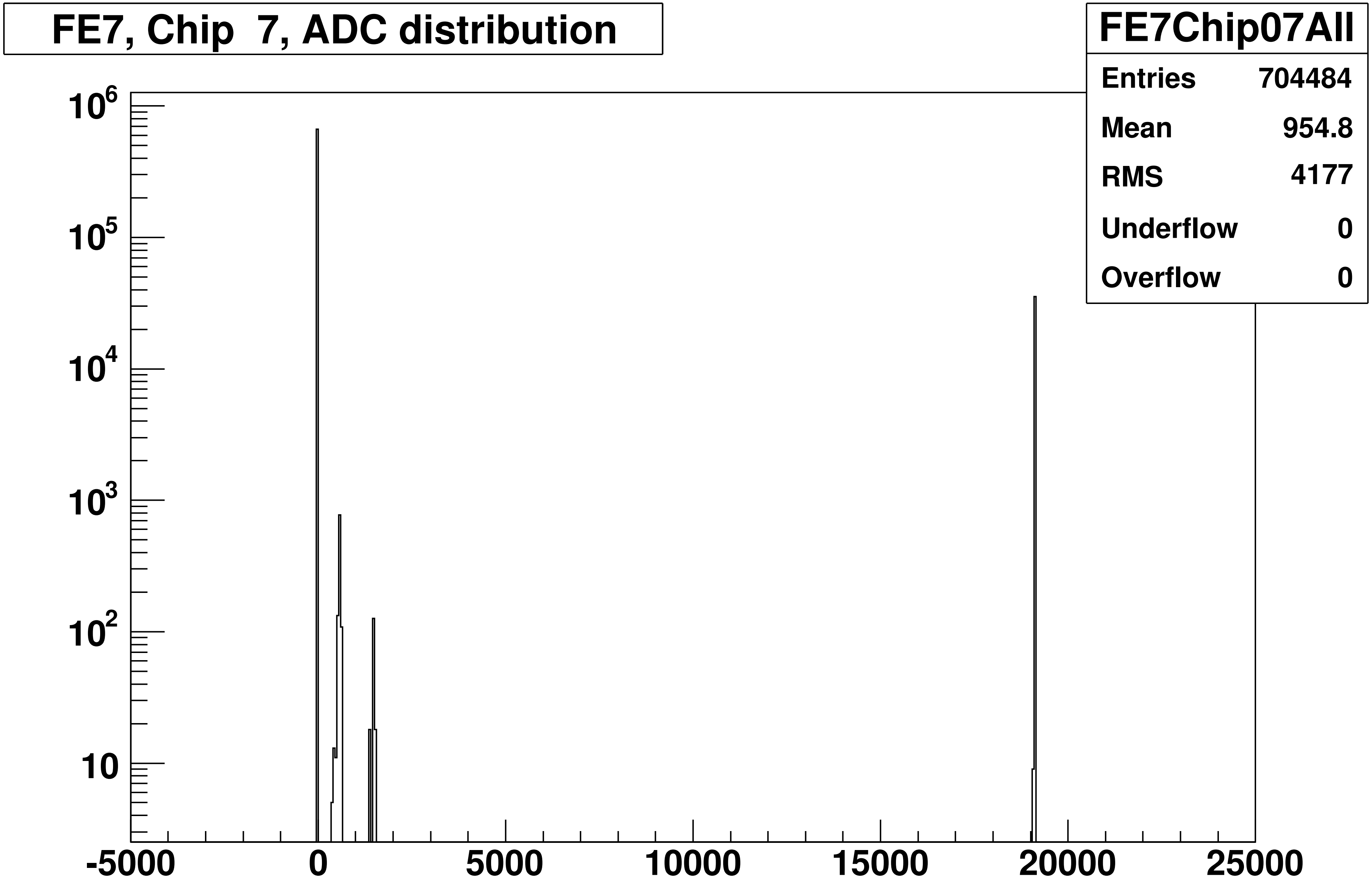
20000



FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 704484
Mean 954.8
RMS 4177
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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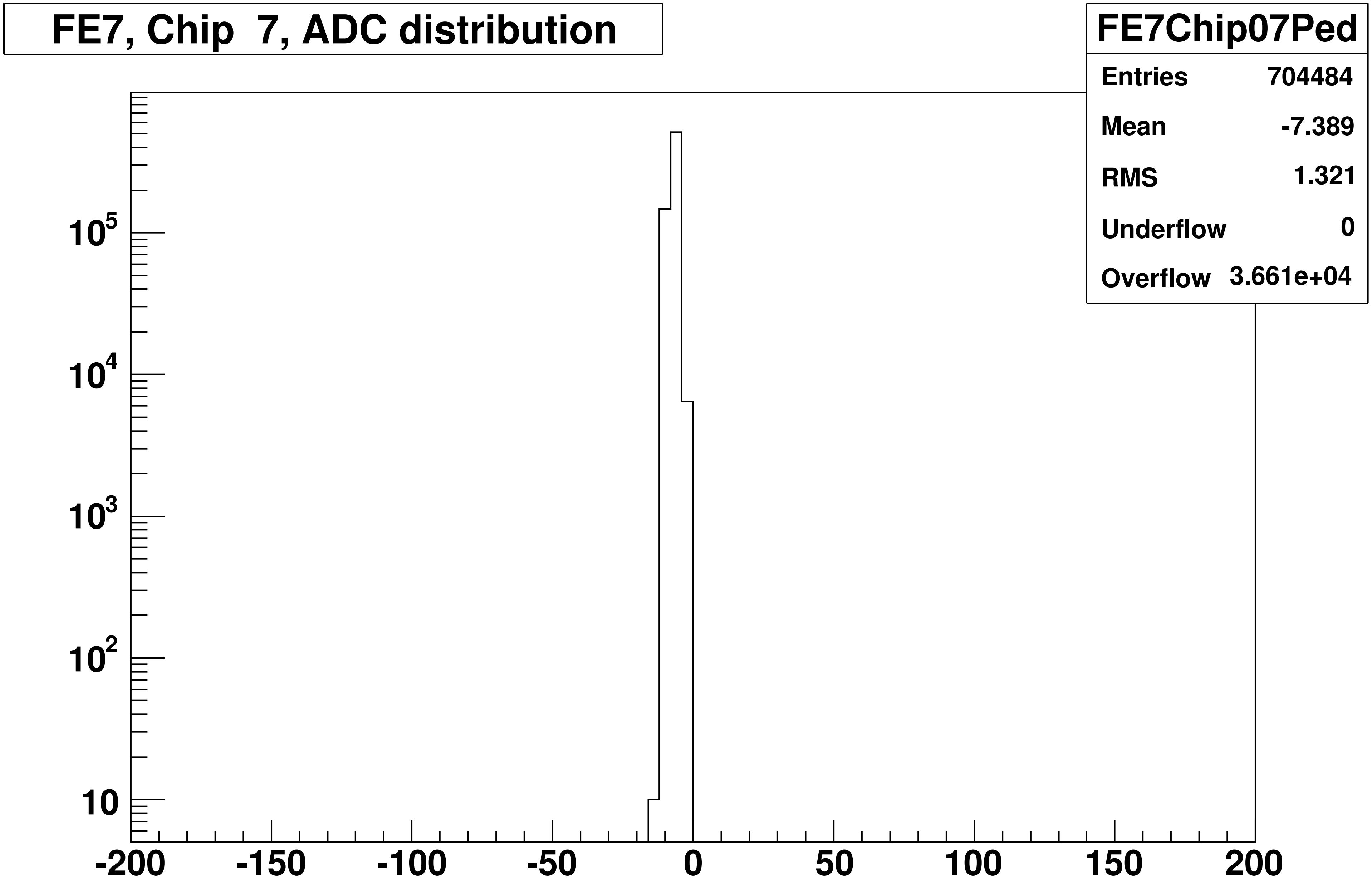
25000



FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 704484
Mean -7.389
RMS 1.321
Underflow 0
Overflow 3.661e+04
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10^4
10^3
10^2
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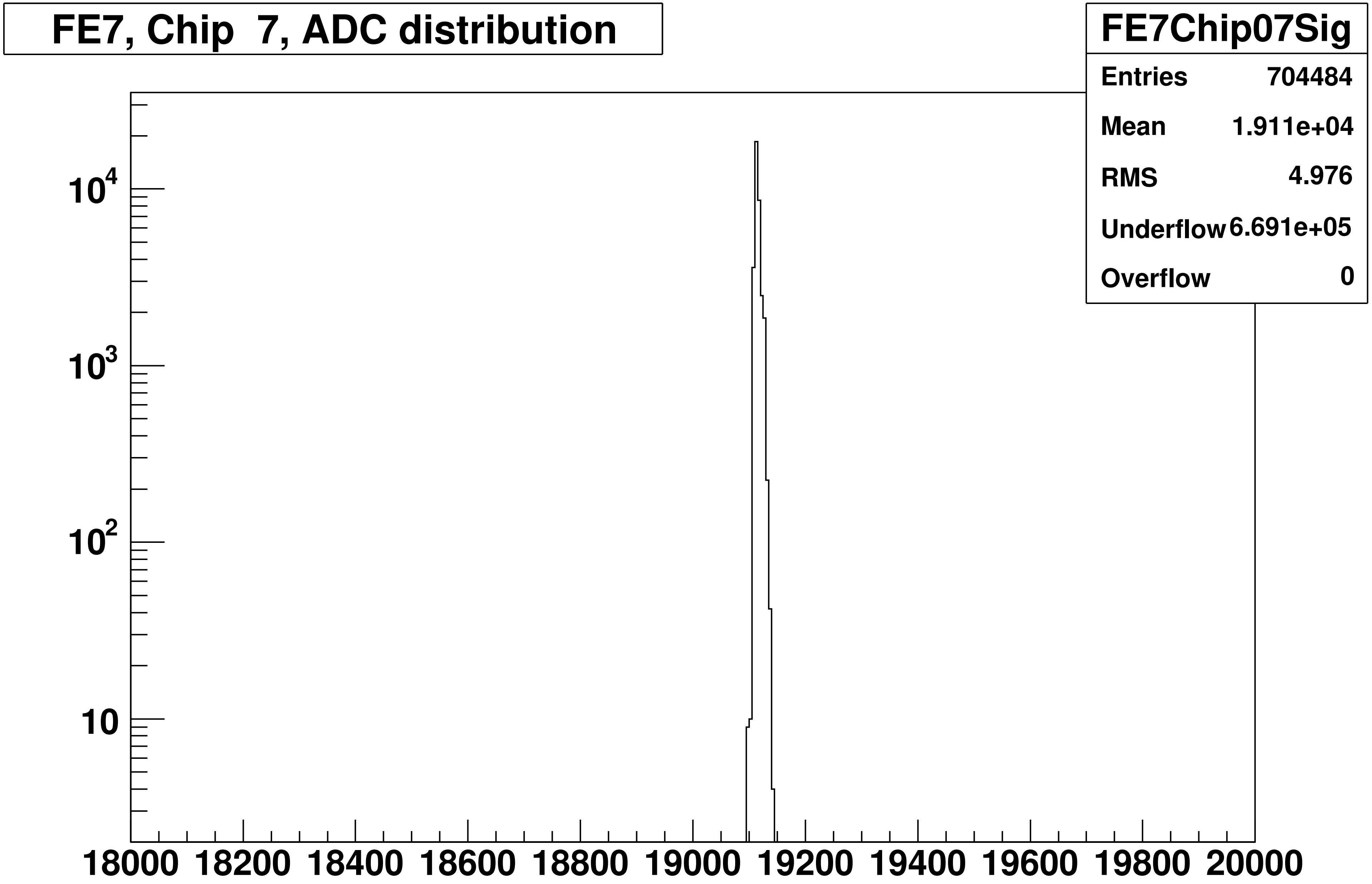
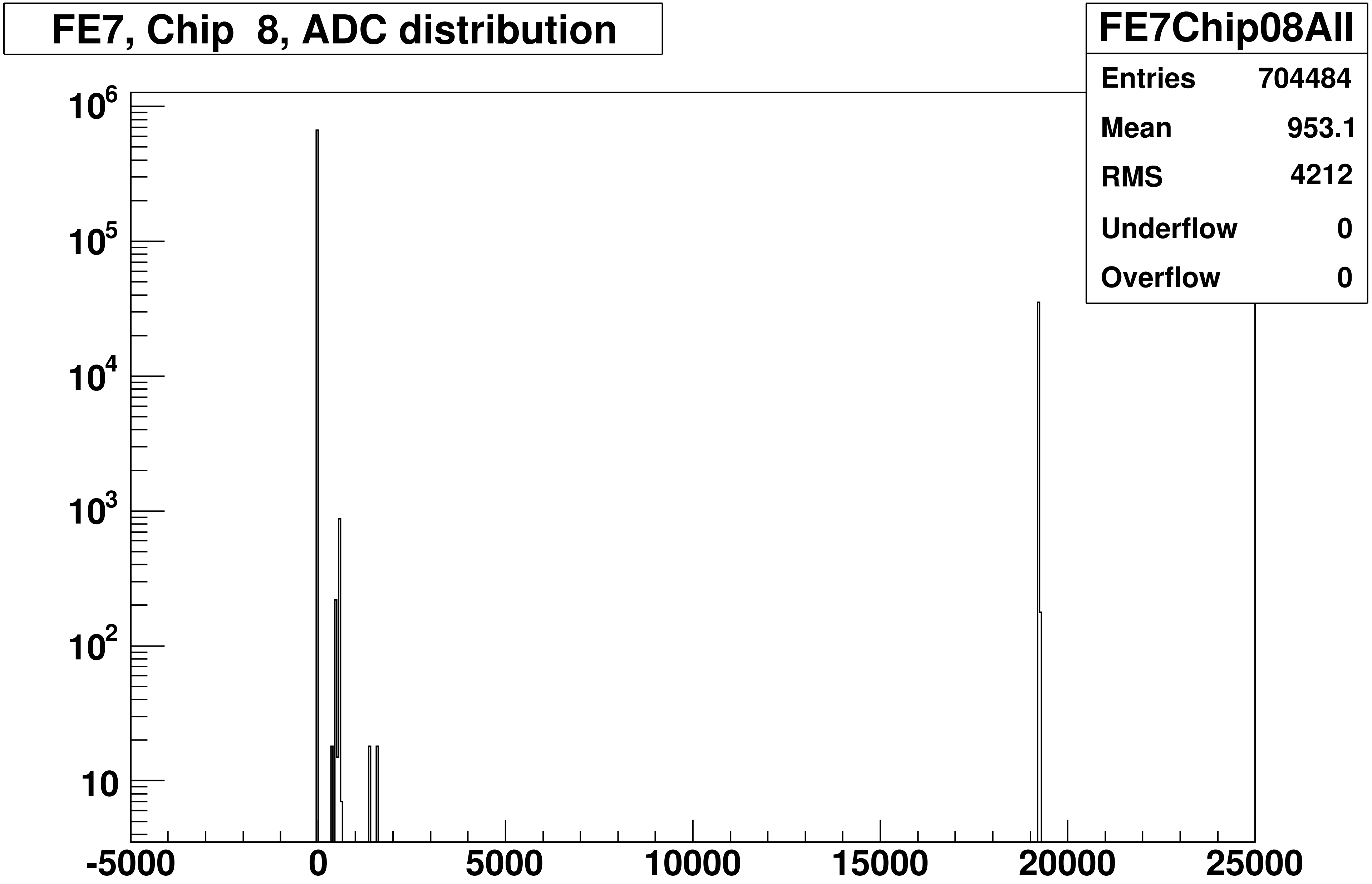
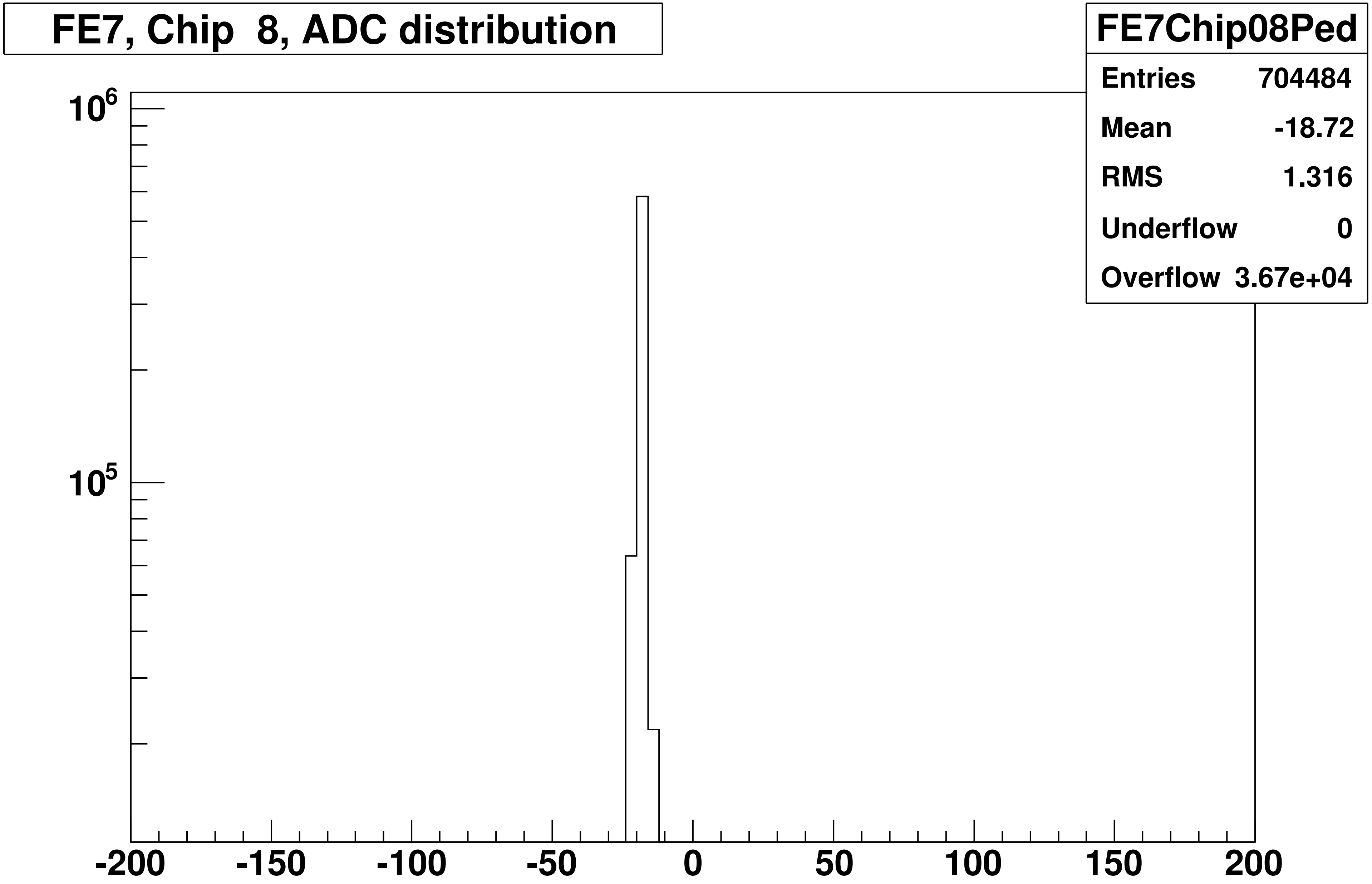
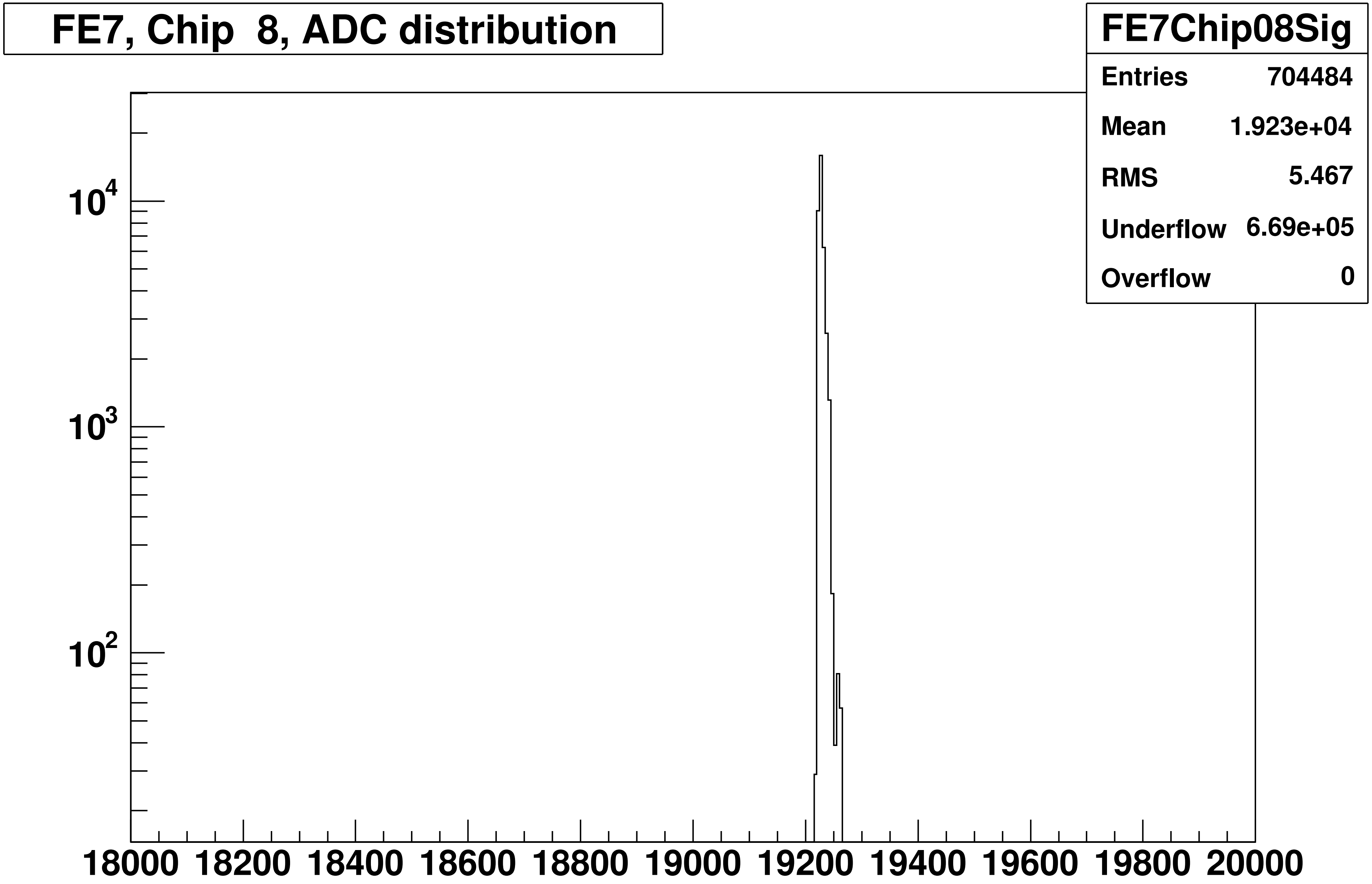
150
200

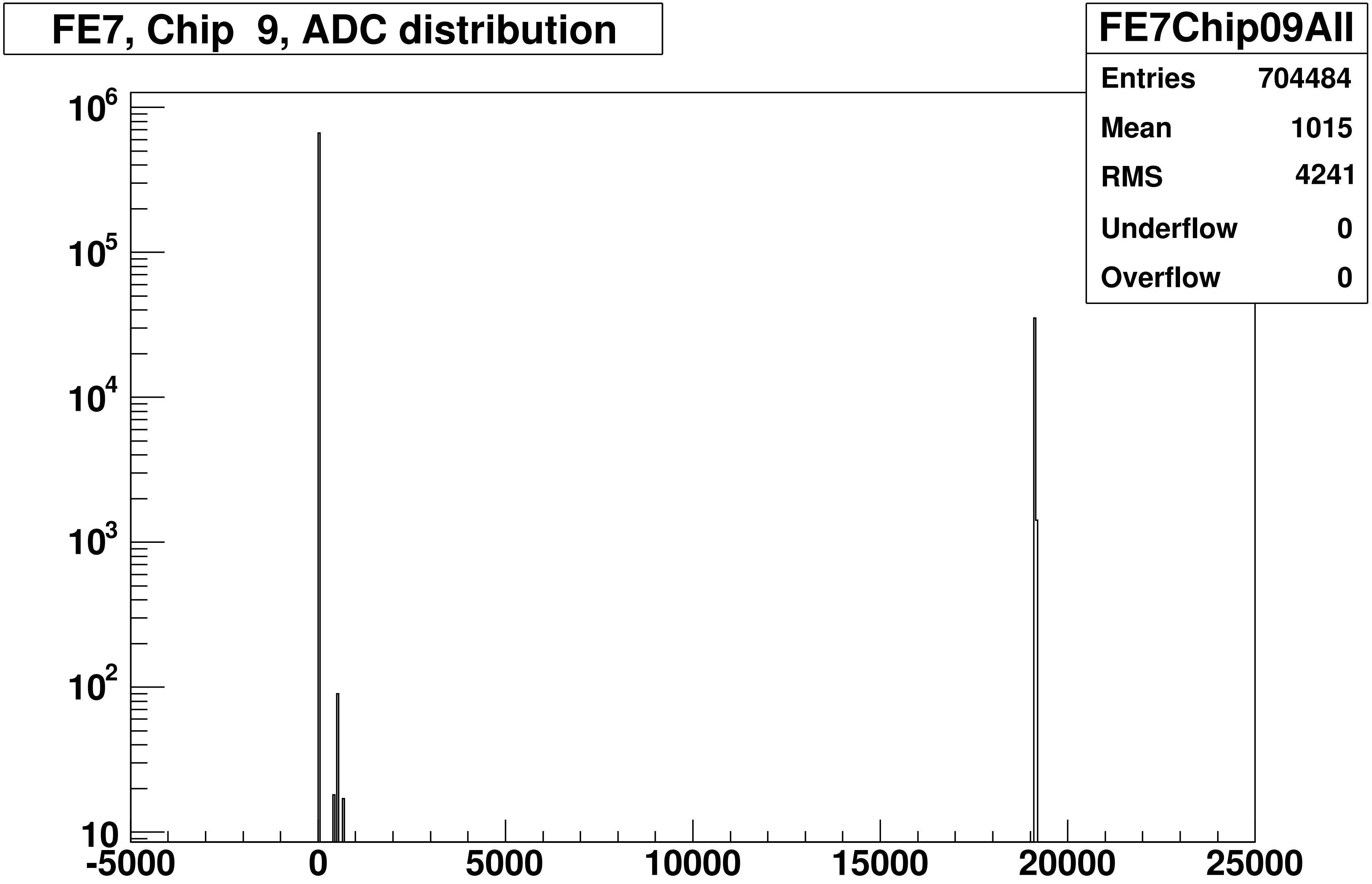

FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 704484
Mean 1.911e+04
RMS 4.976
Underflow 6.691e+05
Overflow 0
10
10^2
10^3
10^4
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

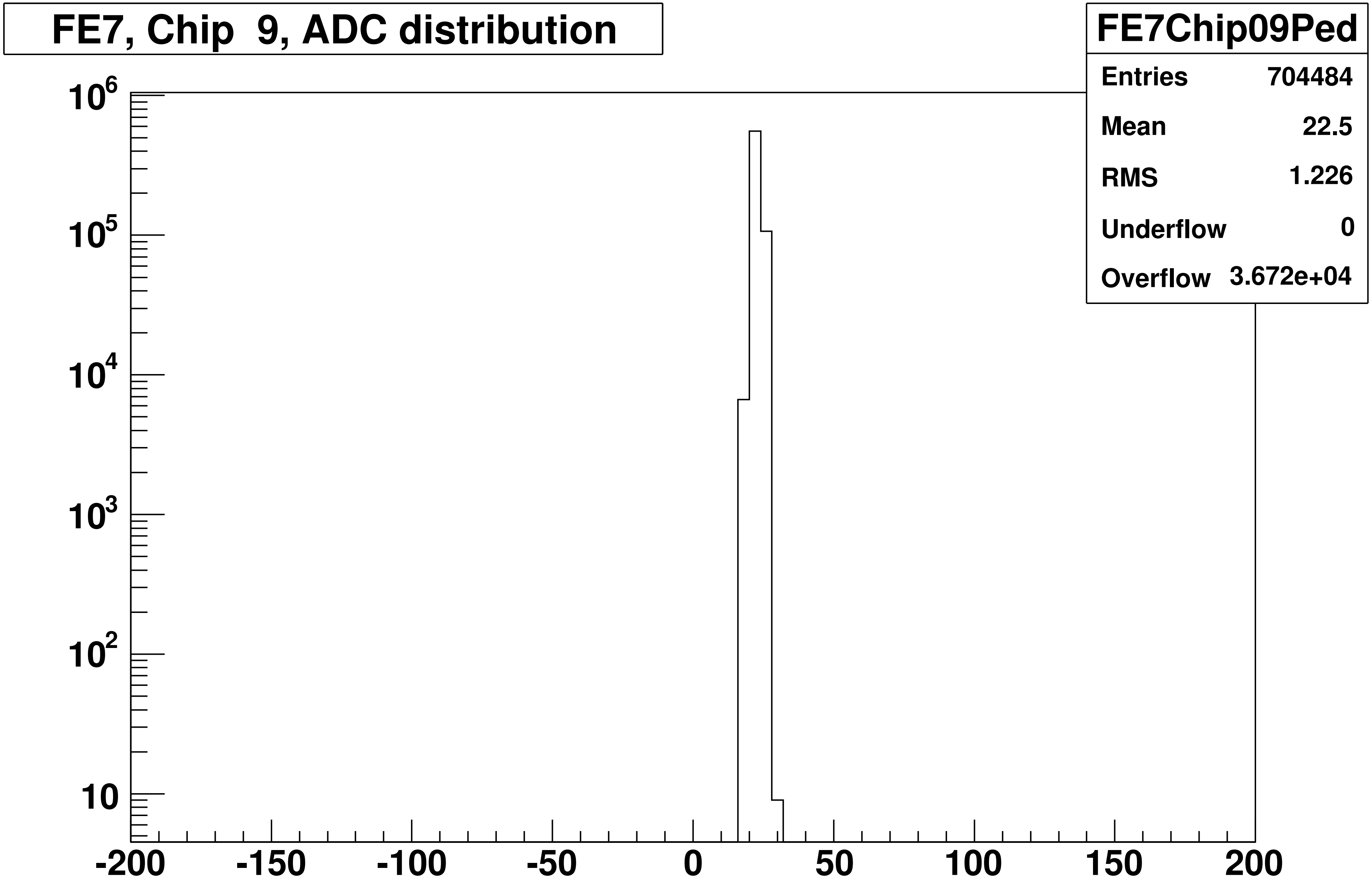


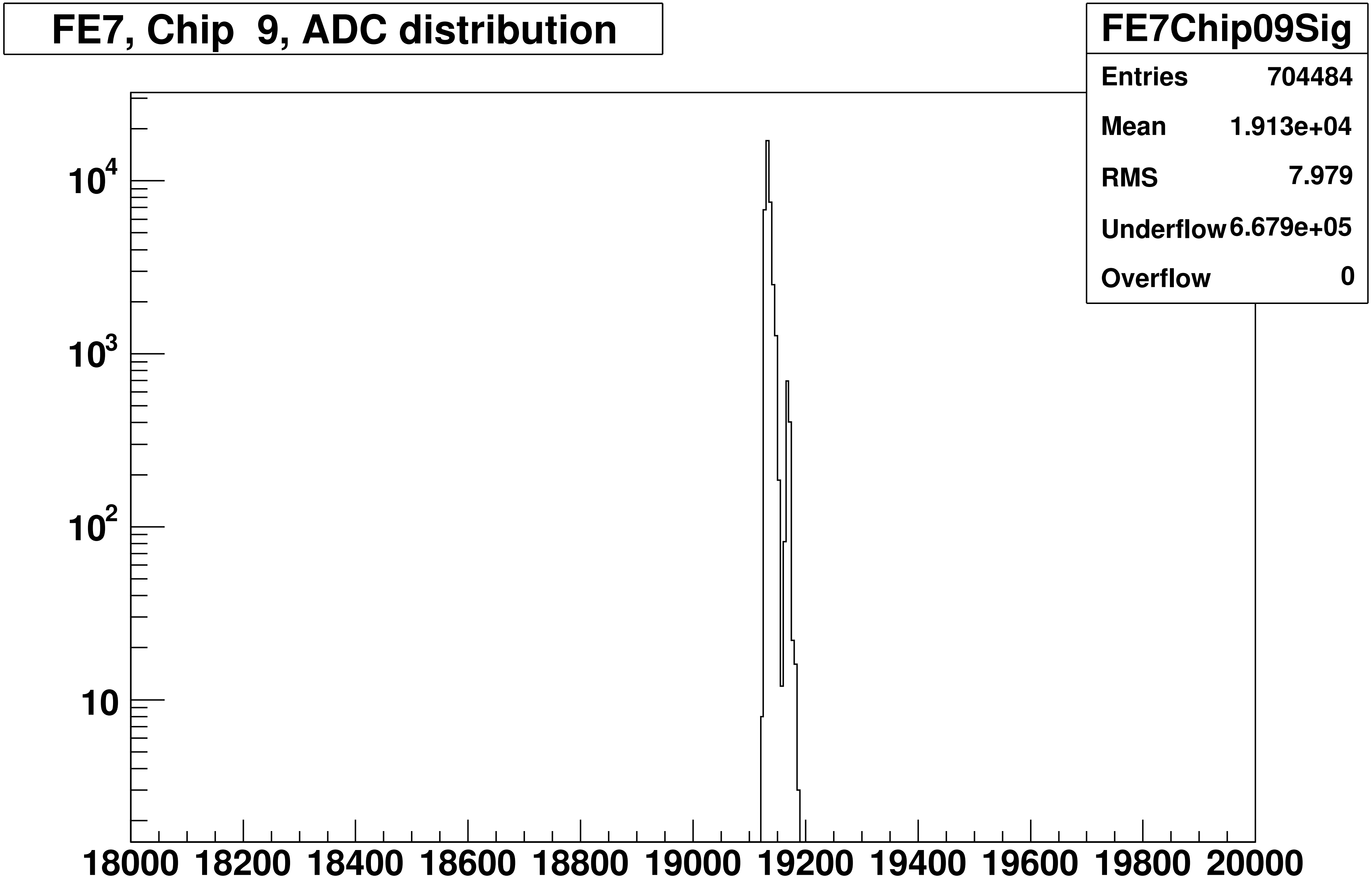
FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 704484
Mean 953.1
RMS 4212
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

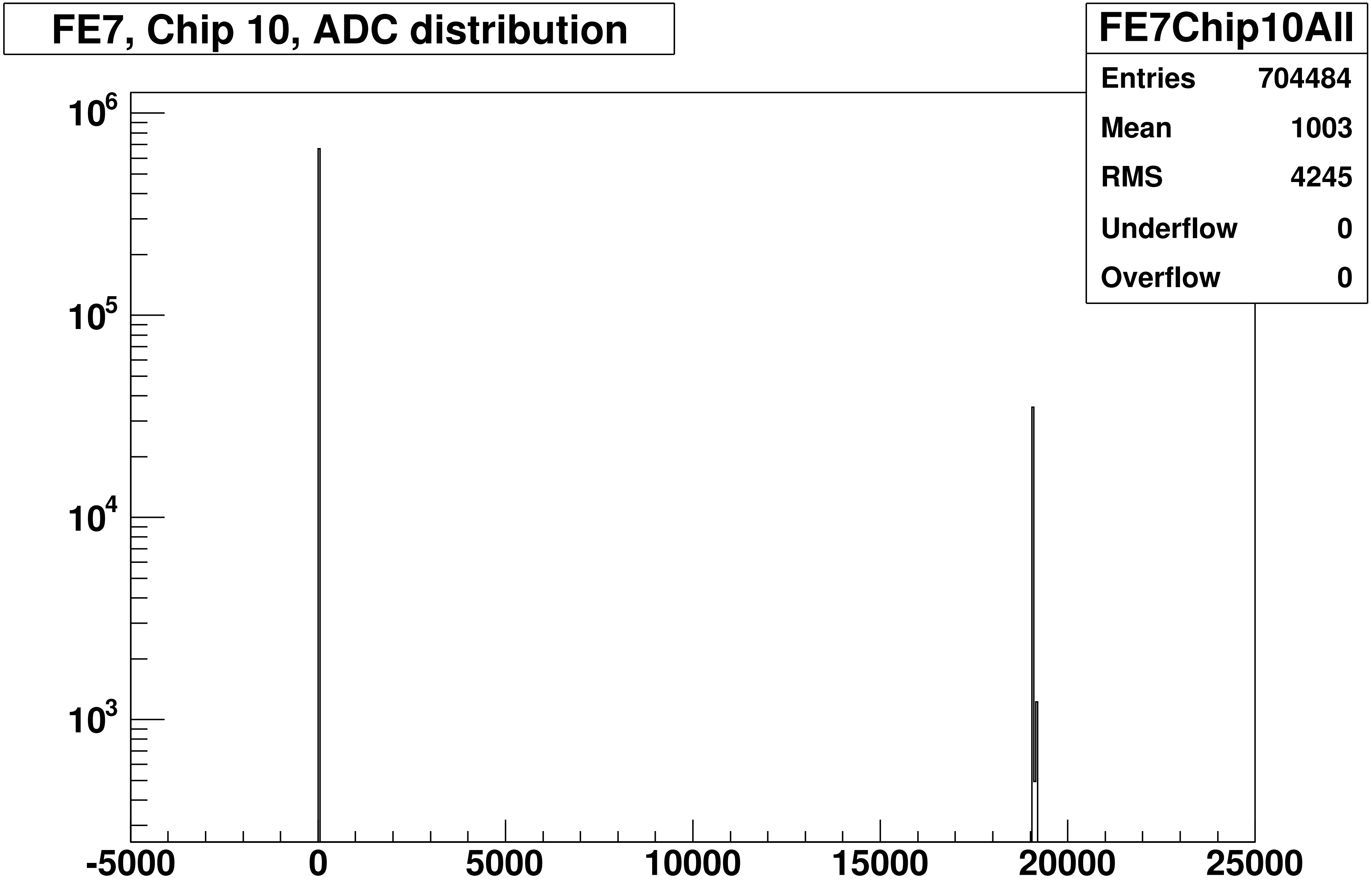

FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 704484
Mean -18.72
RMS 1.316
Underflow 0
Overflow 3.67e+04
10^6
10^5
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 704484
Mean 1.923e+04
RMS 5.467
Underflow 6.69e+05
Overflow 0
10^4
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

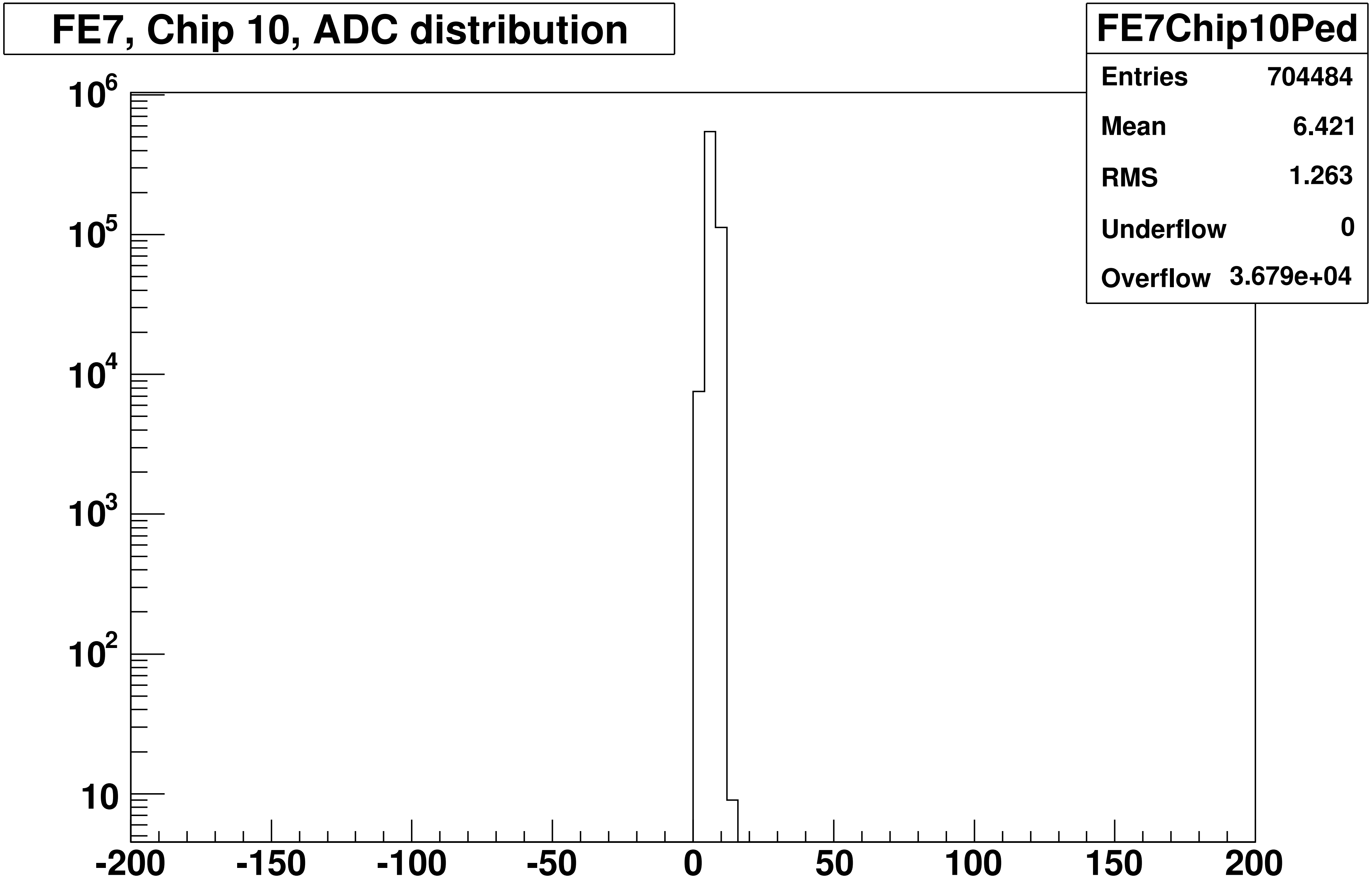

FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 704484
Mean 1015
RMS 4241
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 704484
Mean 22.5
RMS 1.226
Underflow 0
Overflow 3.672e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

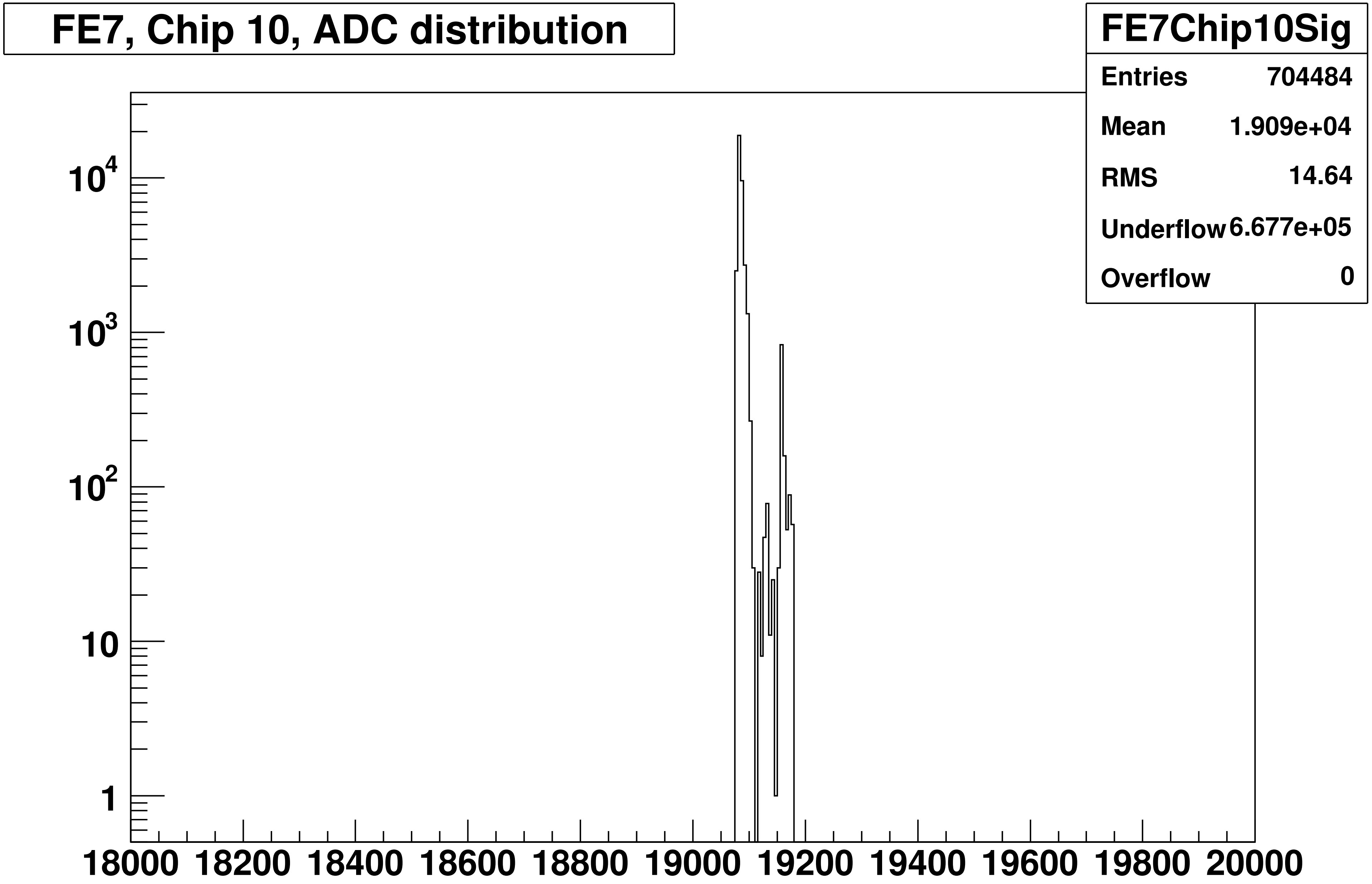

FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 704484
Mean 1.913e+04
RMS 7.979
Underflow 6.679e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

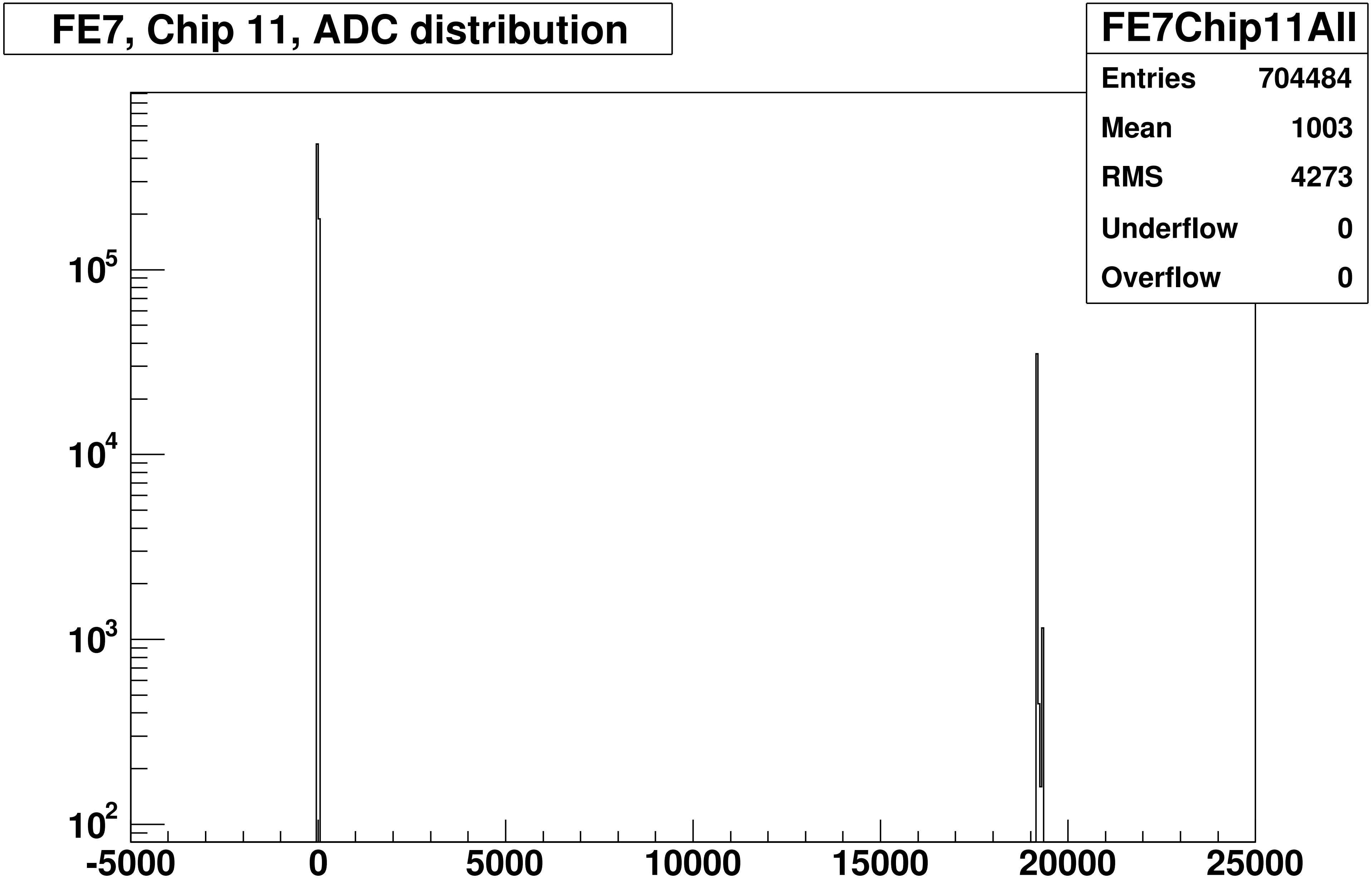


FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 704484
Mean 1003
RMS 4245
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

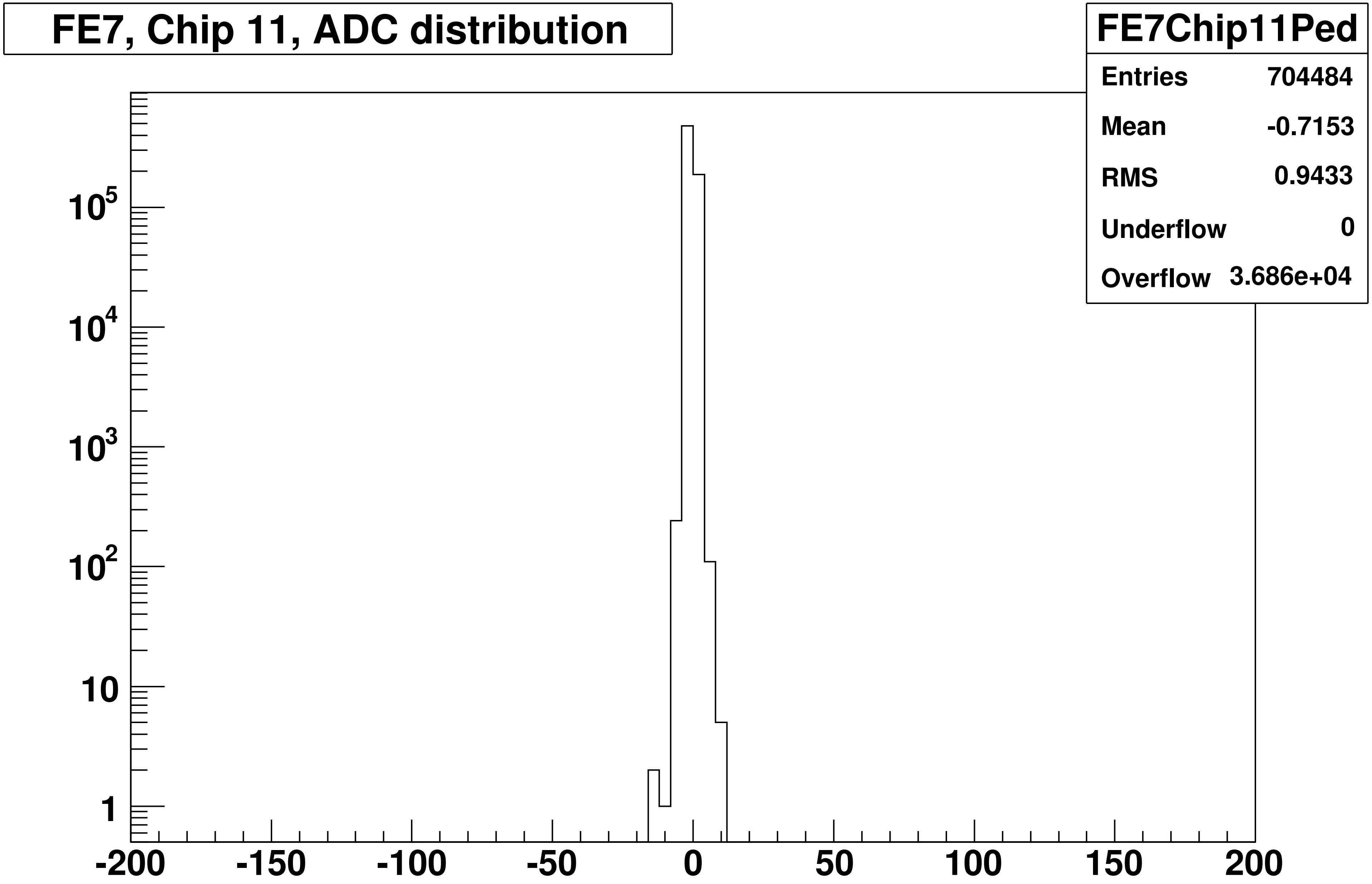


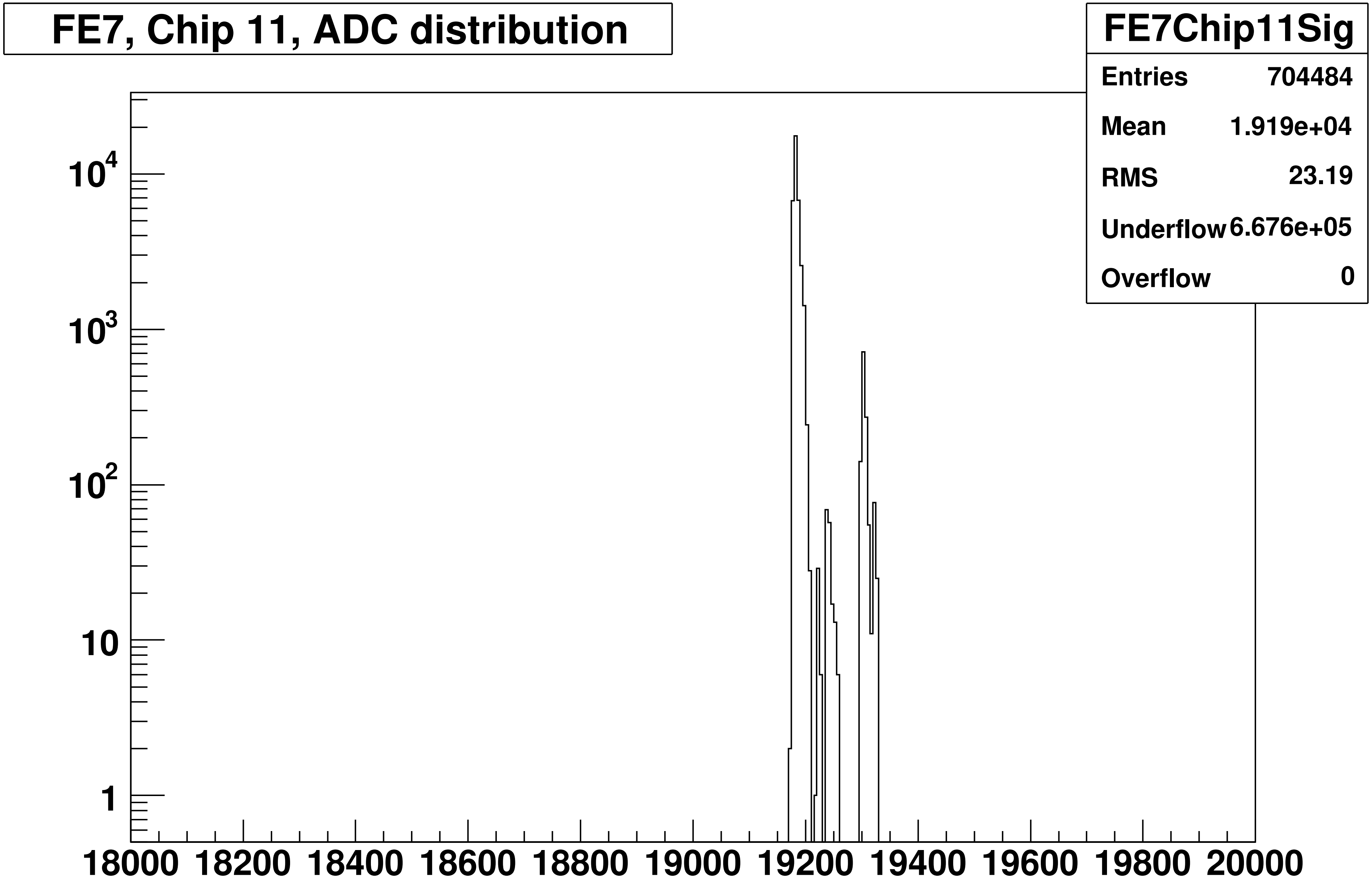
FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 704484
Mean 6.421
RMS 1.263
Underflow 0
Overflow 3.679e+04
10^6
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200

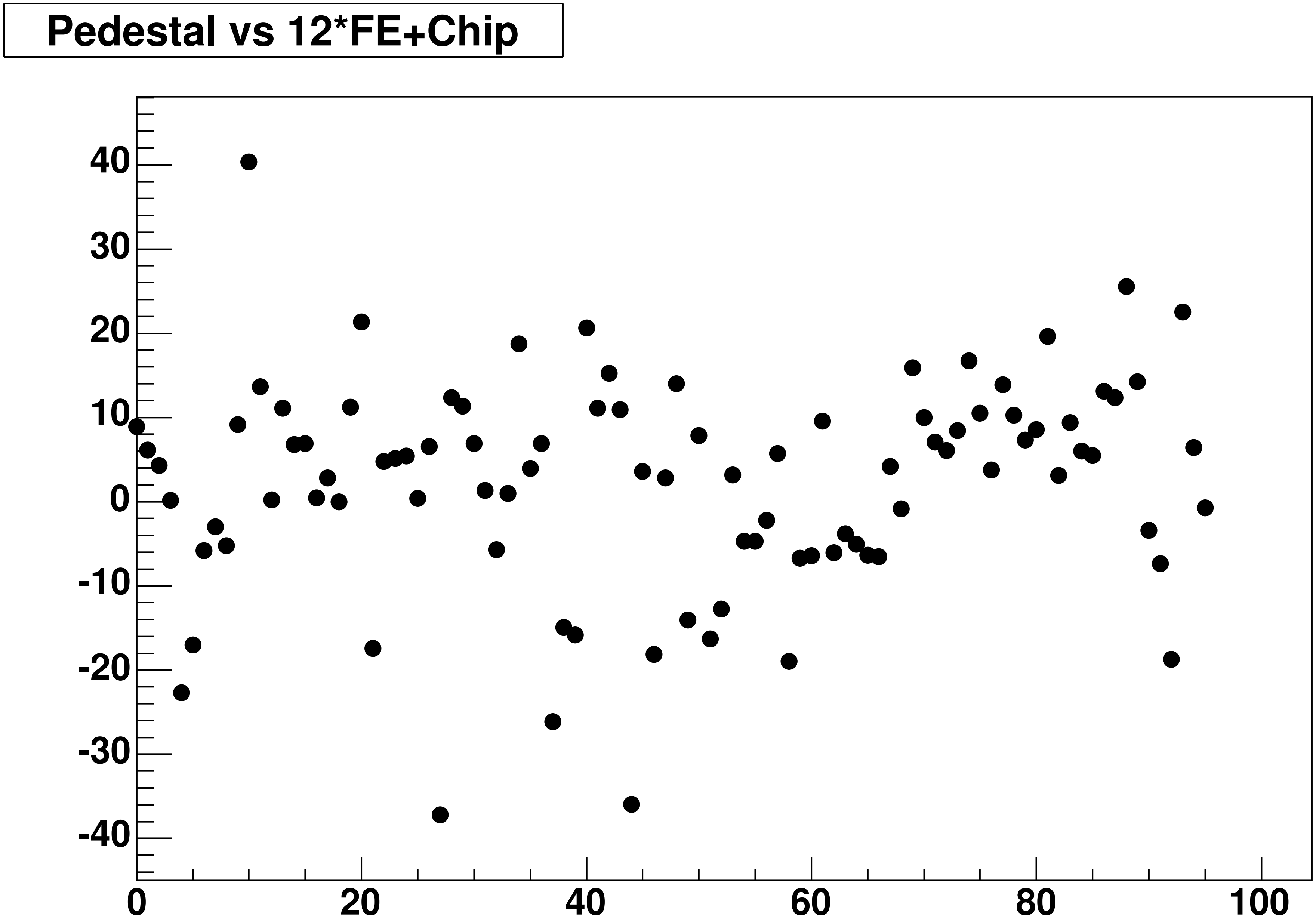

FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 704484
Mean 1.909e+04
RMS 14.64
Underflow 6.677e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

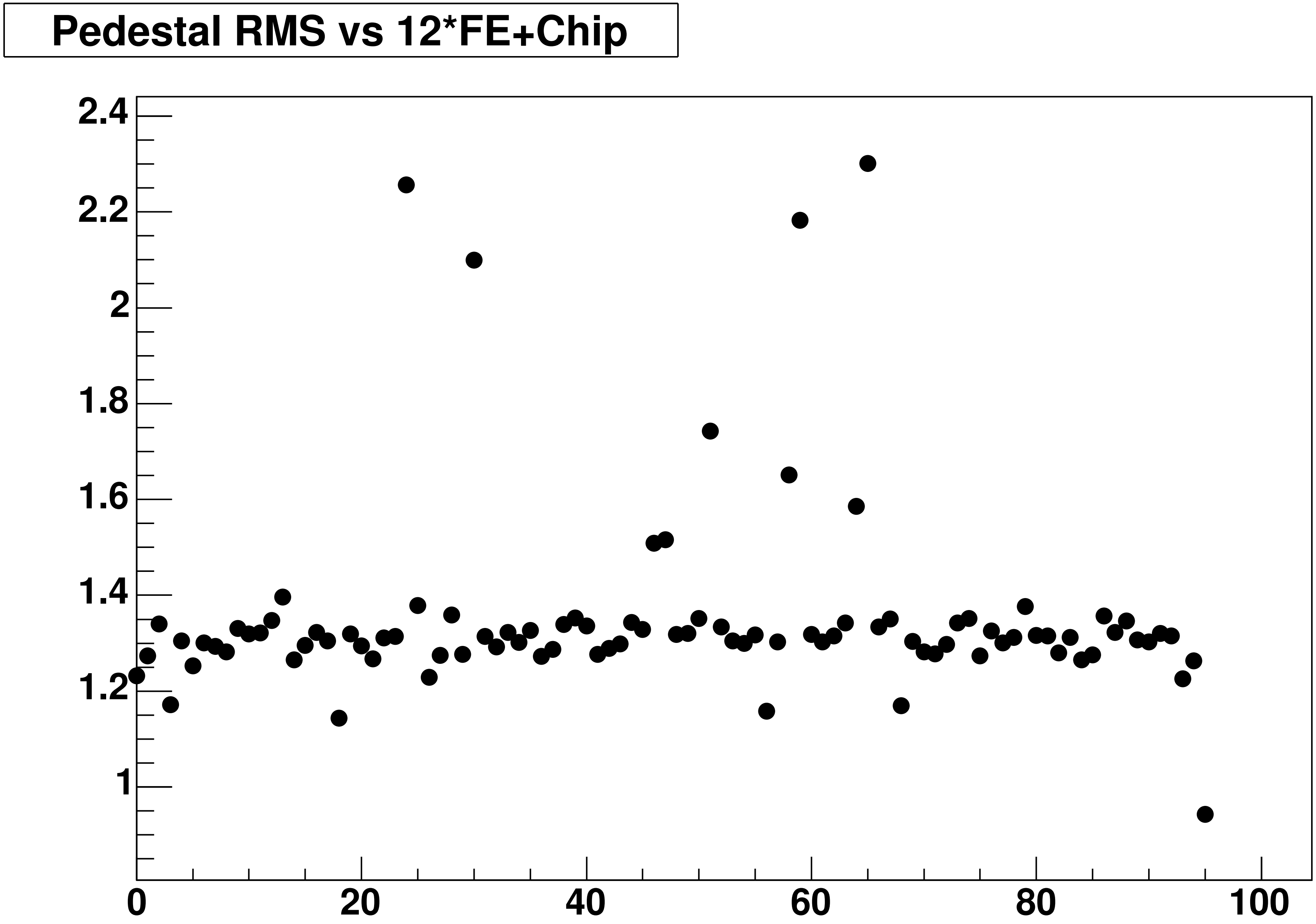


FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 704484
Mean 1003
RMS 4273
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

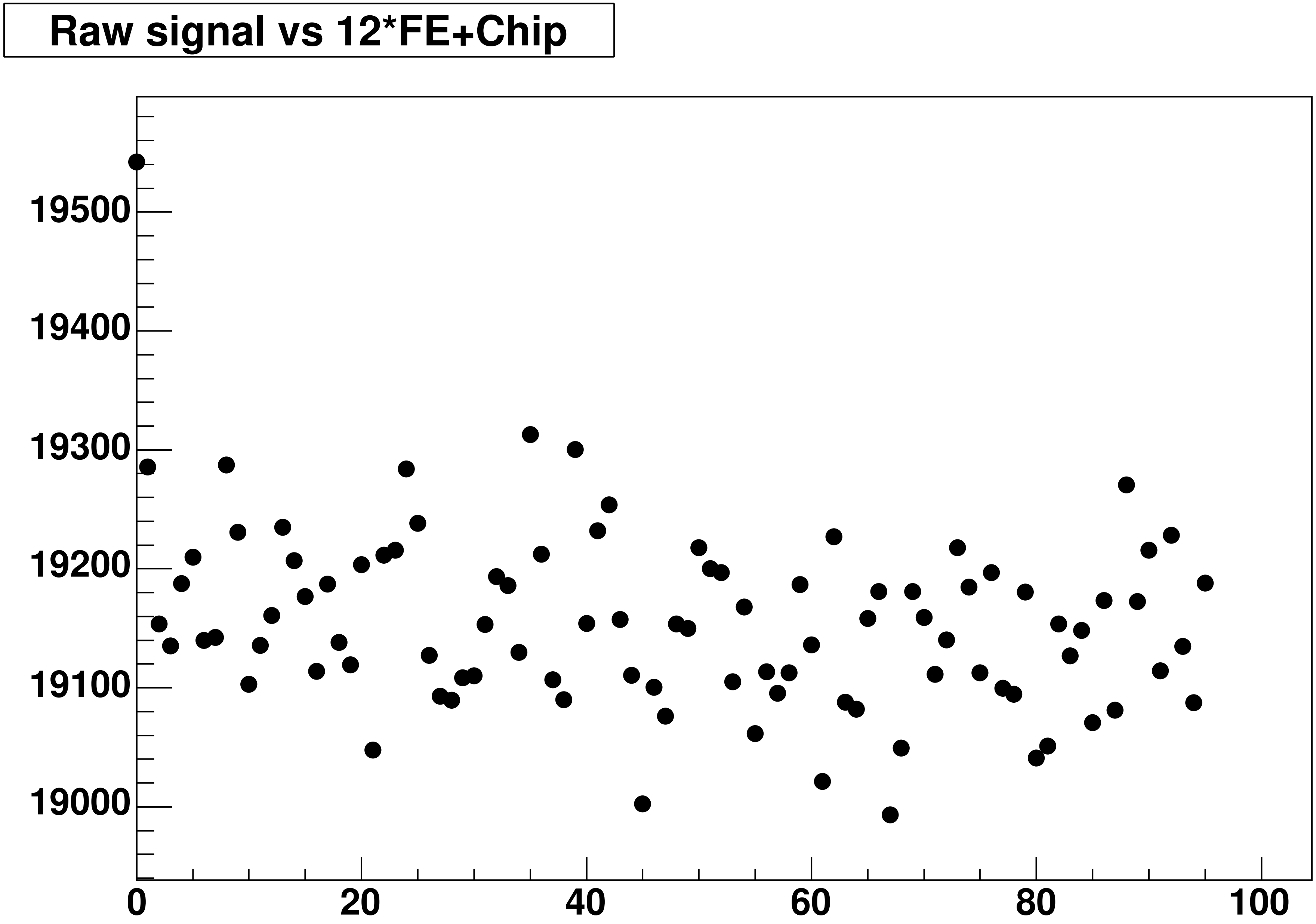

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 704484
Mean -0.7153
RMS 0.9433
Underflow 0
Overflow 3.686e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

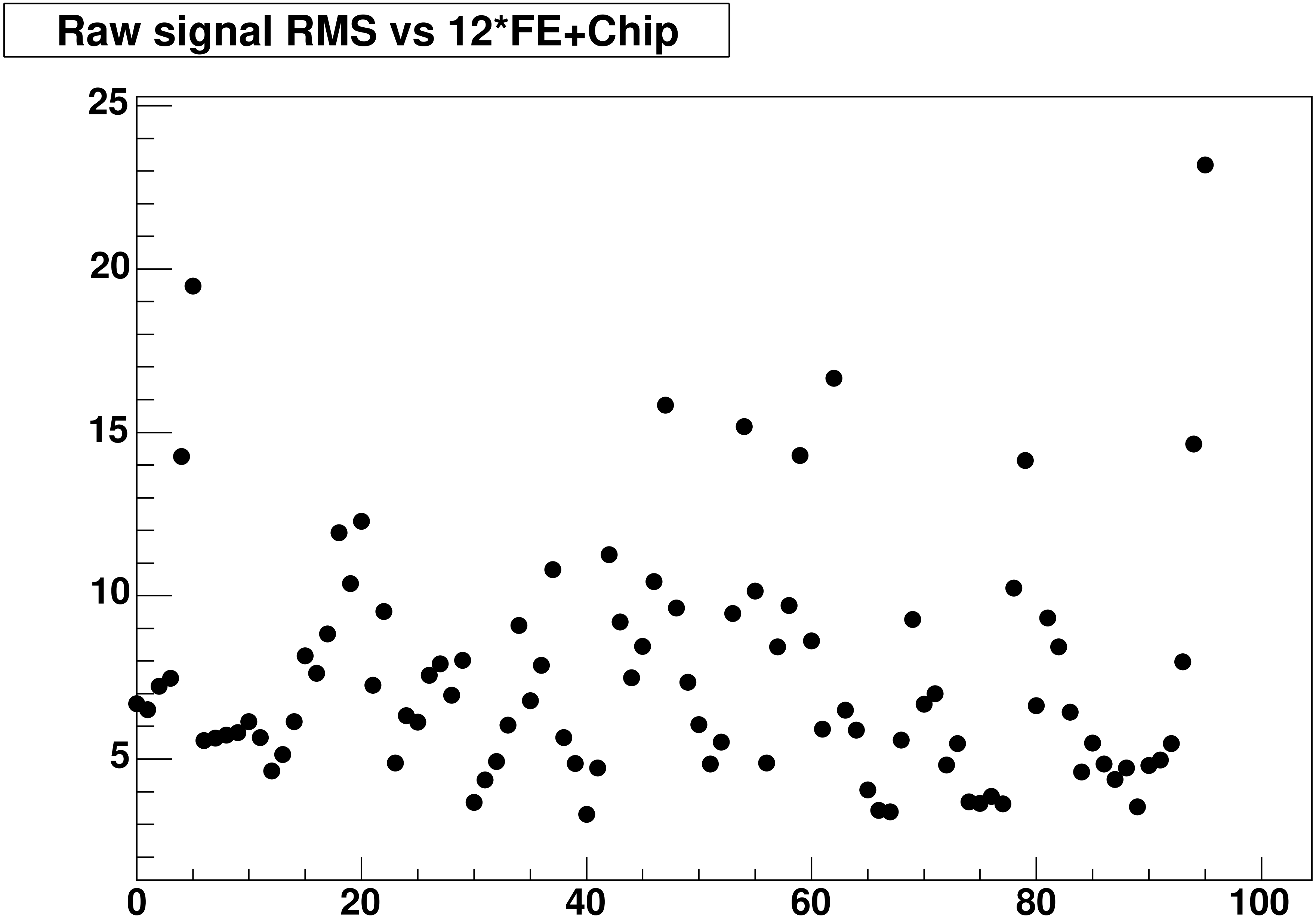

FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 704484
Mean 1.919e+04
RMS 23.19
Underflow 6.676e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


Pedestal vs 12*FE+Chip
40
30
20
10
0
-10
-20
-30
-40
0
20
40
60
80
100

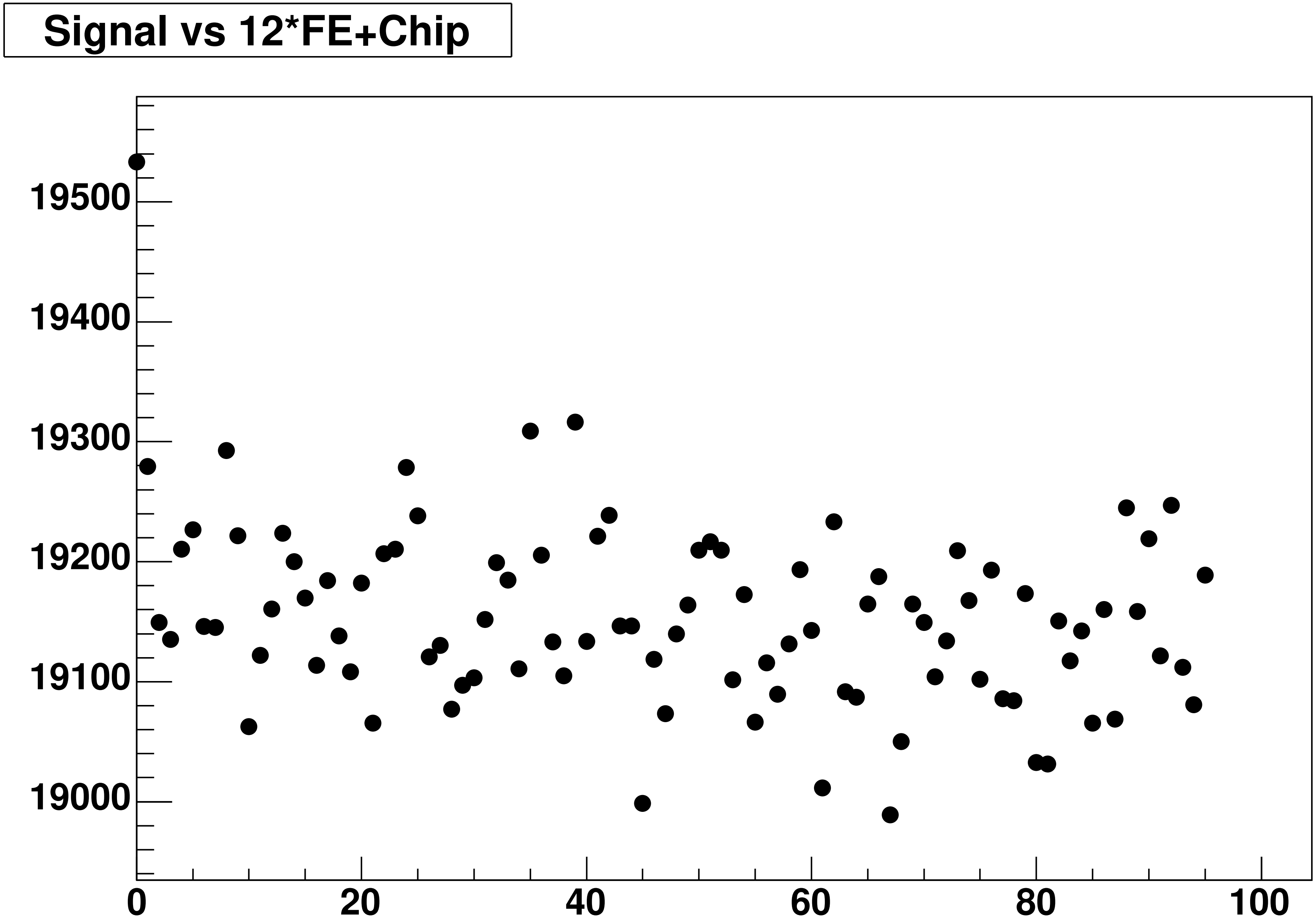


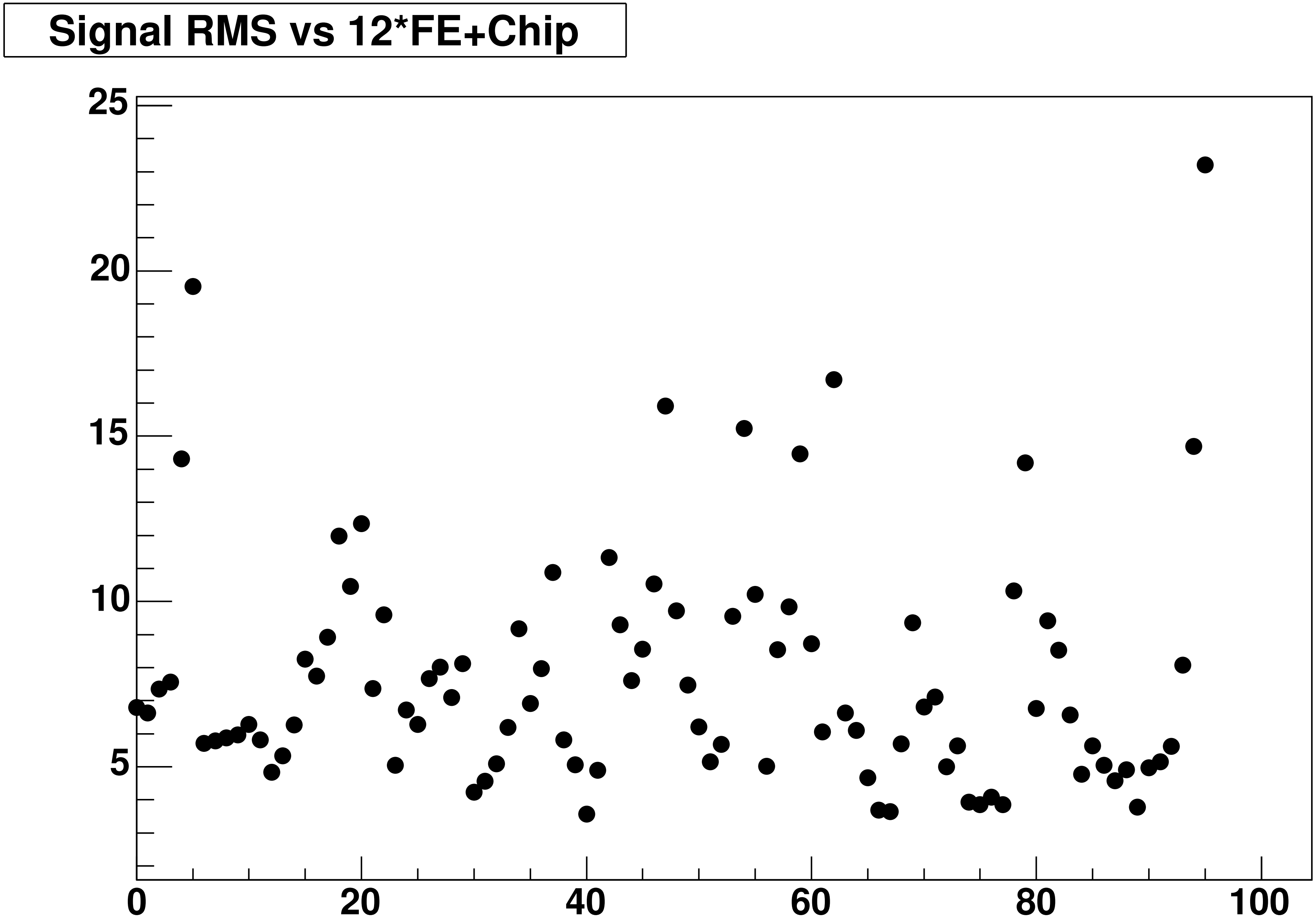
Pedestal RMS vs 12*FE+Chip
2.4
2.2
2
1.8
1.6
1.4
1.2
1
0
20
40
60
80
100


Raw signal vs 12*FE+Chip
19500
19400
19300
19200
19100
19000
0
20
40
60
80
100



Raw signal RMS vs 12*FE+Chip
25
20
15
10
5
0
20
40
60
80
100


Signal vs 12*FE+Chip
19000
19100
19200
19300
19400
19500
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
25
20
15
10
5
0
20
40
60
80
100