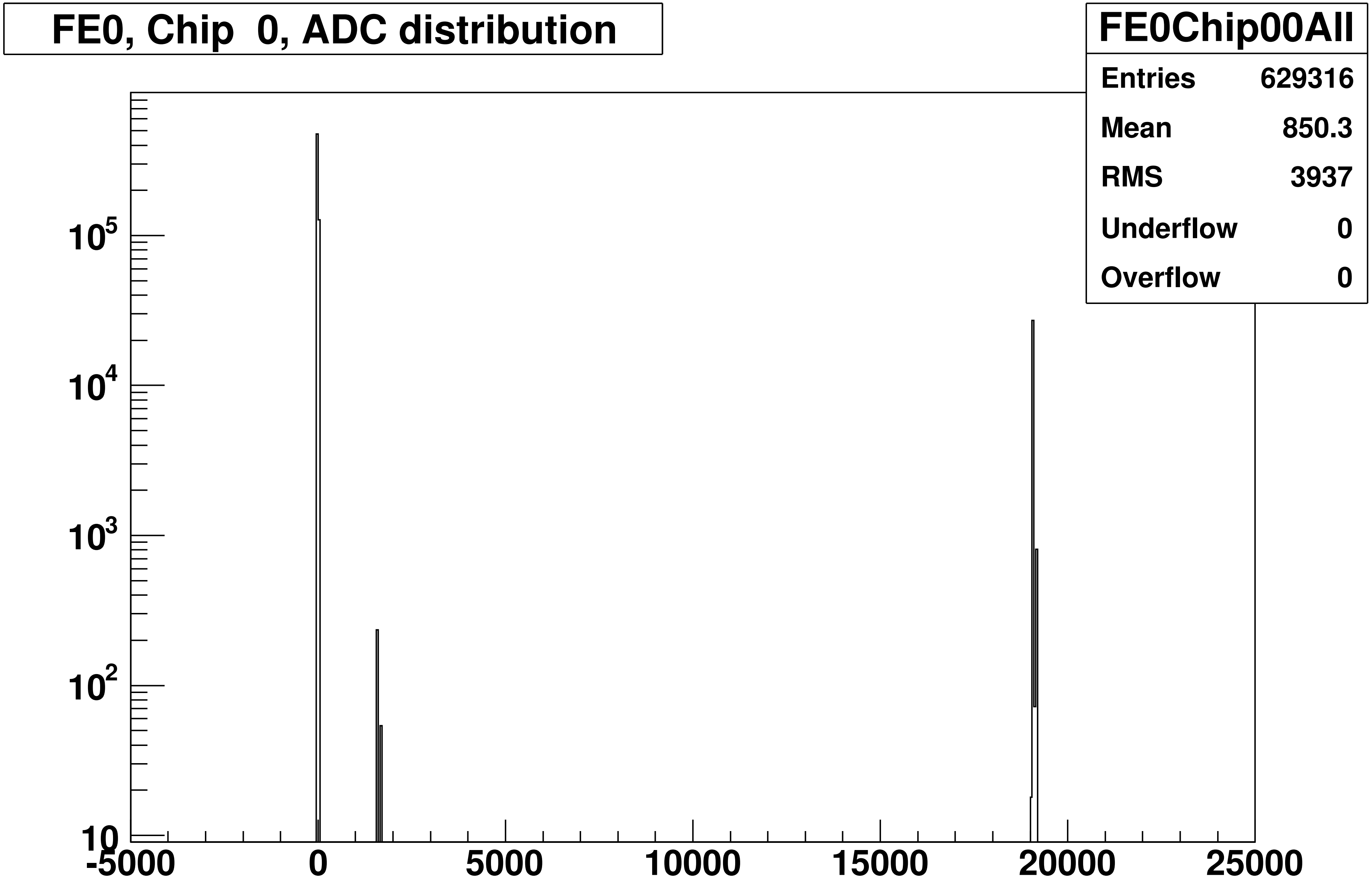
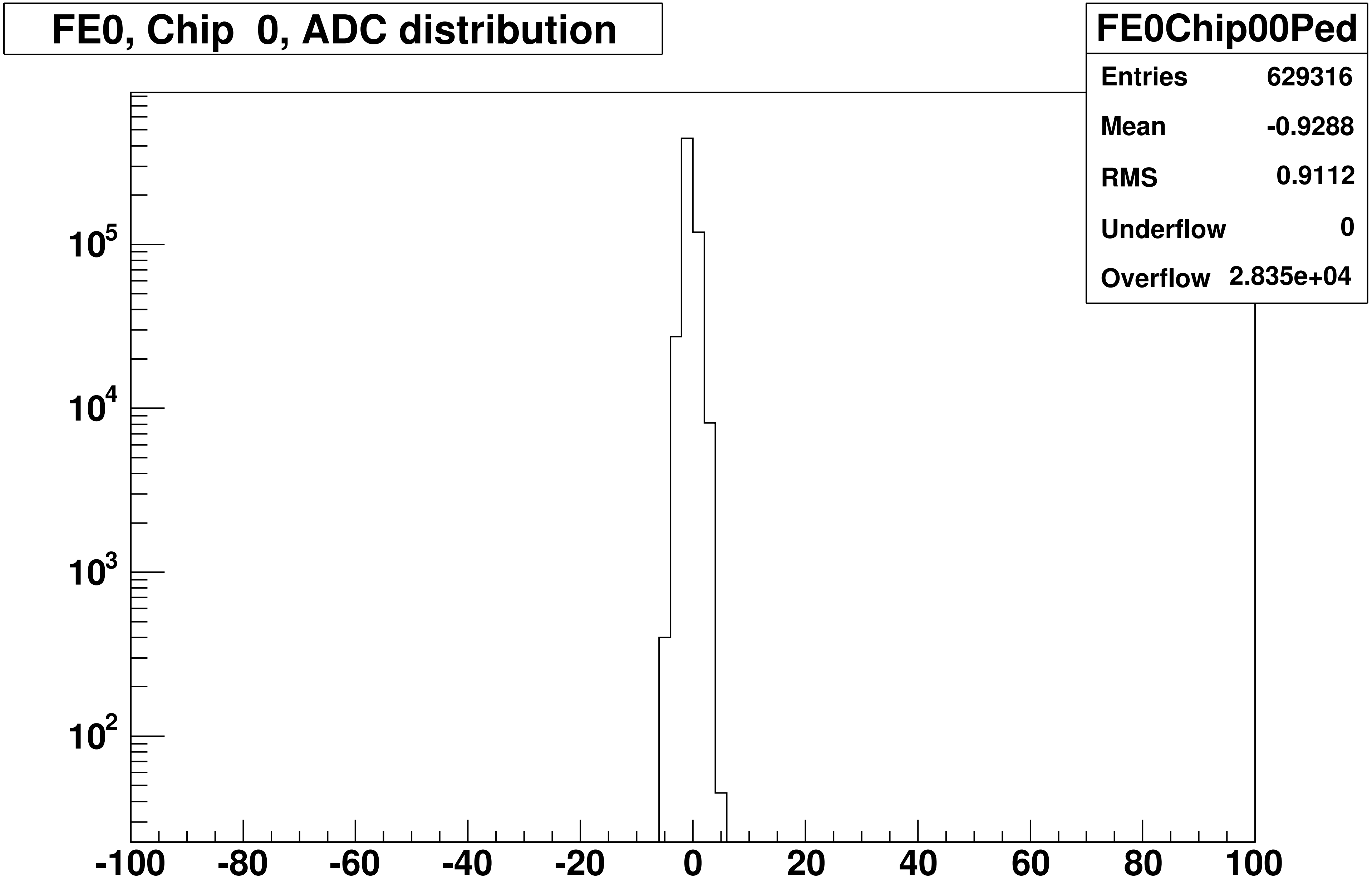
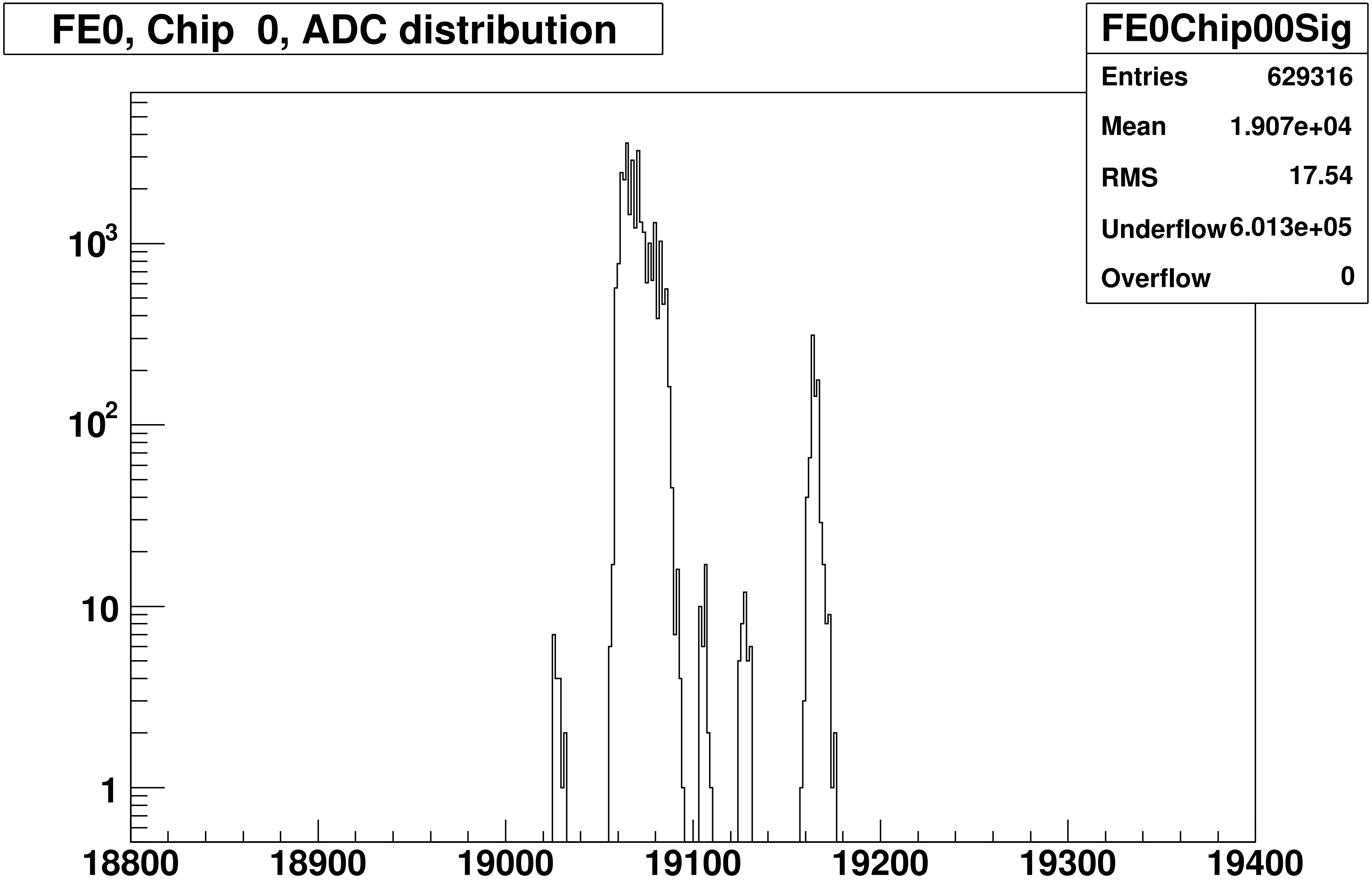
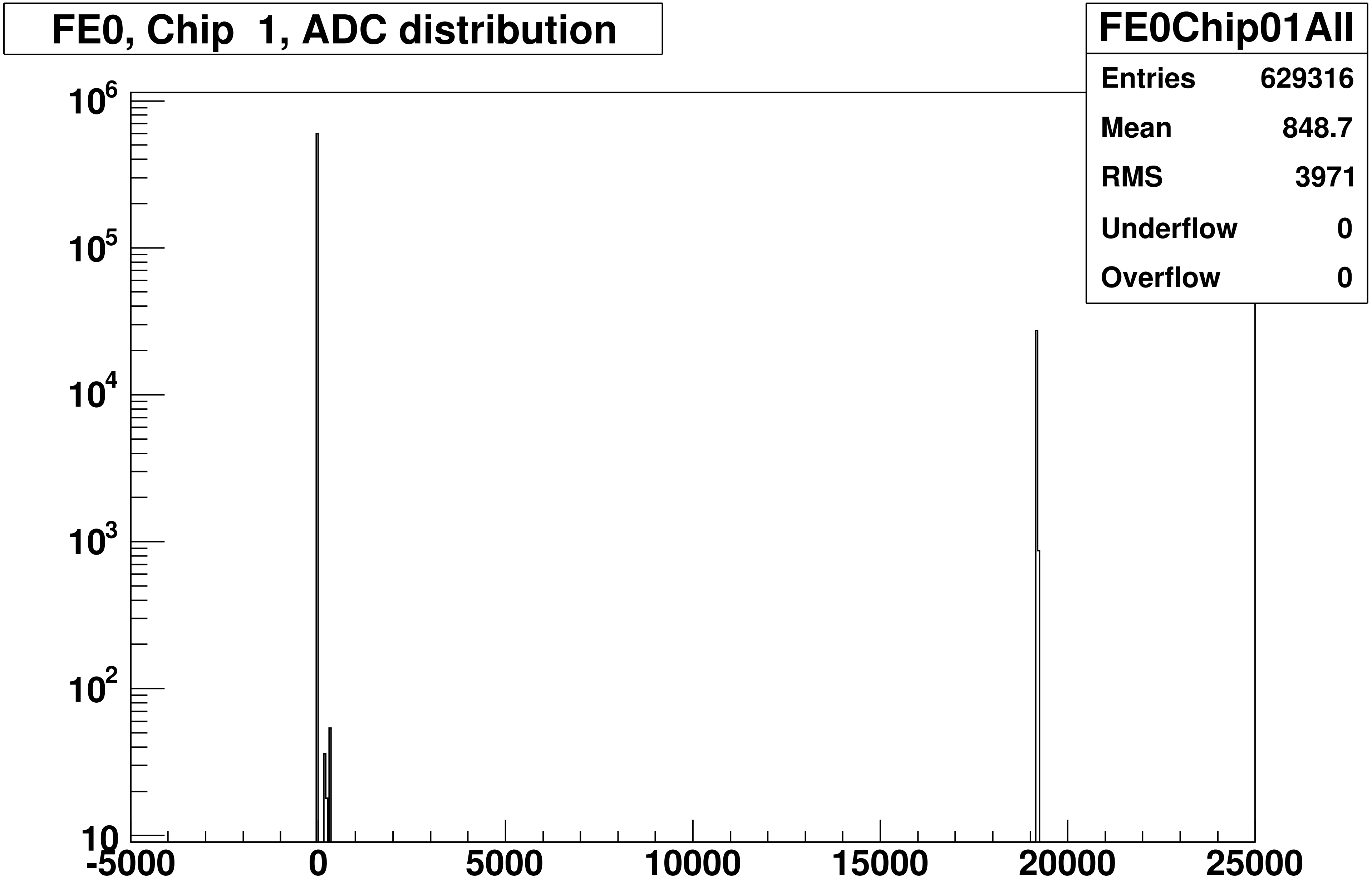


FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 629316
Mean 850.3
RMS 3937
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

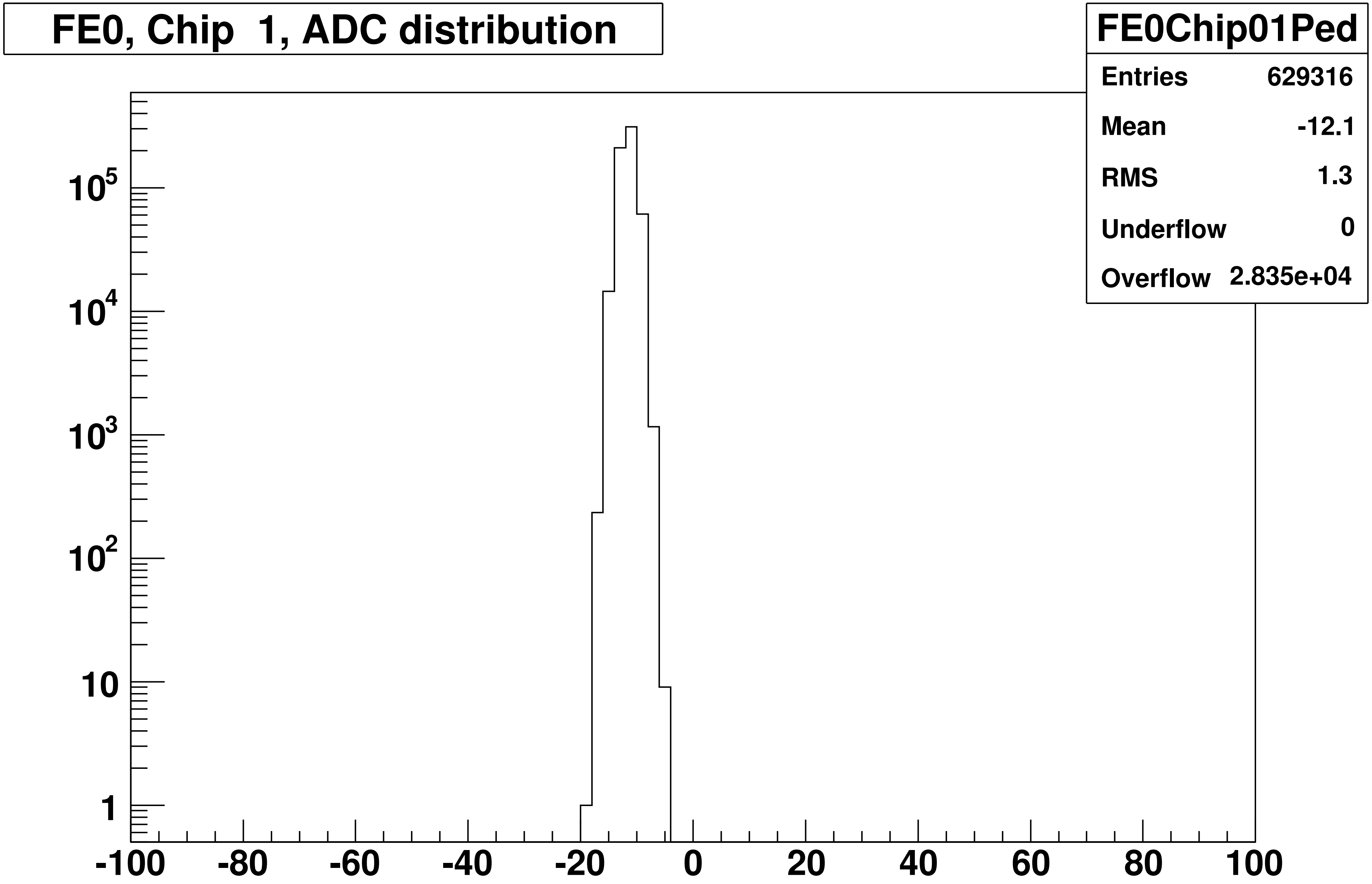

FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 629316
Mean -0.9288
RMS 0.9112
Underflow 0
Overflow 2.835e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

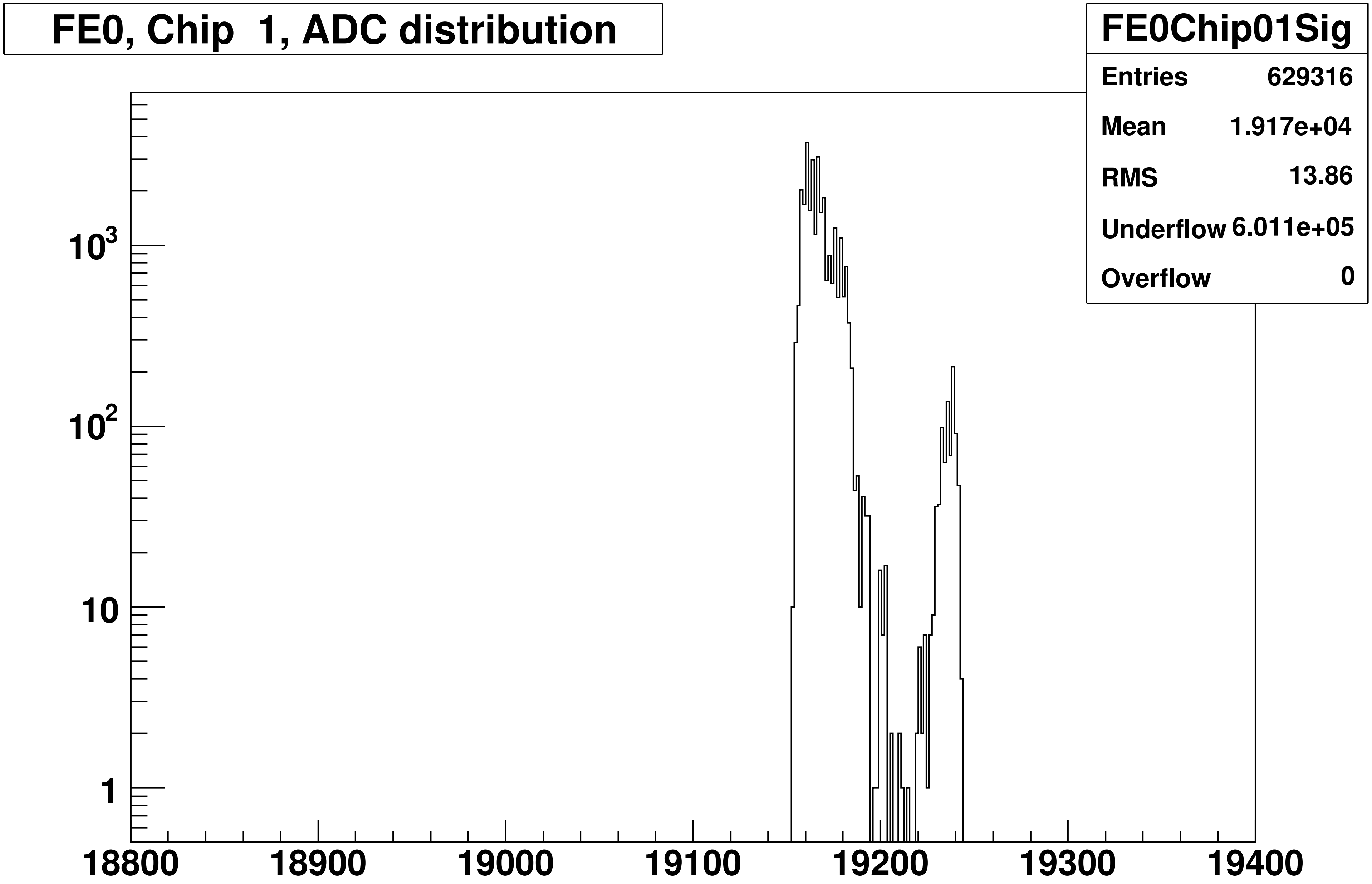


FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 629316
Mean 1.907e+04
RMS 17.54
Underflow 6.013e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

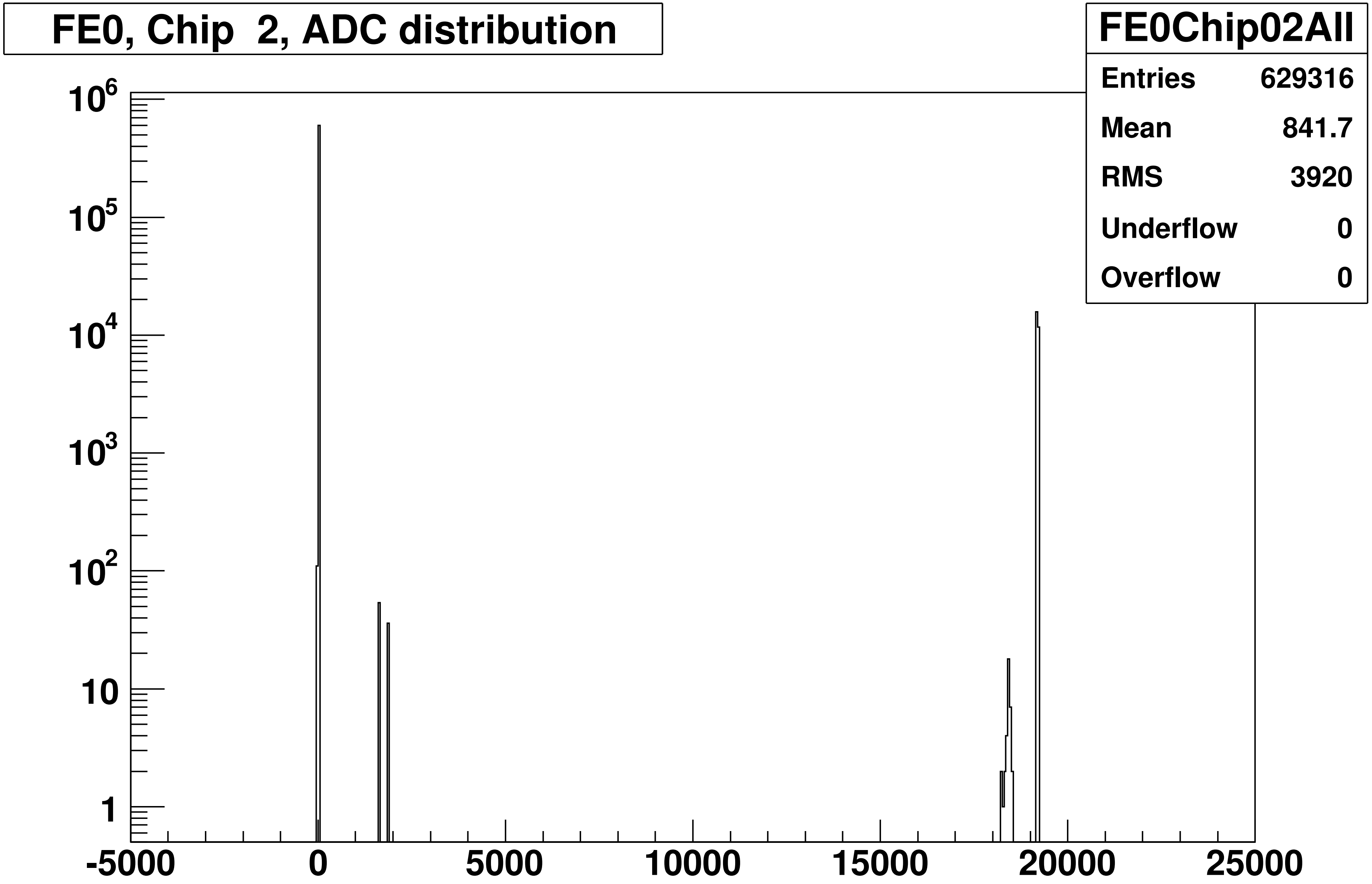


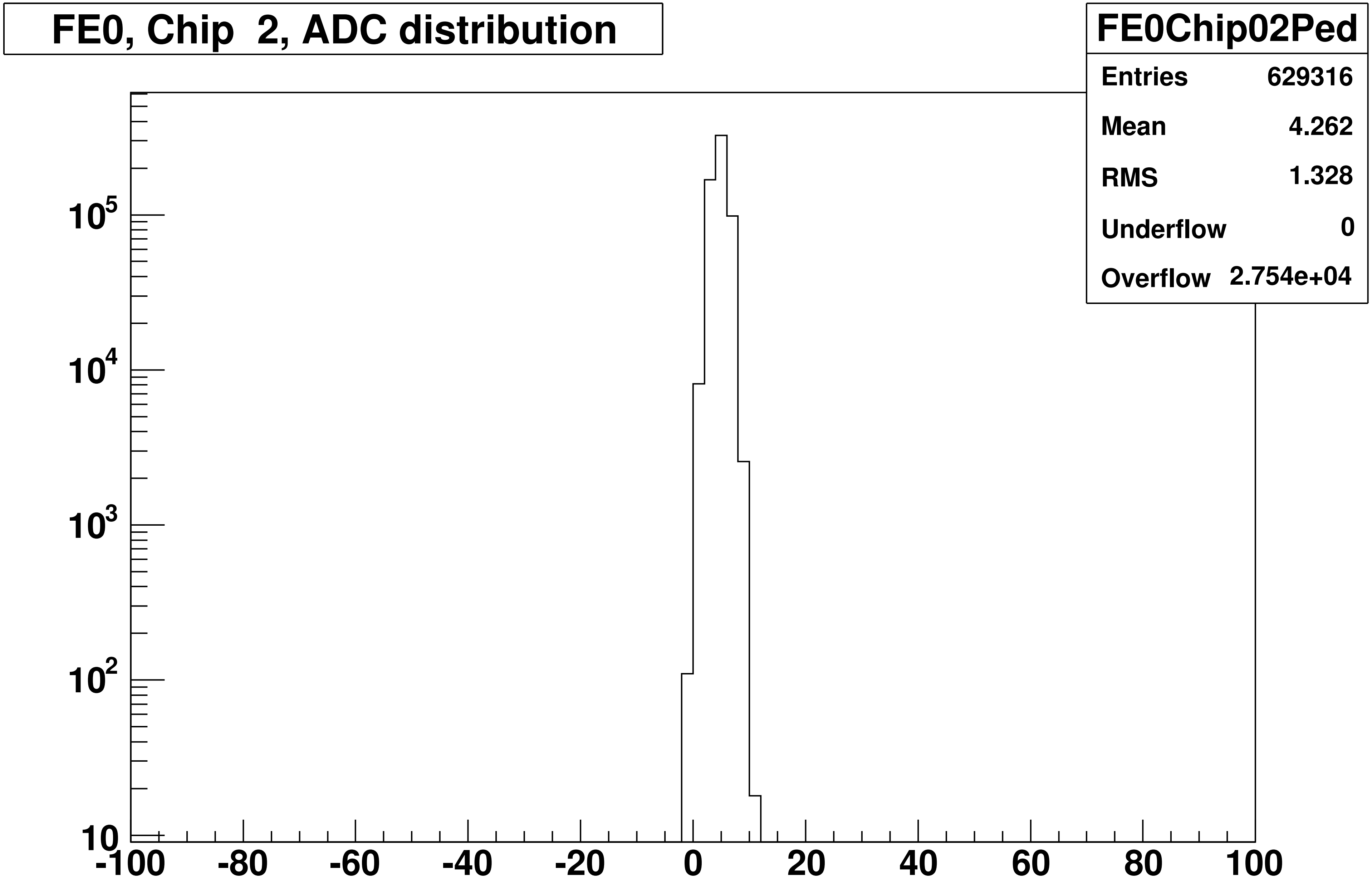
FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 629316
Mean 848.7
RMS 3971
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

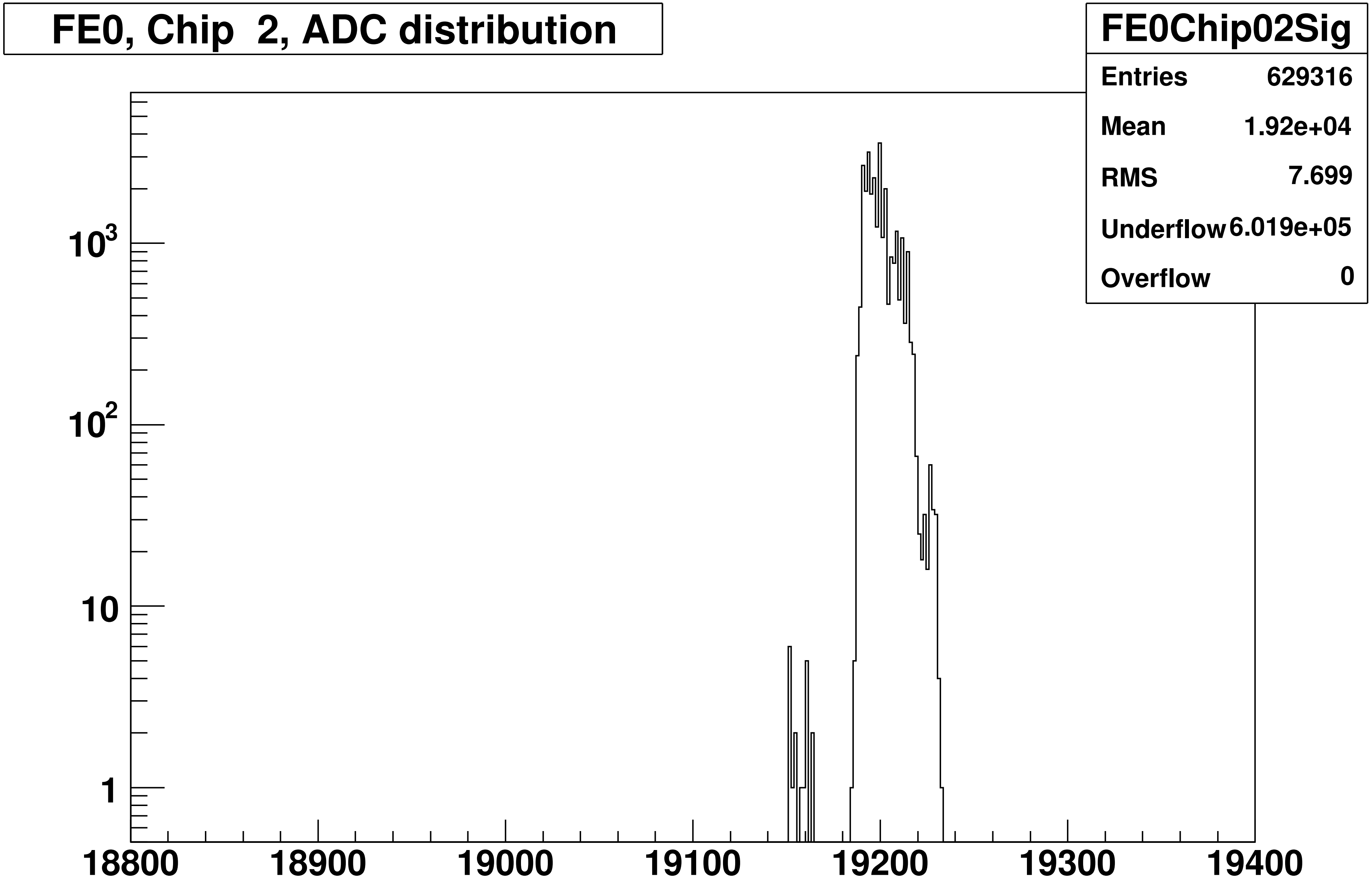

FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 629316
Mean -12.1
RMS 1.3
Underflow 0
Overflow 2.835e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



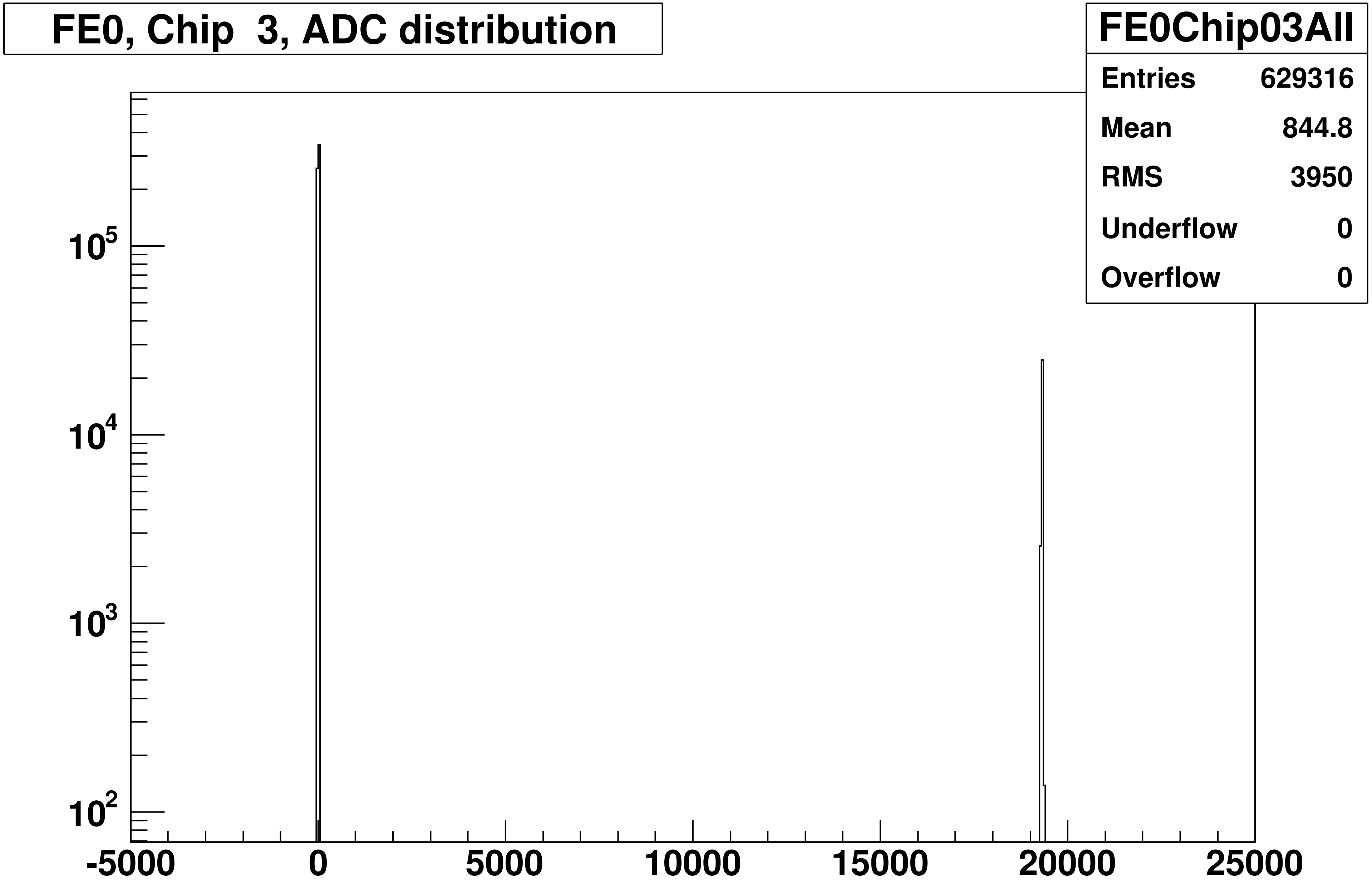
FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 629316
Mean 1.917e+04
RMS 13.86
Underflow 6.011e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 629316
Mean 841.7
RMS 3920
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

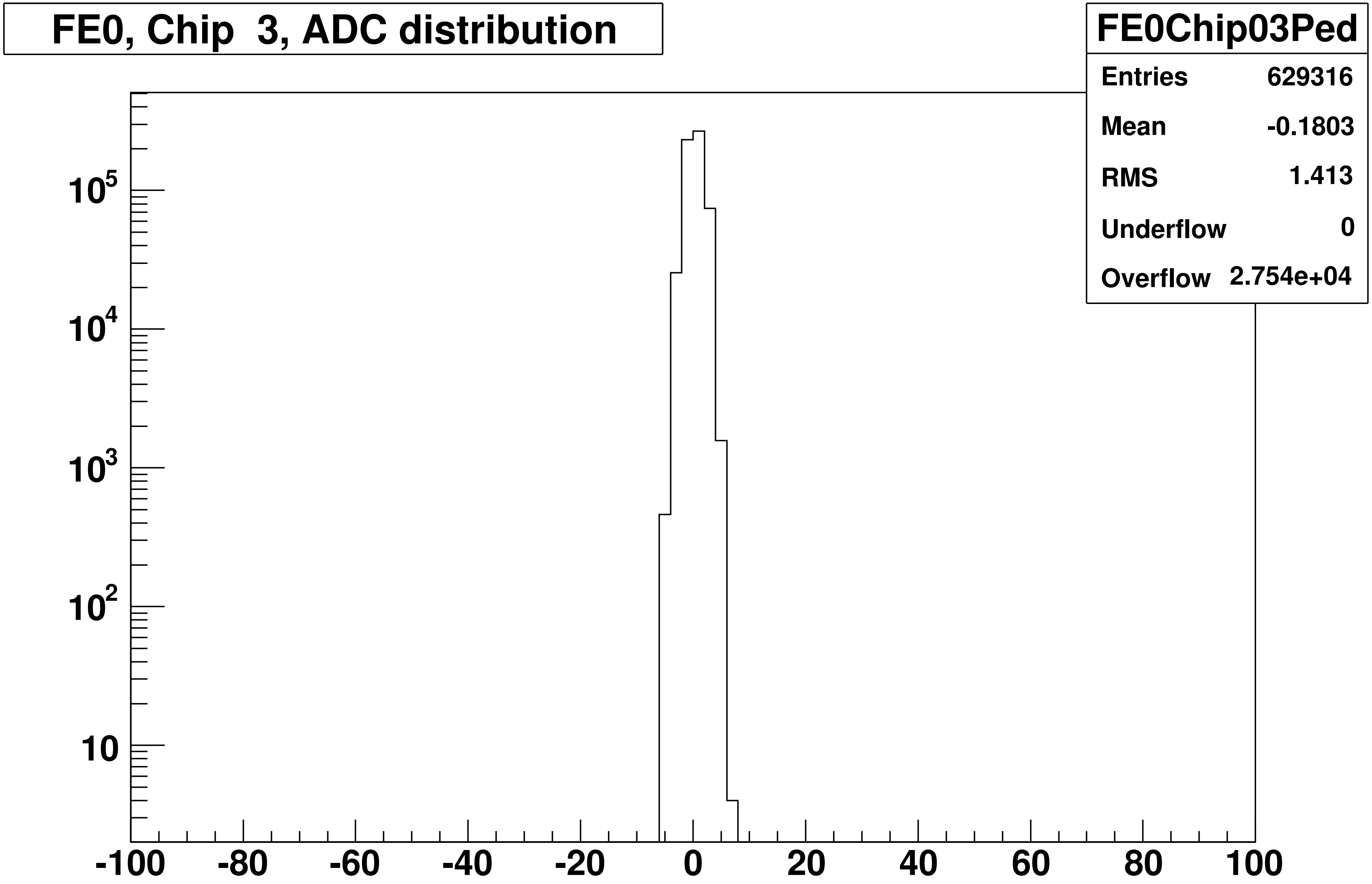

FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 629316
Mean 4.262
RMS 1.328
Underflow 0
Overflow 2.754e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

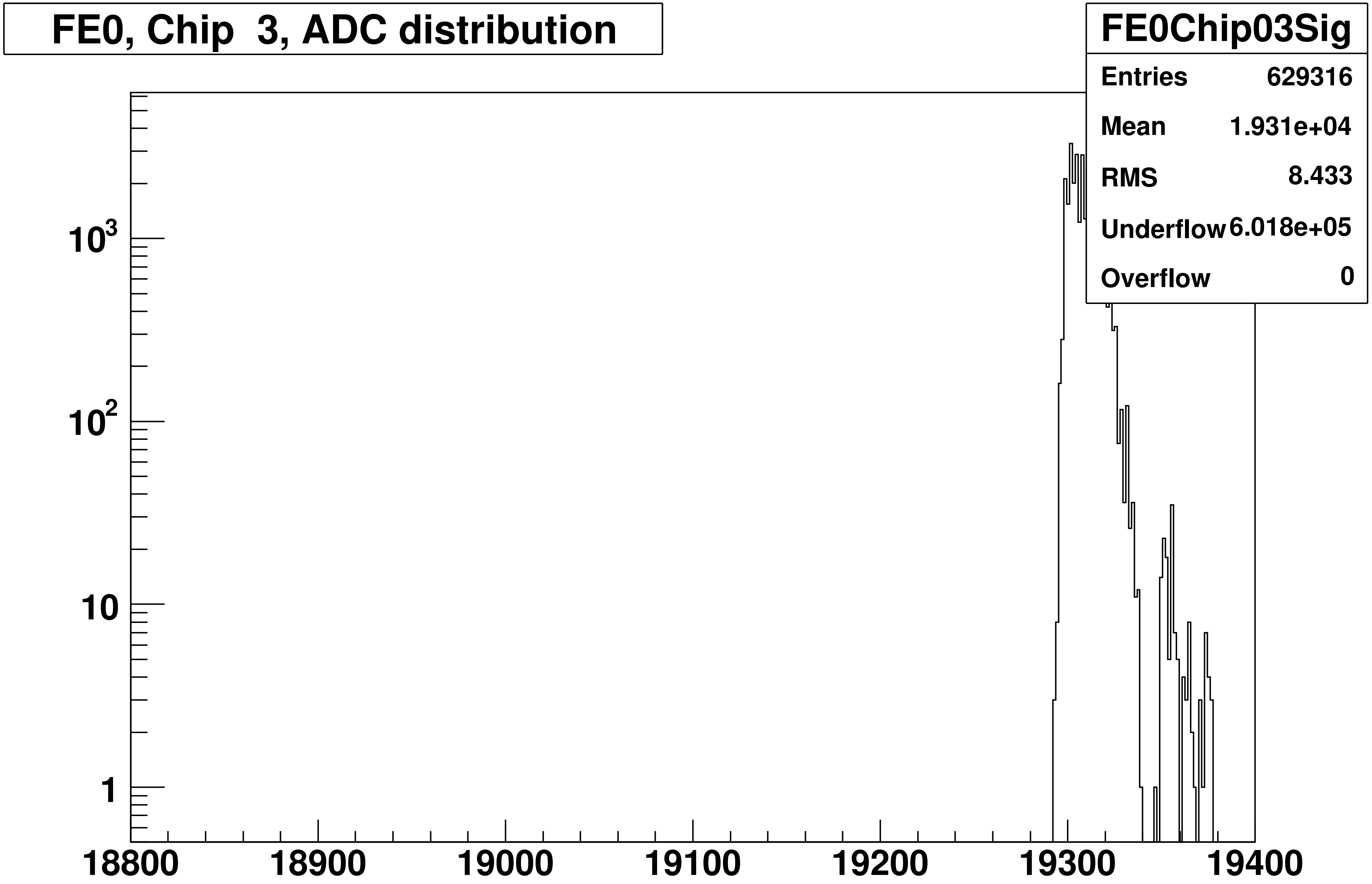


FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 629316
Mean 1.92e+04
RMS 7.699
Underflow 6.019e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

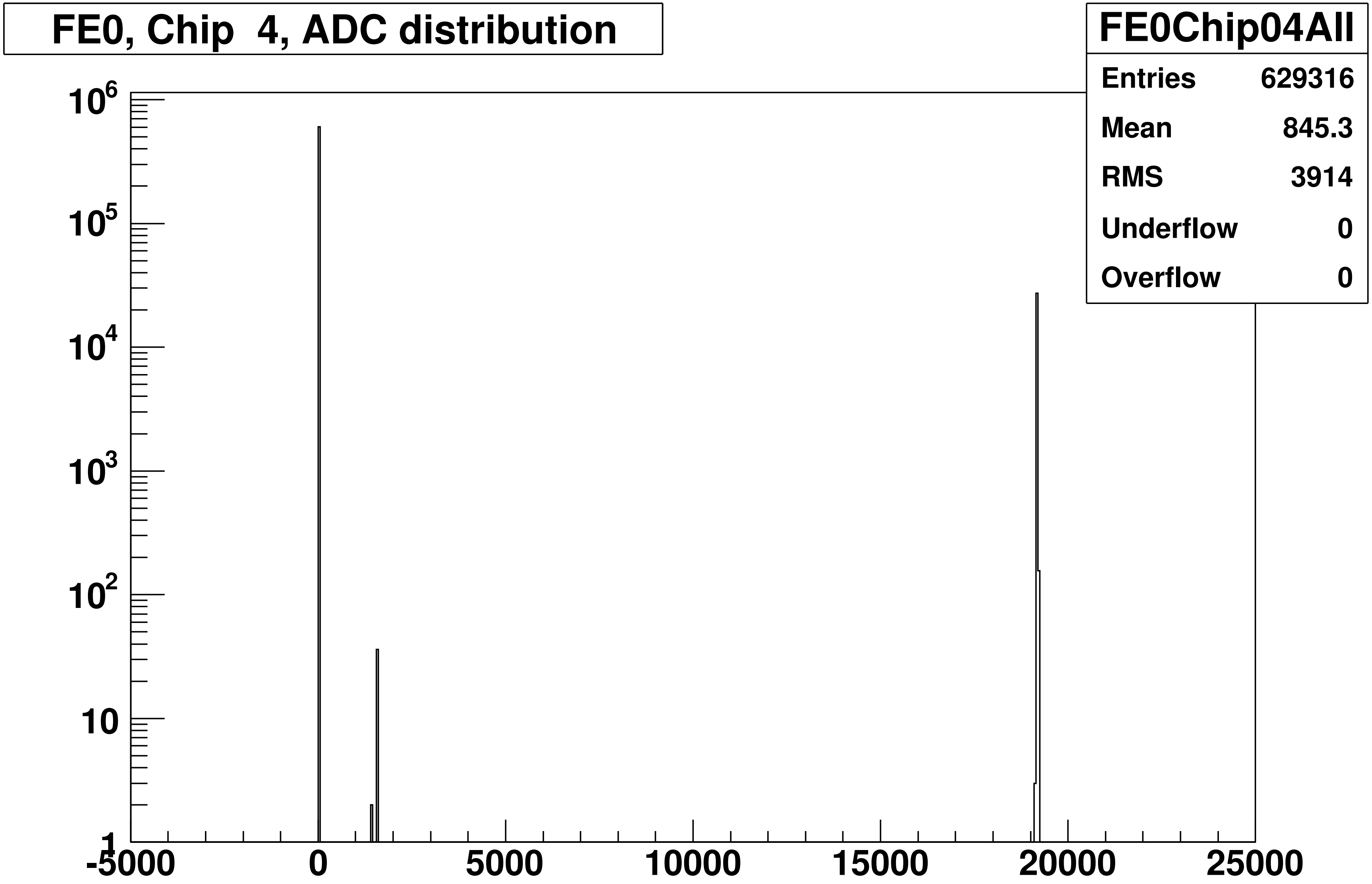


FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 629316
Mean 844.8
RMS 3950
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

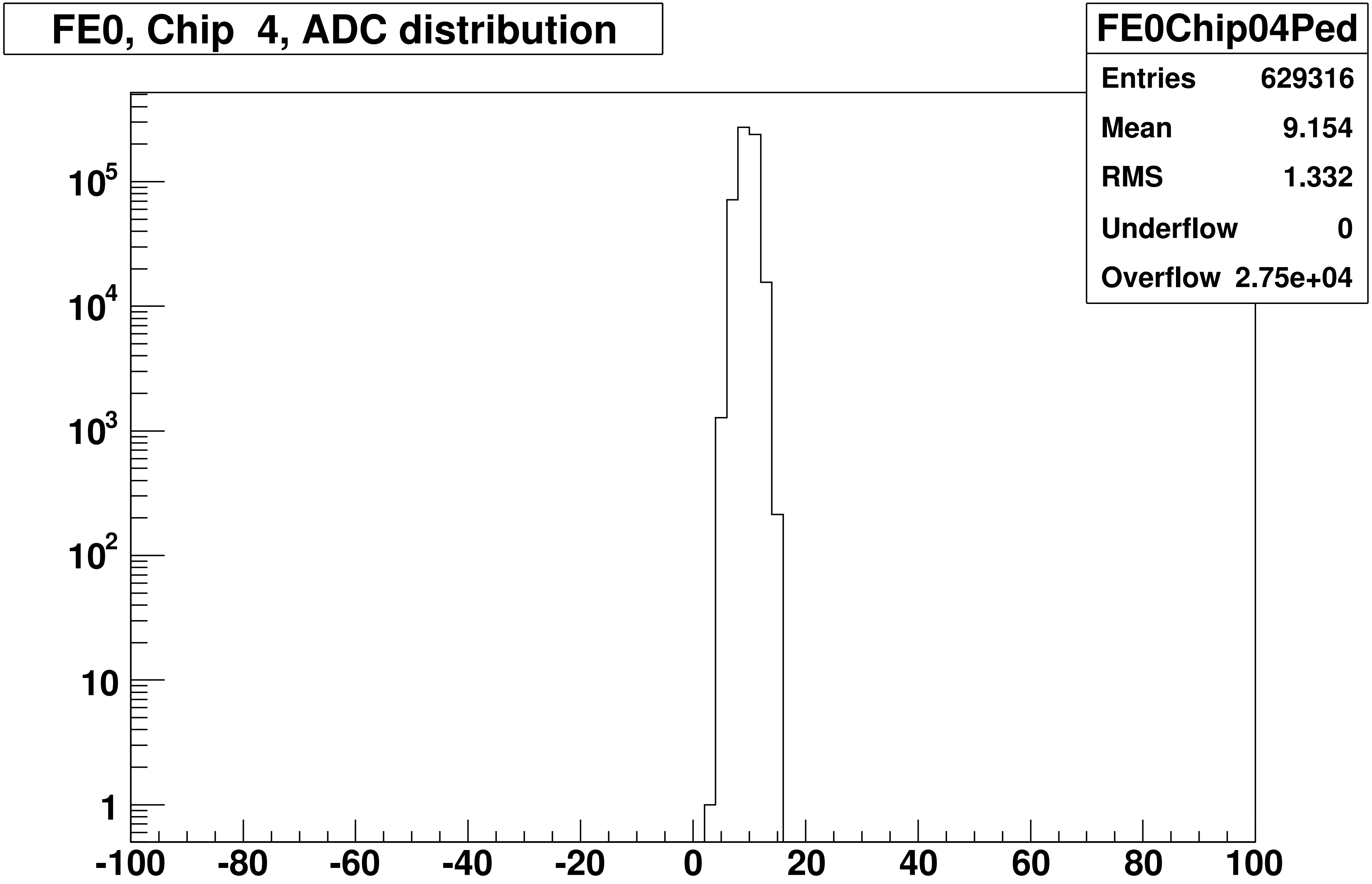

FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 629316
Mean -0.1803
RMS 1.413
Underflow 0
Overflow 2.754e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

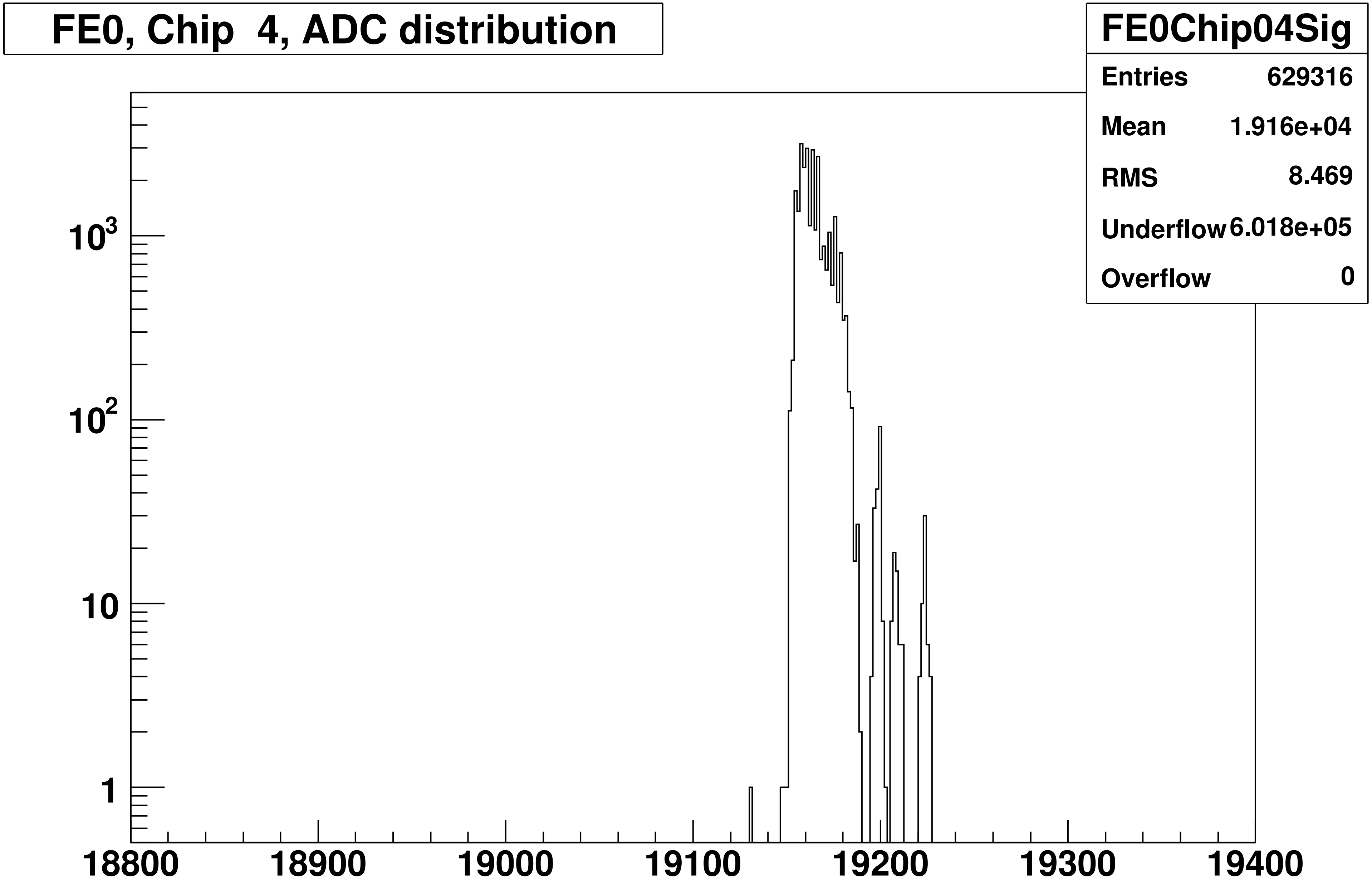


FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 629316
Mean 1.931e+04
RMS 8.433
Underflow 6.018e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

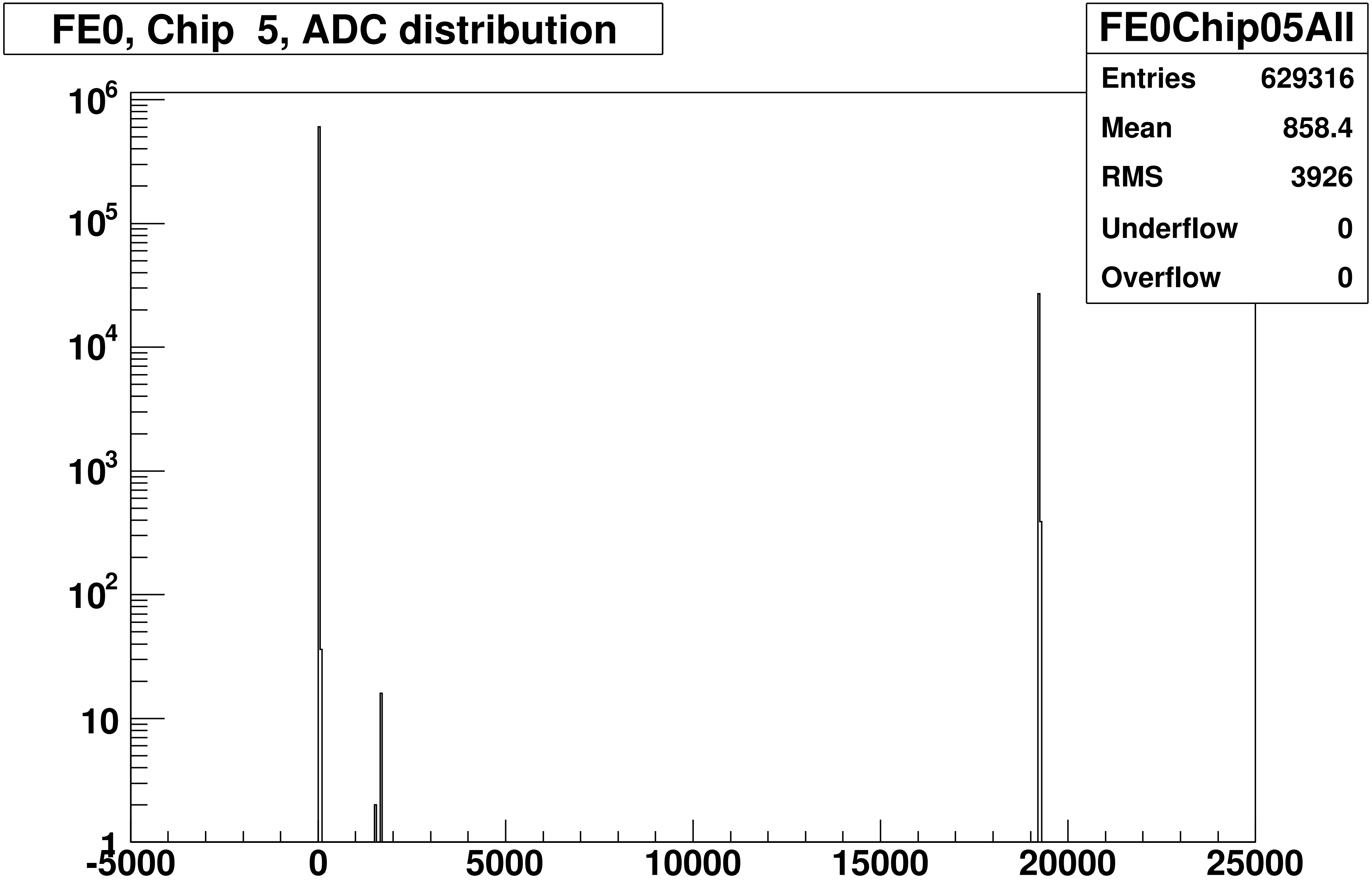


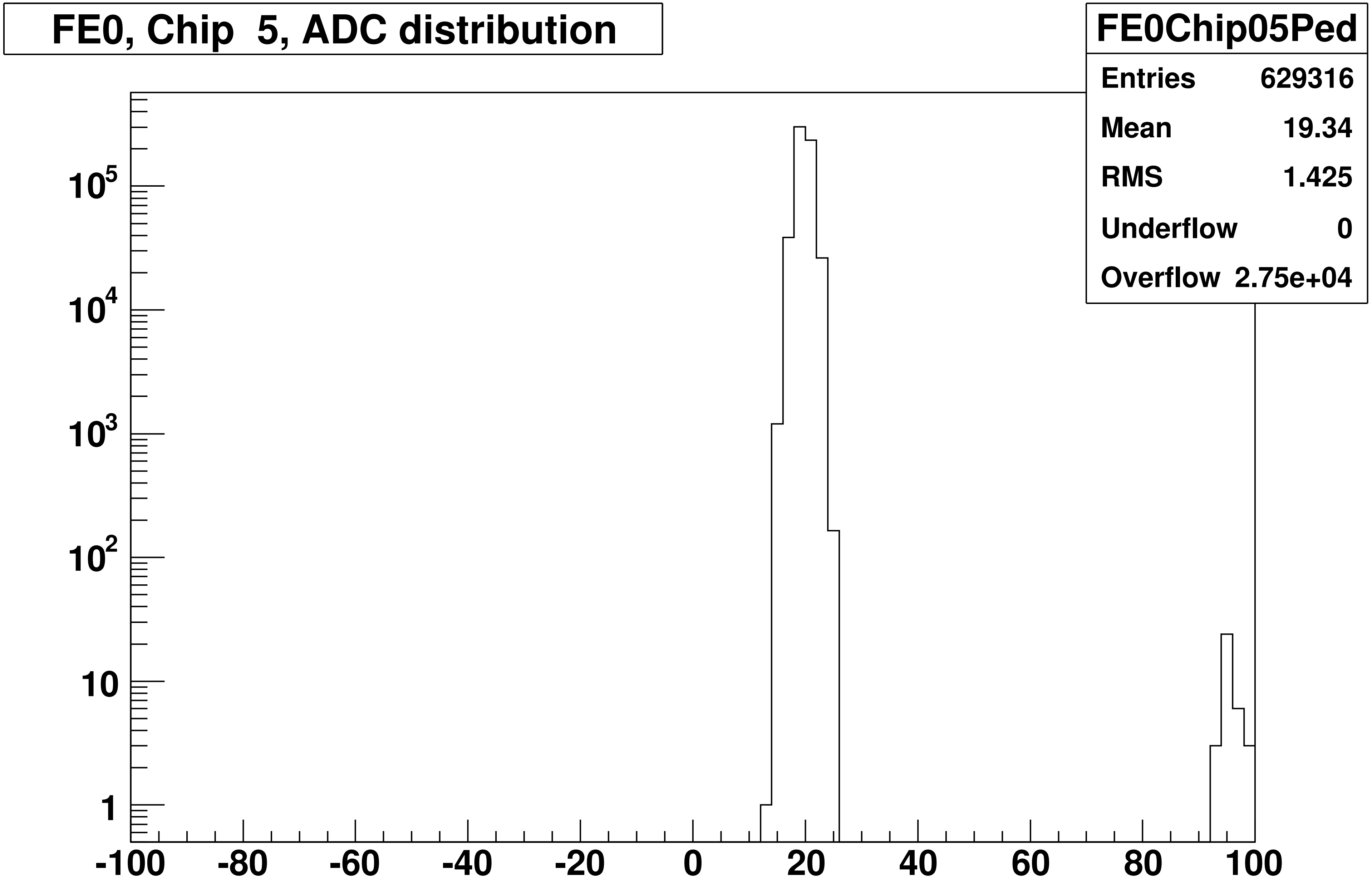
FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 629316
Mean 845.3
RMS 3914
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

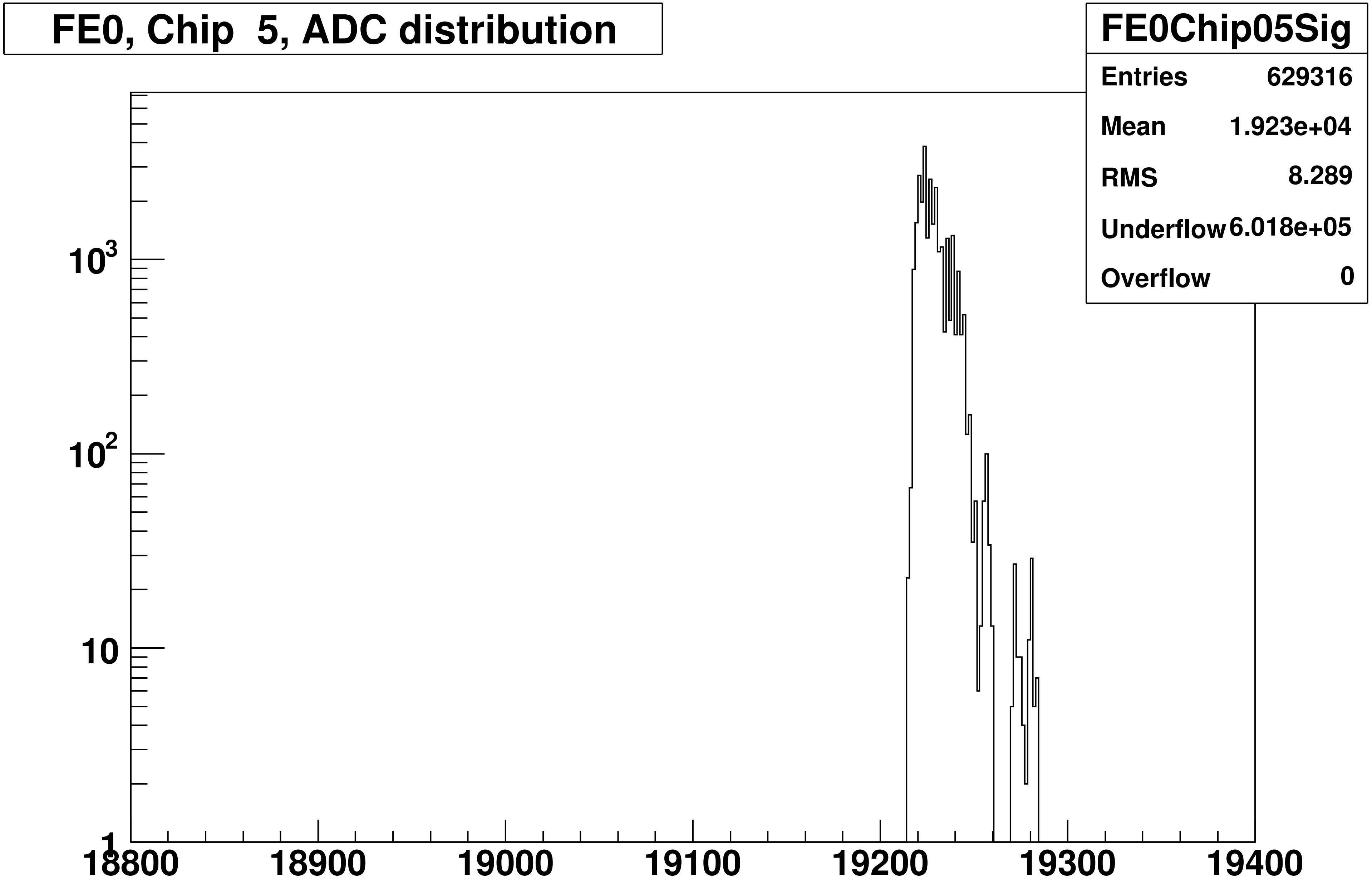

FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 629316
Mean 9.154
RMS 1.332
Underflow 0
Overflow 2.75e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

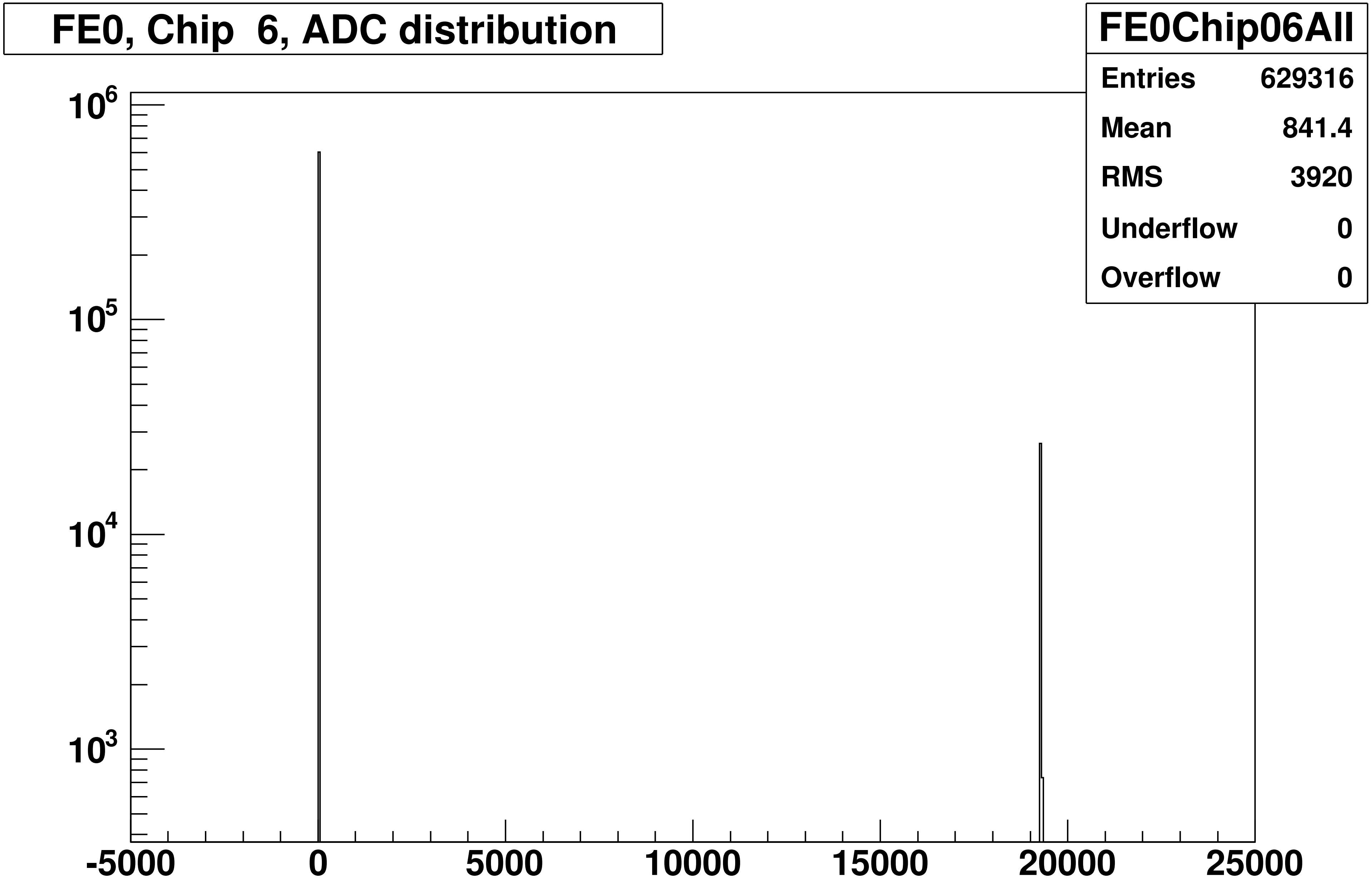


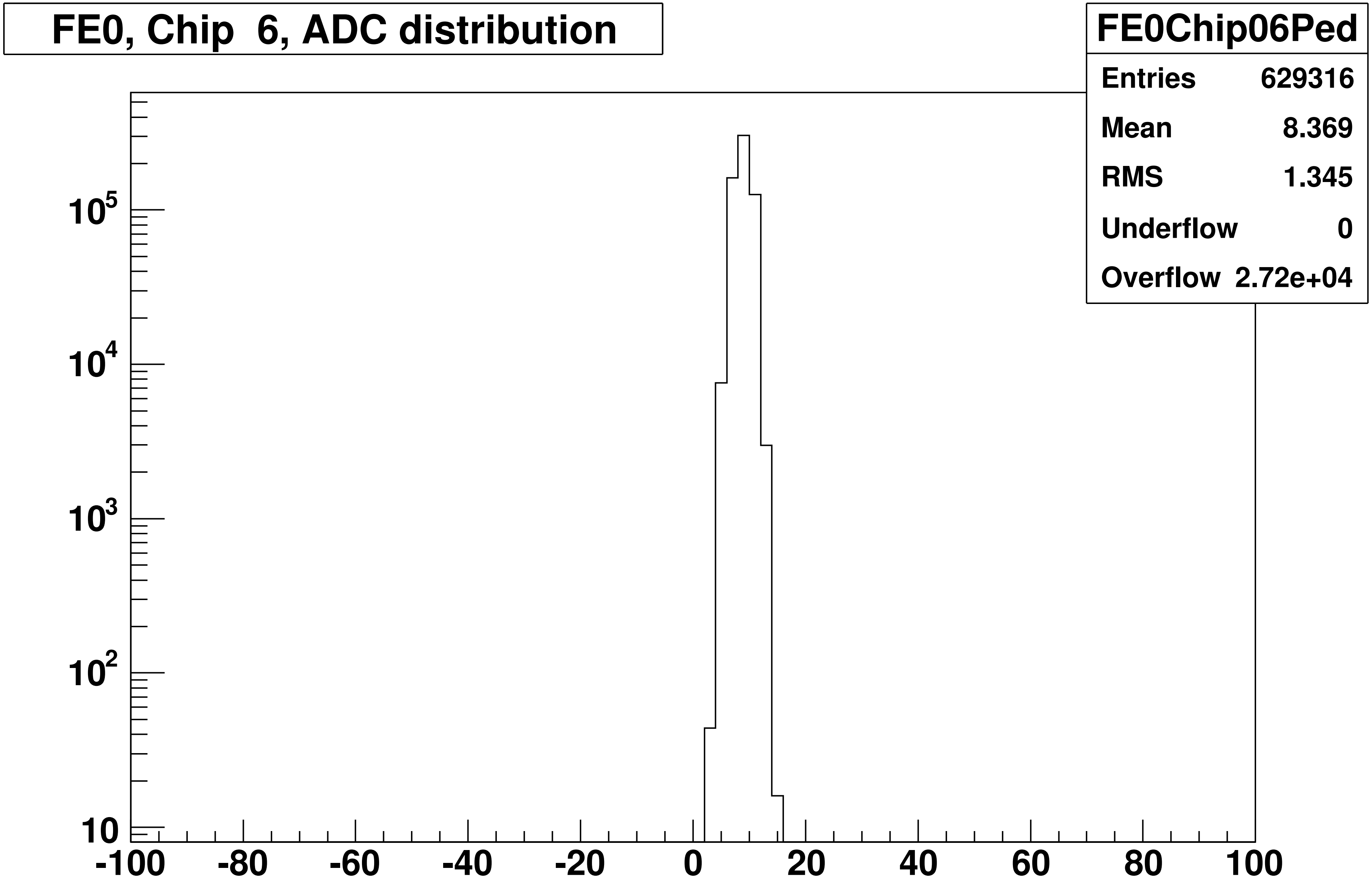
FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 629316
Mean 1.916e+04
RMS 8.469
Underflow 6.018e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

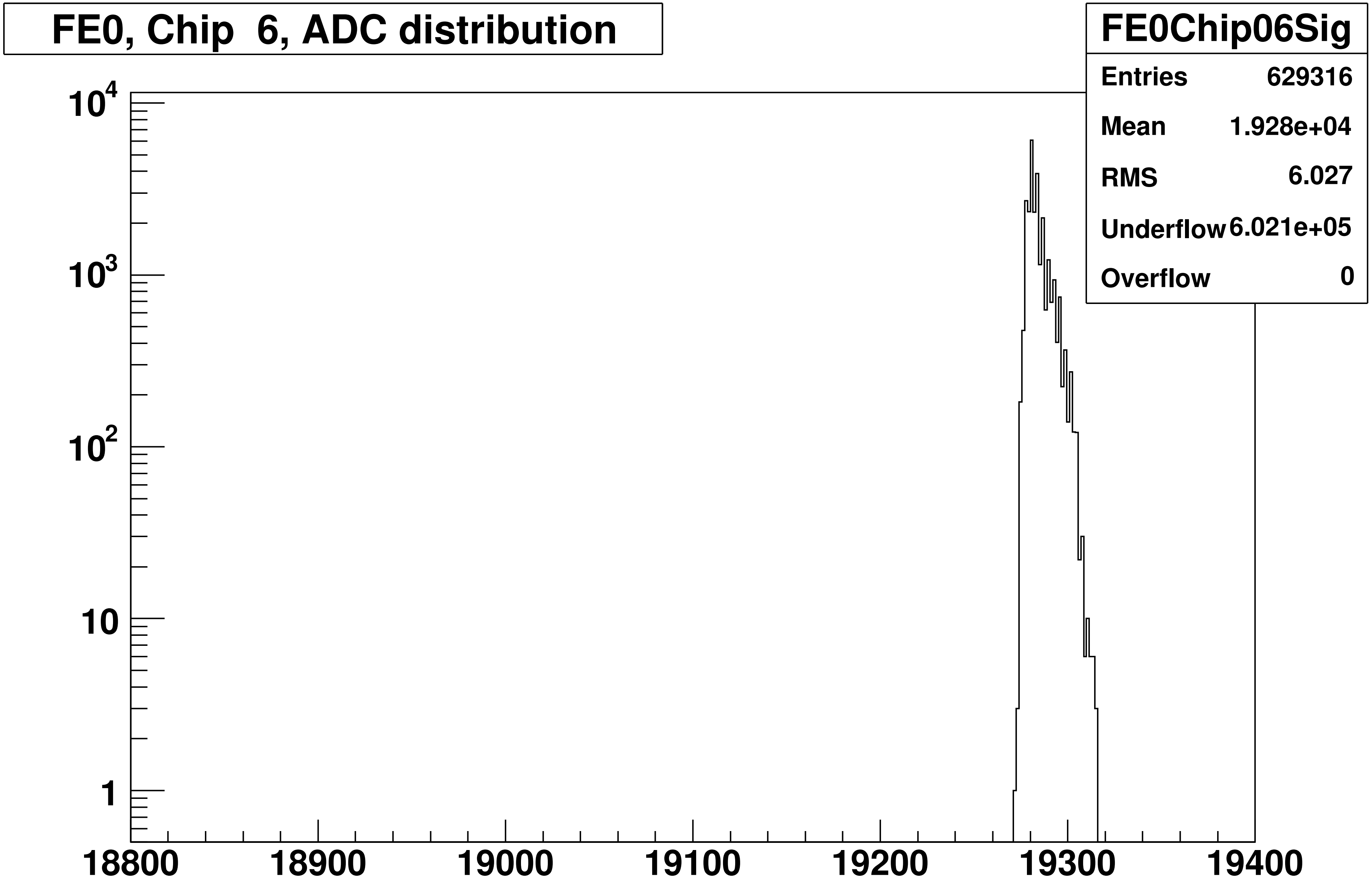

FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 629316
Mean 858.4
RMS 3926
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

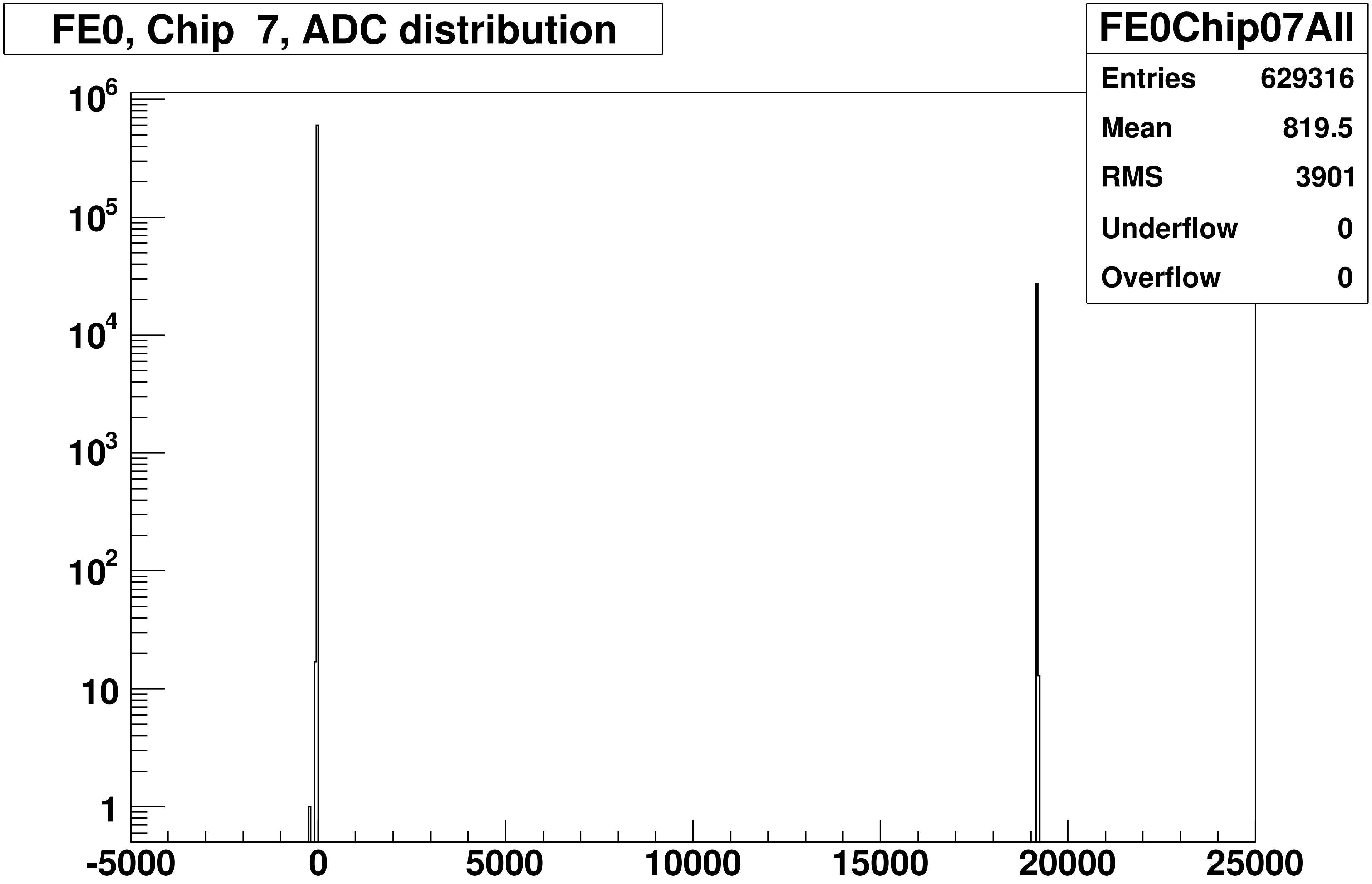

FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 629316
Mean 19.34
RMS 1.425
Underflow 0
Overflow 2.75e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 629316
Mean 1.923e+04
RMS 8.289
Underflow 6.018e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400

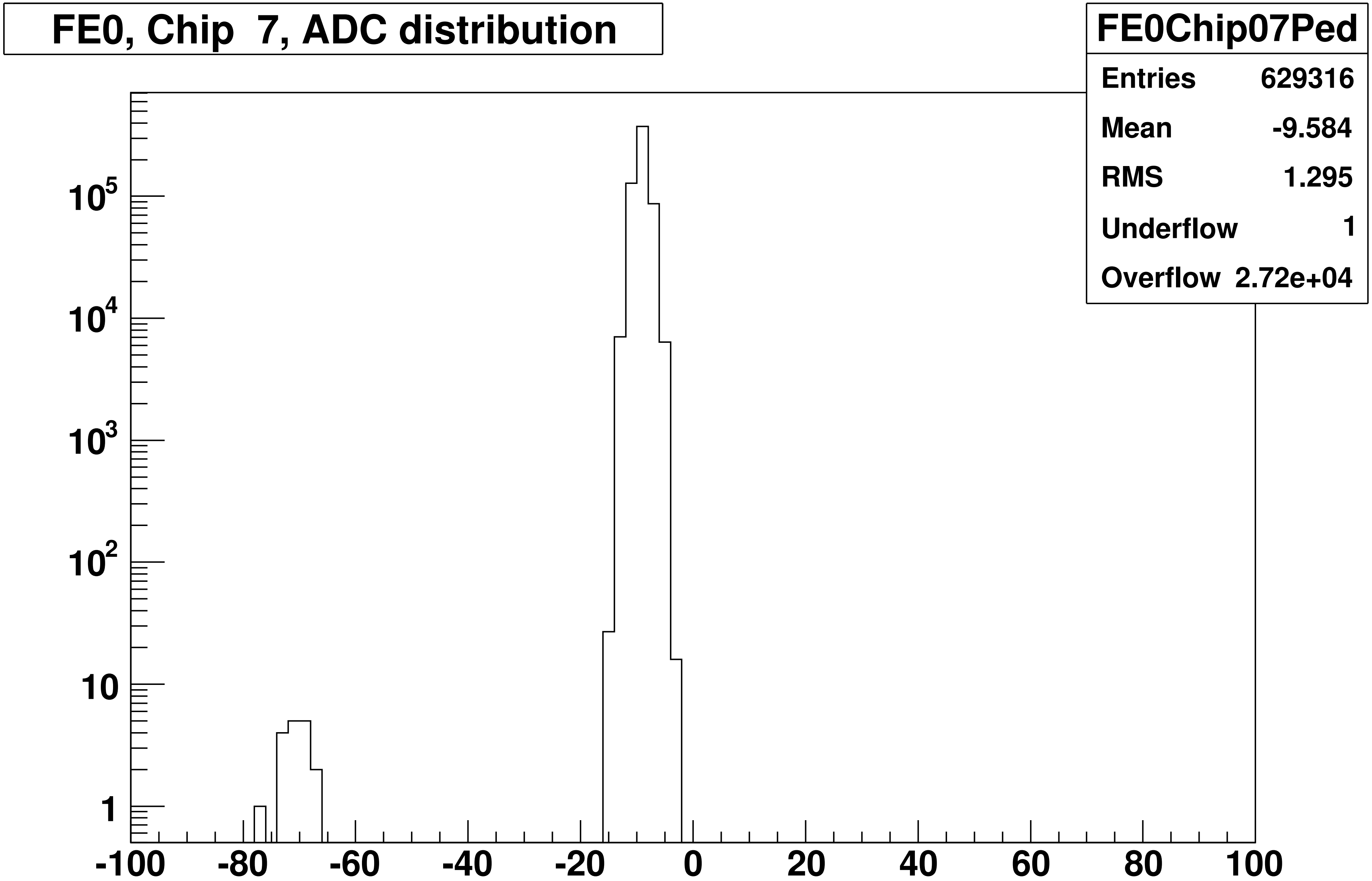

FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 629316
Mean 841.4
RMS 3920
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

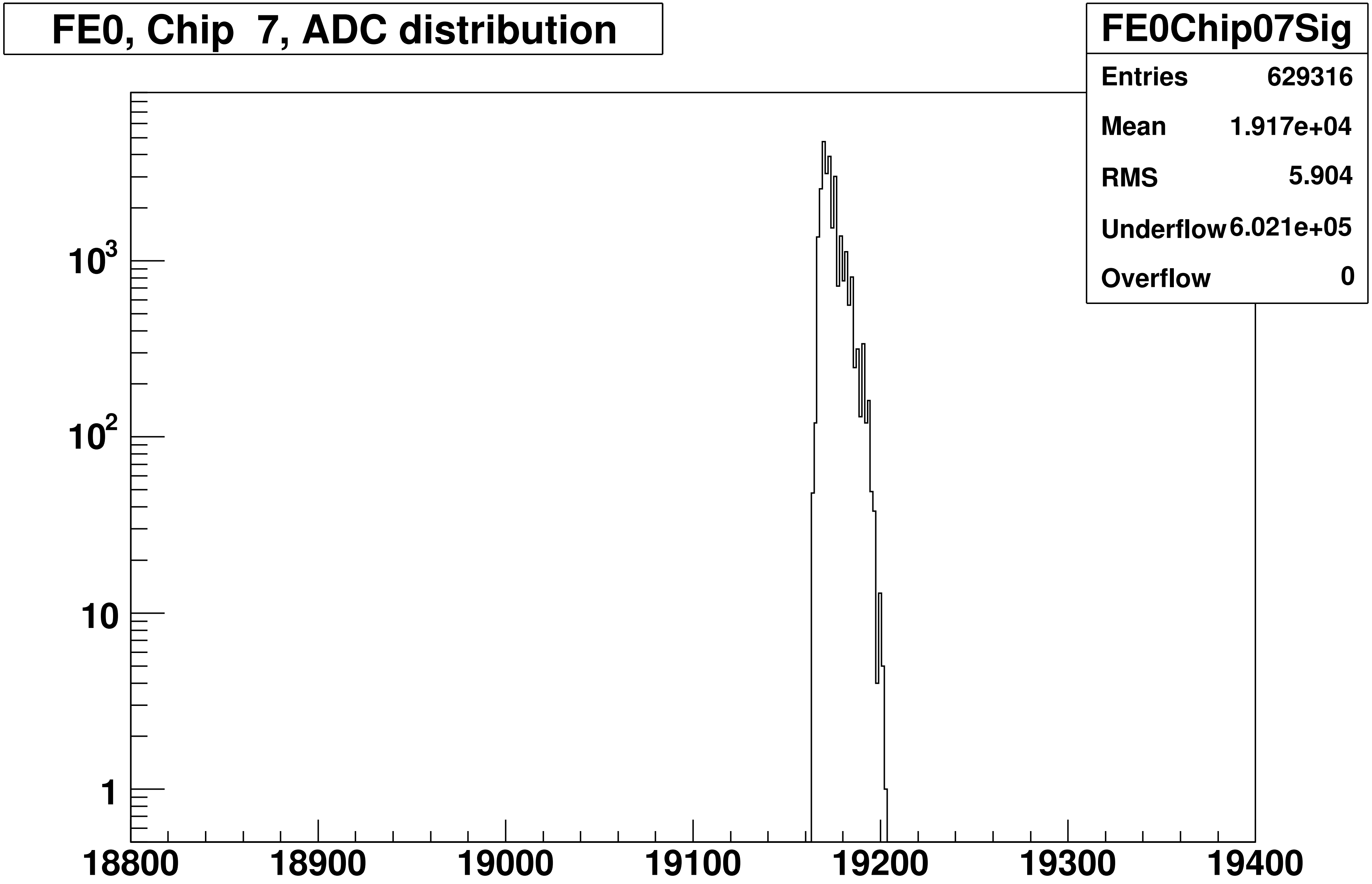

FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 629316
Mean 8.369
RMS 1.345
Underflow 0
Overflow 2.72e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 629316
Mean 1.928e+04
RMS 6.027
Underflow 6.021e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

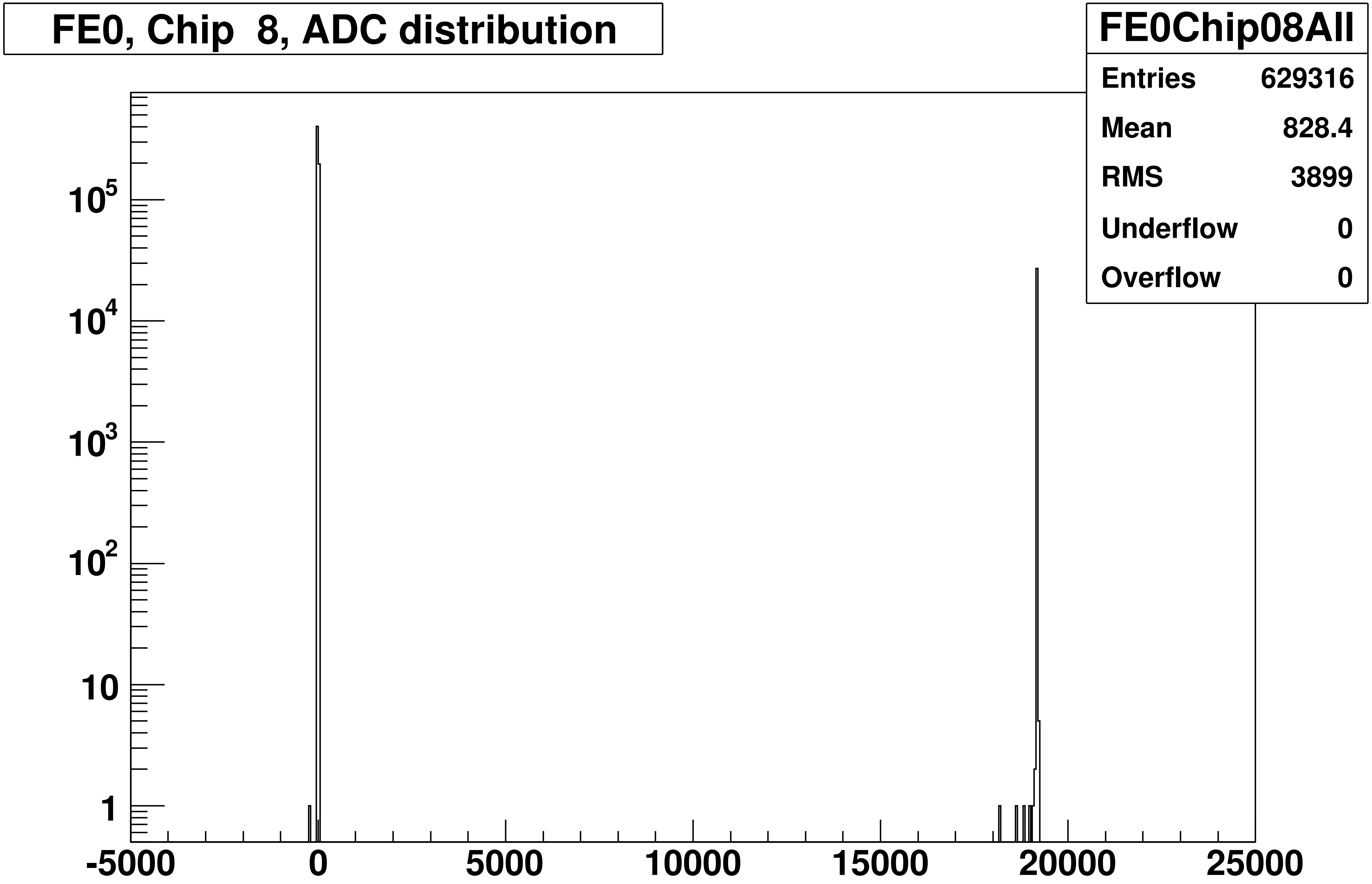


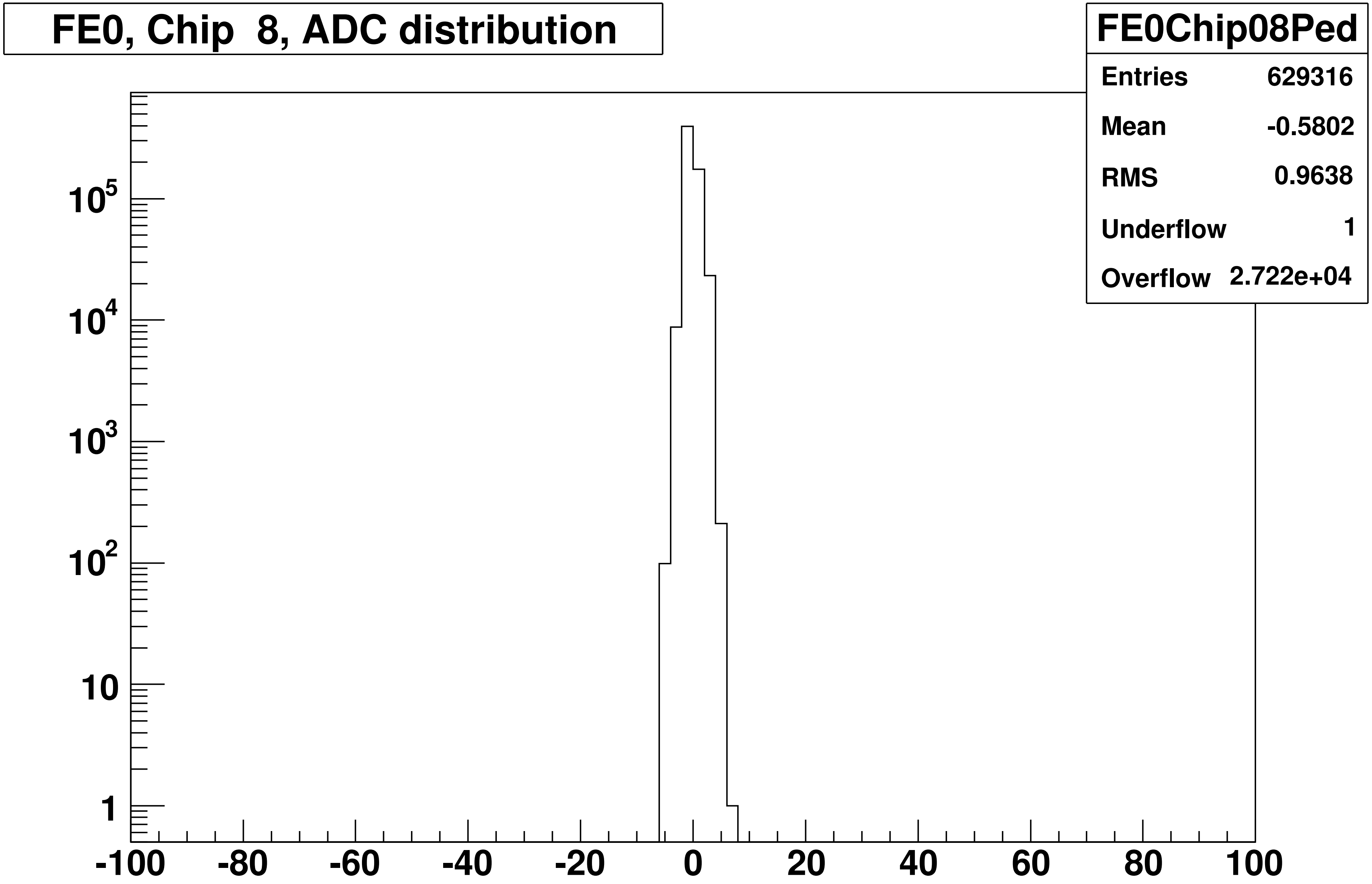
FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 629316
Mean 819.5
RMS 3901
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

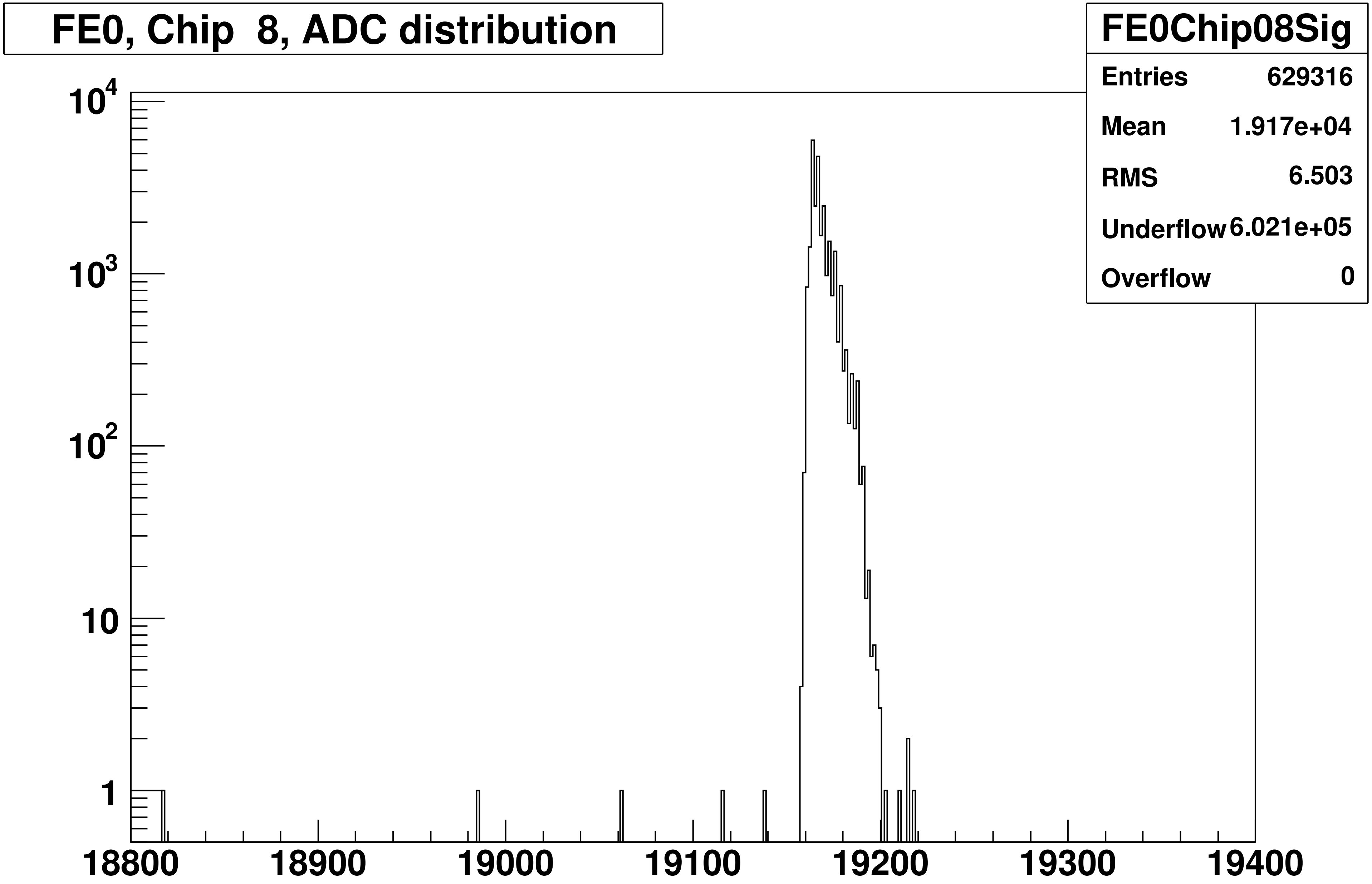

FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 629316
Mean -9.584
RMS 1.295
Underflow 1
Overflow 2.72e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

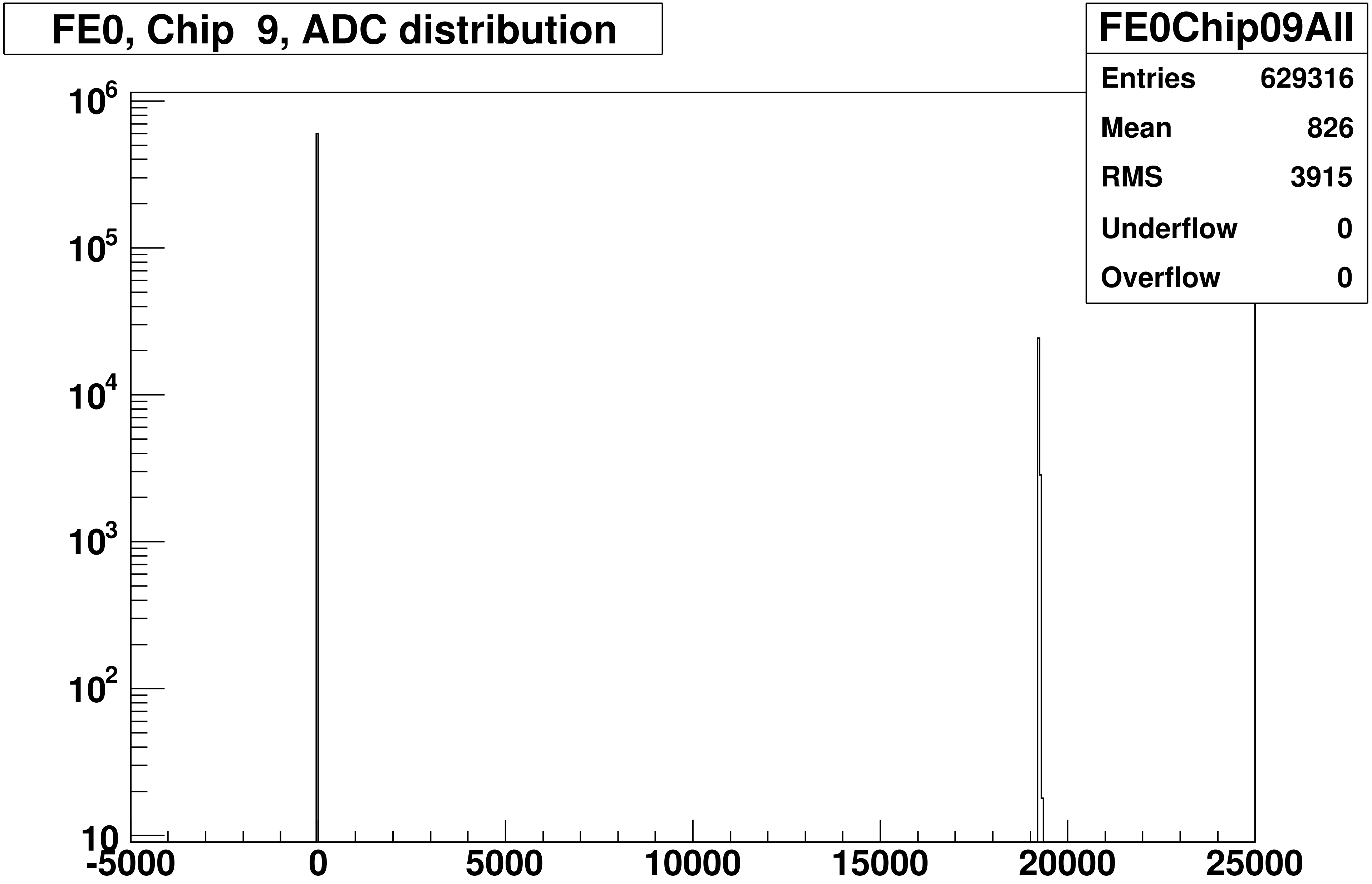


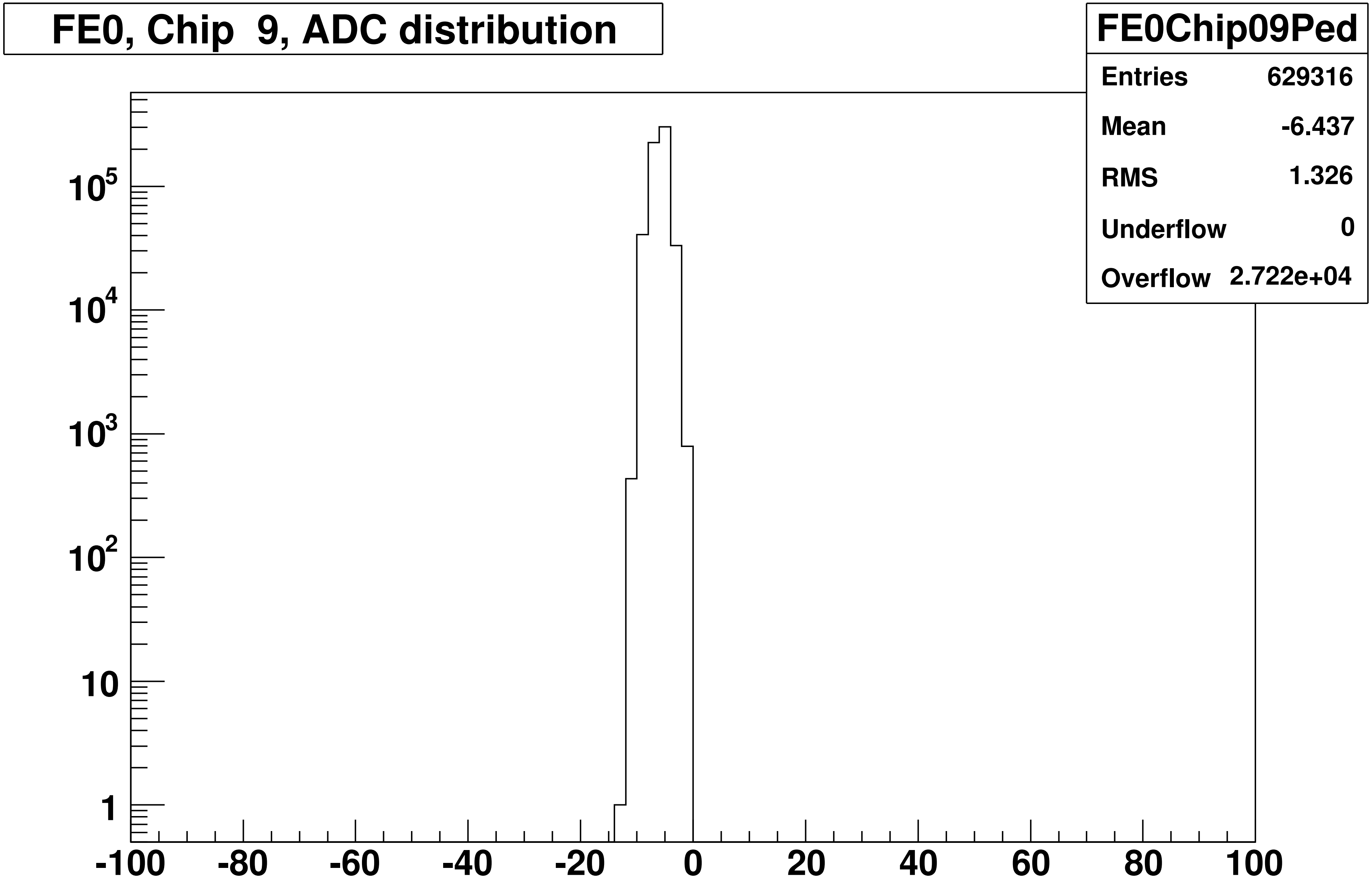
FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 629316
Mean 1.917e+04
RMS 5.904
Underflow 6.021e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

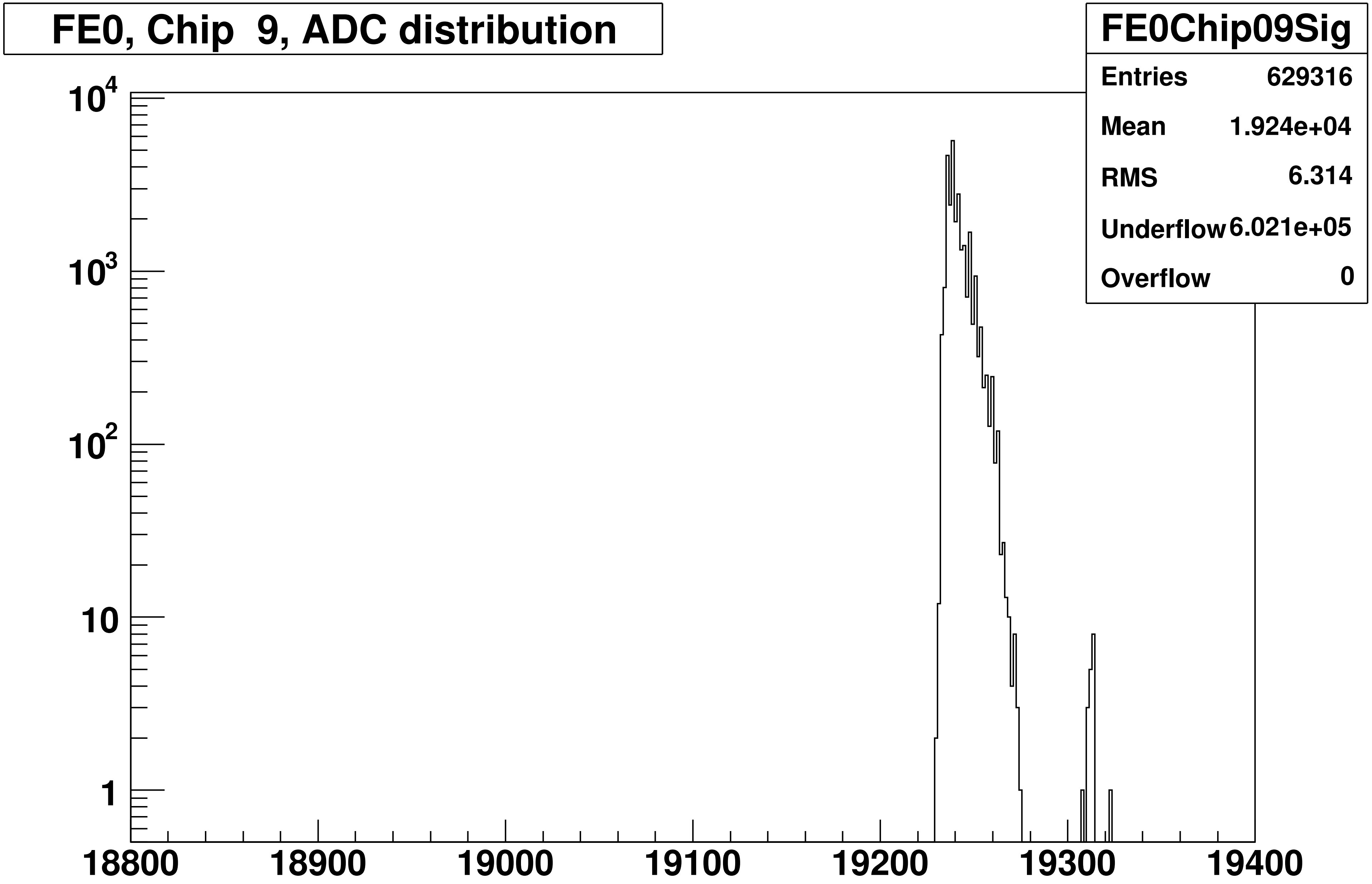

FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 629316
Mean 828.4
RMS 3899
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 629316
Mean -0.5802
RMS 0.9638
Underflow 1
Overflow 2.722e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

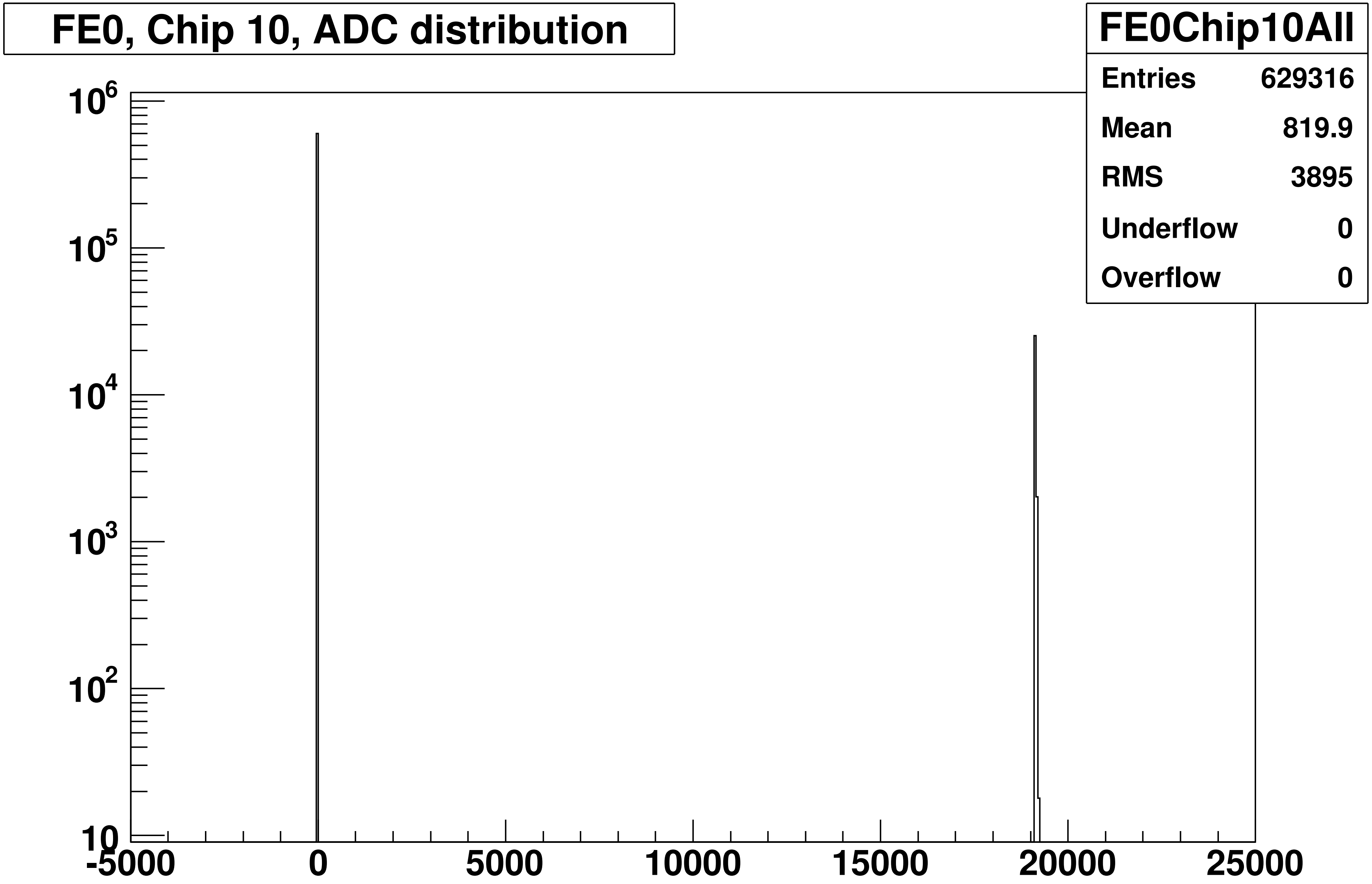

FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 629316
Mean 1.917e+04
RMS 6.503
Underflow 6.021e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 629316
Mean 826
RMS 3915
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

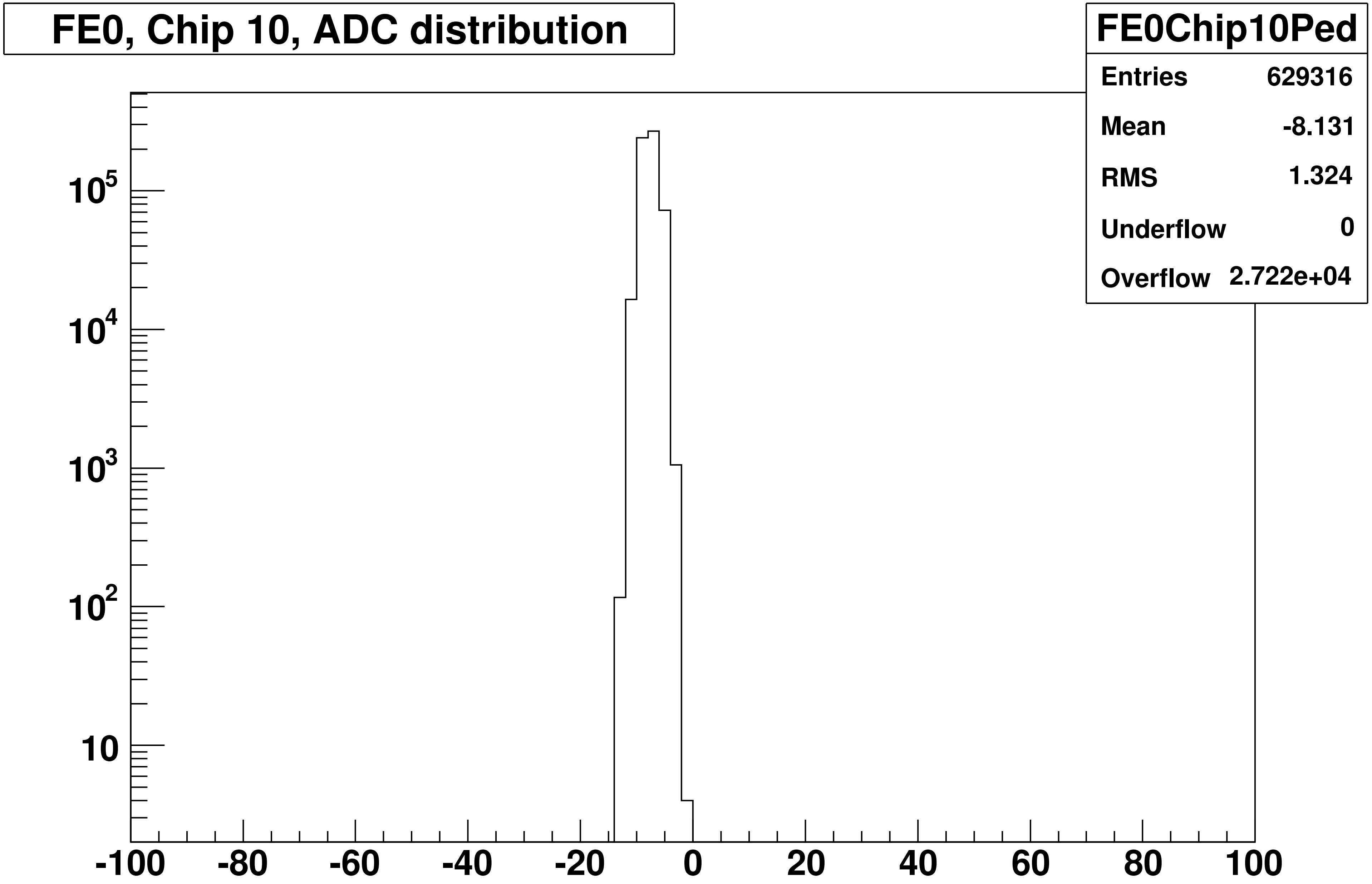

FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 629316
Mean -6.437
RMS 1.326
Underflow 0
Overflow 2.722e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

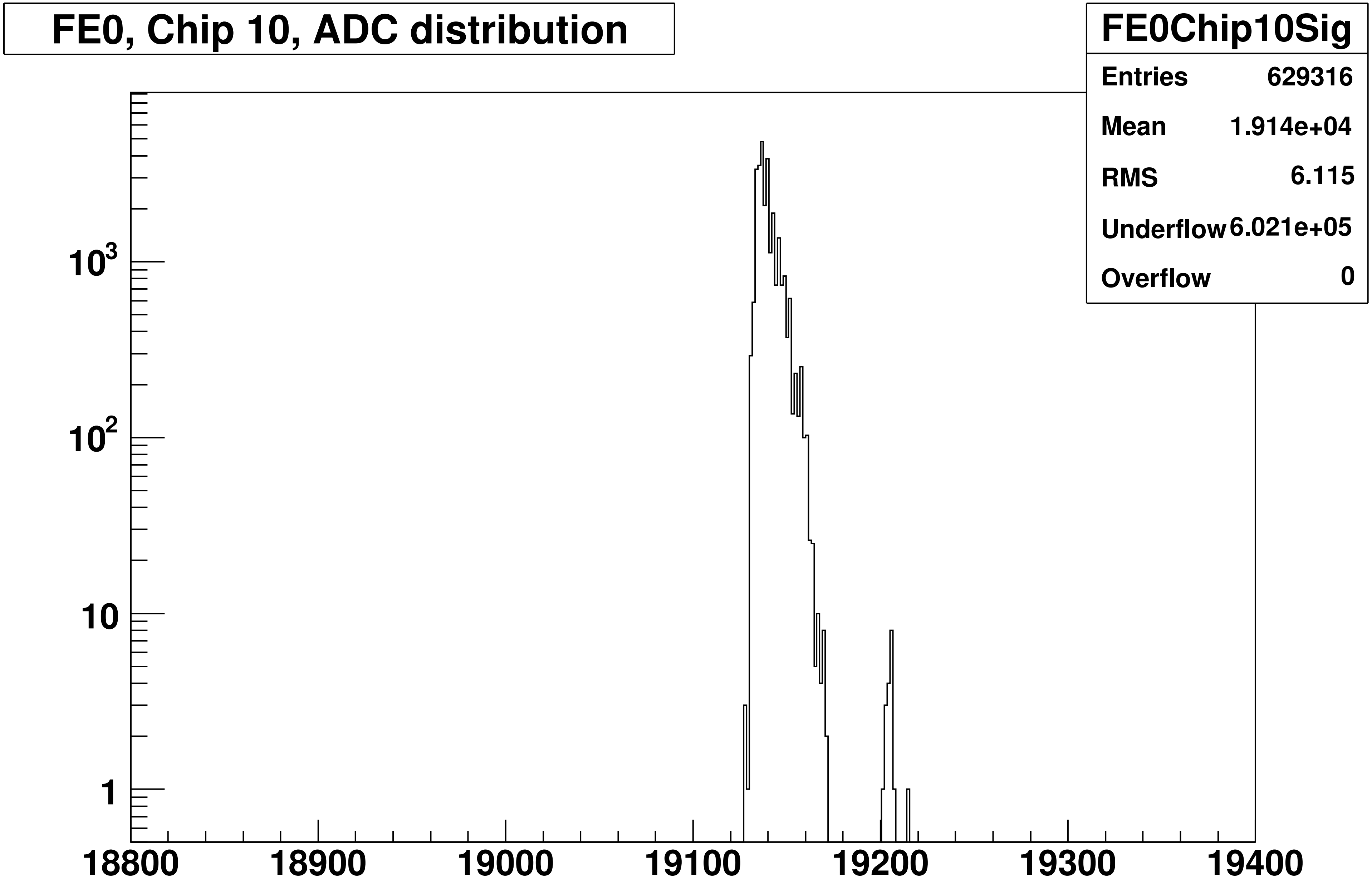


FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 629316
Mean 1.924e+04
RMS 6.314
Underflow 6.021e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

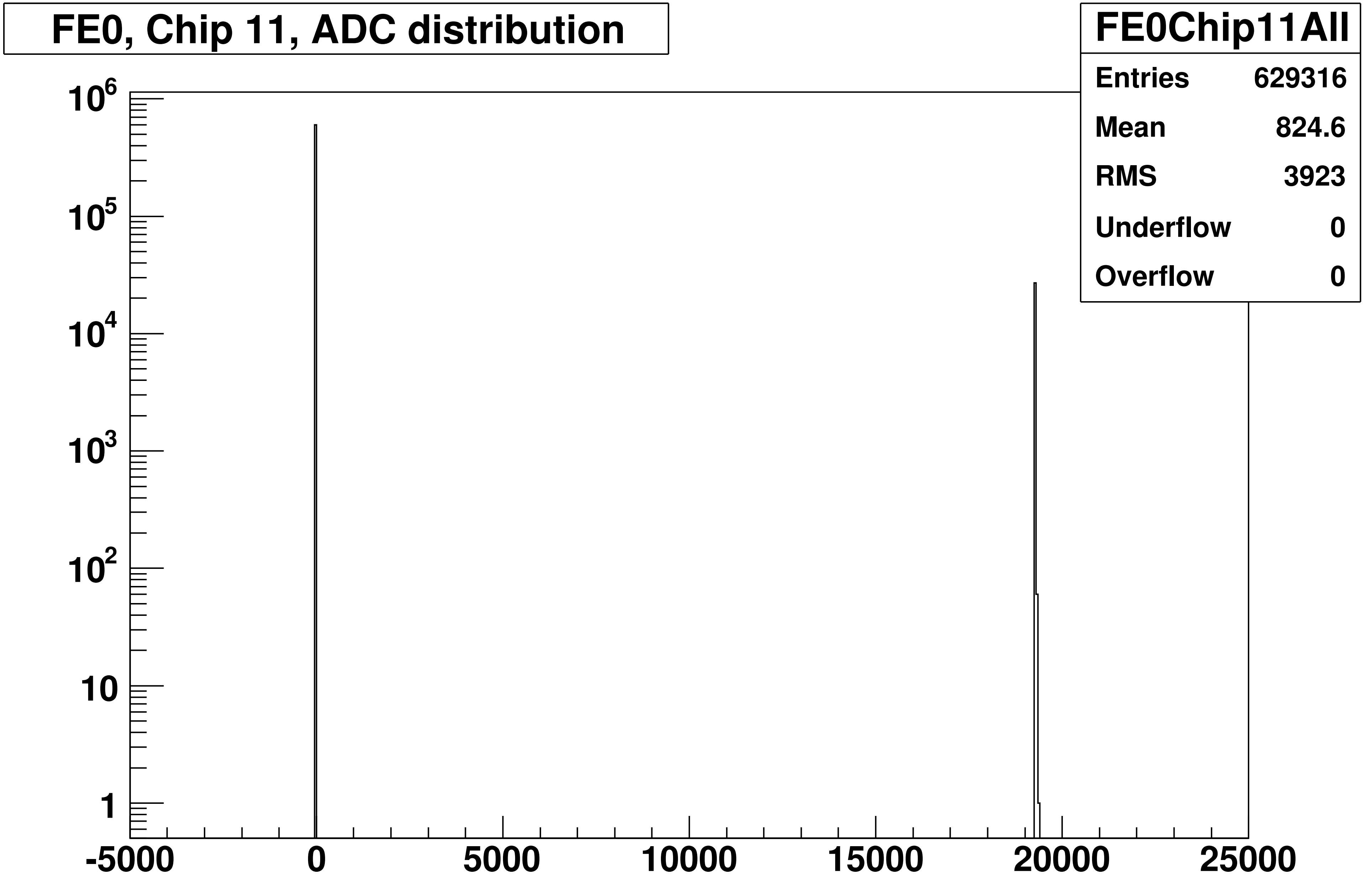


FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 629316
Mean 819.9
RMS 3895
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

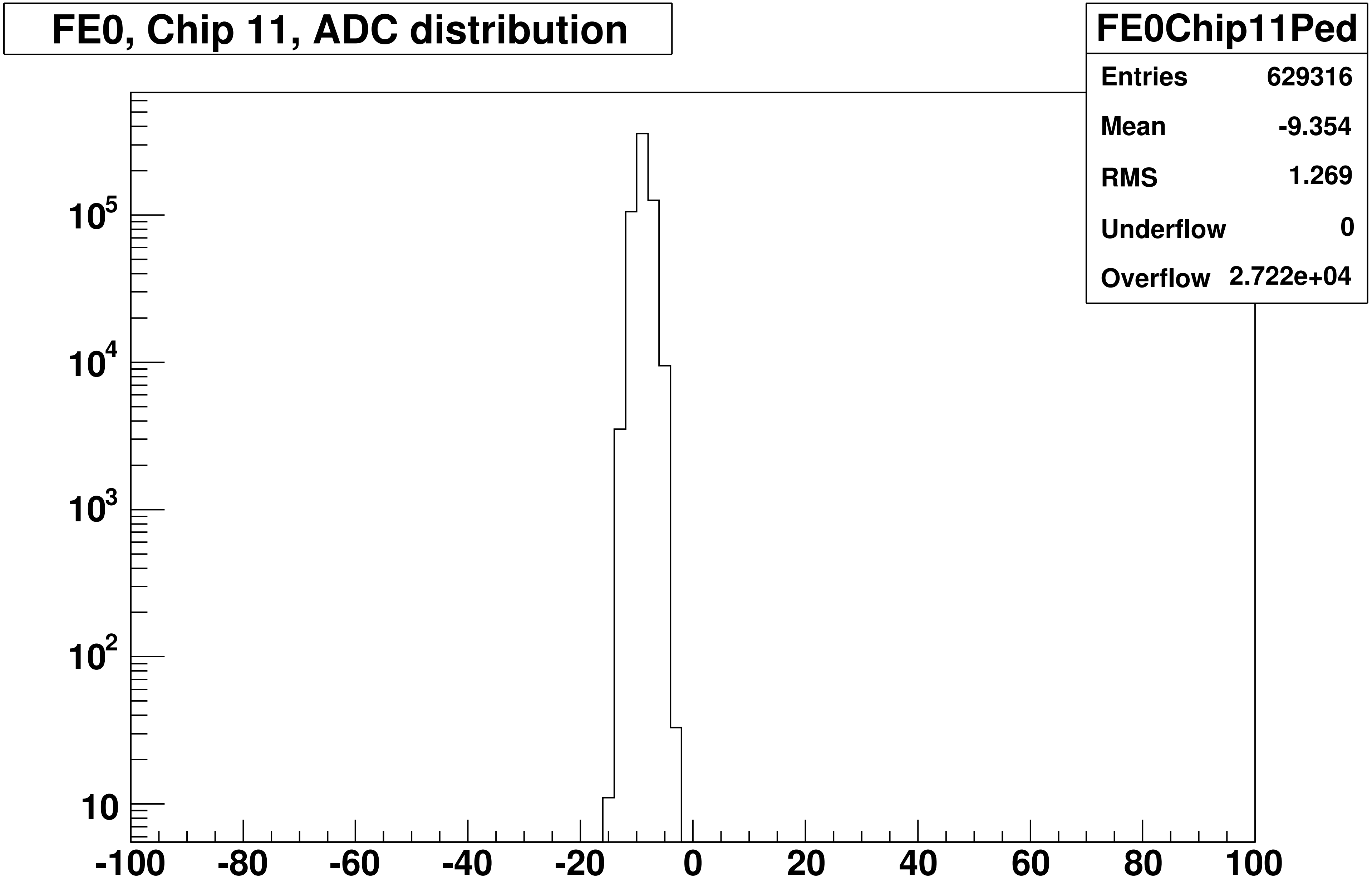

FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 629316
Mean -8.131
RMS 1.324
Underflow 0
Overflow 2.722e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

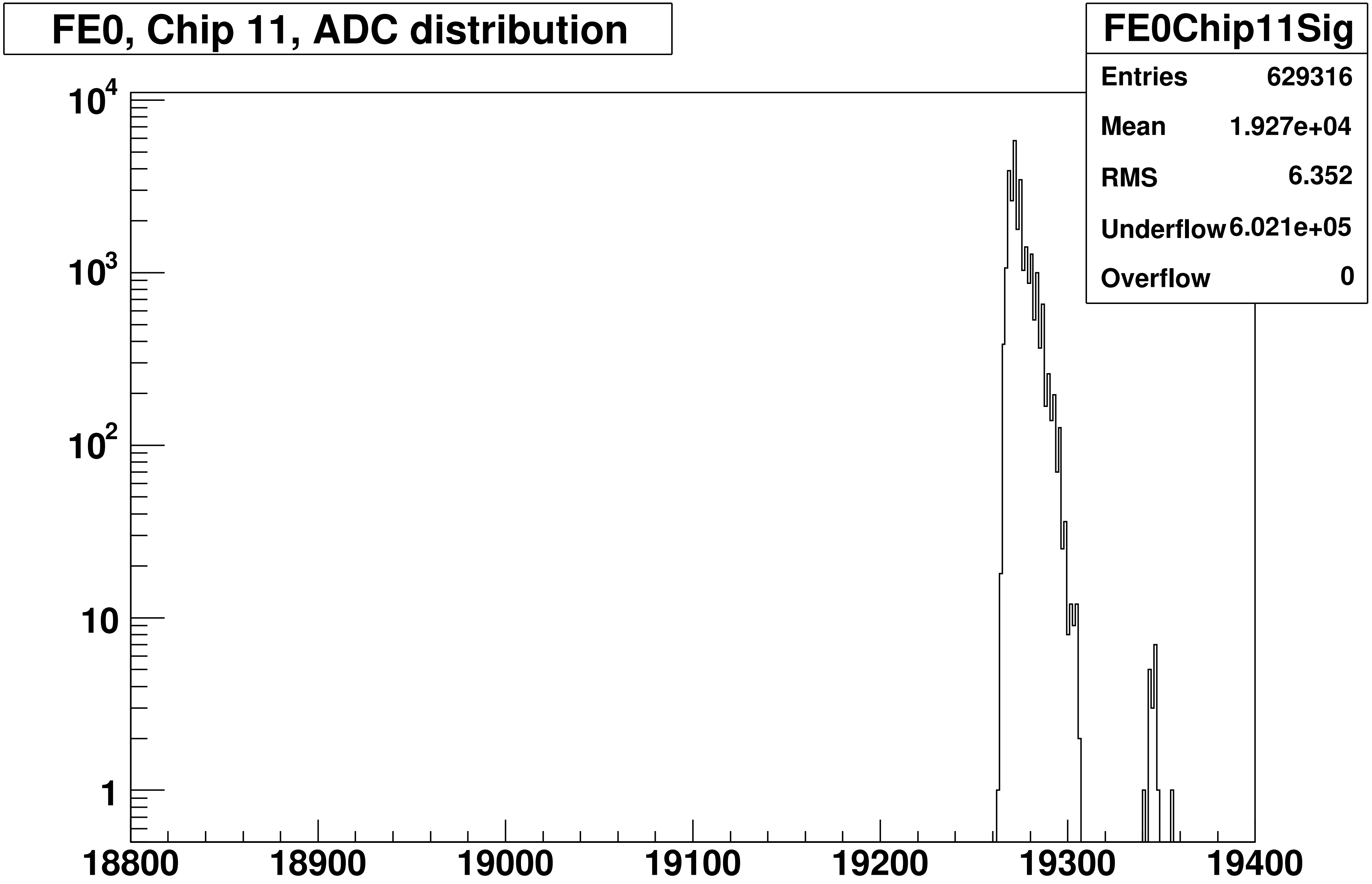


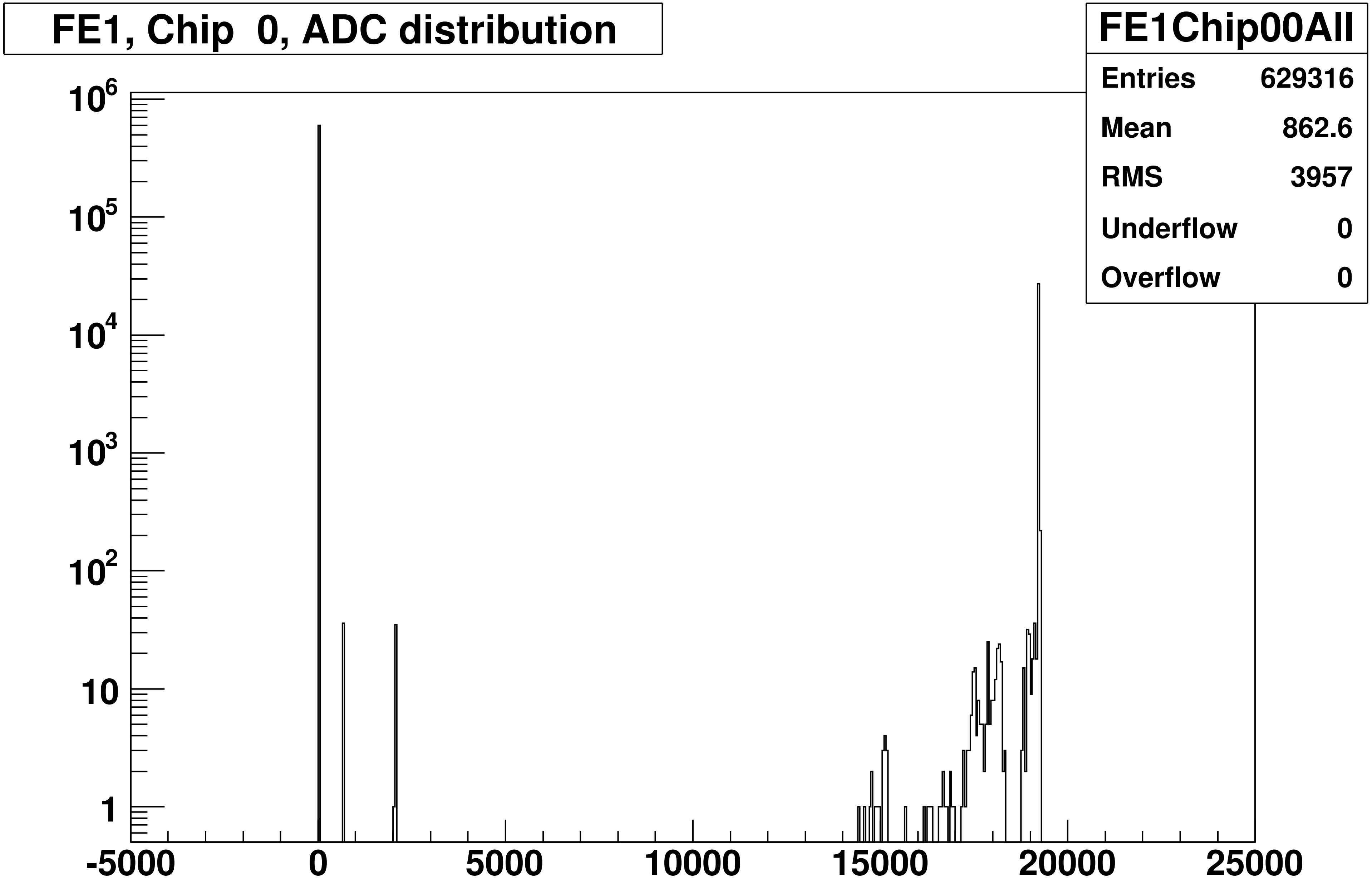
FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 629316
Mean 1.914e+04
RMS 6.115
Underflow 6.021e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

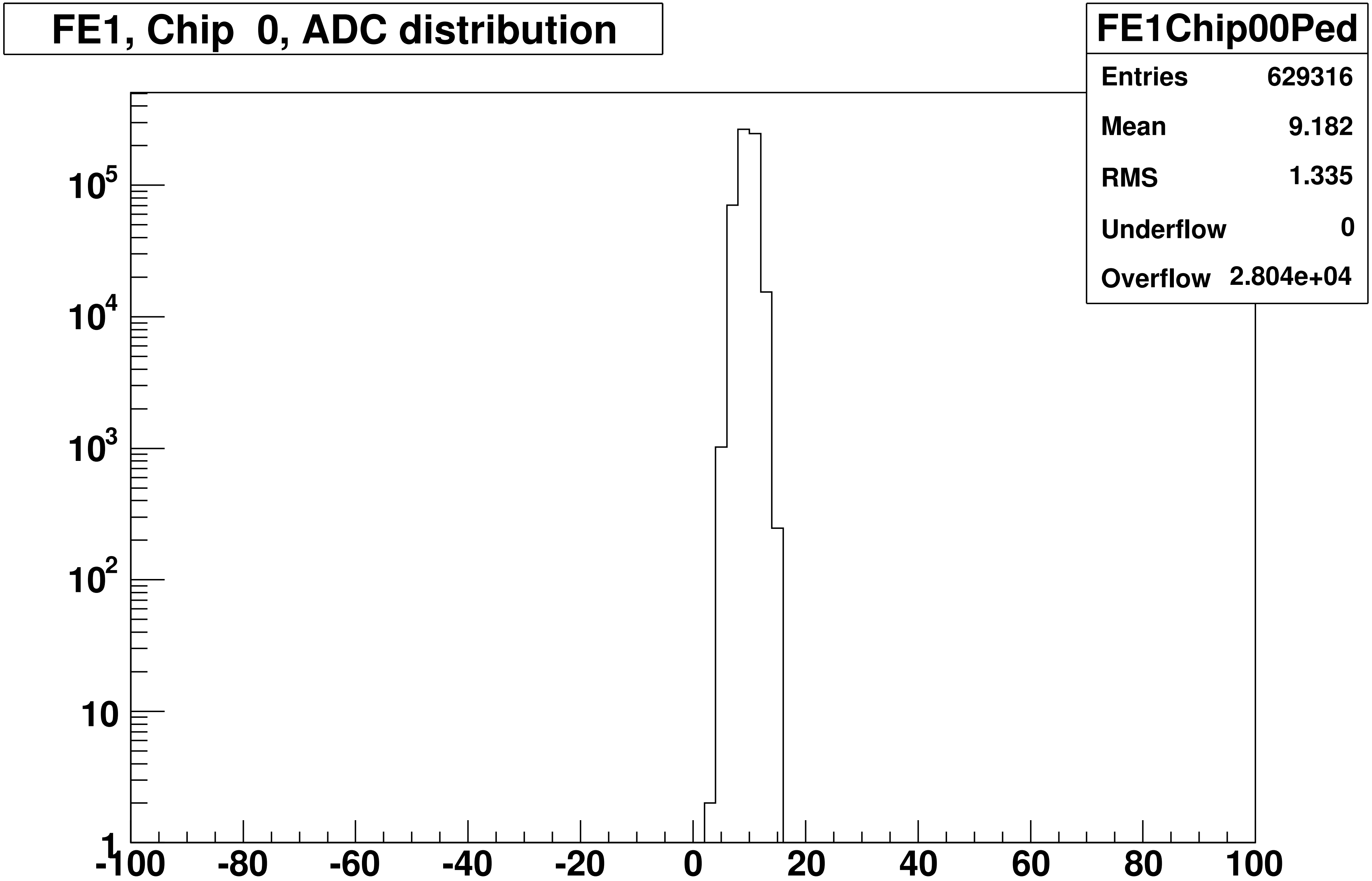


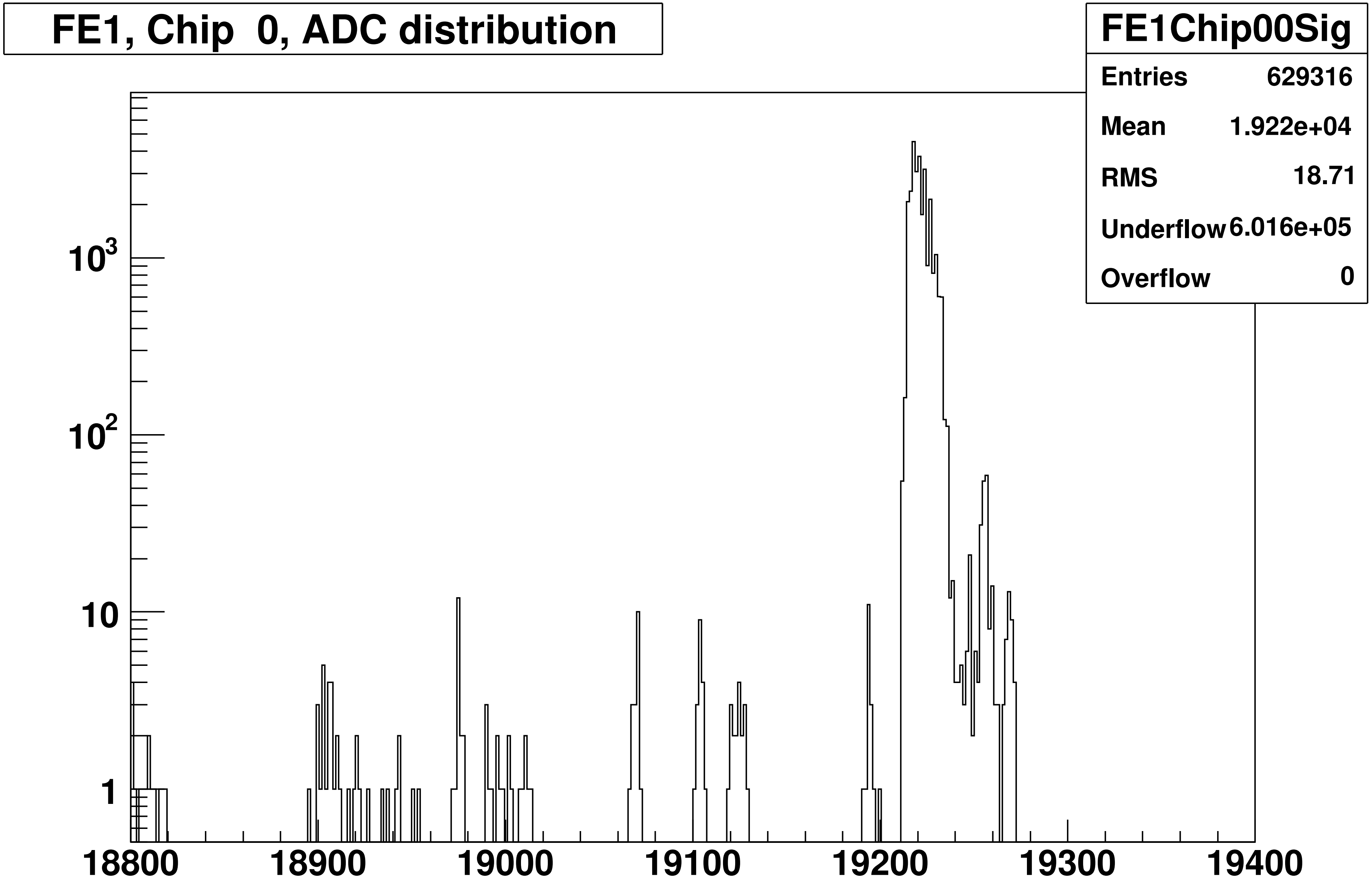
FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 629316
Mean 824.6
RMS 3923
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 629316
Mean -9.354
RMS 1.269
Underflow 0
Overflow 2.722e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

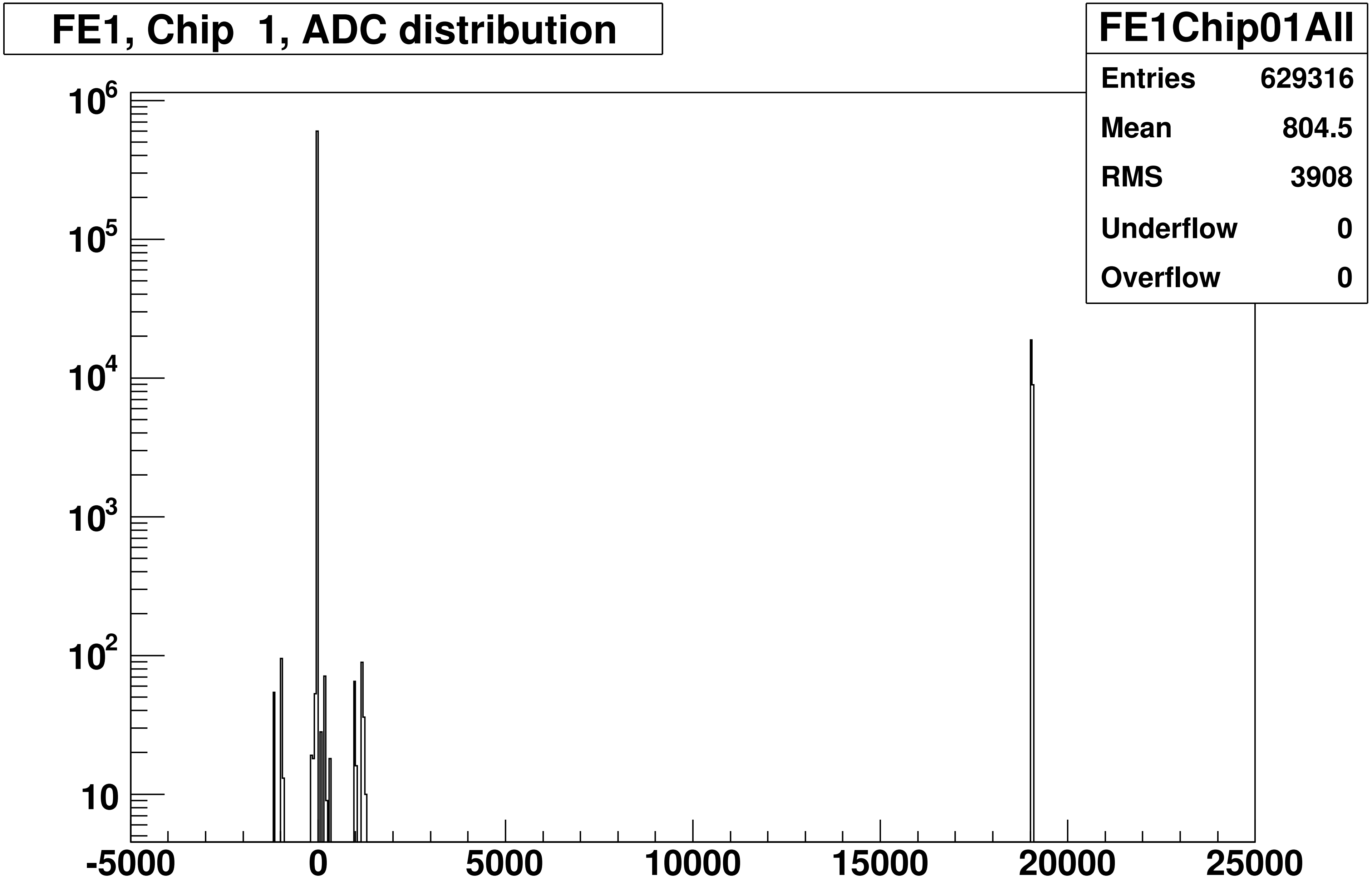

FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 629316
Mean 1.927e+04
RMS 6.352
Underflow 6.021e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 629316
Mean 862.6
RMS 3957
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

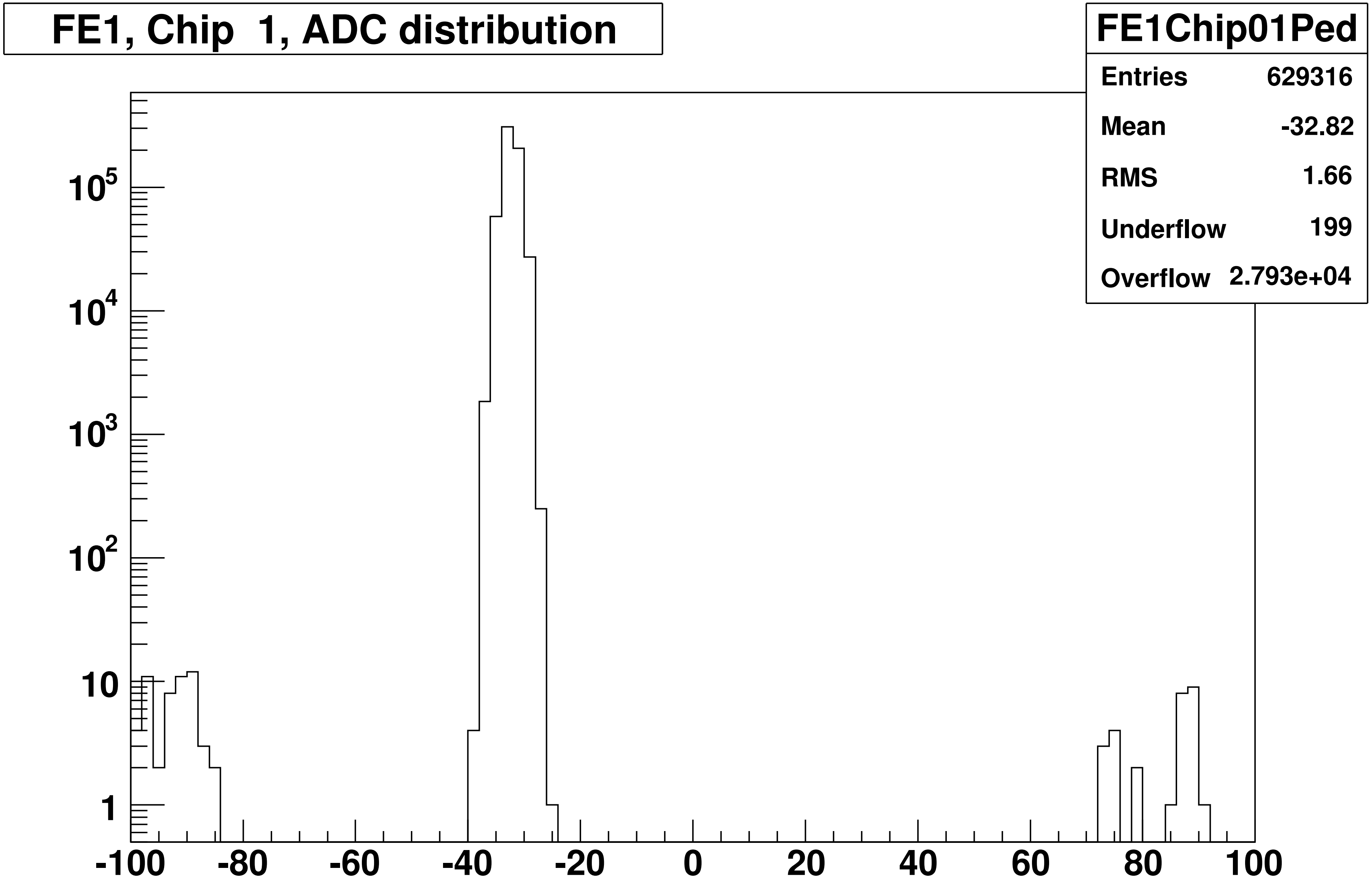

FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 629316
Mean 9.182
RMS 1.335
Underflow 0
Overflow 2.804e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



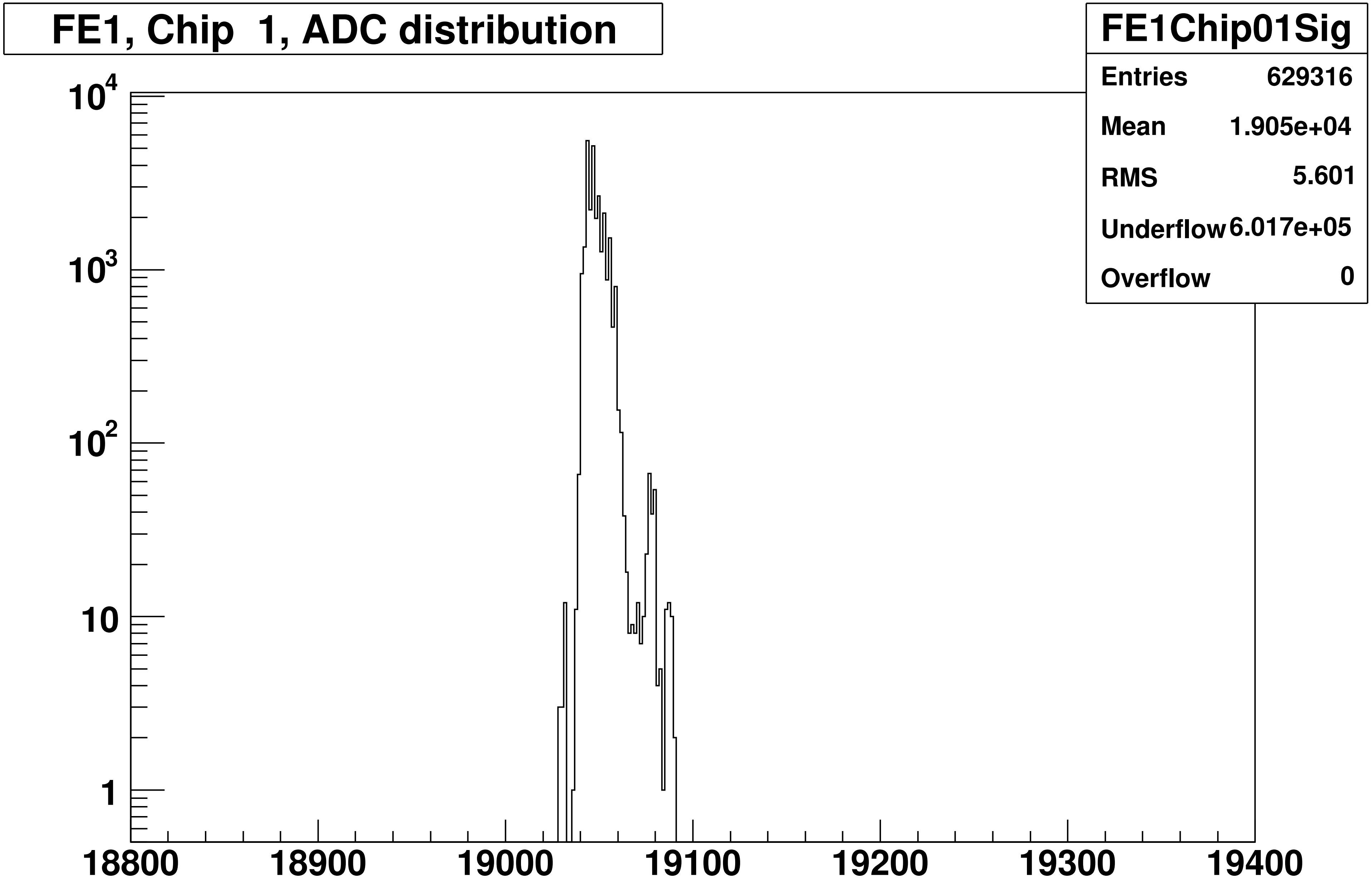
FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 629316
Mean 1.922e+04
RMS 18.71
Underflow 6.016e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

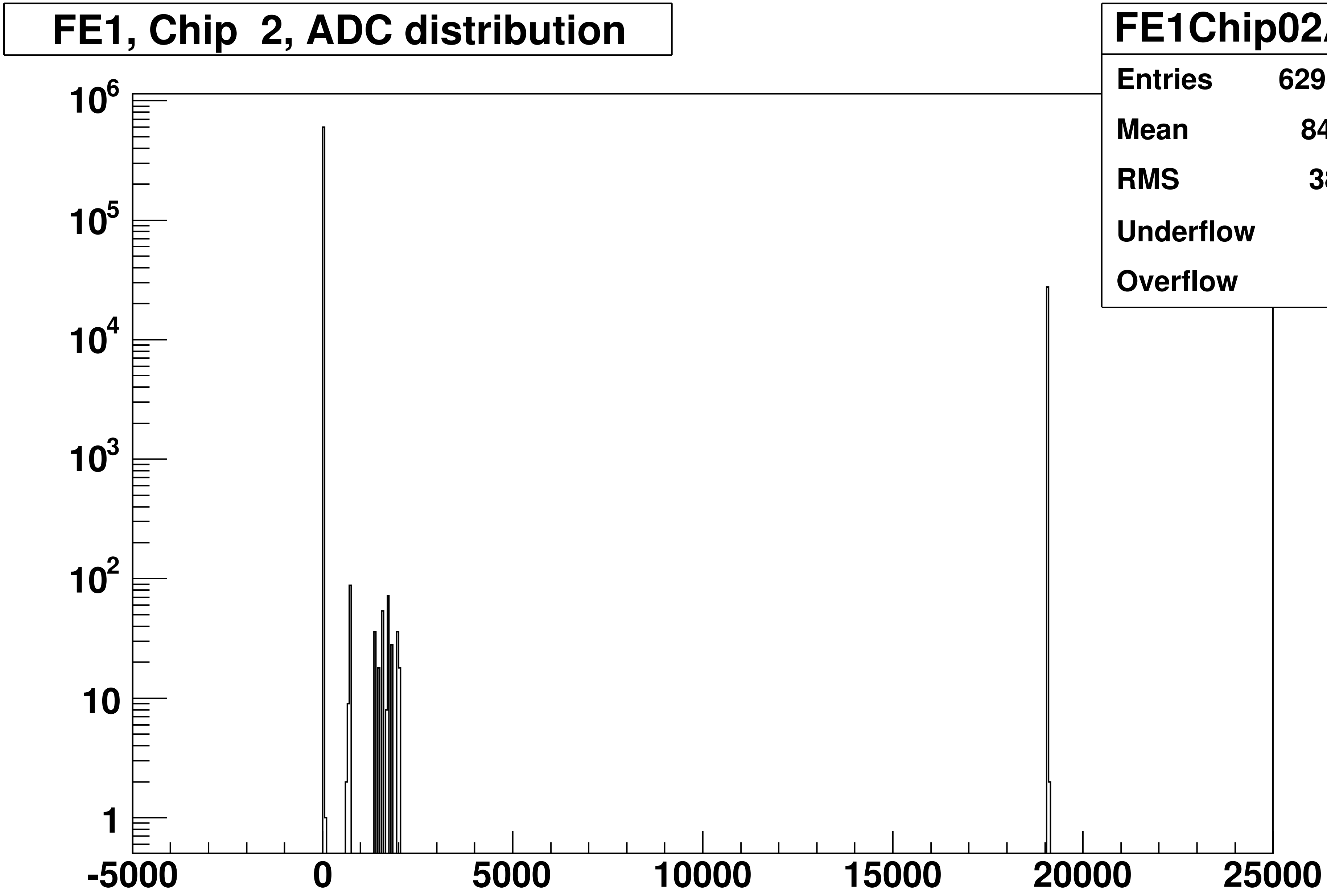


FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 629316
Mean 804.5
RMS 3908
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

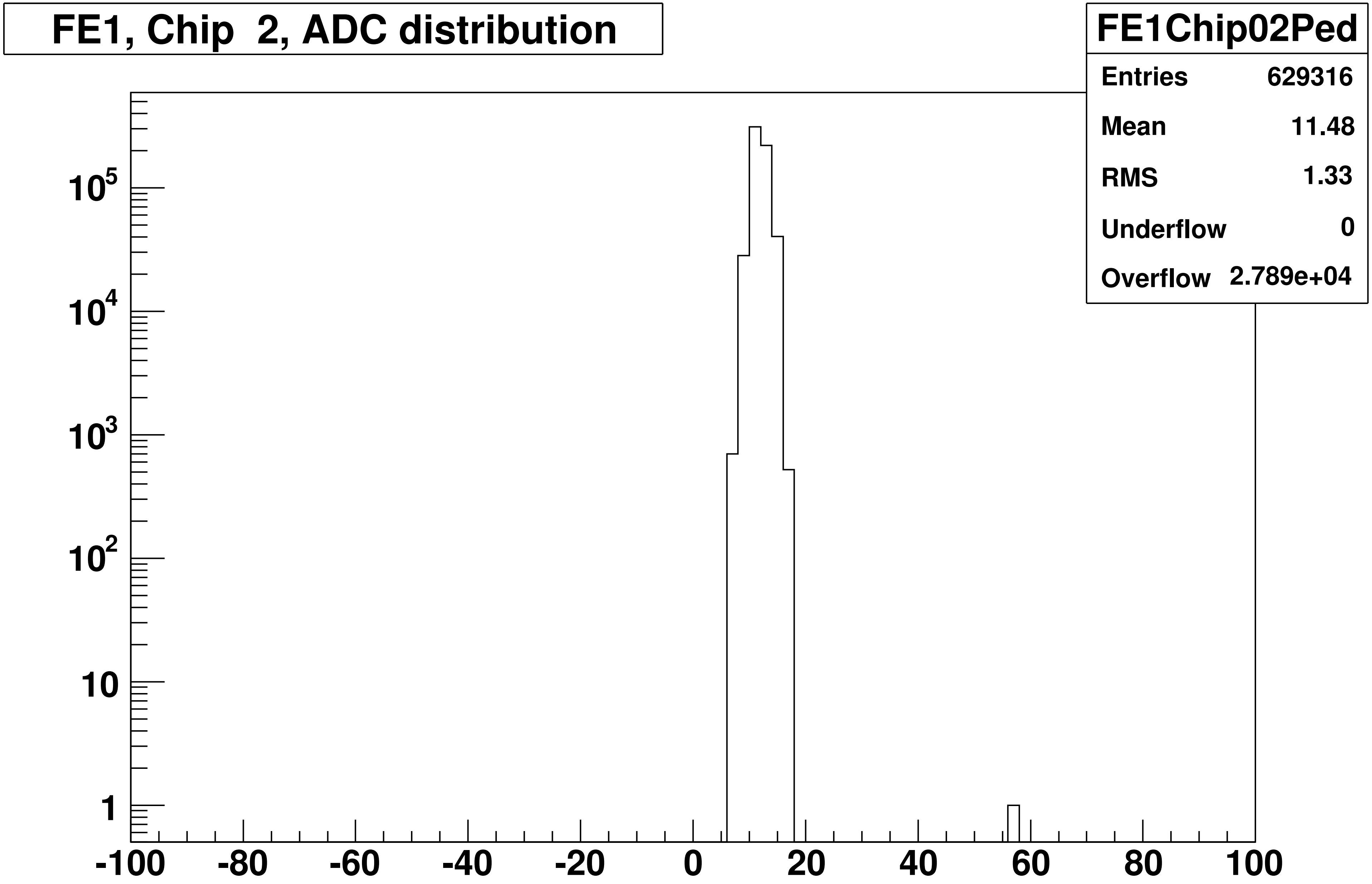


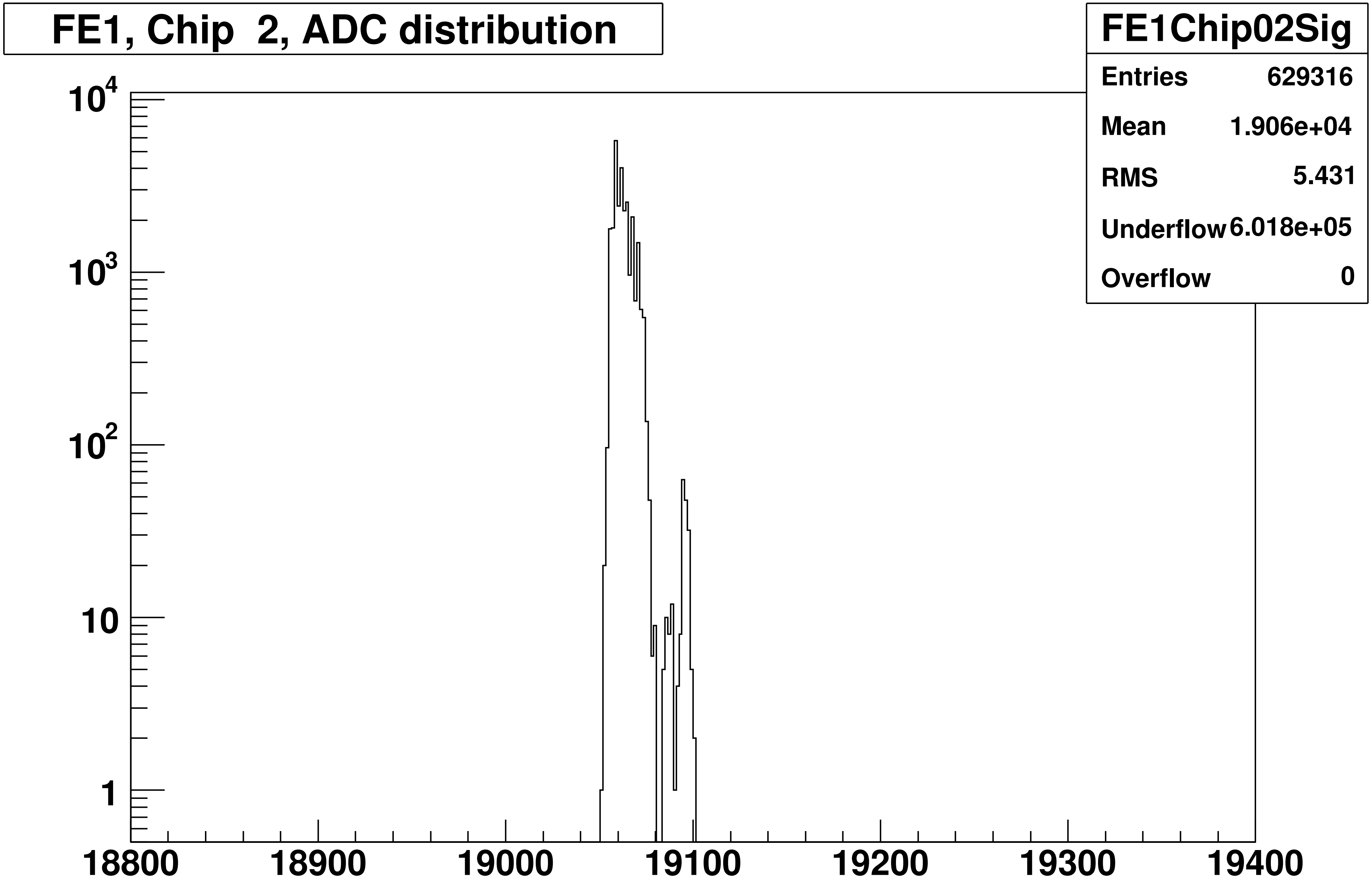
FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 629316
Mean -32.82
RMS 1.66
Underflow 199
Overflow 2.793e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

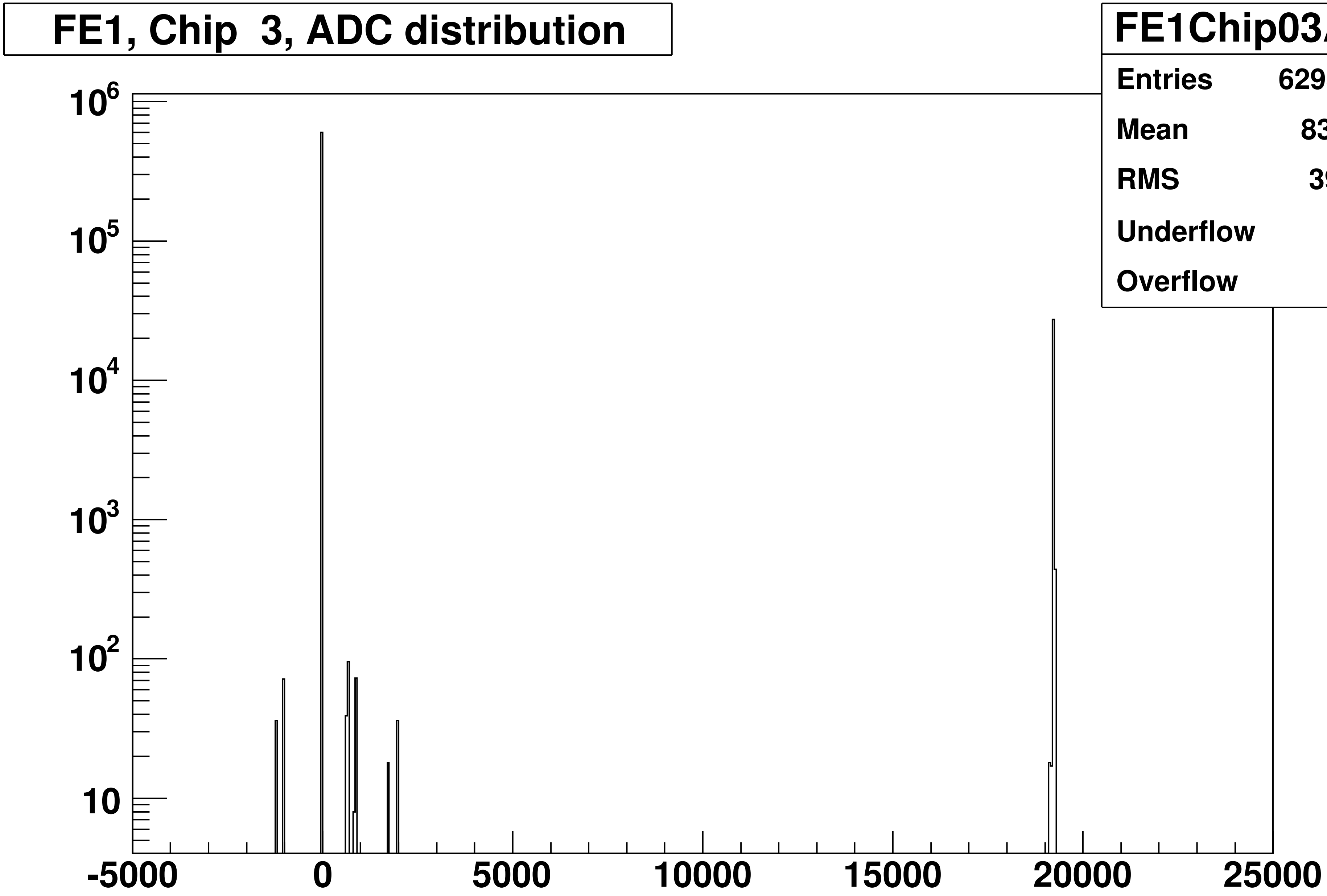

FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 629316
Mean 1.905e+04
RMS 5.601
Underflow 6.017e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

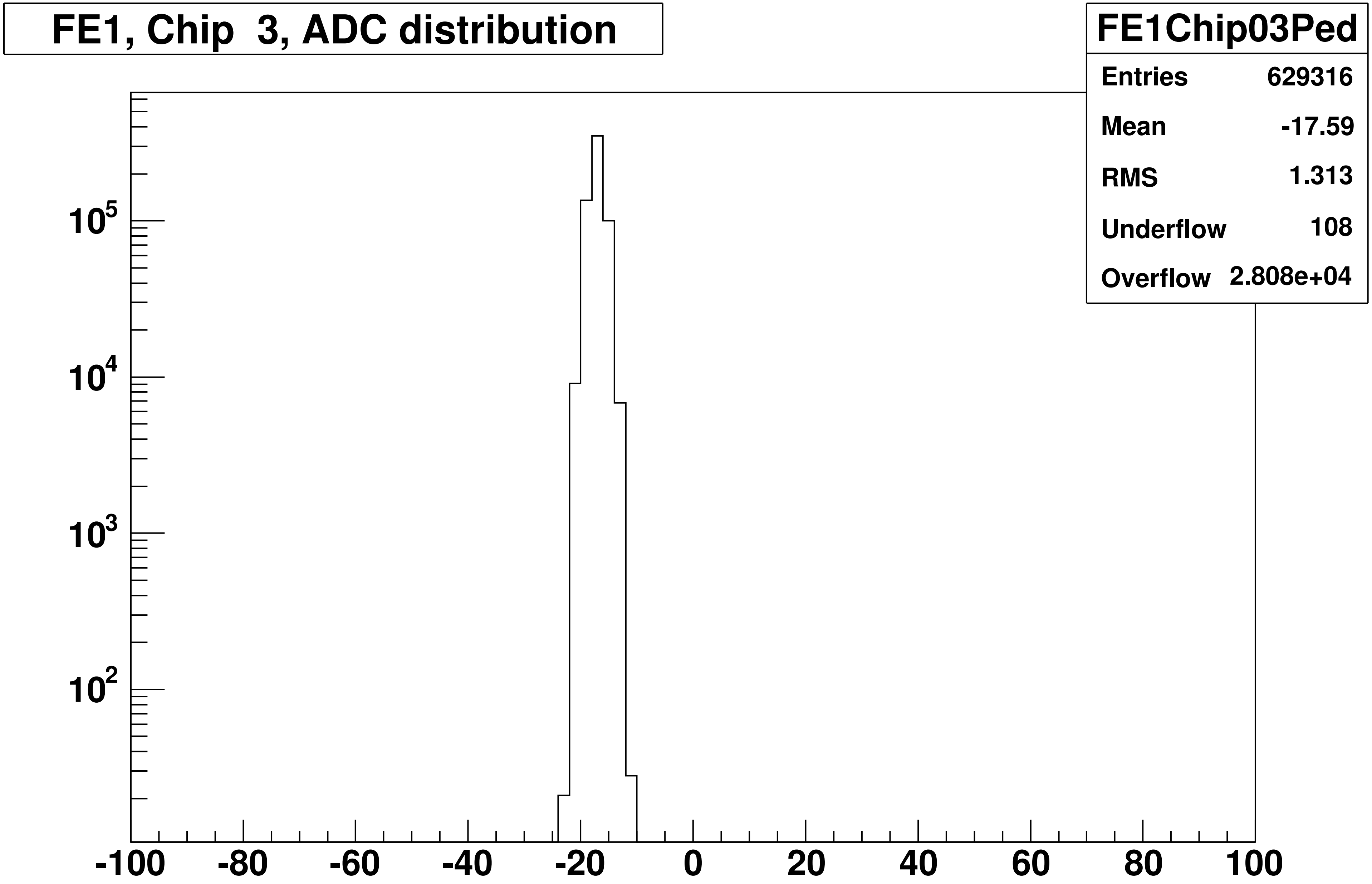


FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 629316
Mean 845.5
RMS 3896
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

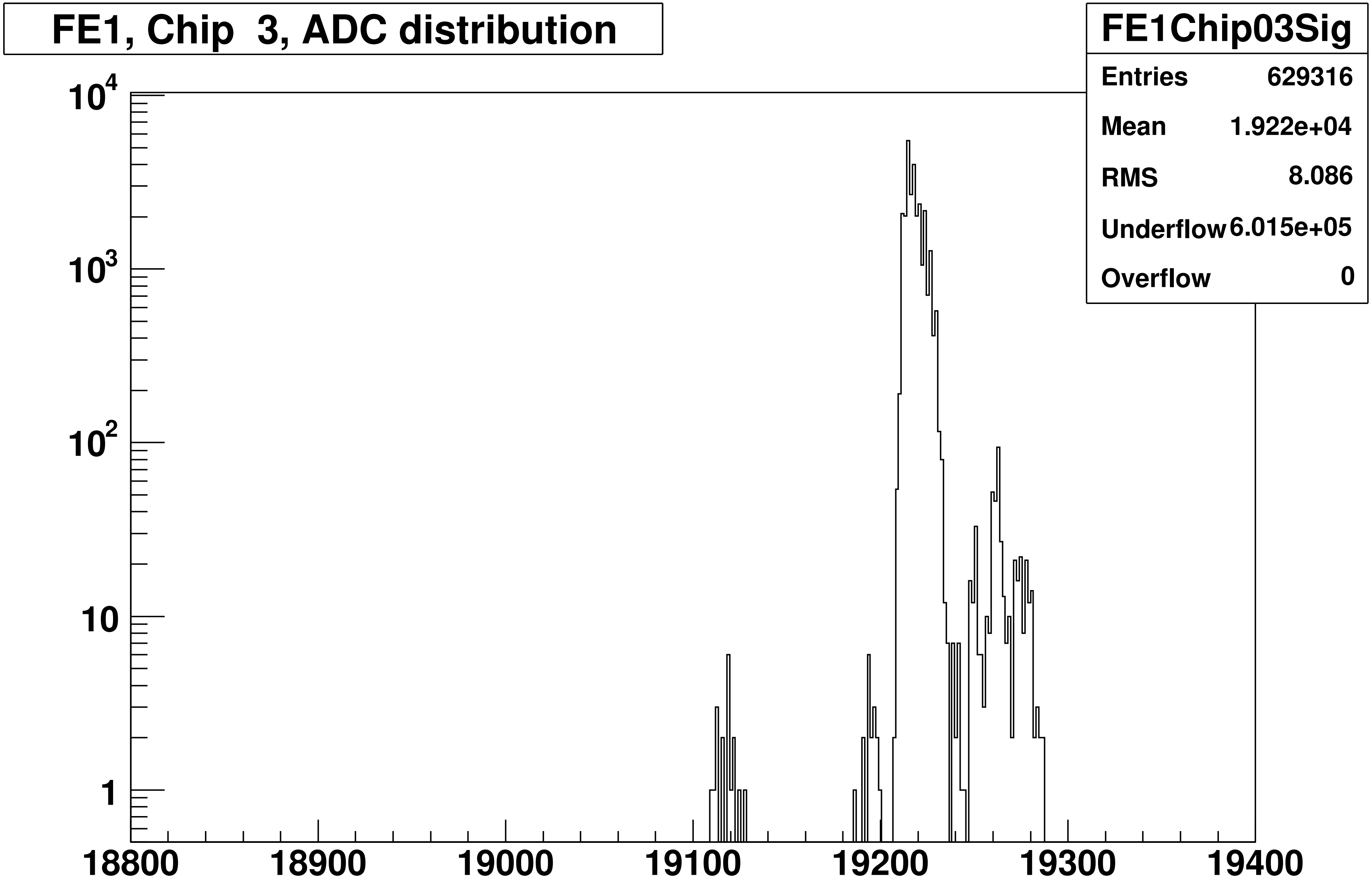

FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 629316
Mean 11.48
RMS 1.33
Underflow 0
Overflow 2.789e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

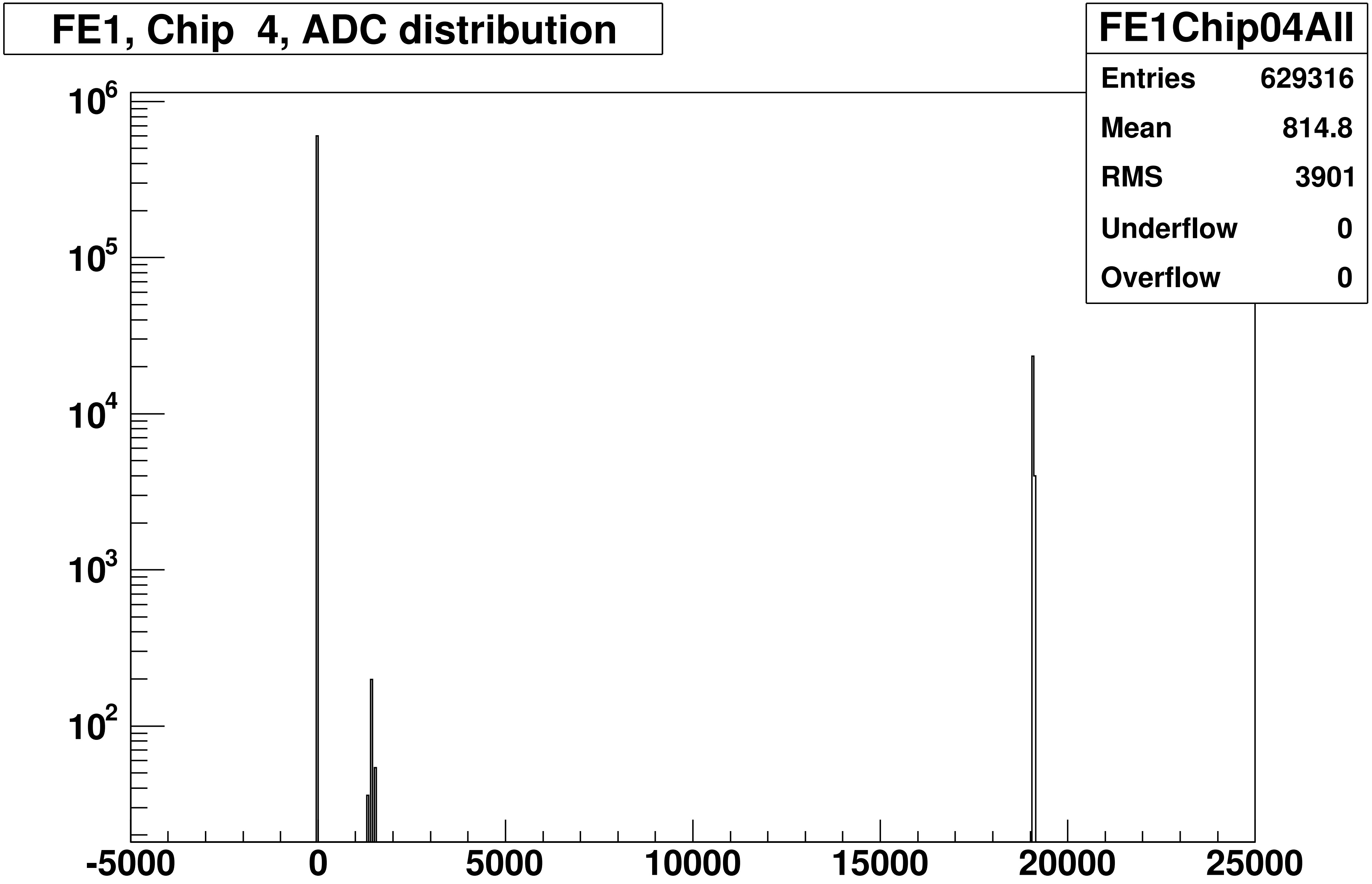

FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 629316
Mean 1.906e+04
RMS 5.431
Underflow 6.018e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

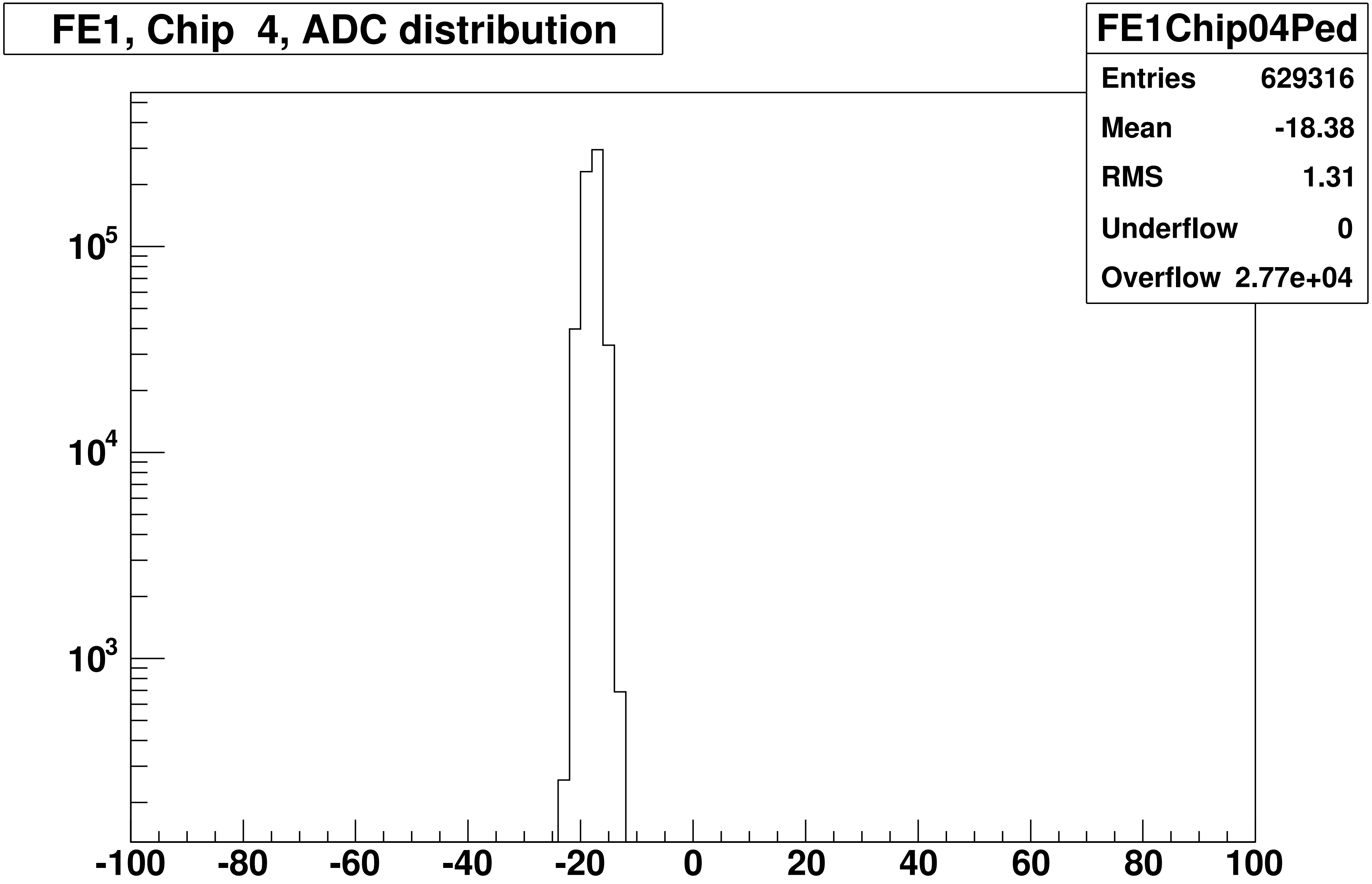

FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 629316
Mean 832.7
RMS 3953
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

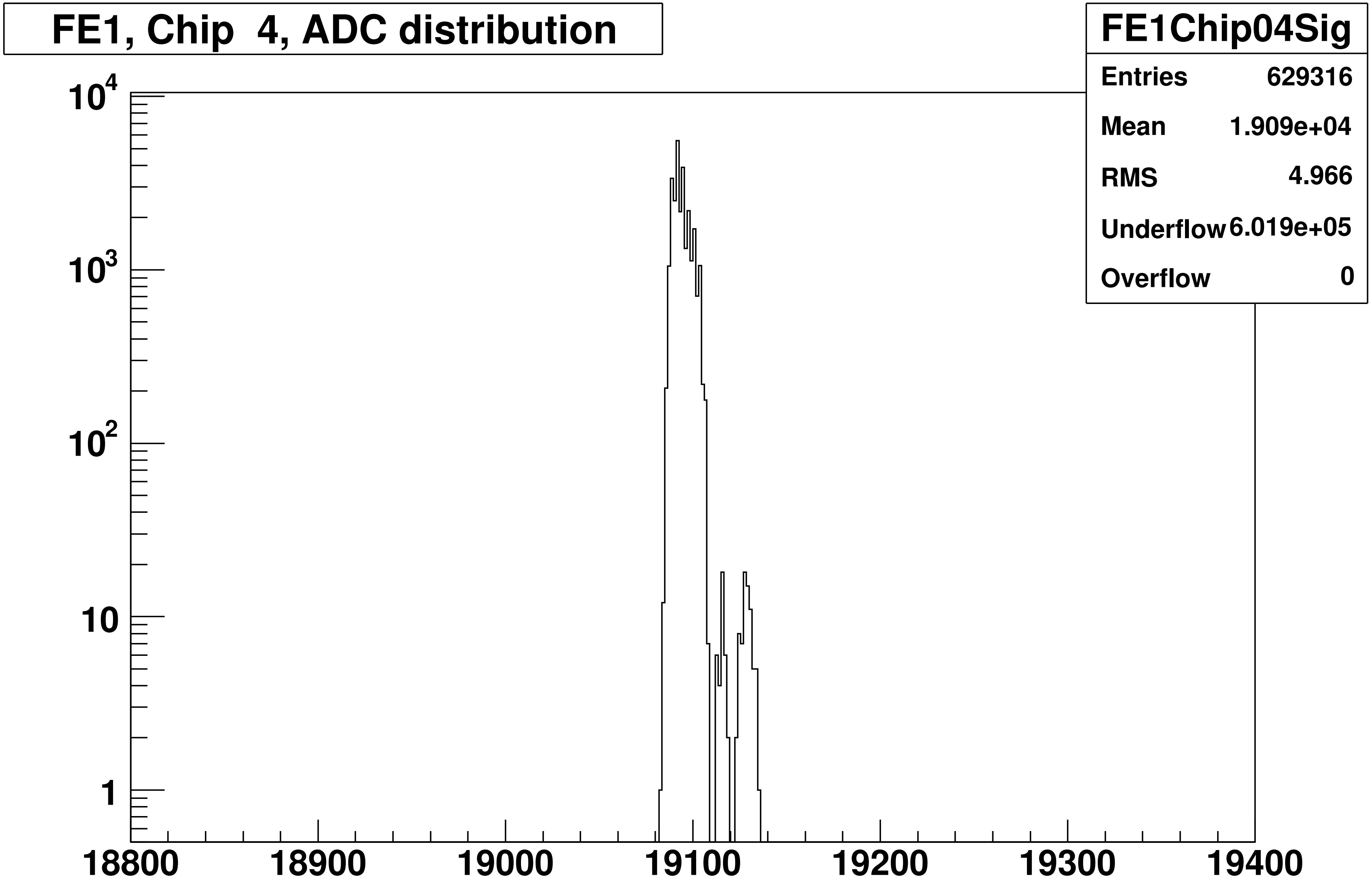


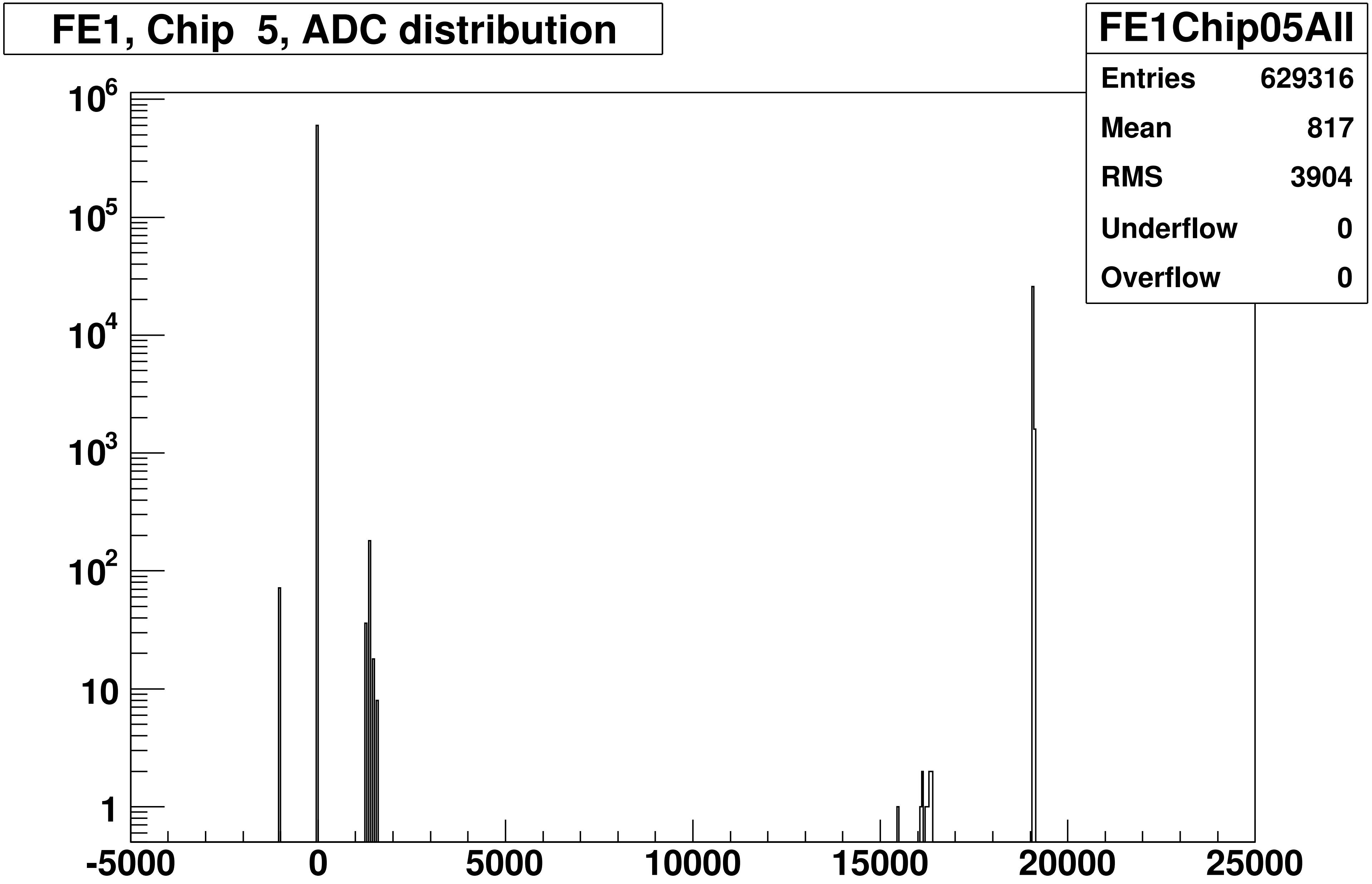
FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 629316
Mean -17.59
RMS 1.313
Underflow 108
Overflow 2.808e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 629316
Mean 1.922e+04
RMS 8.086
Underflow 6.015e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

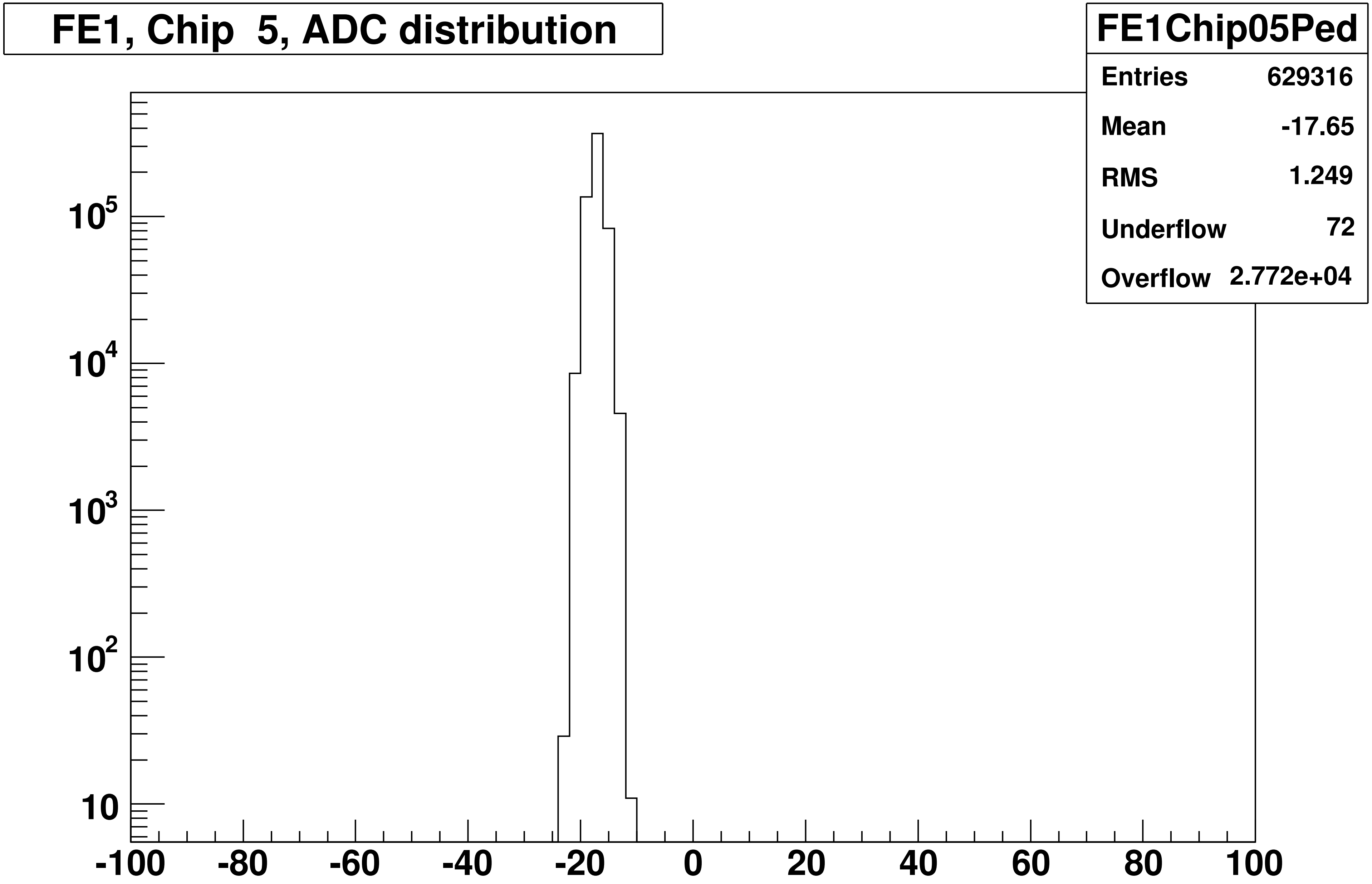

FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 629316
Mean 814.8
RMS 3901
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 629316
Mean -18.38
RMS 1.31
Underflow 0
Overflow 2.77e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100

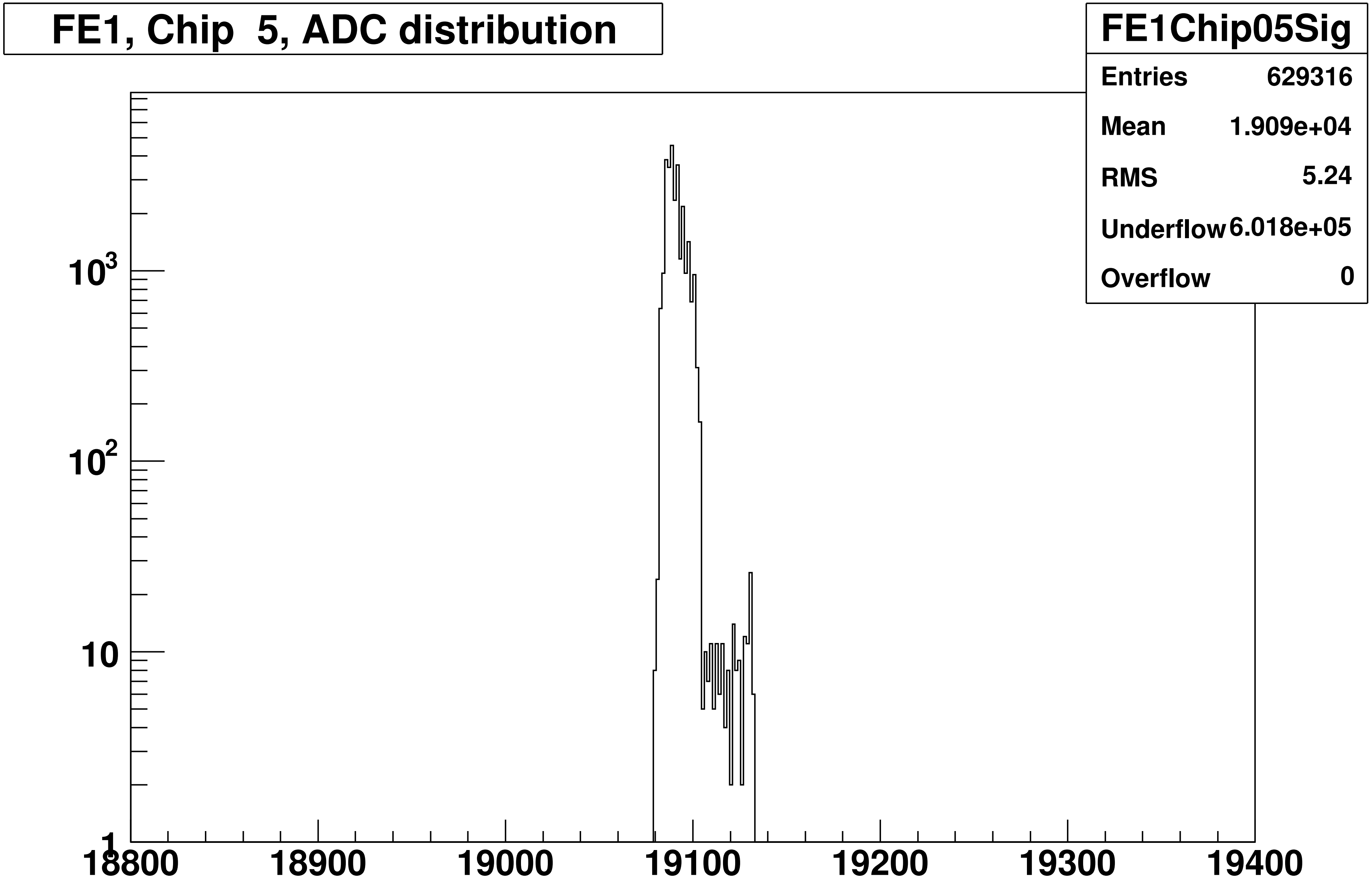

FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 629316
Mean 1.909e+04
RMS 4.966
Underflow 6.019e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

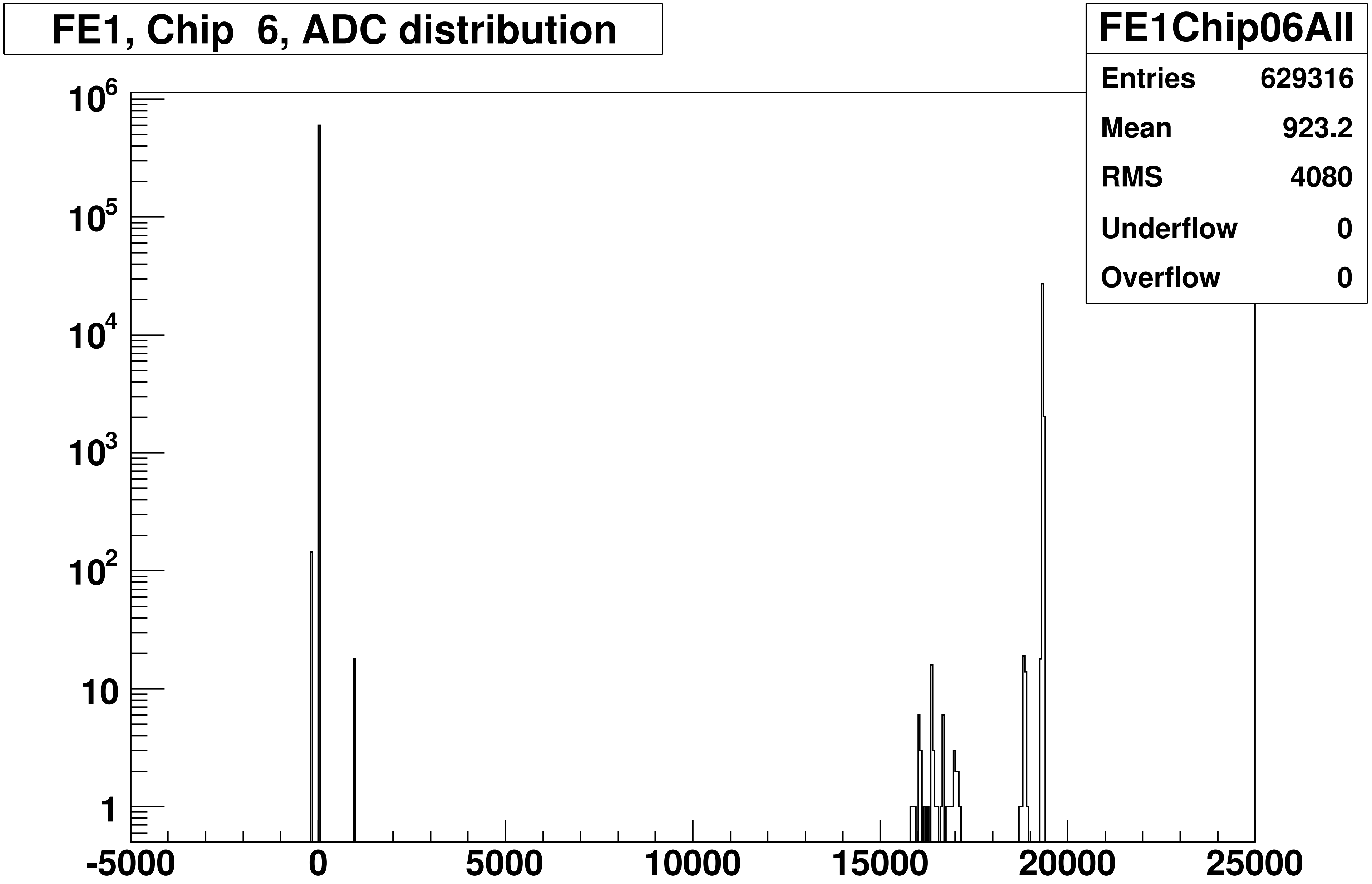


FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 629316
Mean 817
RMS 3904
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

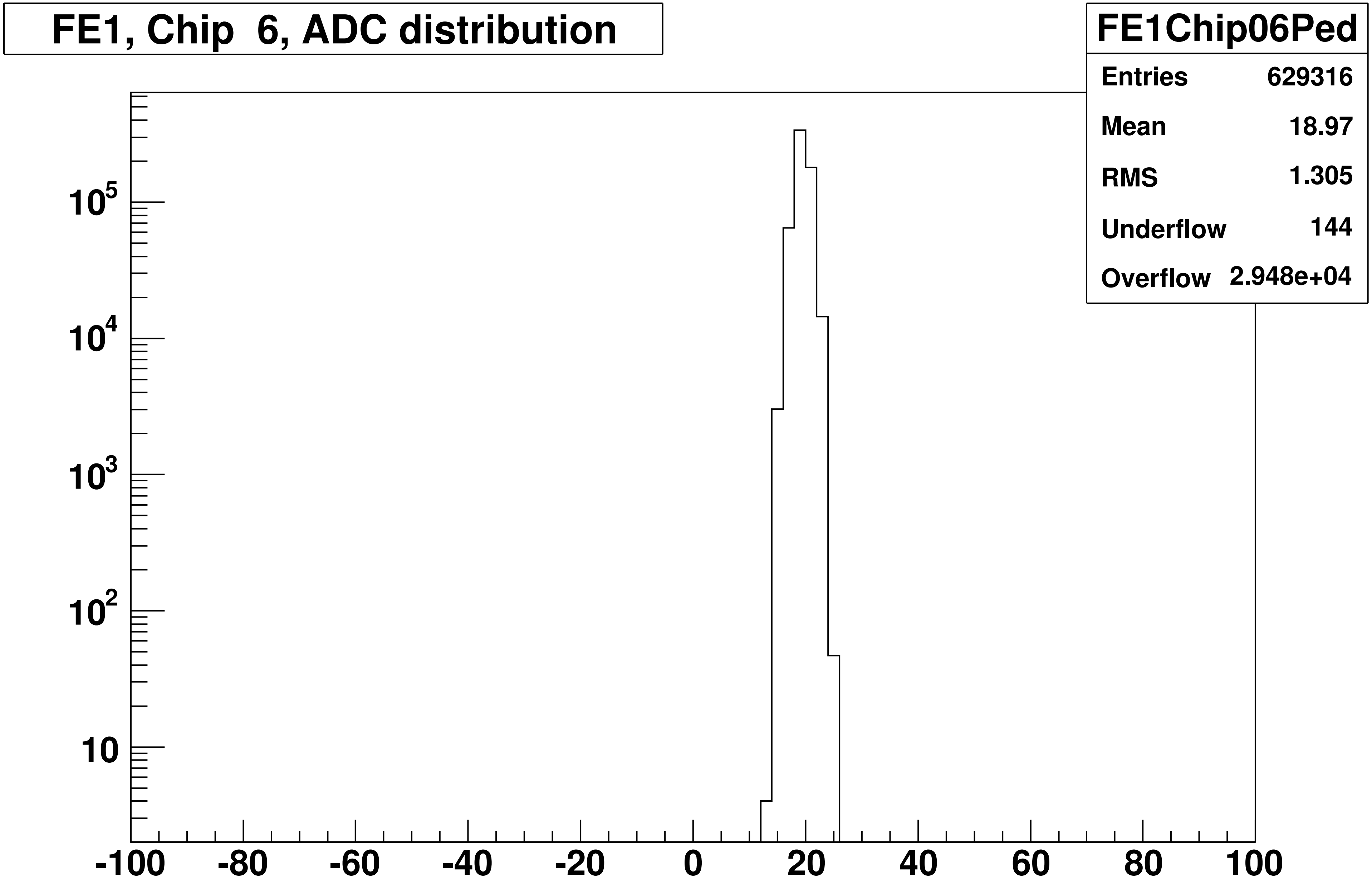


FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 629316
Mean -17.65
RMS 1.249
Underflow 72
Overflow 2.772e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

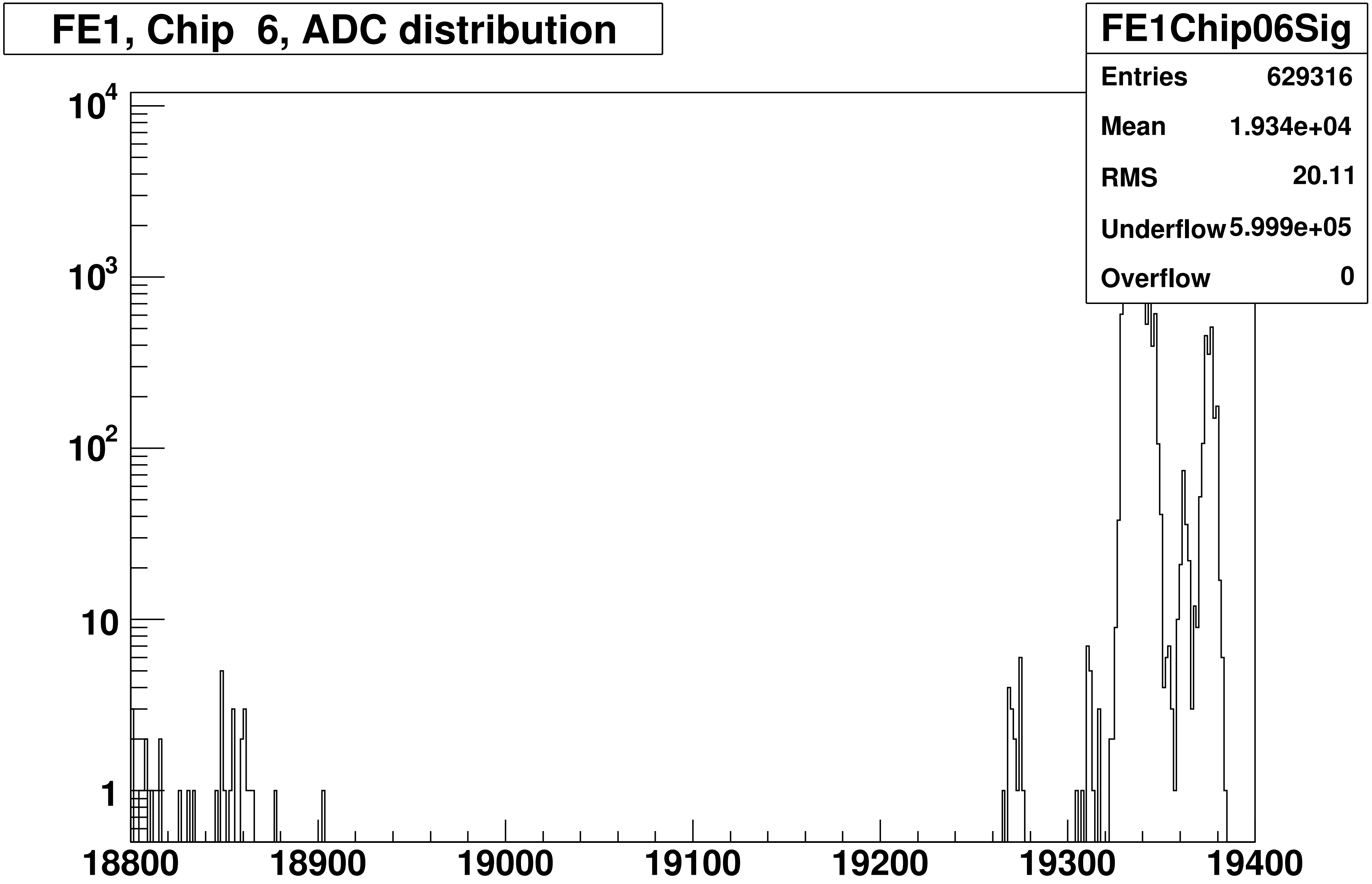

FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 629316
Mean 1.909e+04
RMS 5.24
Underflow 6.018e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

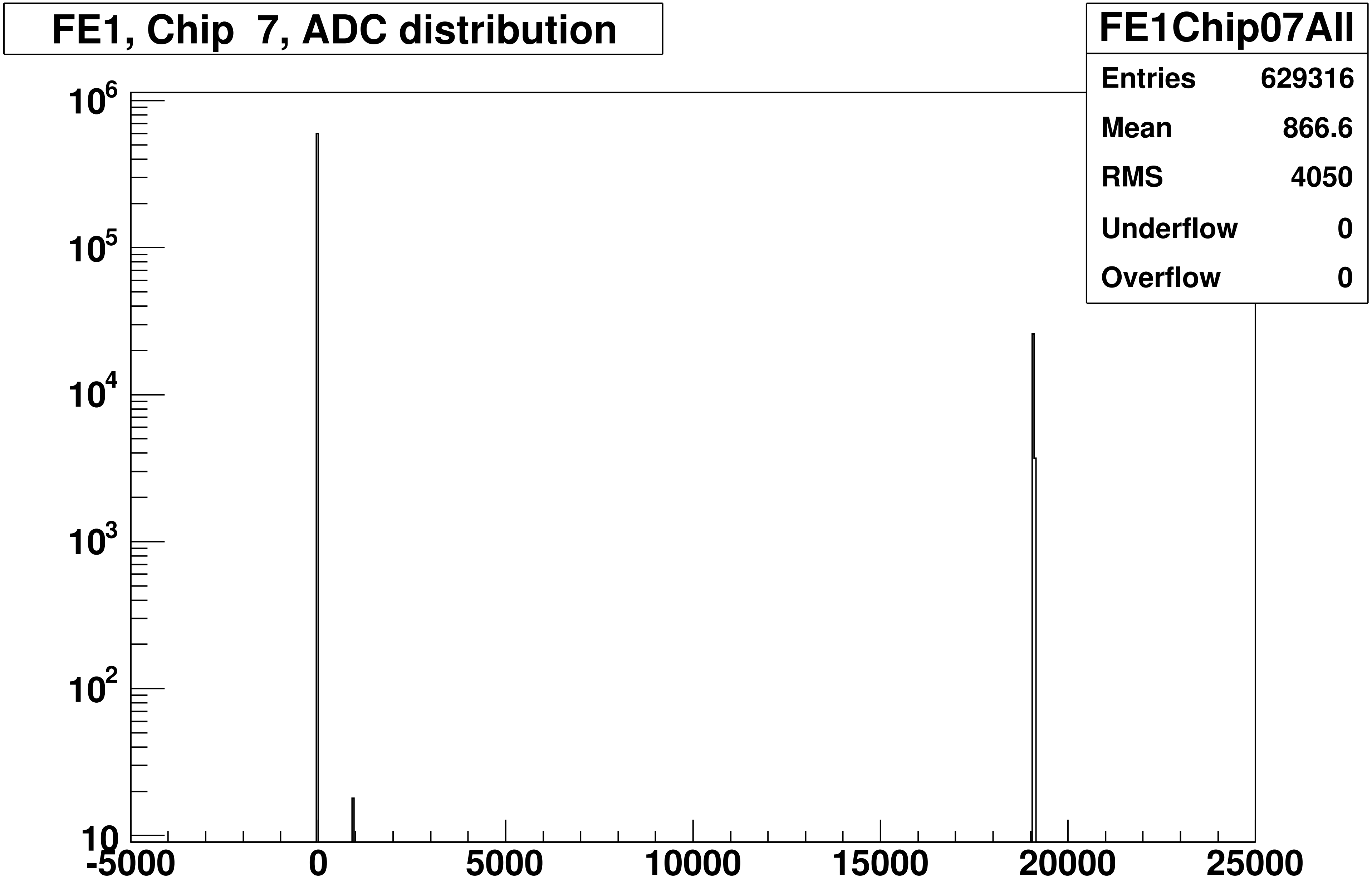


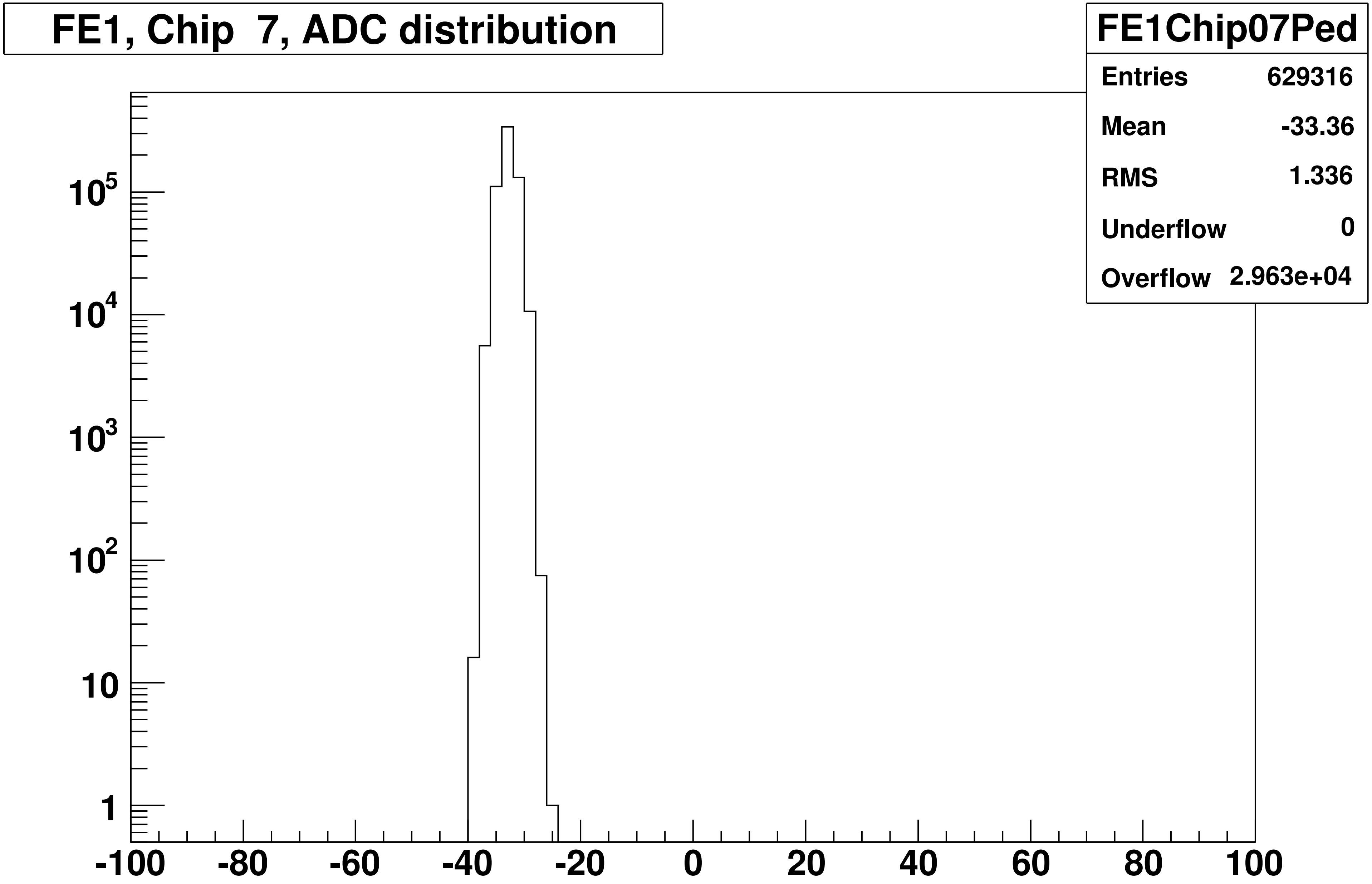
FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 629316
Mean 923.2
RMS 4080
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

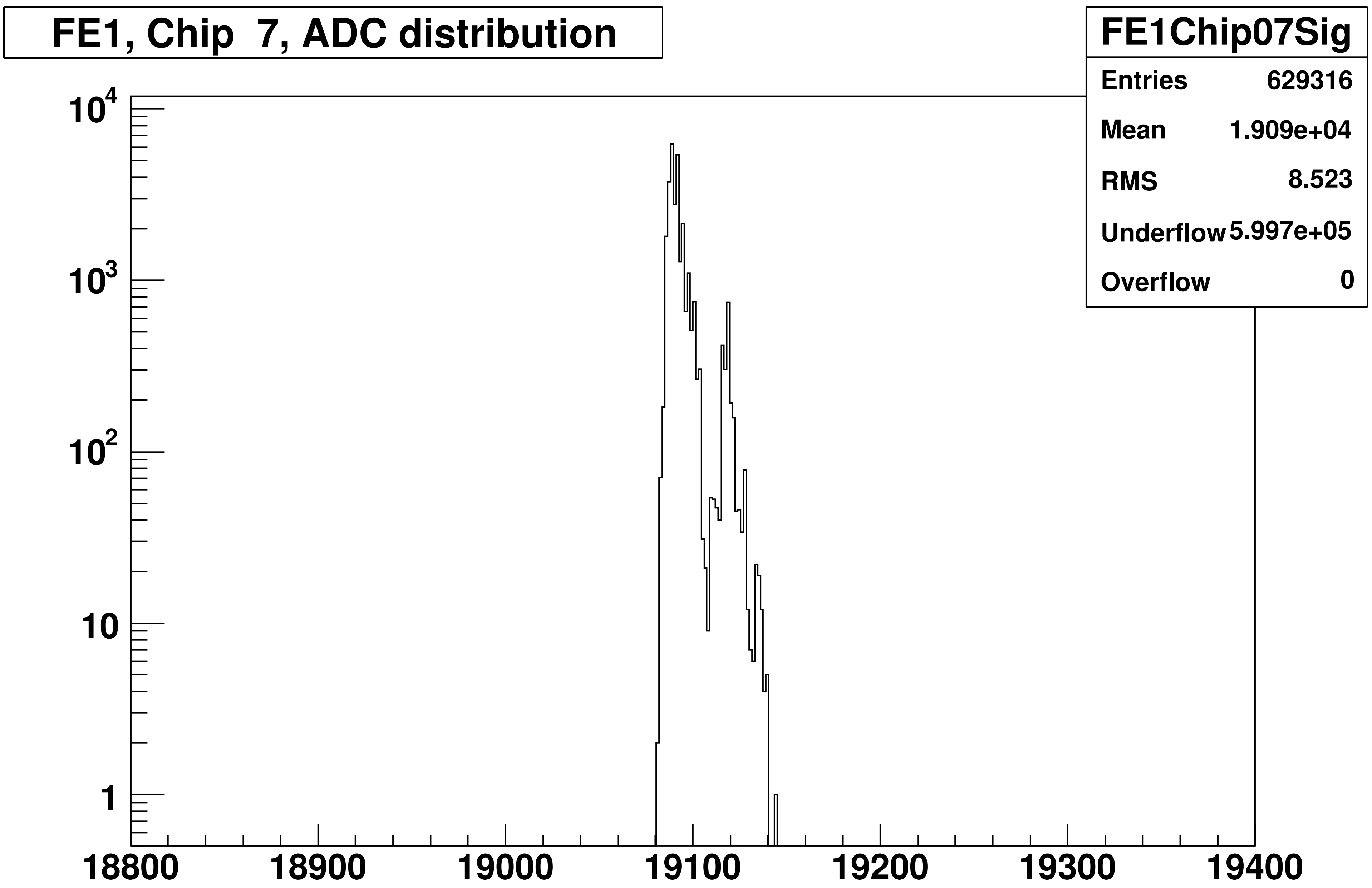


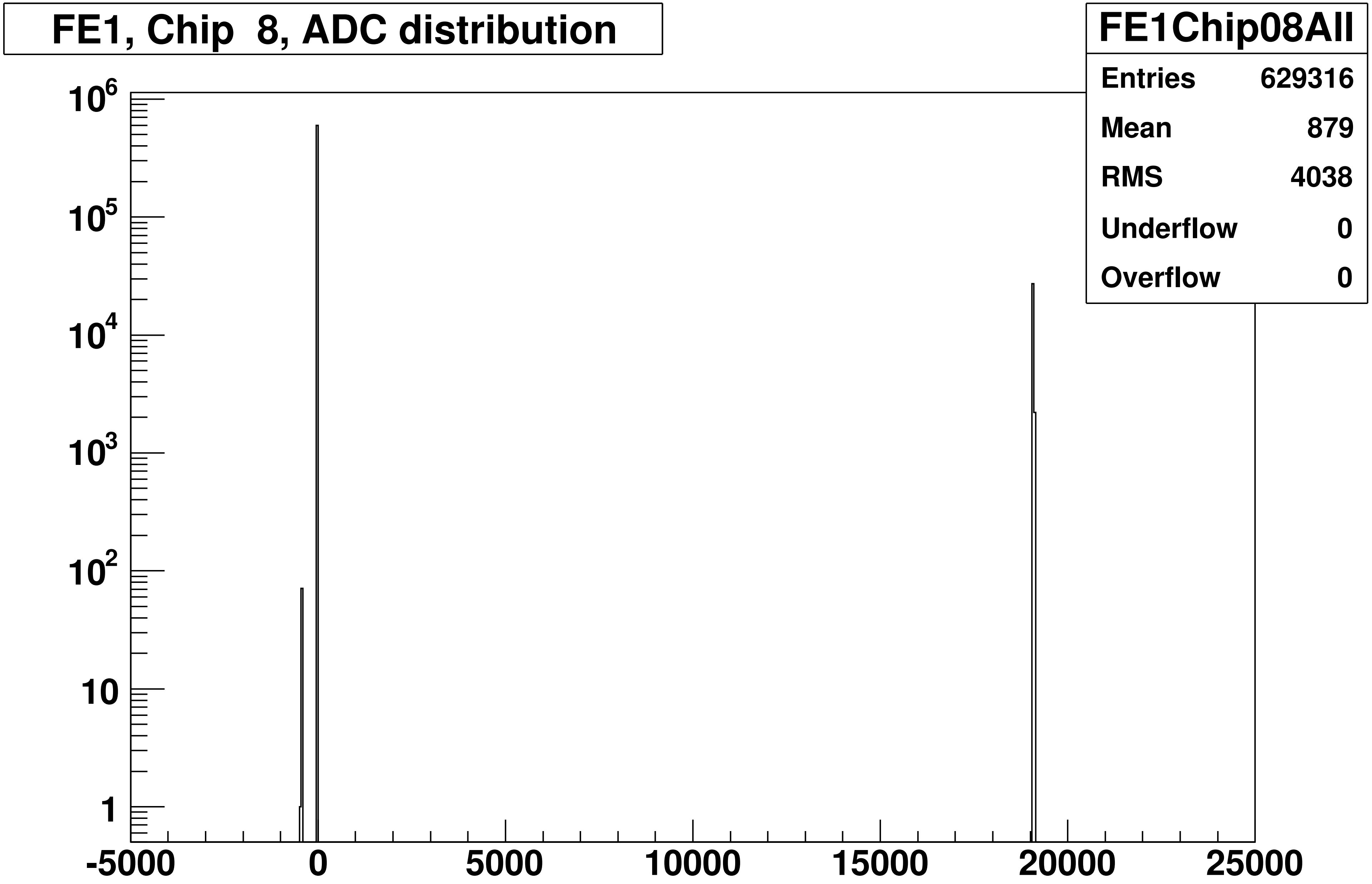
FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 629316
Mean 18.97
RMS 1.305
Underflow 144
Overflow 2.948e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 629316
Mean 1.934e+04
RMS 20.11
Underflow 5.999e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

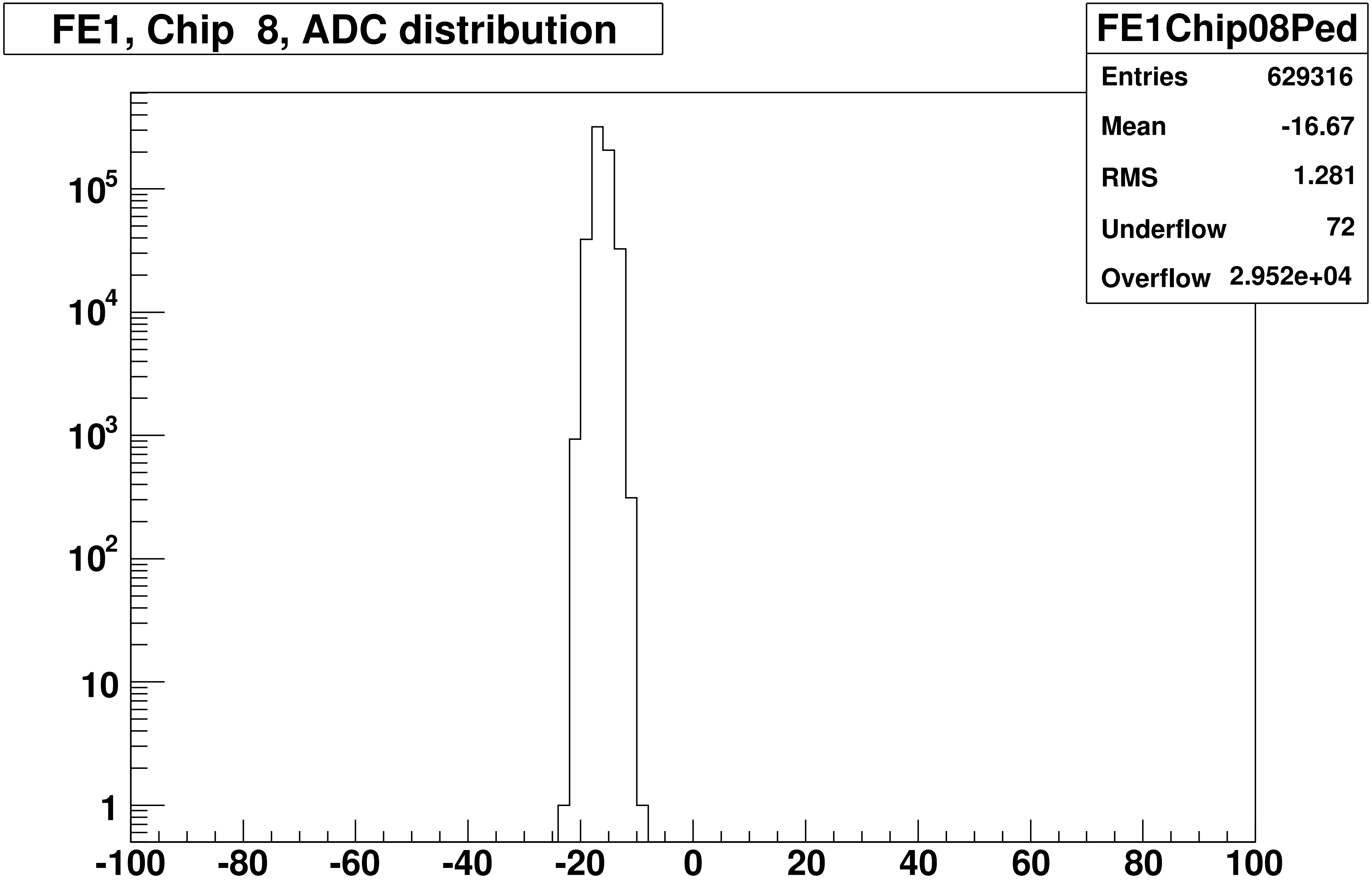

FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 629316
Mean 866.6
RMS 4050
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 629316
Mean -33.36
RMS 1.336
Underflow 0
Overflow 2.963e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

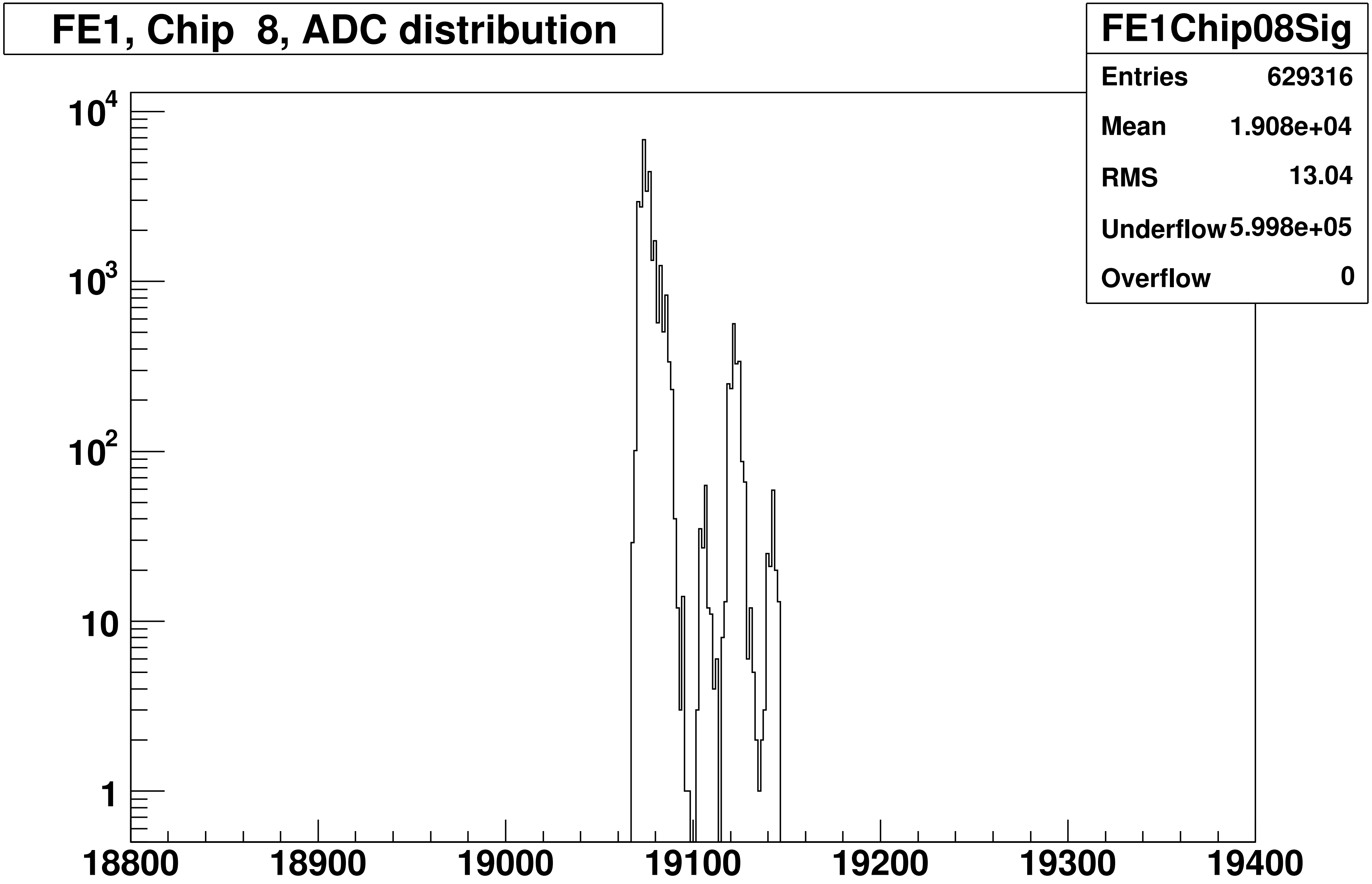

FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 629316
Mean 1.909e+04
RMS 8.523
Underflow 5.997e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

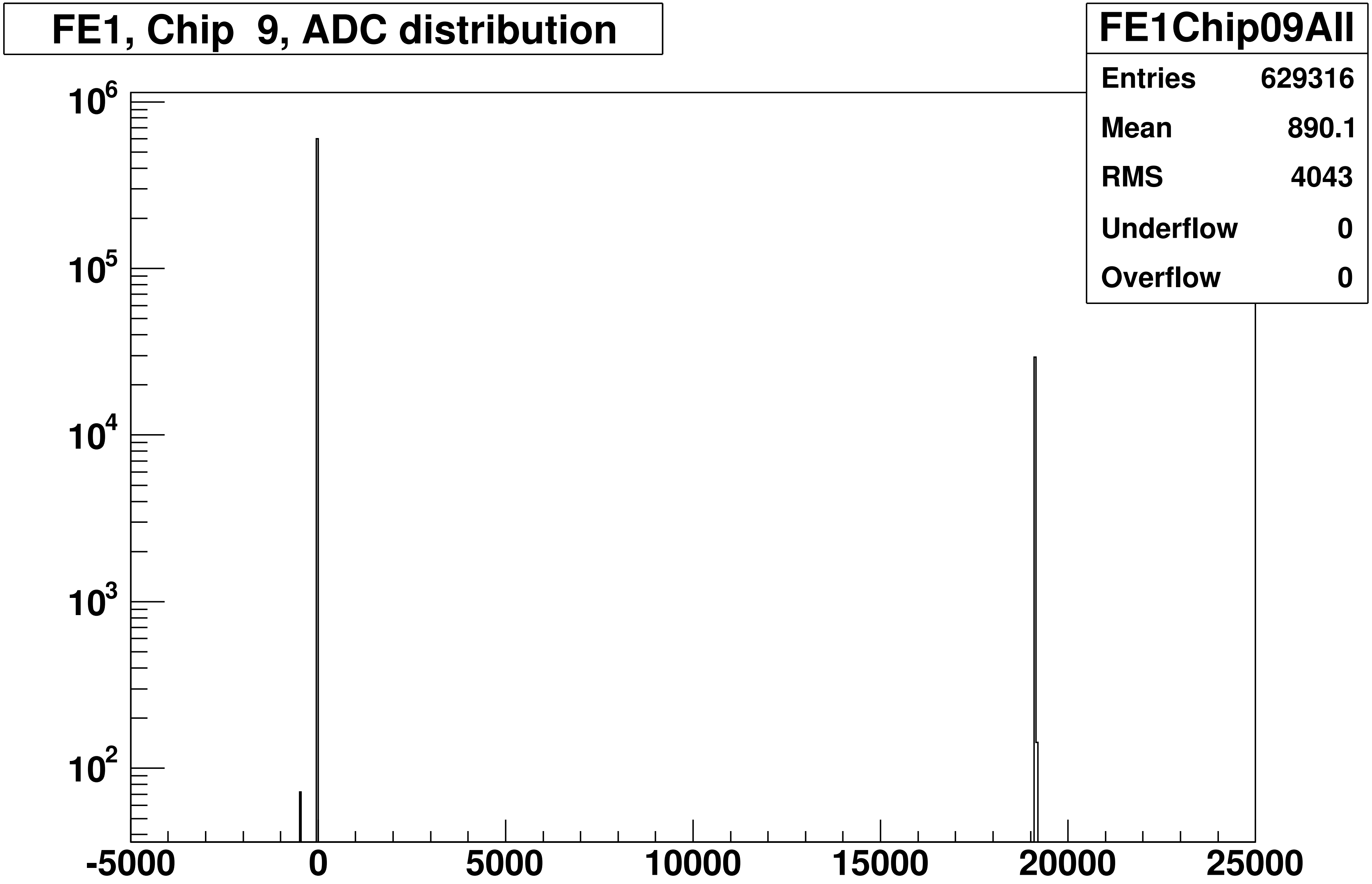


FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 629316
Mean 879
RMS 4038
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

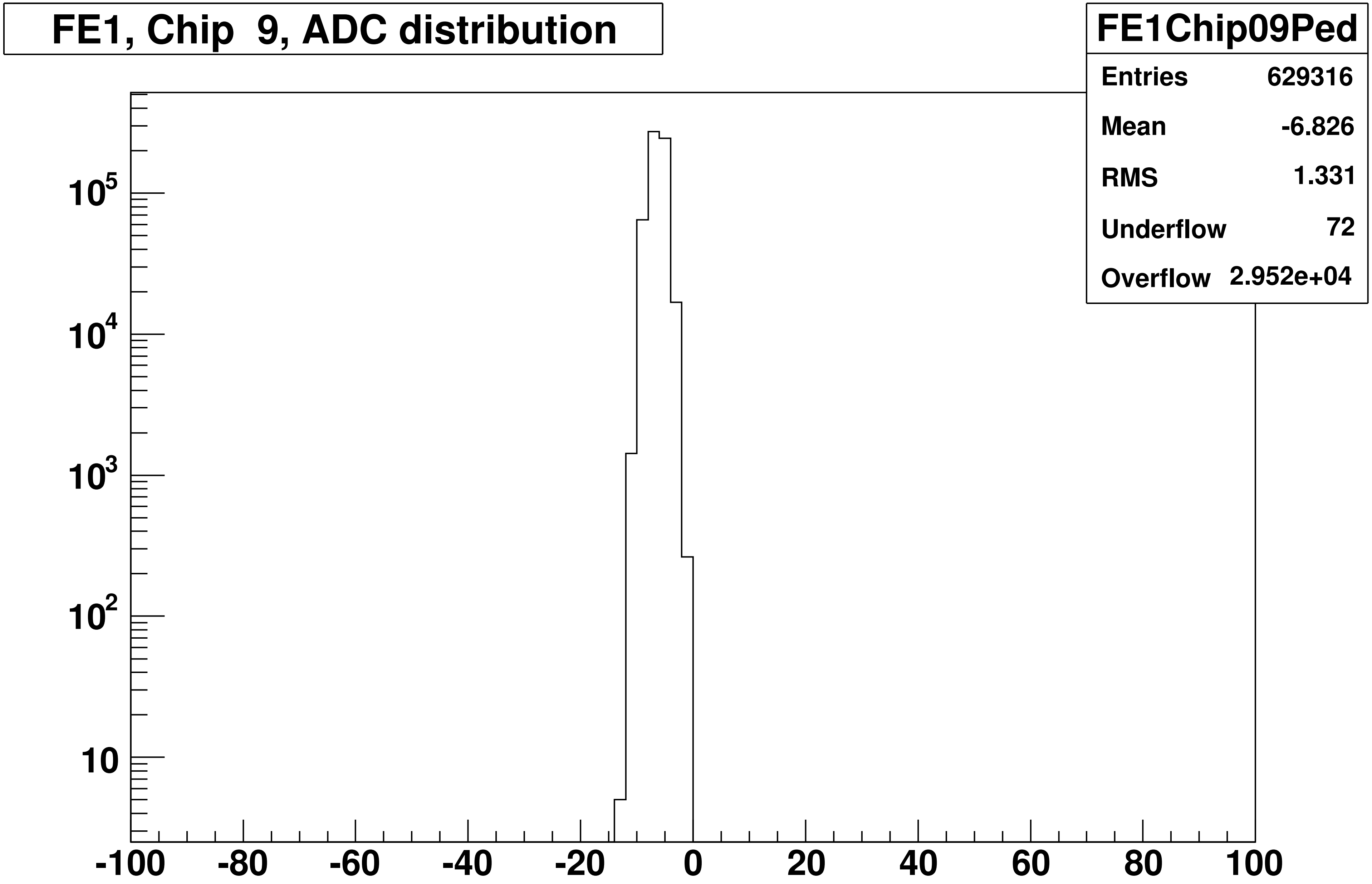


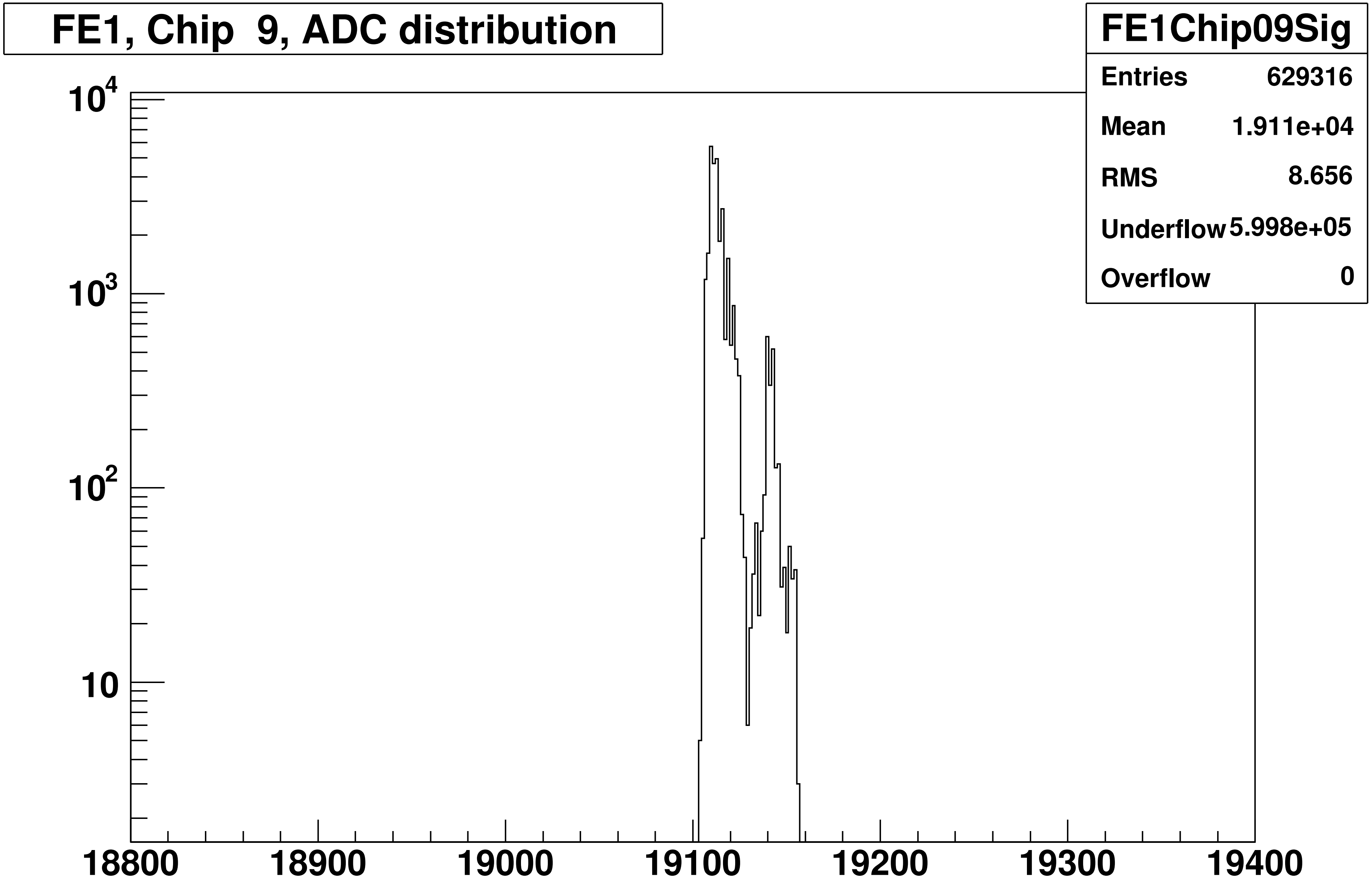
FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 629316
Mean -16.67
RMS 1.281
Underflow 72
Overflow 2.952e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

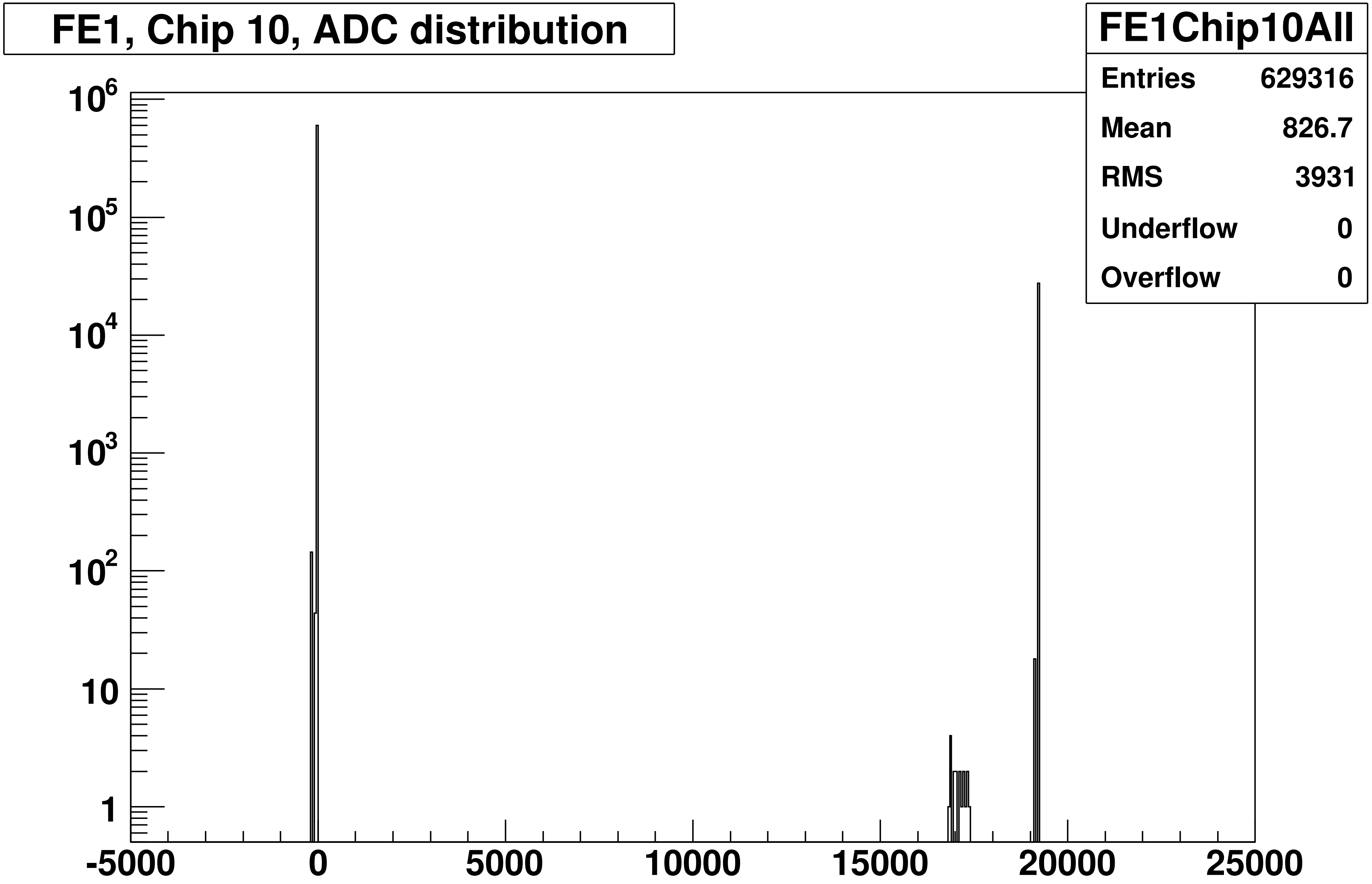

FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 629316
Mean 1.908e+04
RMS 13.04
Underflow 5.998e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



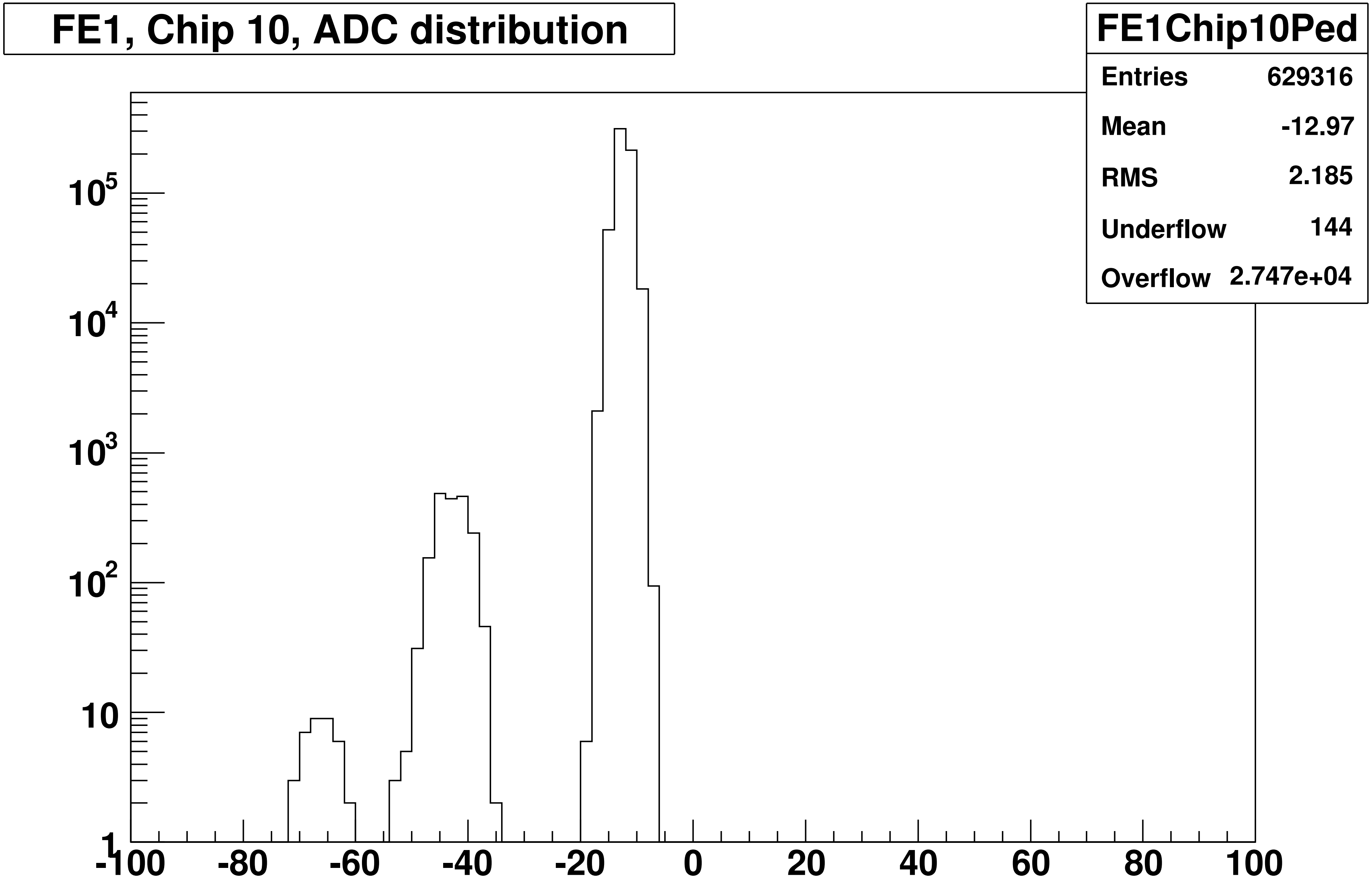
FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 629316
Mean 890.1
RMS 4043
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 629316
Mean -6.826
RMS 1.331
Underflow 72
Overflow 2.952e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

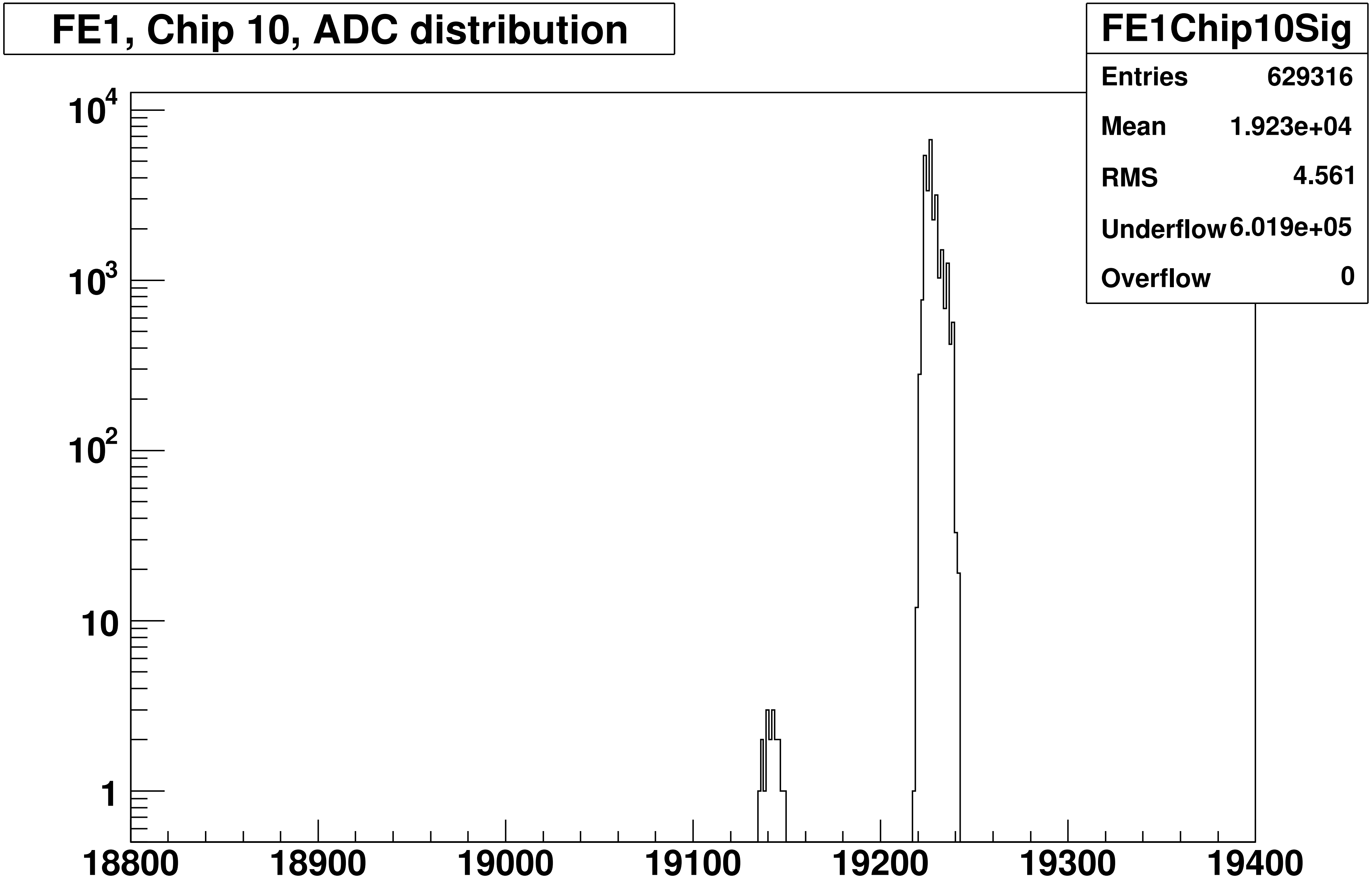

FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 629316
Mean 1.911e+04
RMS 8.656
Underflow 5.998e+05
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400



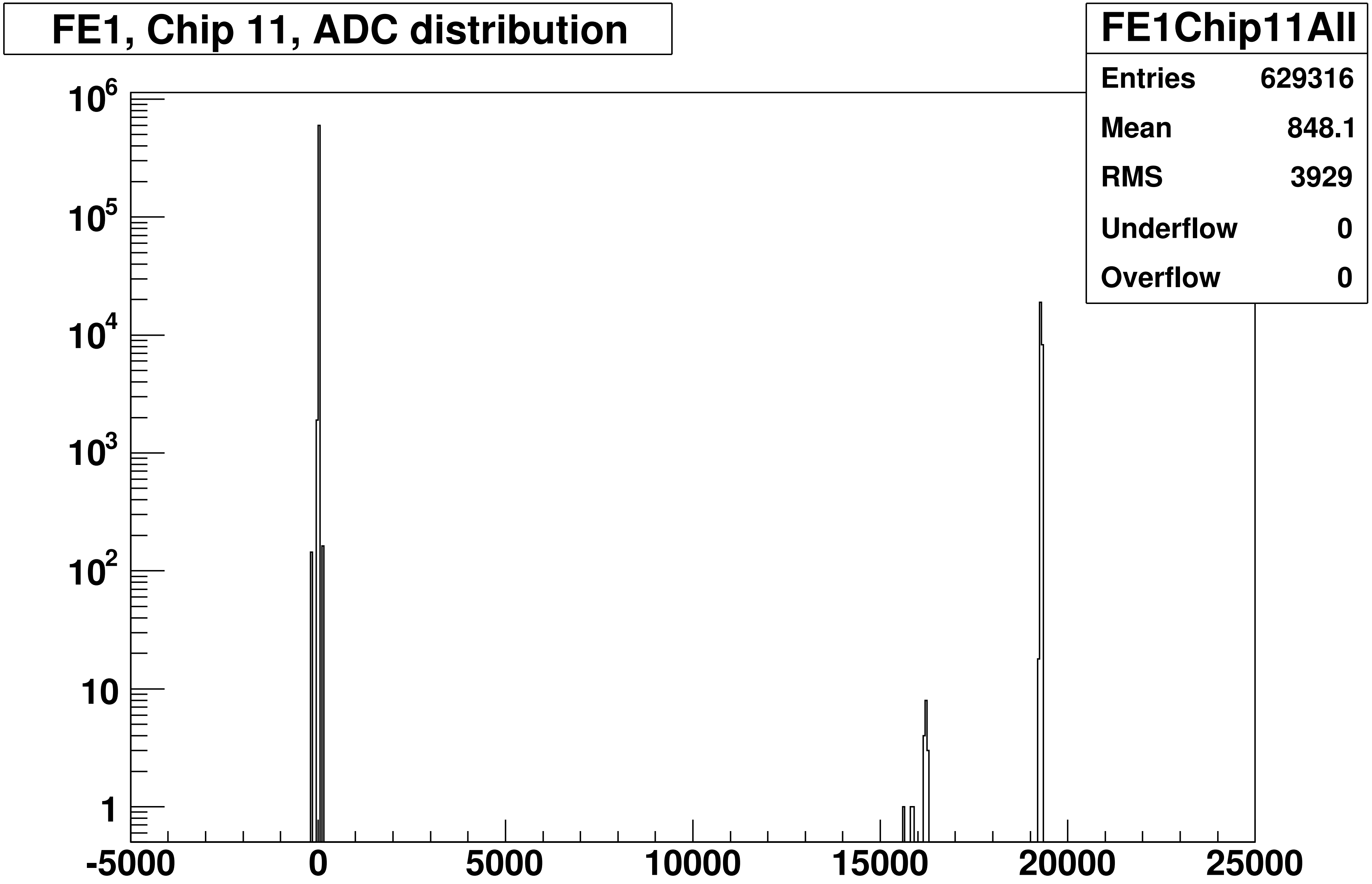
FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 629316
Mean 826.7
RMS 3931
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



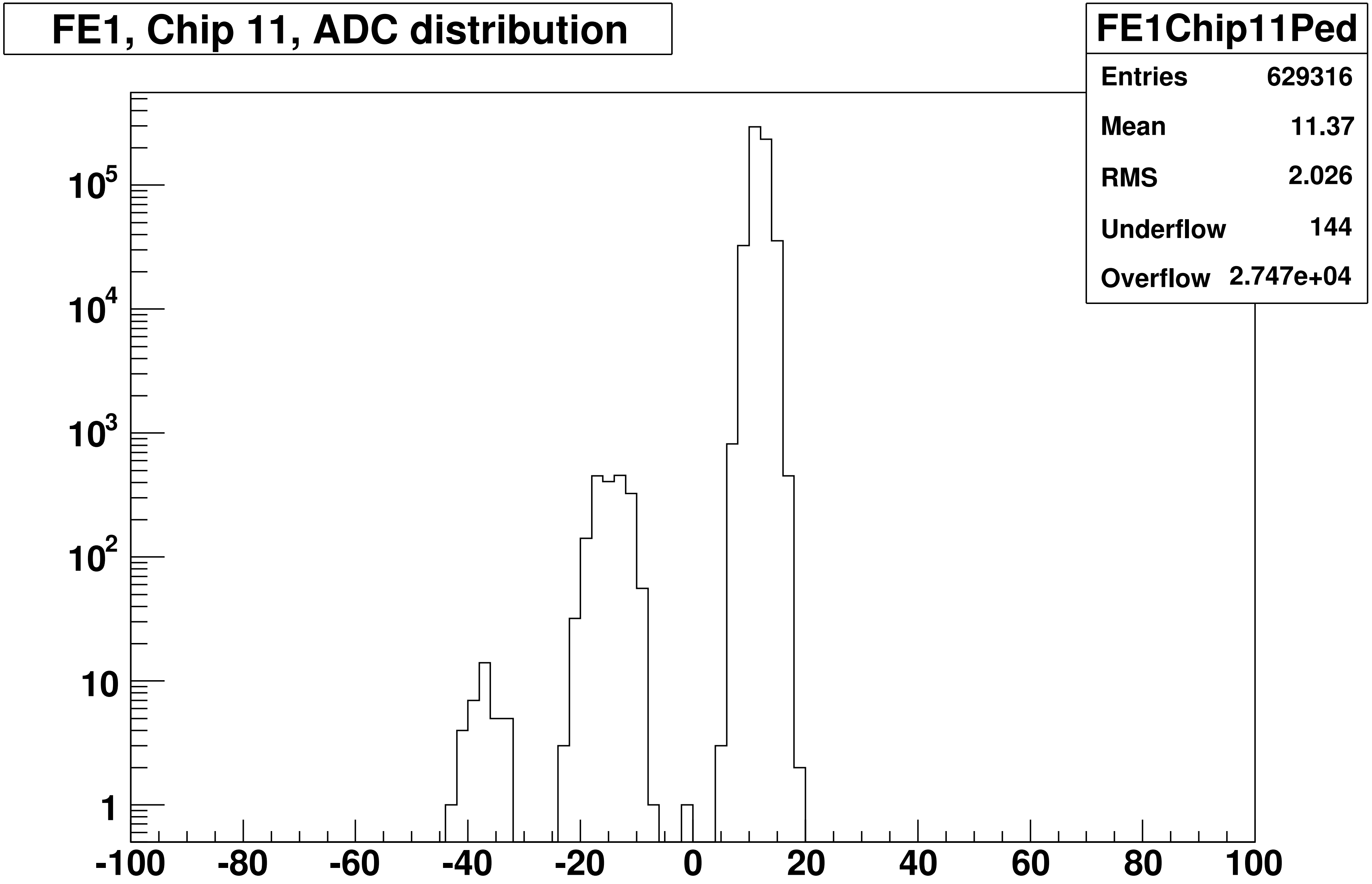
FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 629316
Mean -12.97
RMS 2.185
Underflow 144
Overflow 2.747e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

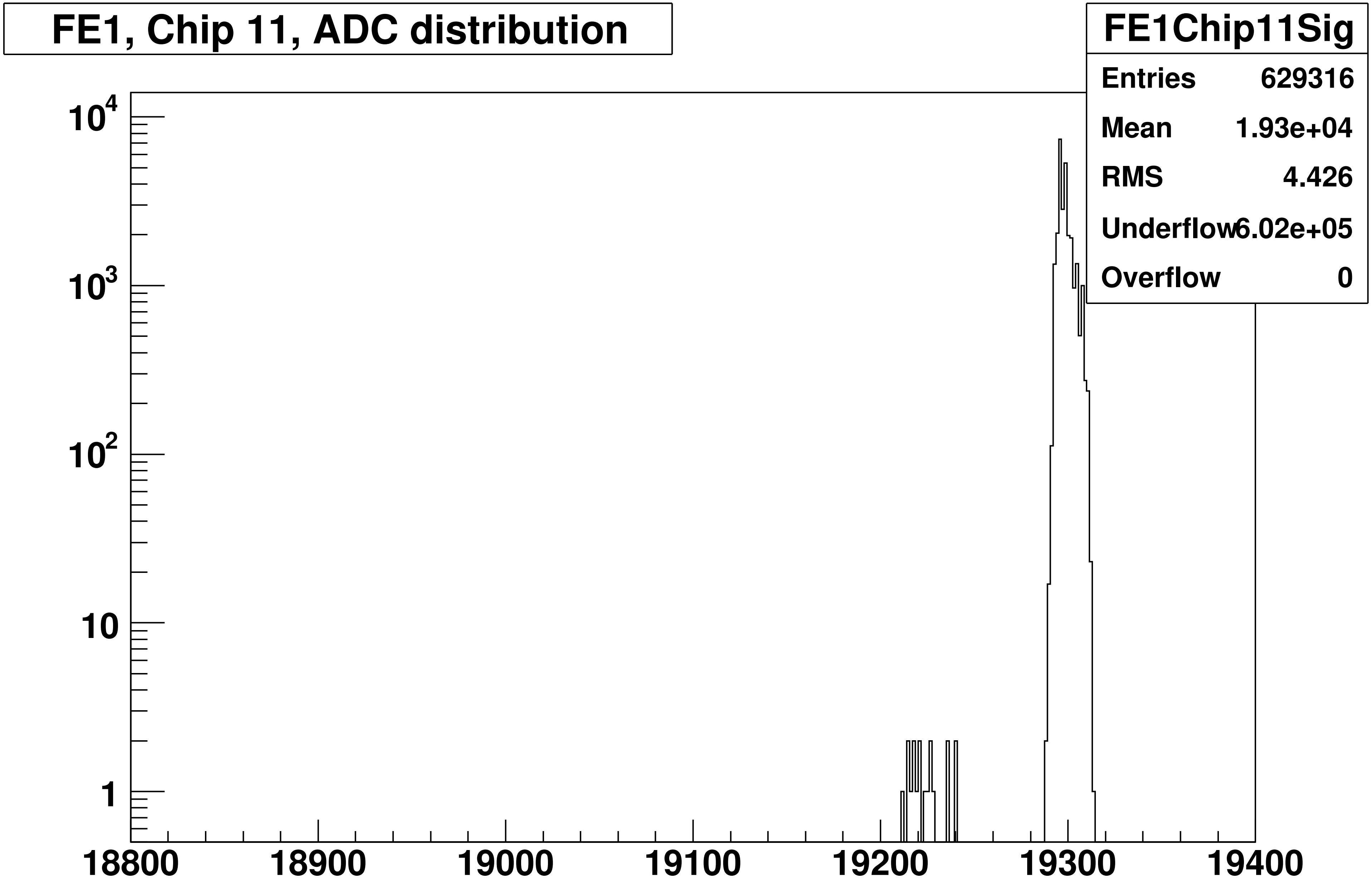


FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 629316
Mean 1.923e+04
RMS 4.561
Underflow 6.019e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

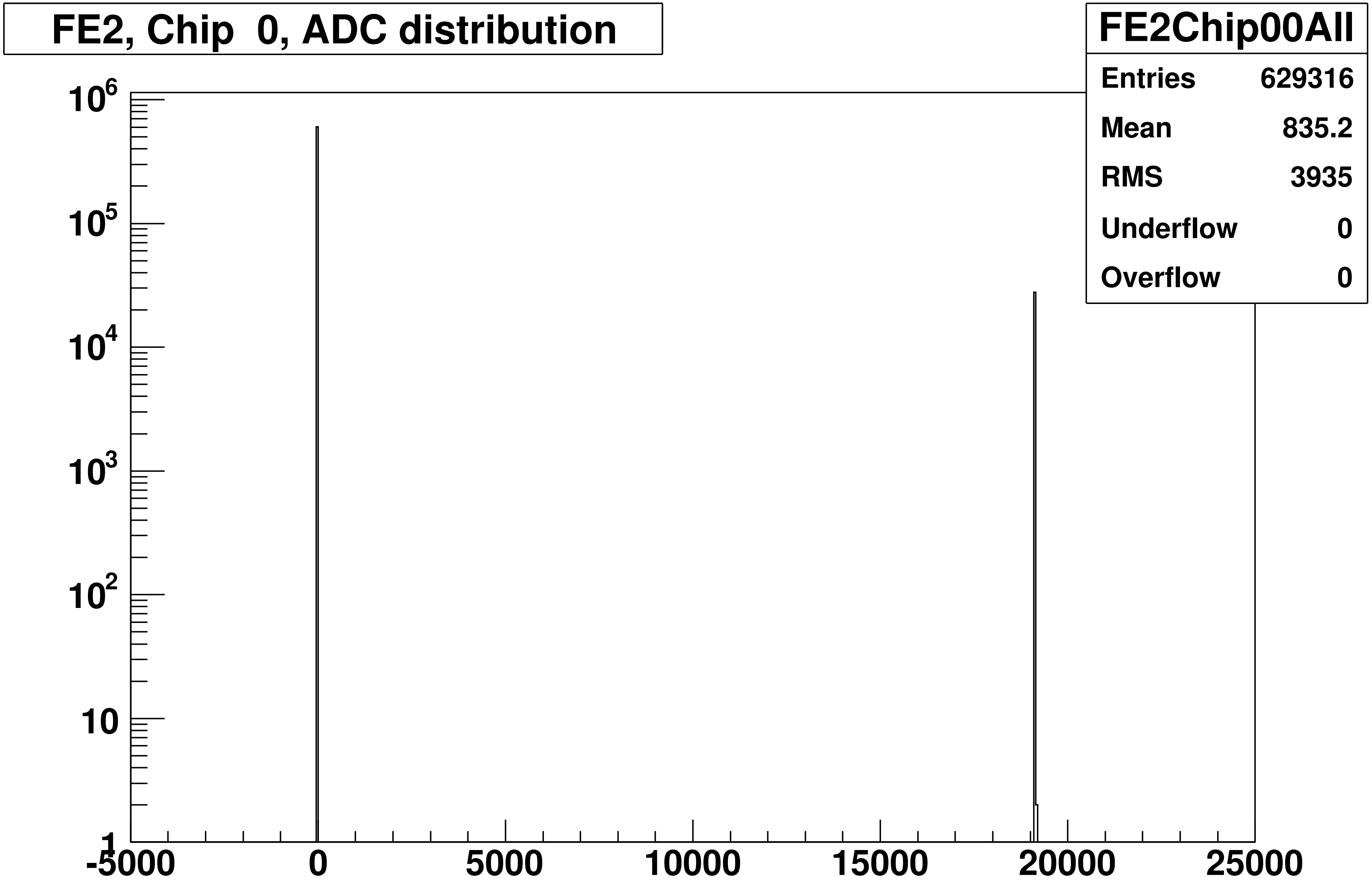


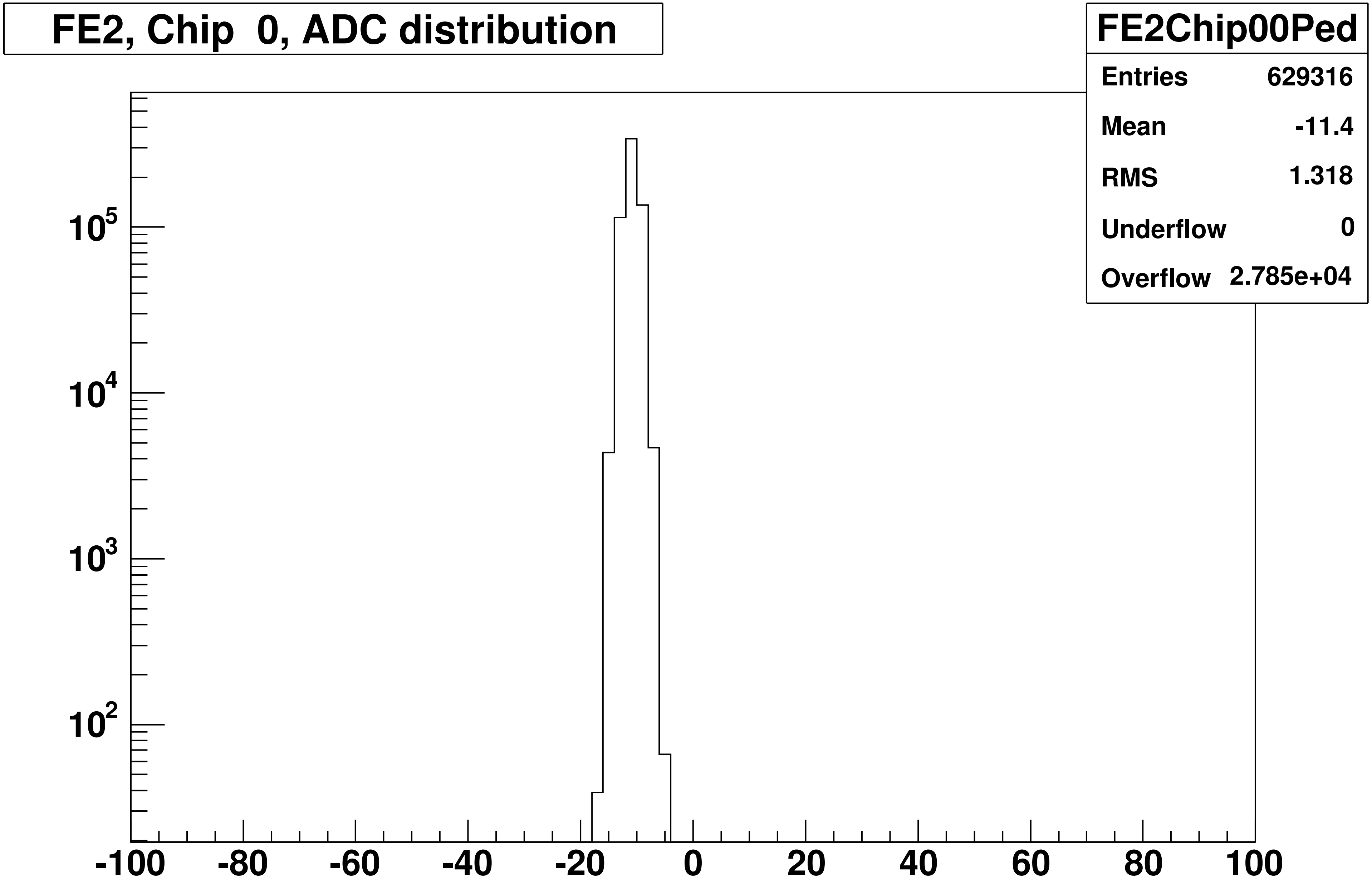
FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 629316
Mean 848.1
RMS 3929
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

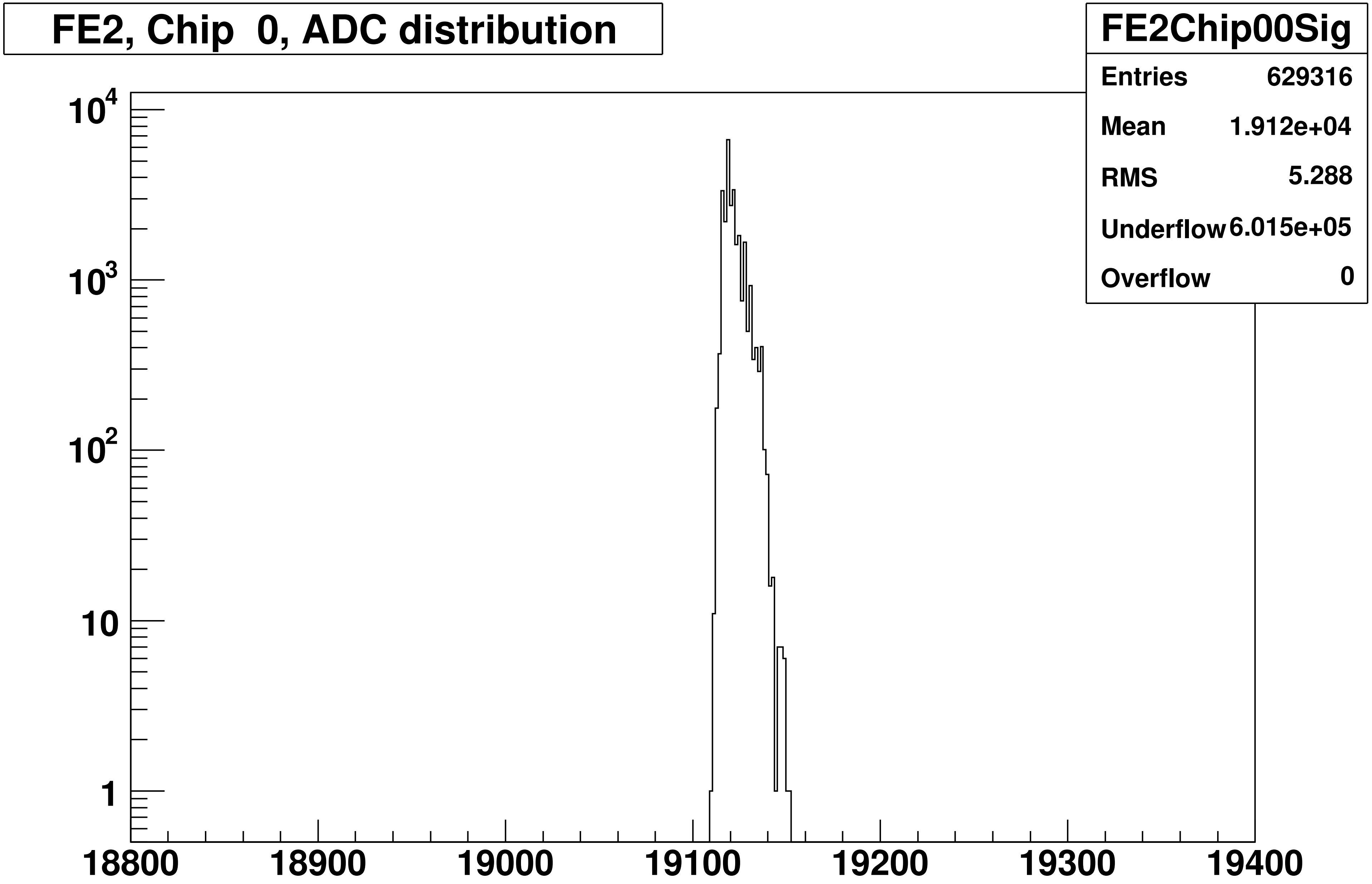

FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 629316
Mean 11.37
RMS 2.026
Underflow 144
Overflow 2.747e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

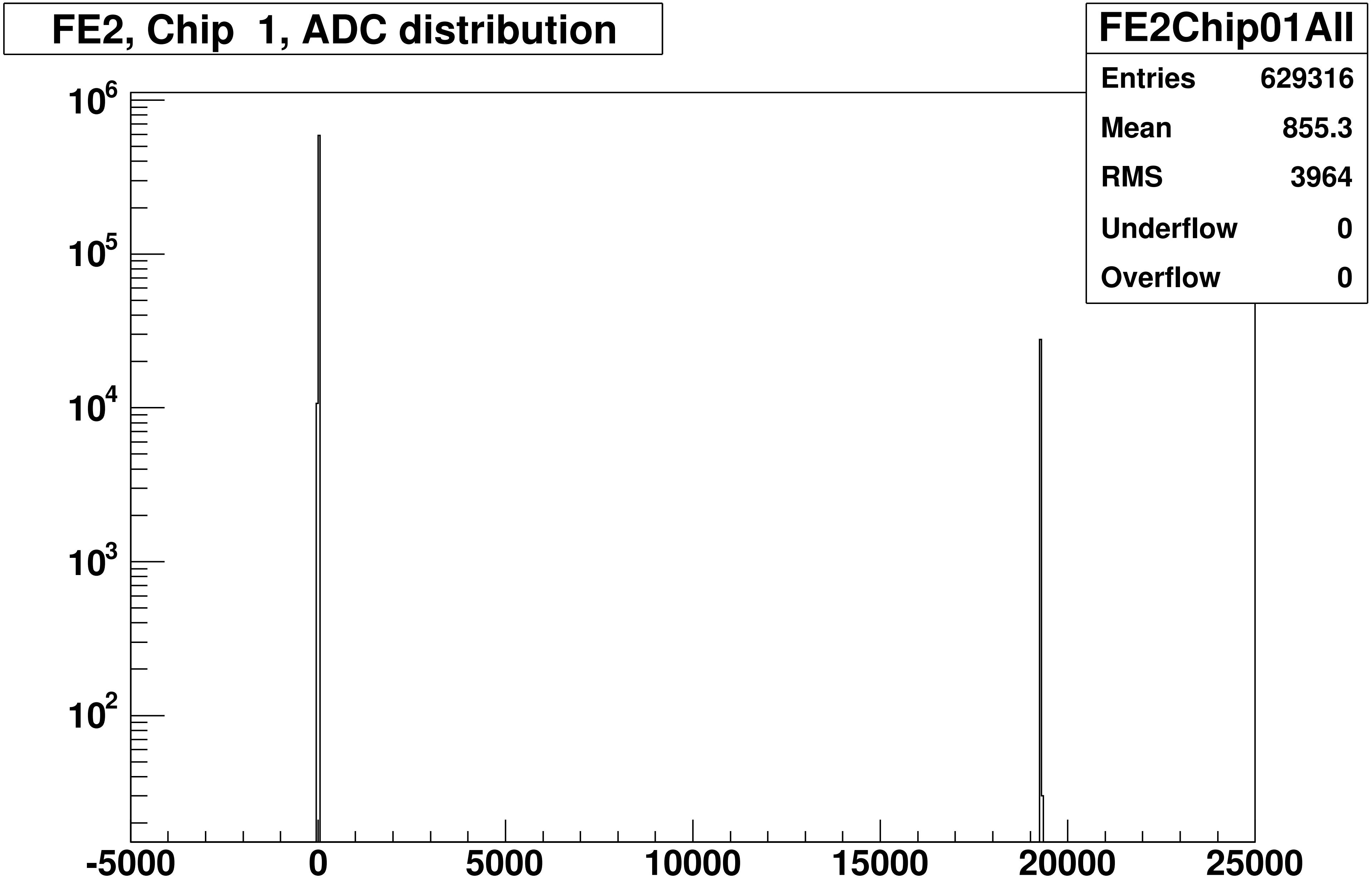


FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 629316
Mean 1.93e+04
RMS 4.426
Underflow 6.02e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

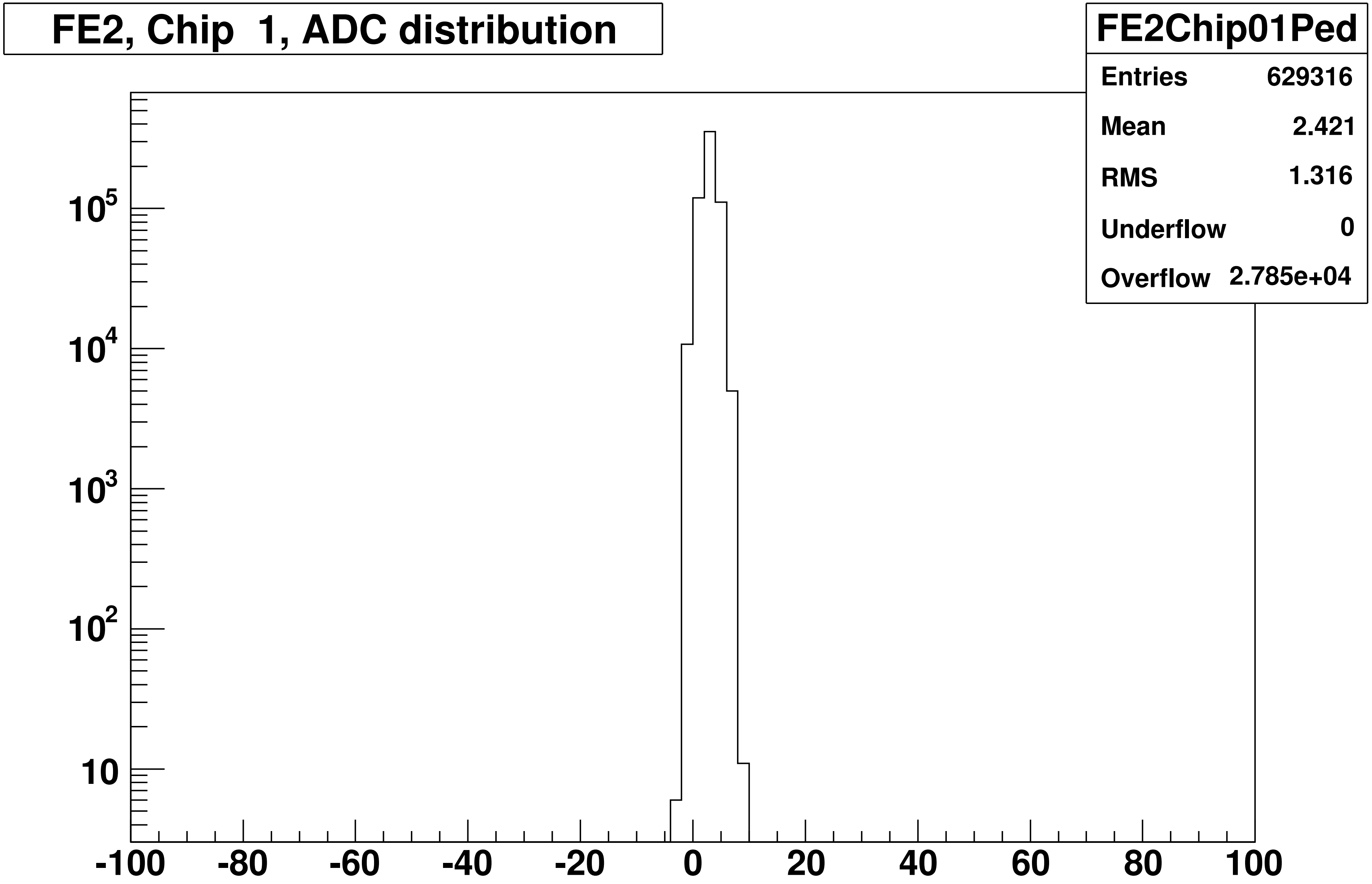

FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 629316
Mean 835.2
RMS 3935
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

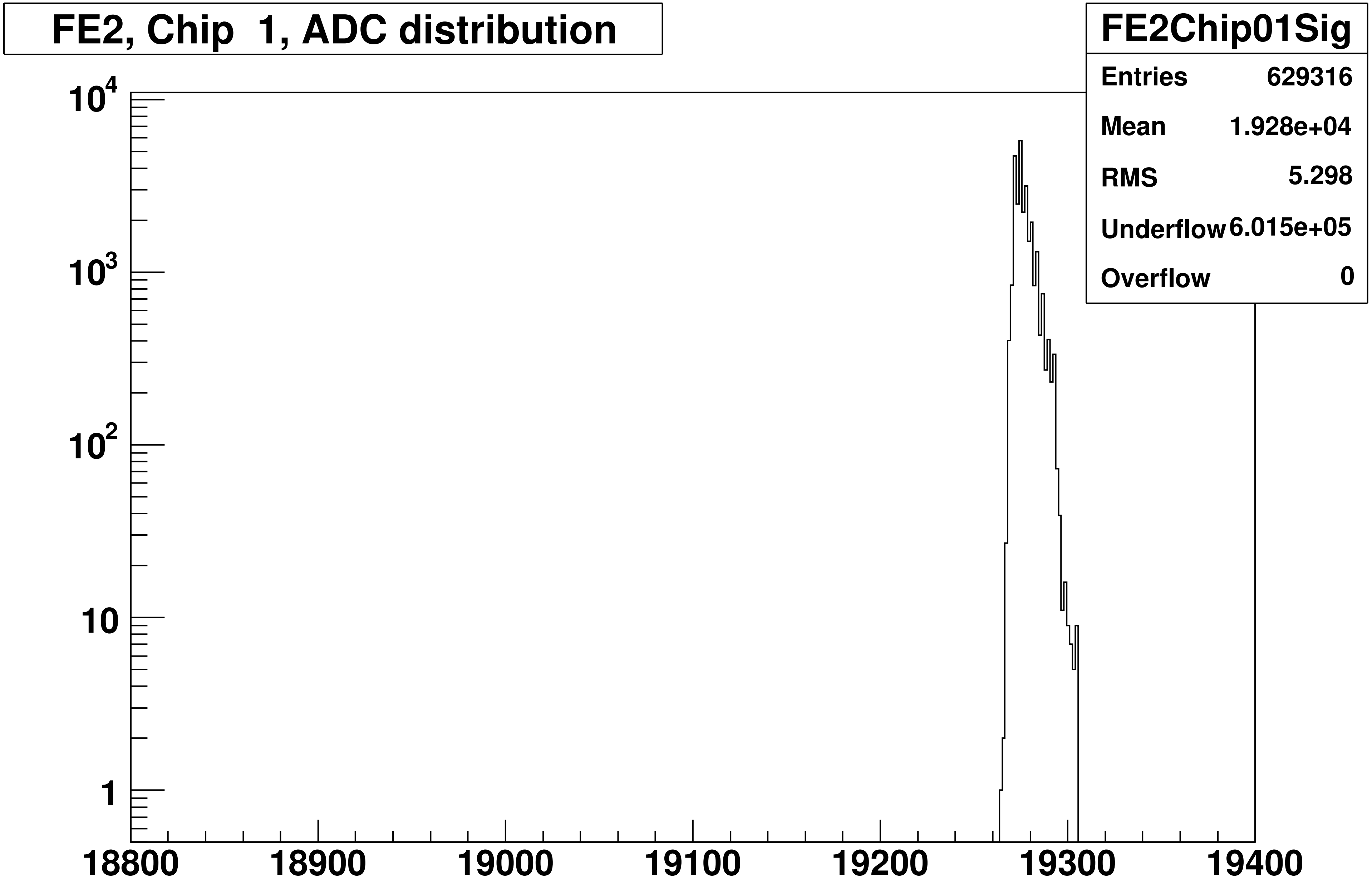

FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 629316
Mean -11.4
RMS 1.318
Underflow 0
Overflow 2.785e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

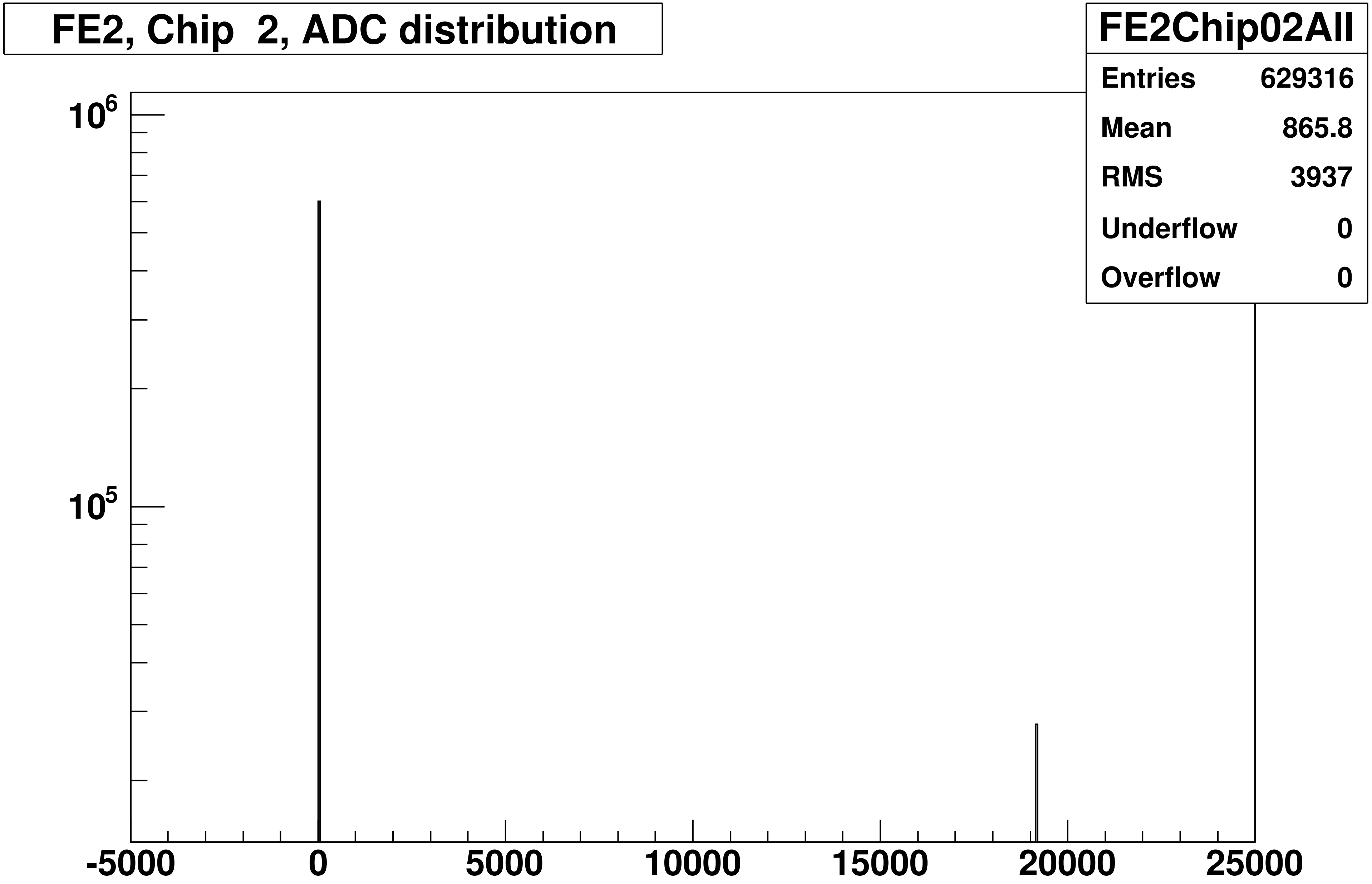

FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 629316
Mean 1.912e+04
RMS 5.288
Underflow 6.015e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

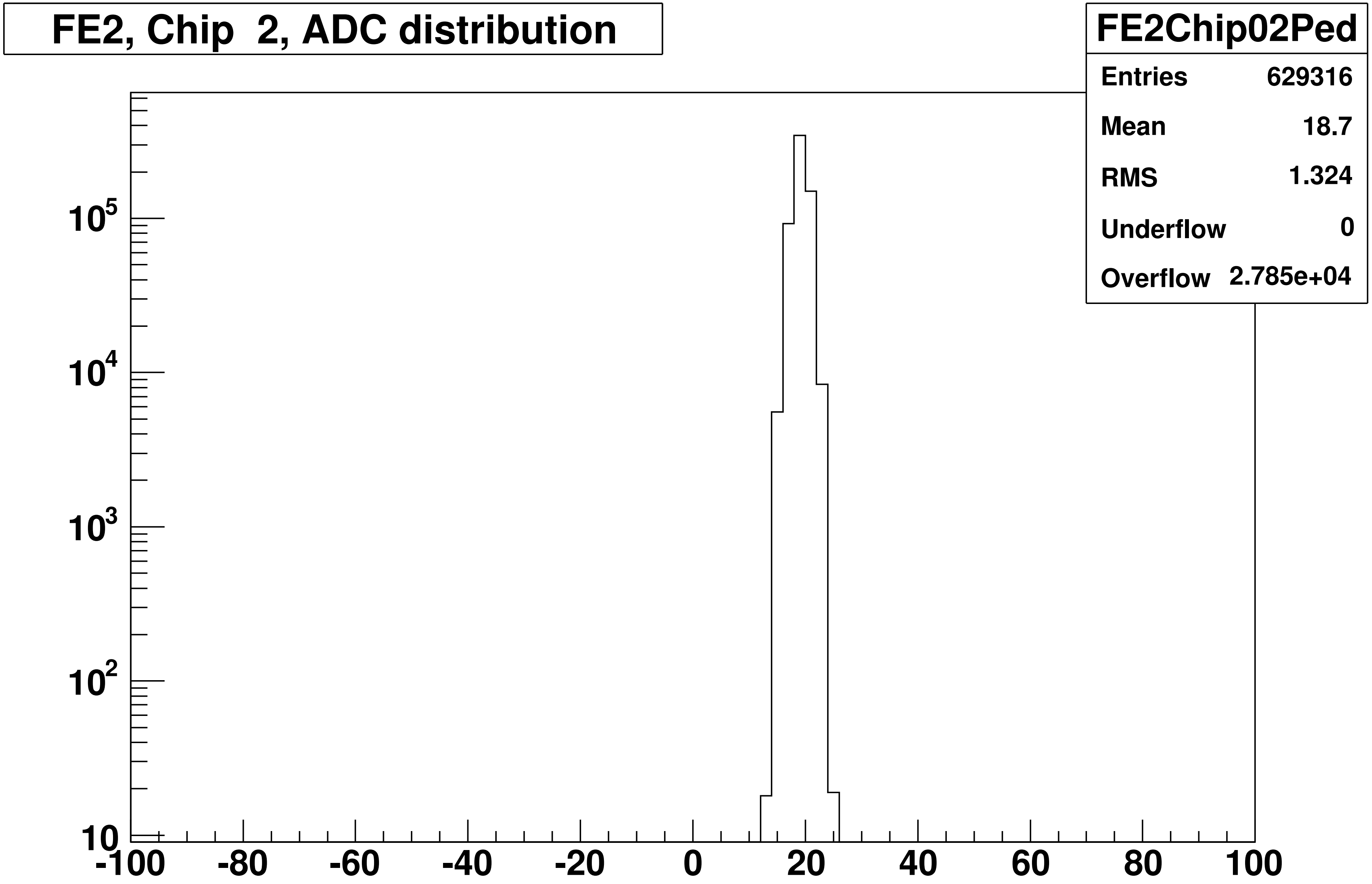


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 629316
Mean 855.3
RMS 3964
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

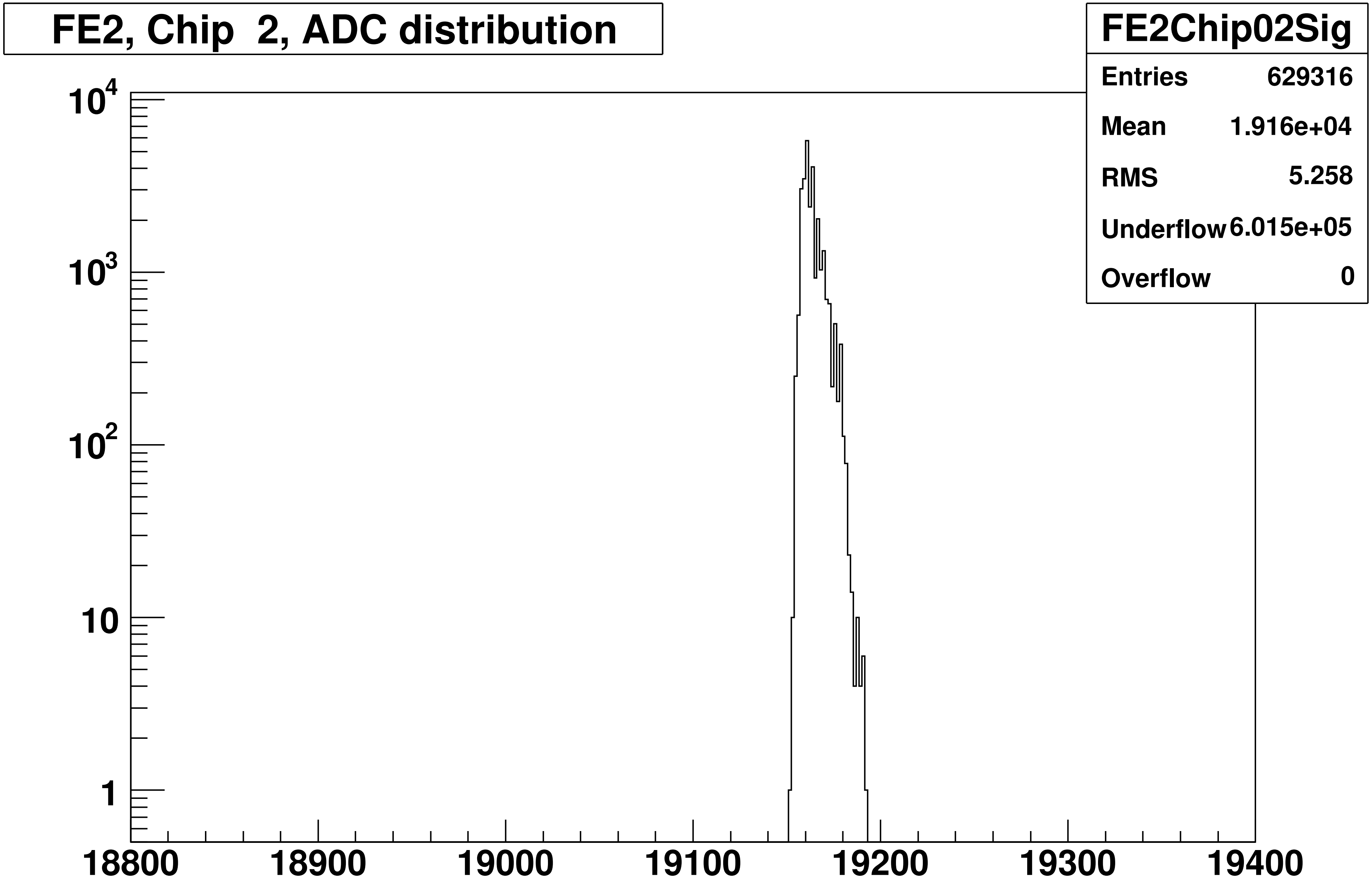

FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 629316
Mean 2.421
RMS 1.316
Underflow 0
Overflow 2.785e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 629316
Mean 1.928e+04
RMS 5.298
Underflow 6.015e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

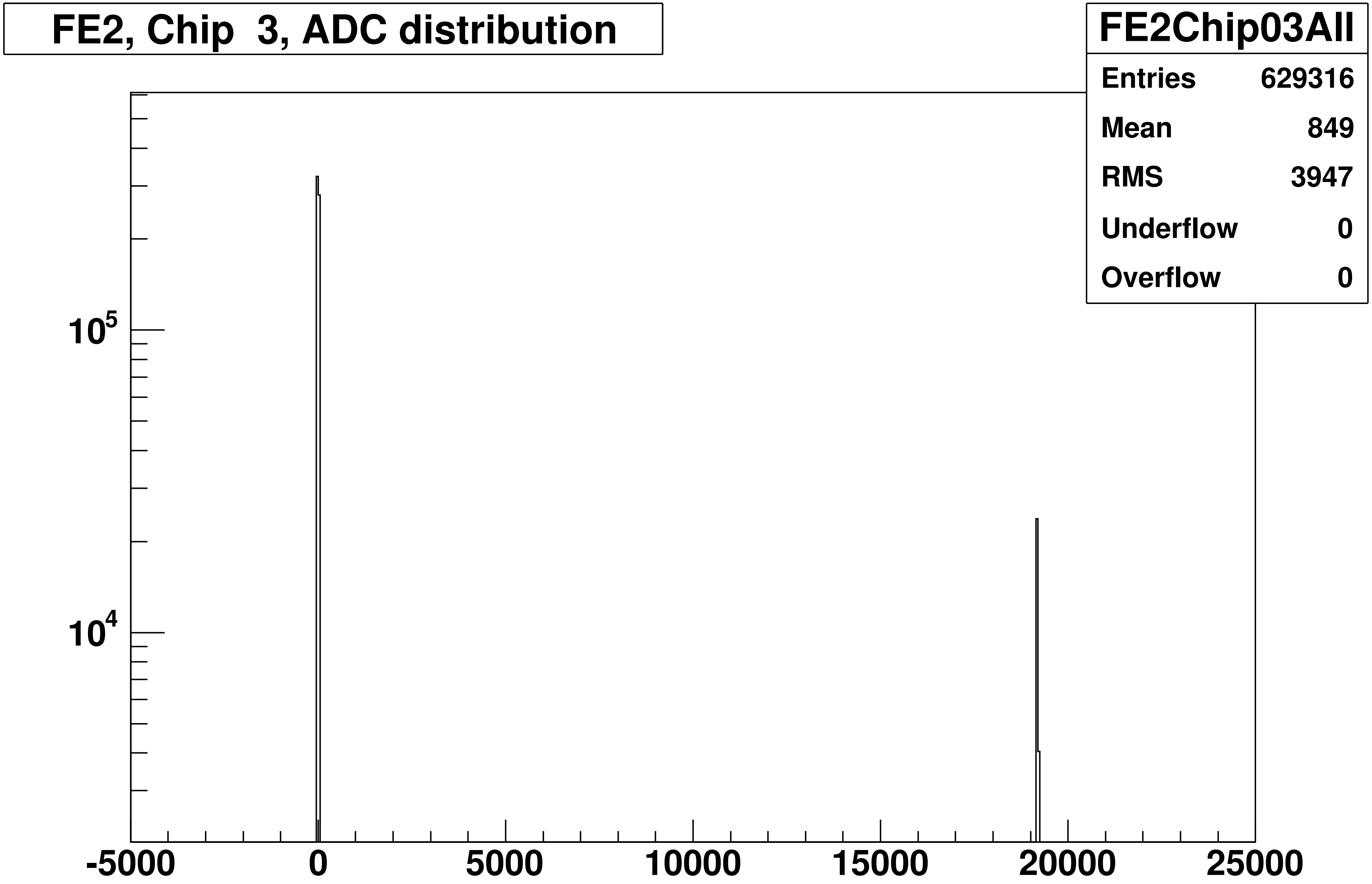

FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 629316
Mean 865.8
RMS 3937
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000

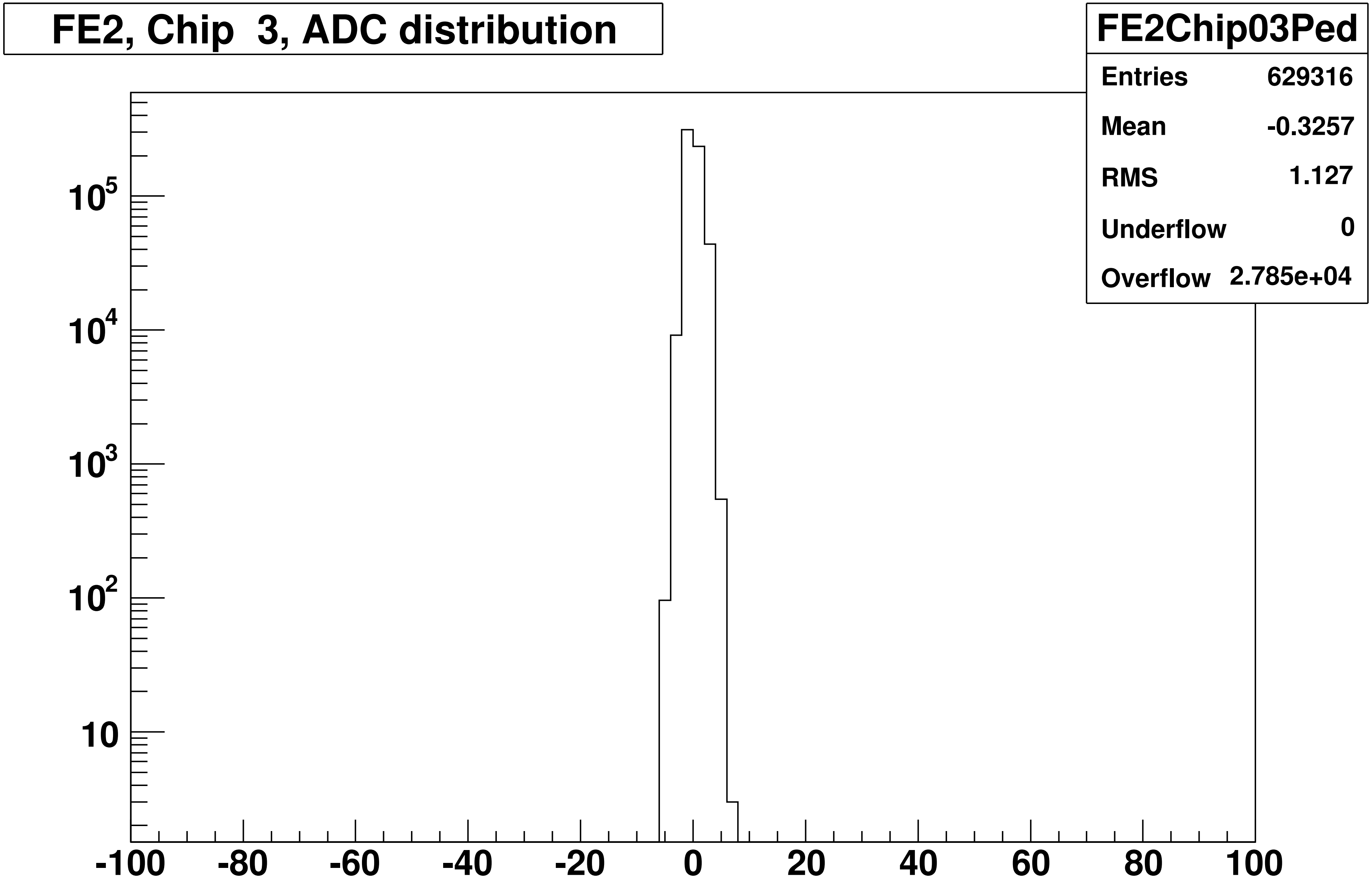


FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 629316
Mean 18.7
RMS 1.324
Underflow 0
Overflow 2.785e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

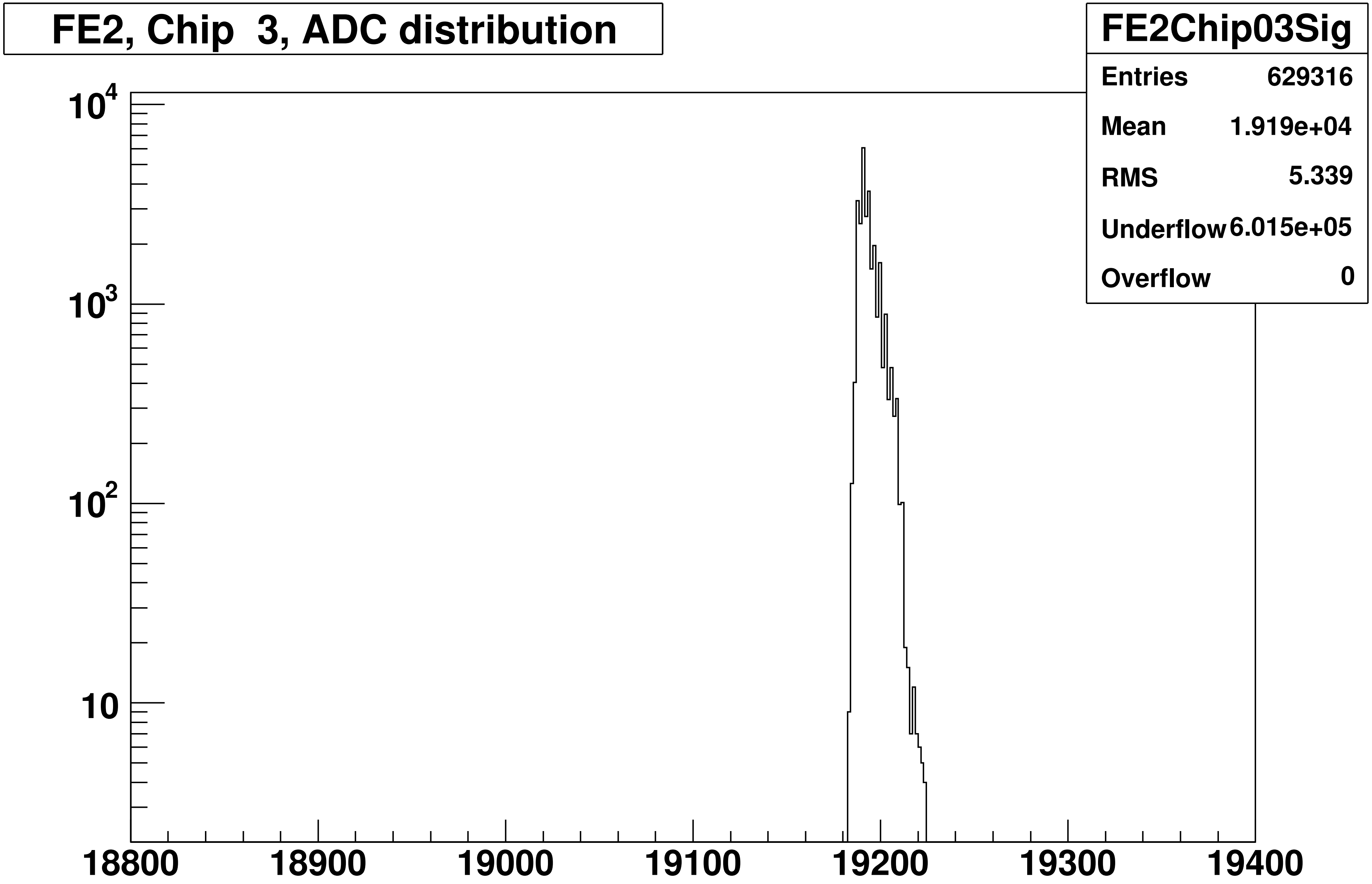


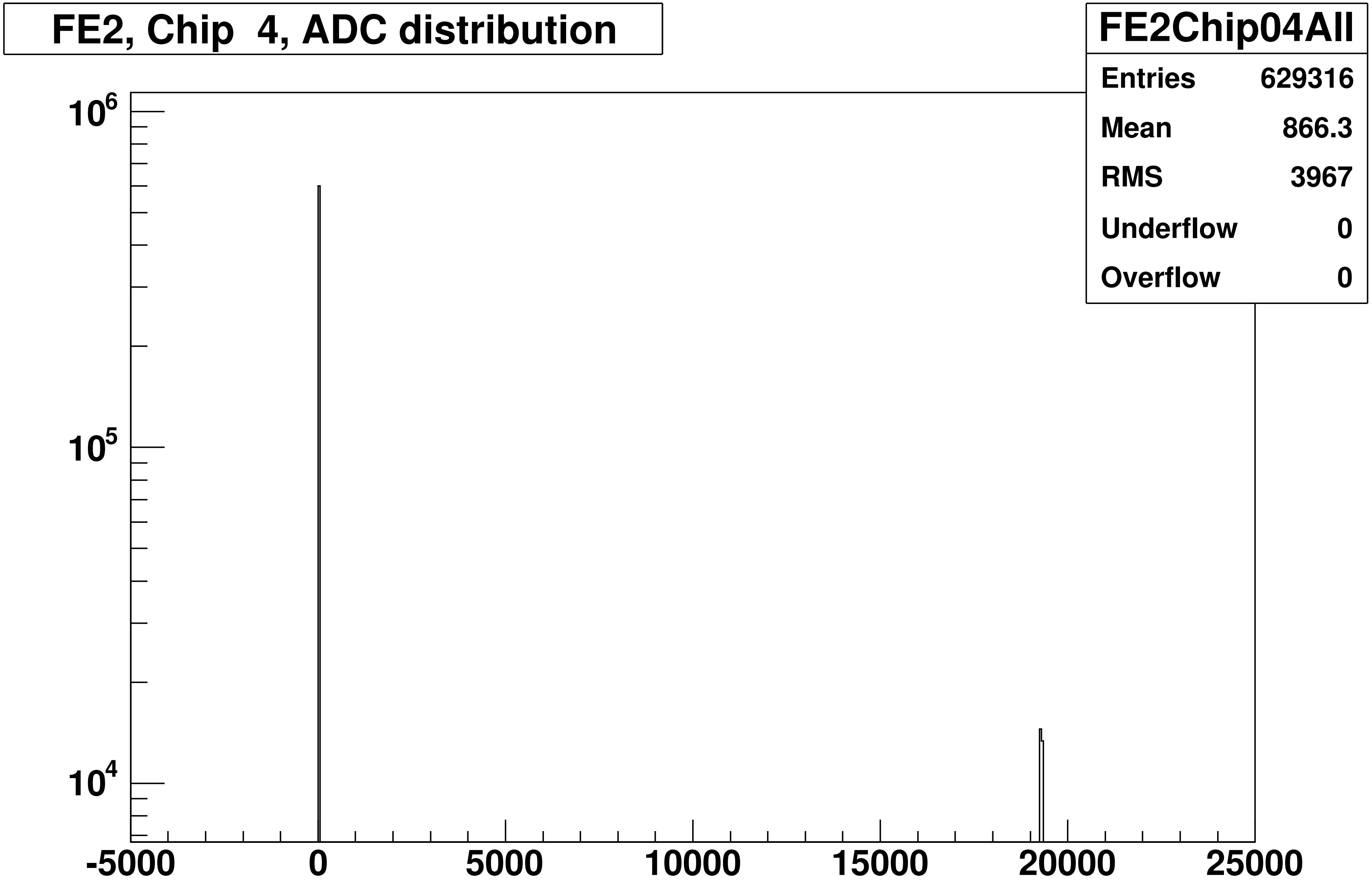
FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 629316
Mean 1.916e+04
RMS 5.258
Underflow 6.015e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

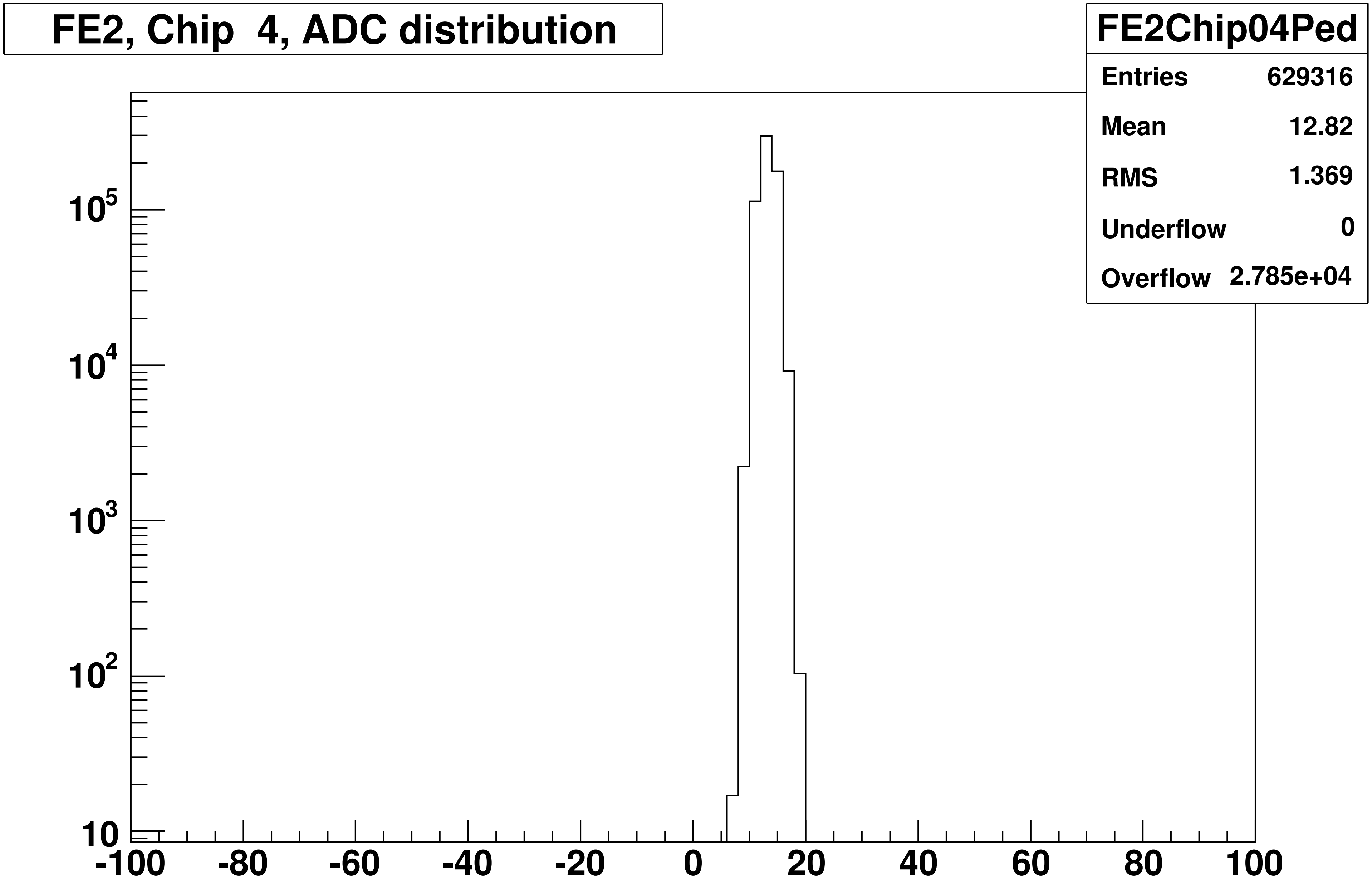

FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 629316
Mean 849
RMS 3947
Underflow 0
Overflow 0
10^5
10^4
-5000
0
5000
10000
15000
20000
25000

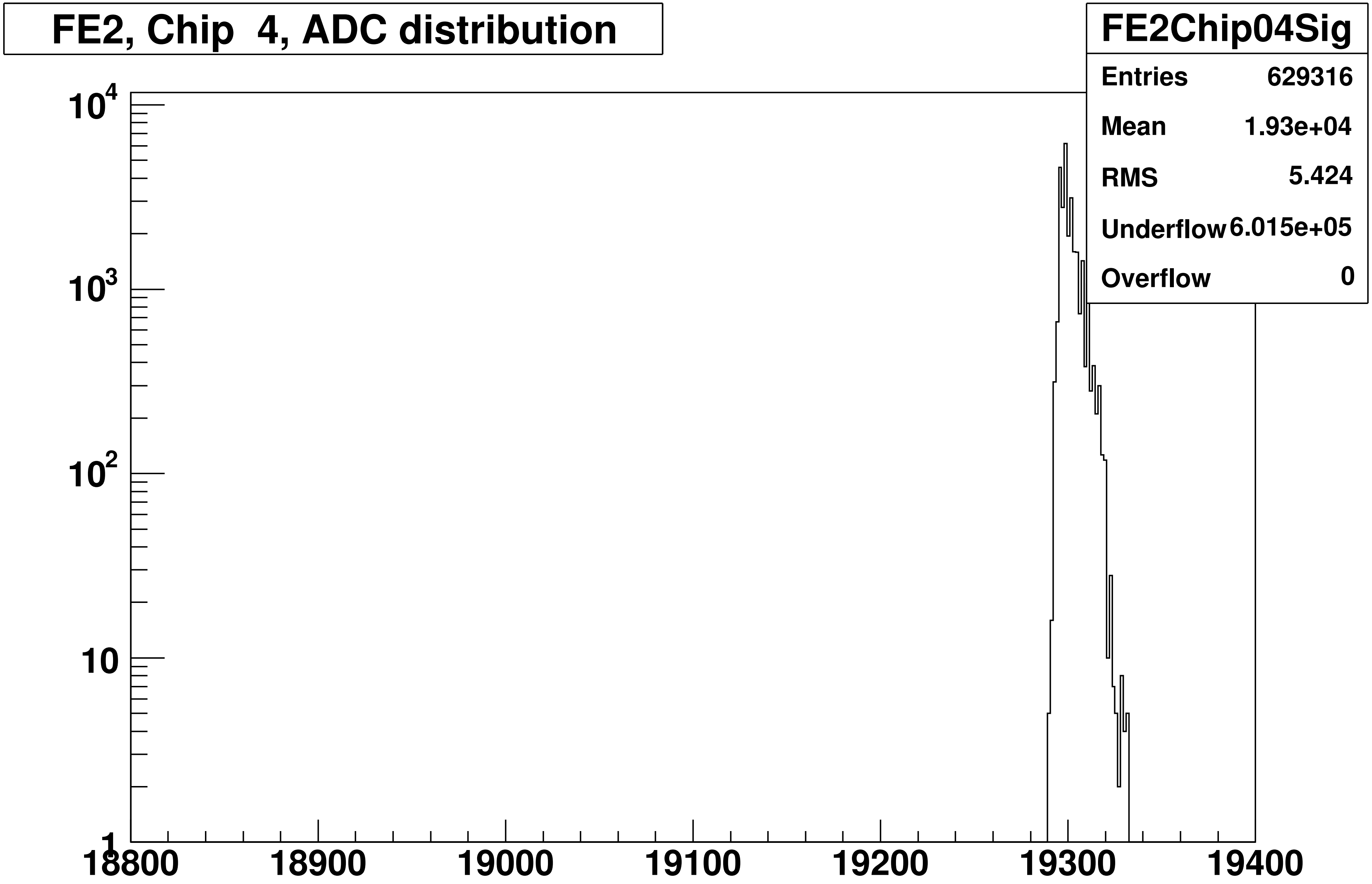


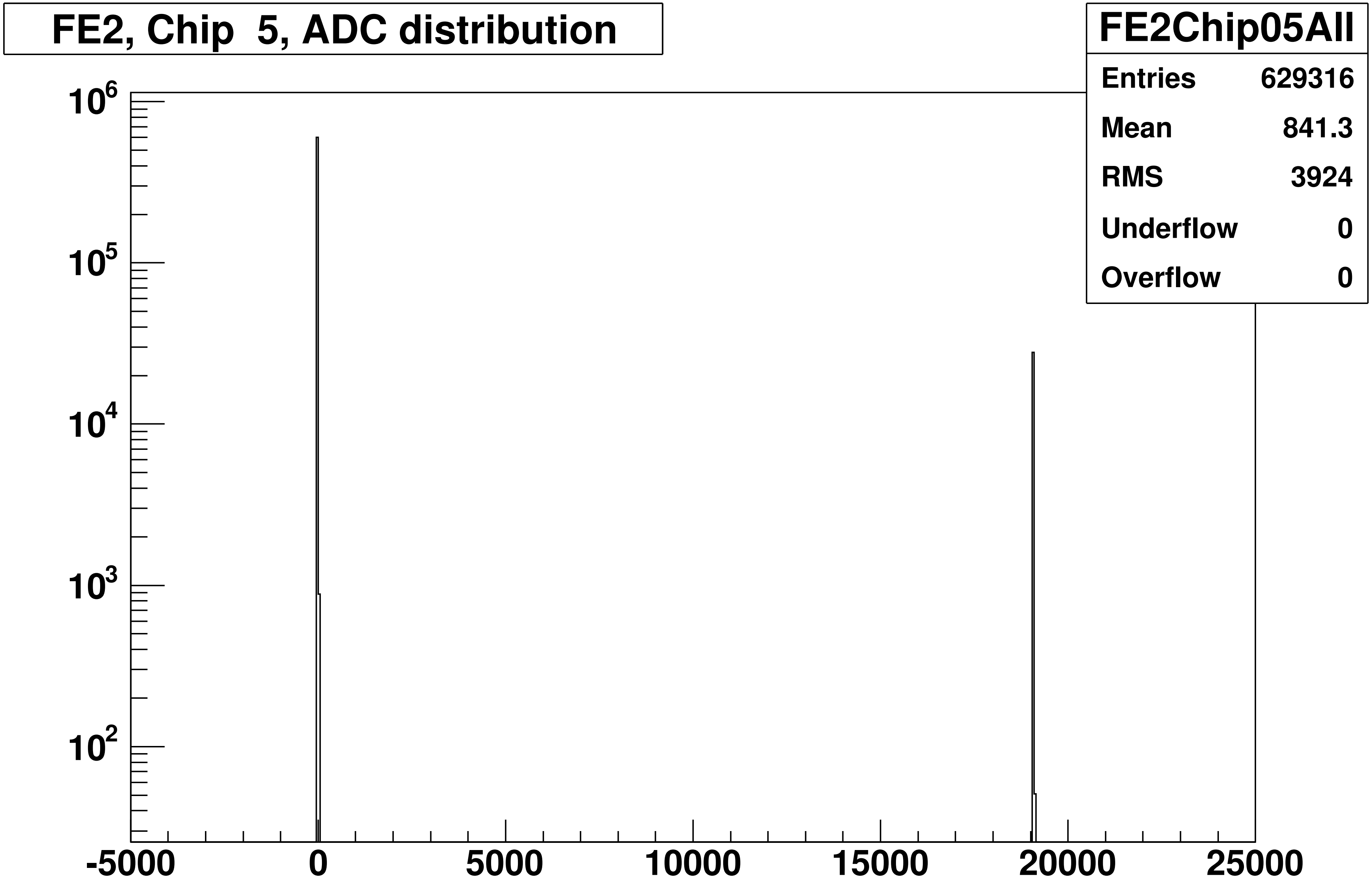
FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 629316
Mean -0.3257
RMS 1.127
Underflow 0
Overflow 2.785e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 629316
Mean 1.919e+04
RMS 5.339
Underflow 6.015e+05
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400

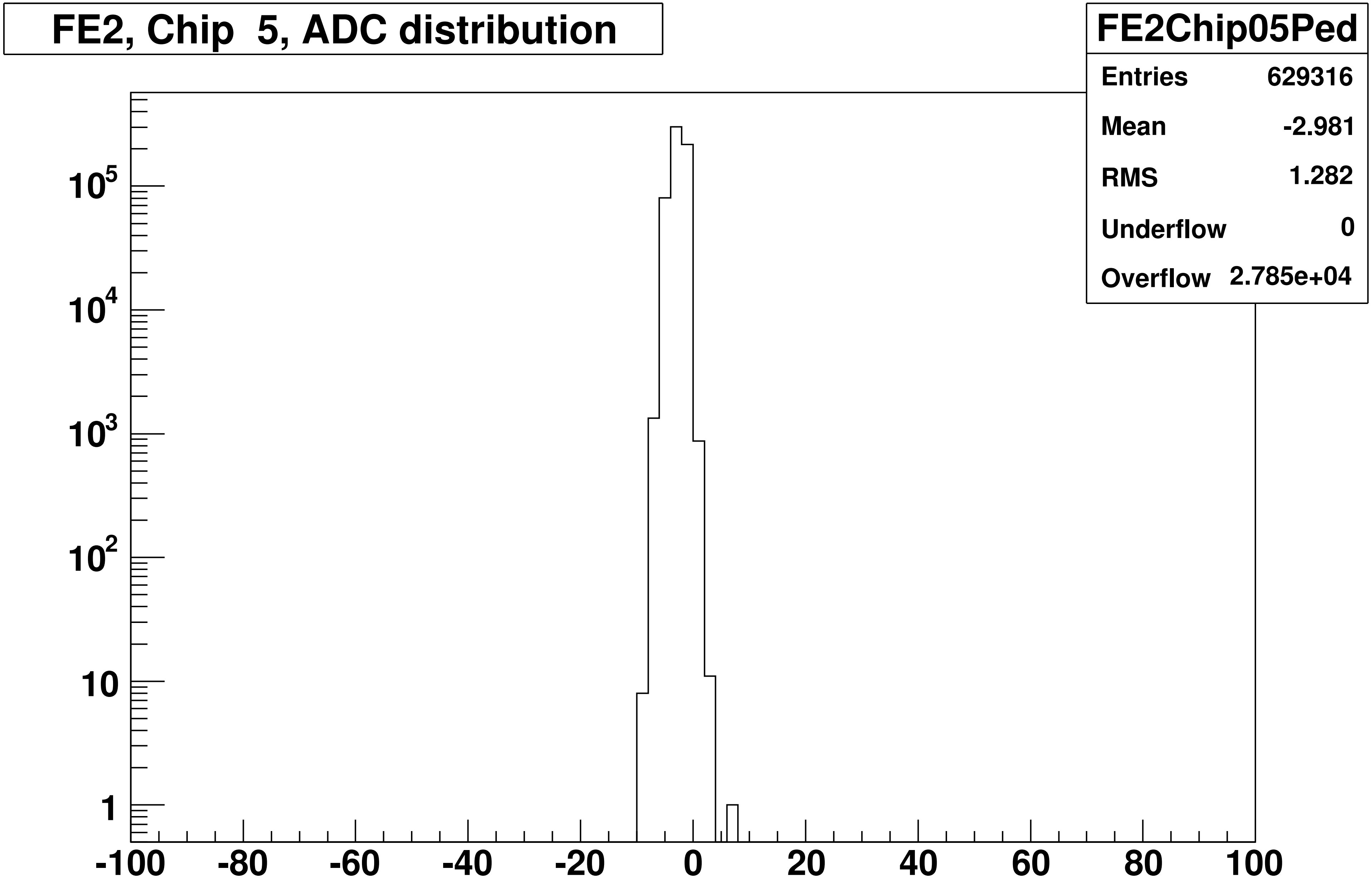

FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 629316
Mean 866.3
RMS 3967
Underflow 0
Overflow 0
10^6
10^5
10^4
-5000
0
5000
10000
15000
20000
25000

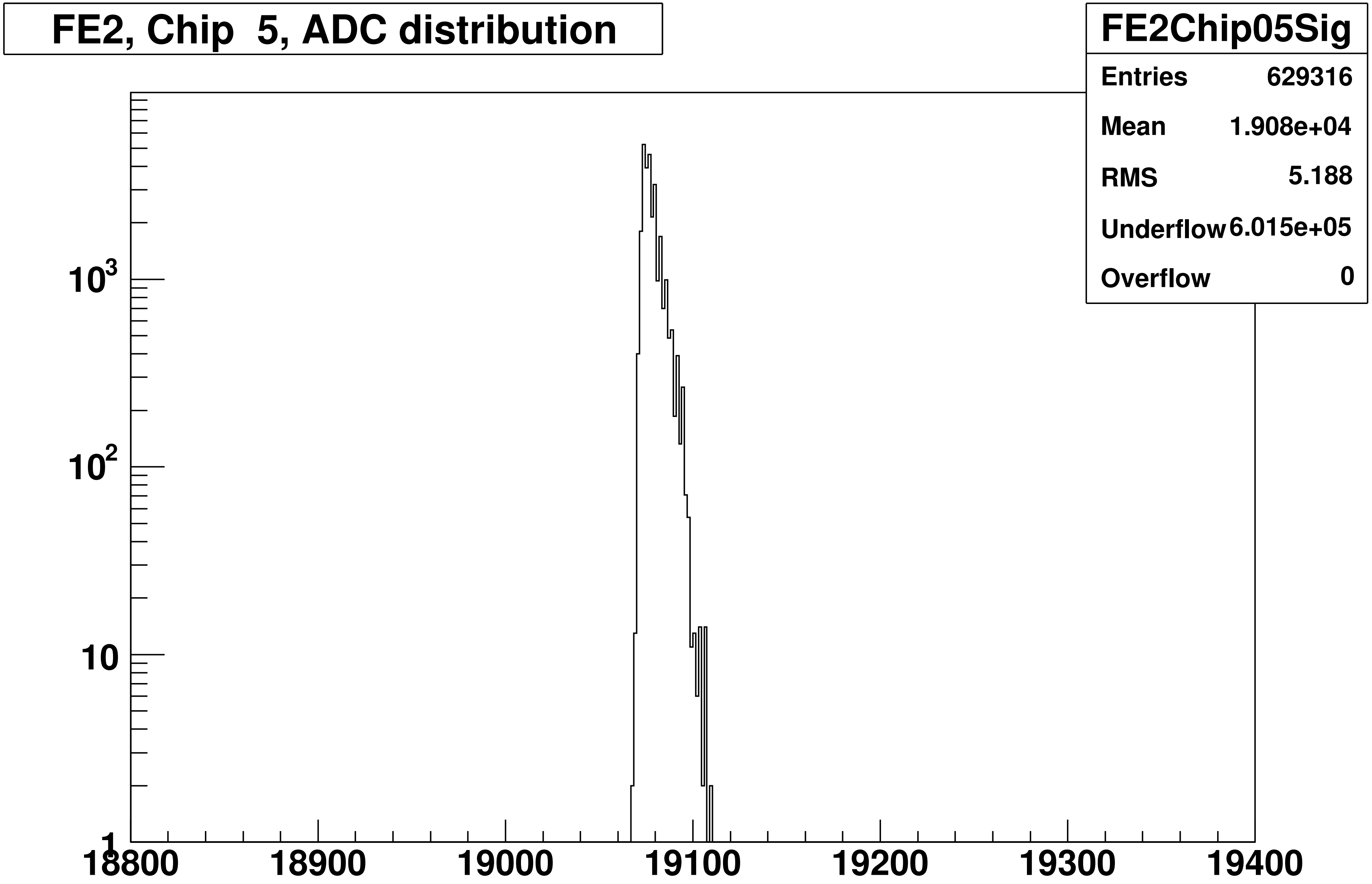

FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 629316
Mean 12.82
RMS 1.369
Underflow 0
Overflow 2.785e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 629316
Mean 1.93e+04
RMS 5.424
Underflow 6.015e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

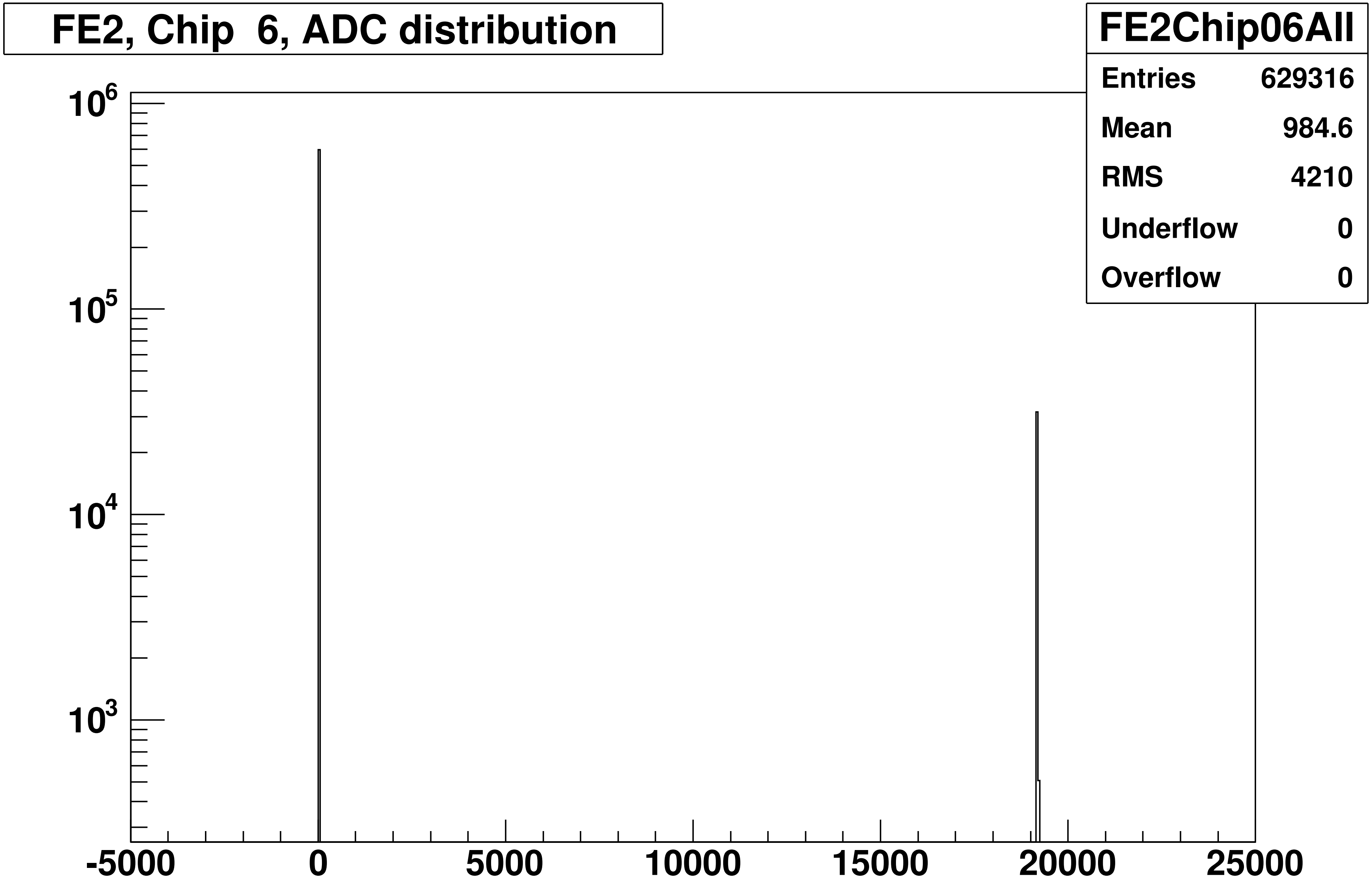


FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 629316
Mean 841.3
RMS 3924
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

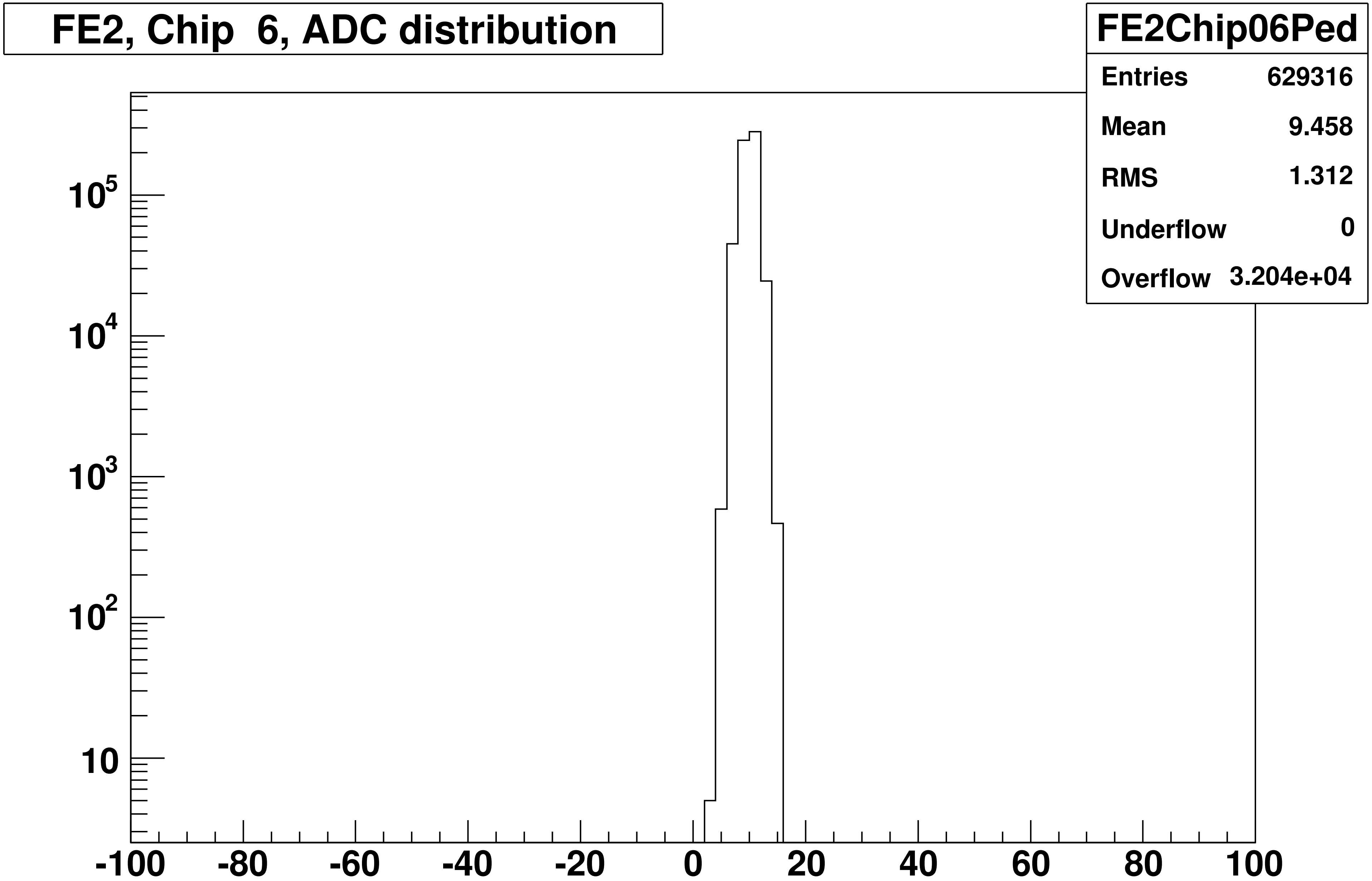

FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 629316
Mean -2.981
RMS 1.282
Underflow 0
Overflow 2.785e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

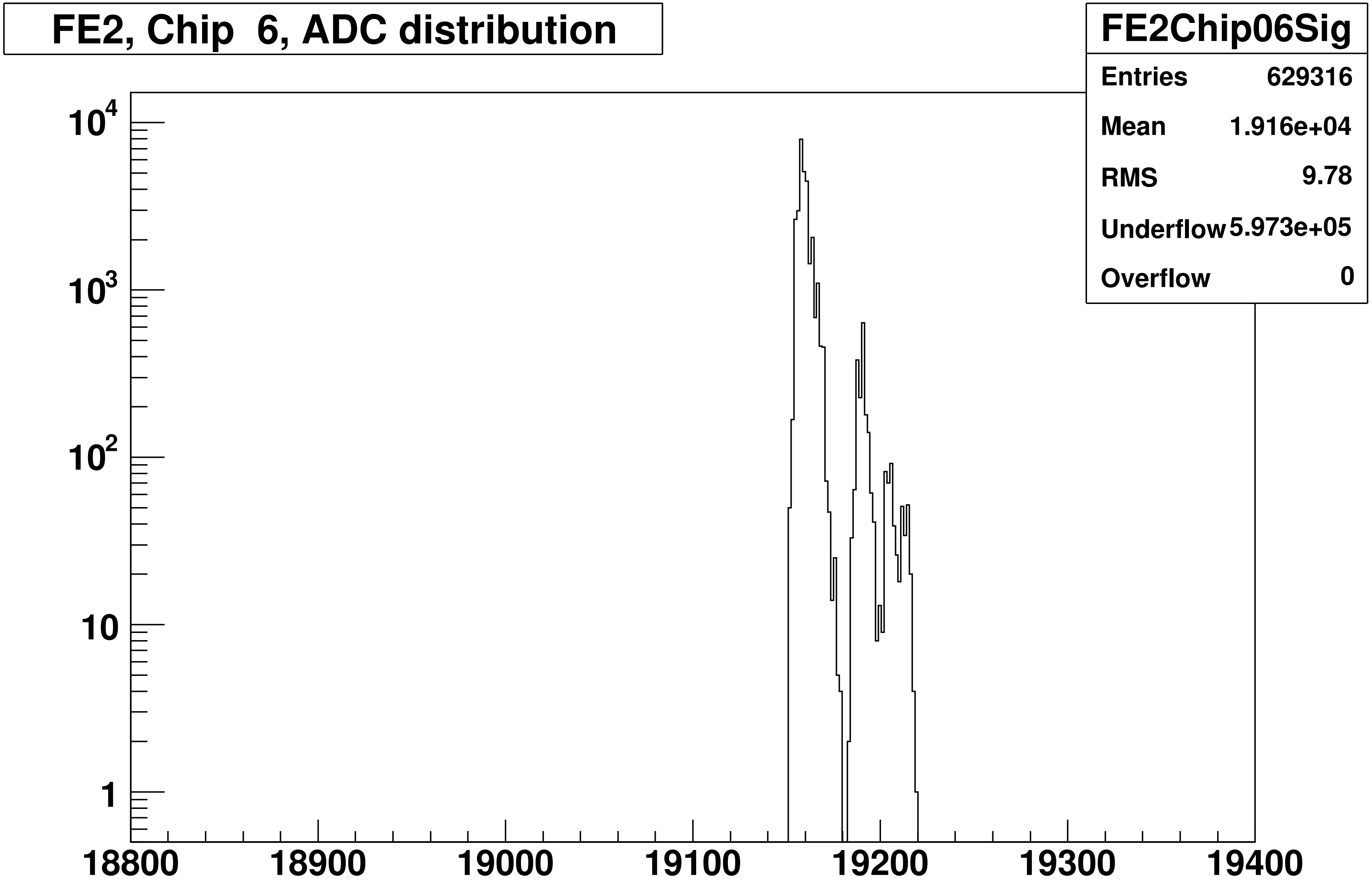


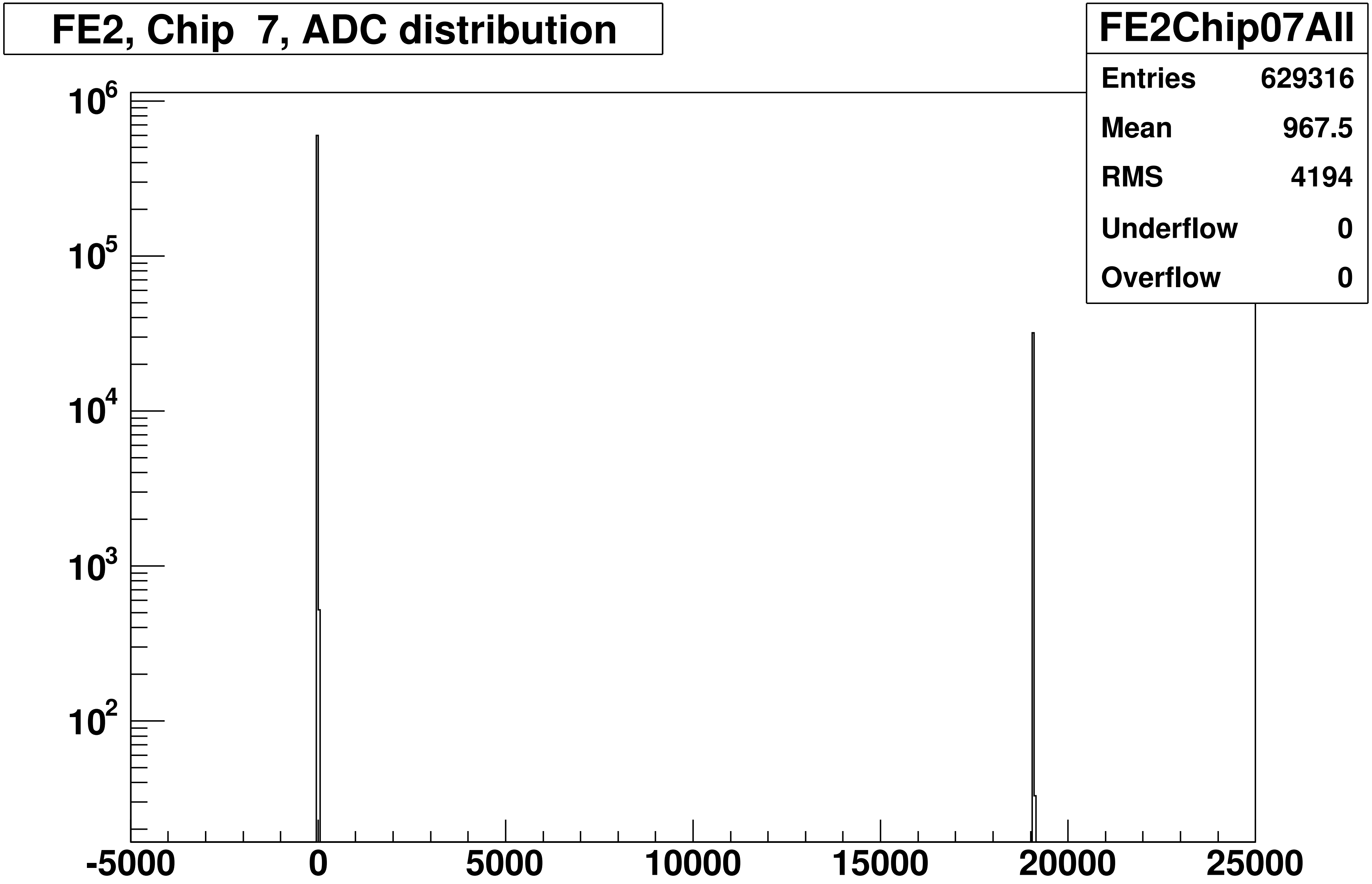
FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 629316
Mean 1.908e+04
RMS 5.188
Underflow 6.015e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

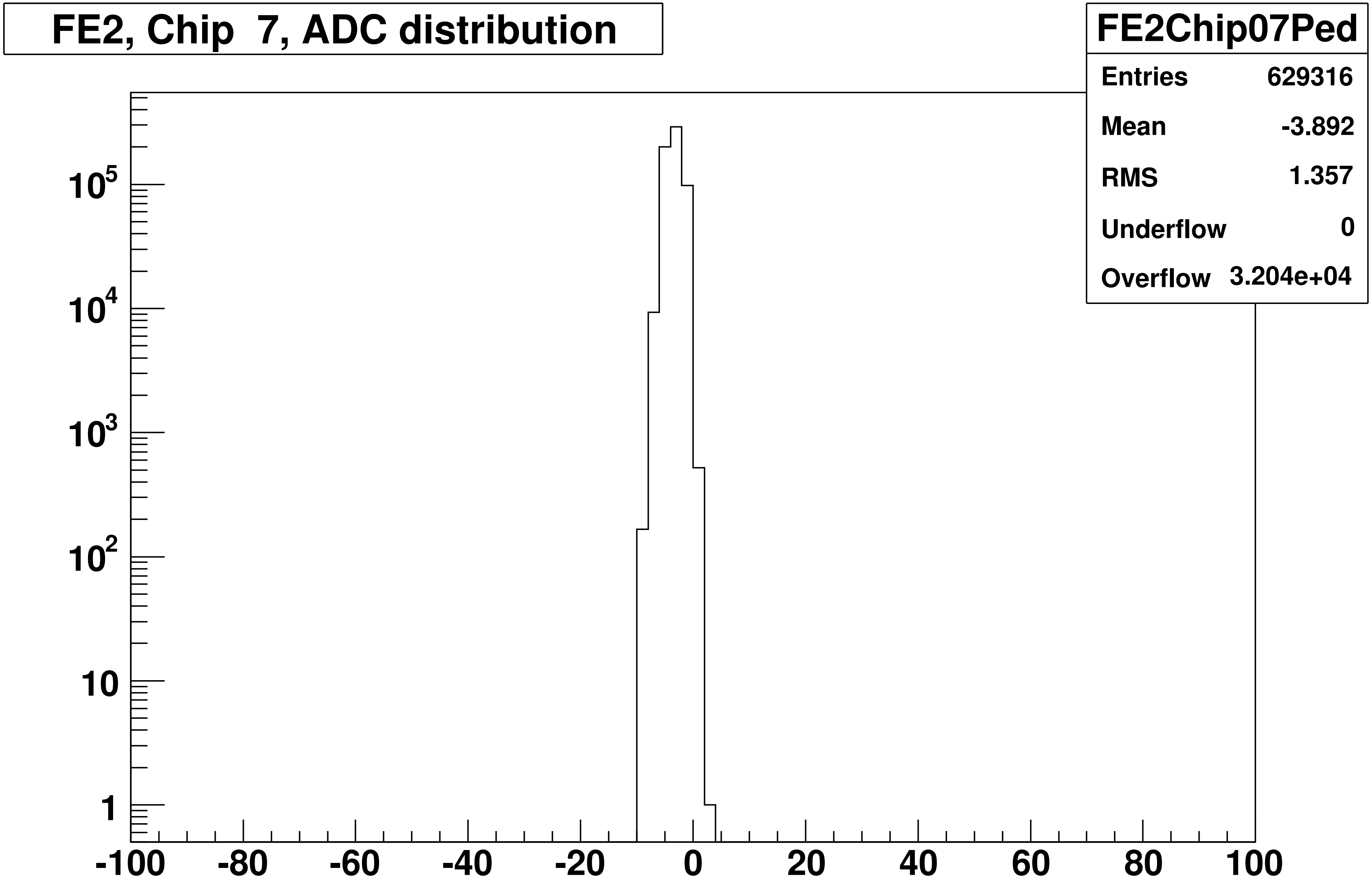


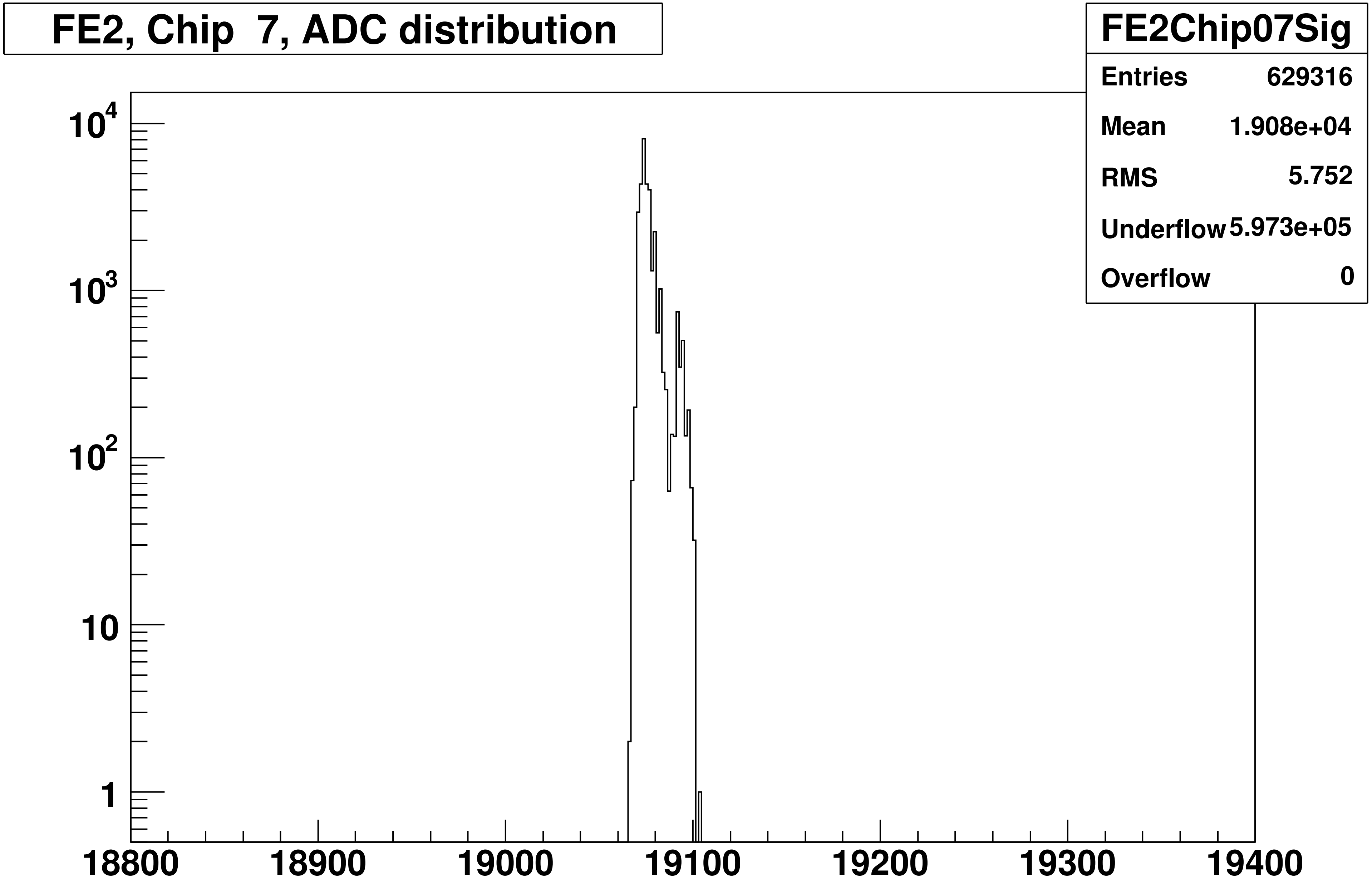
FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 629316
Mean 984.6
RMS 4210
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

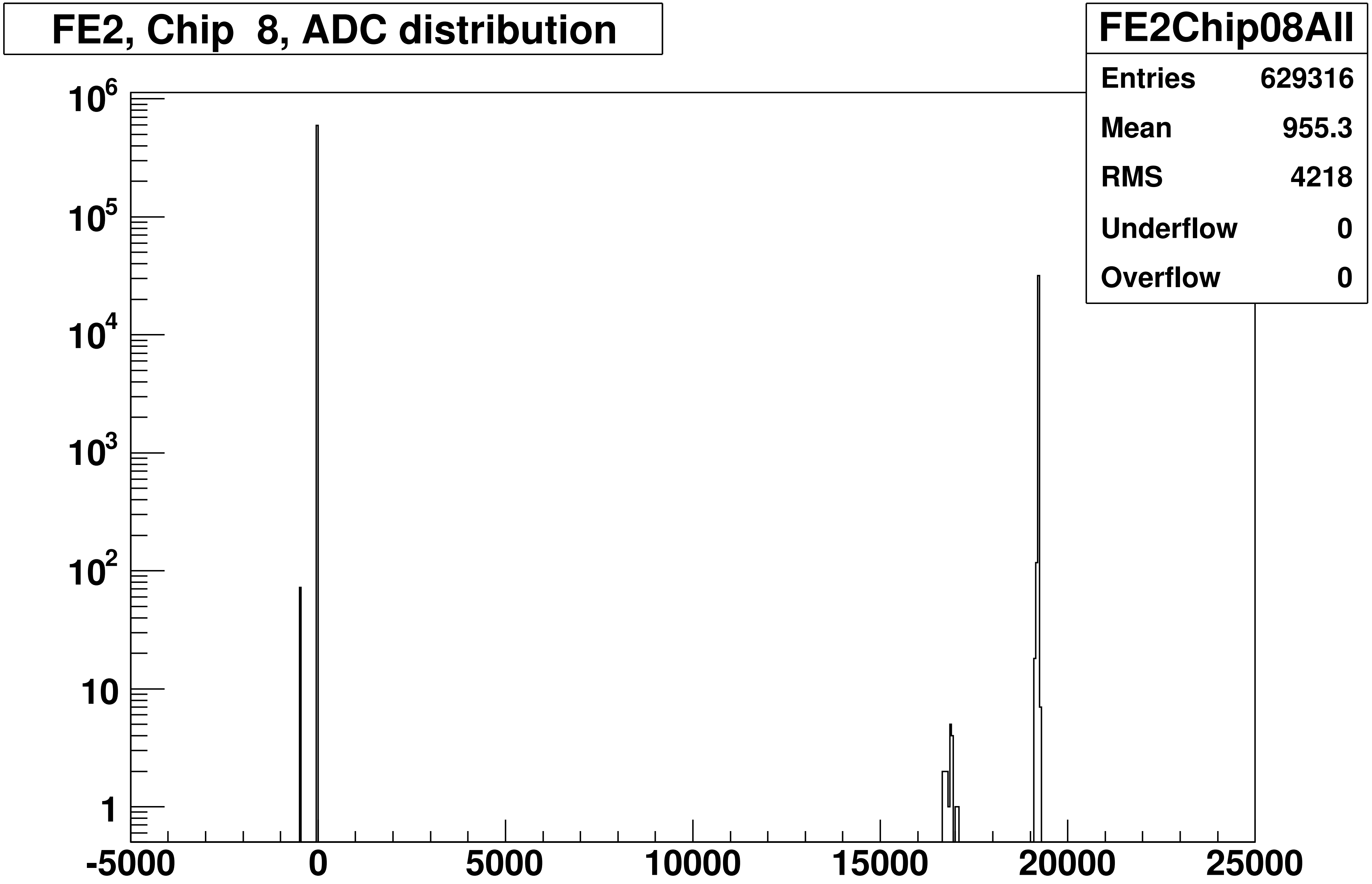

FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 629316
Mean 9.458
RMS 1.312
Underflow 0
Overflow 3.204e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

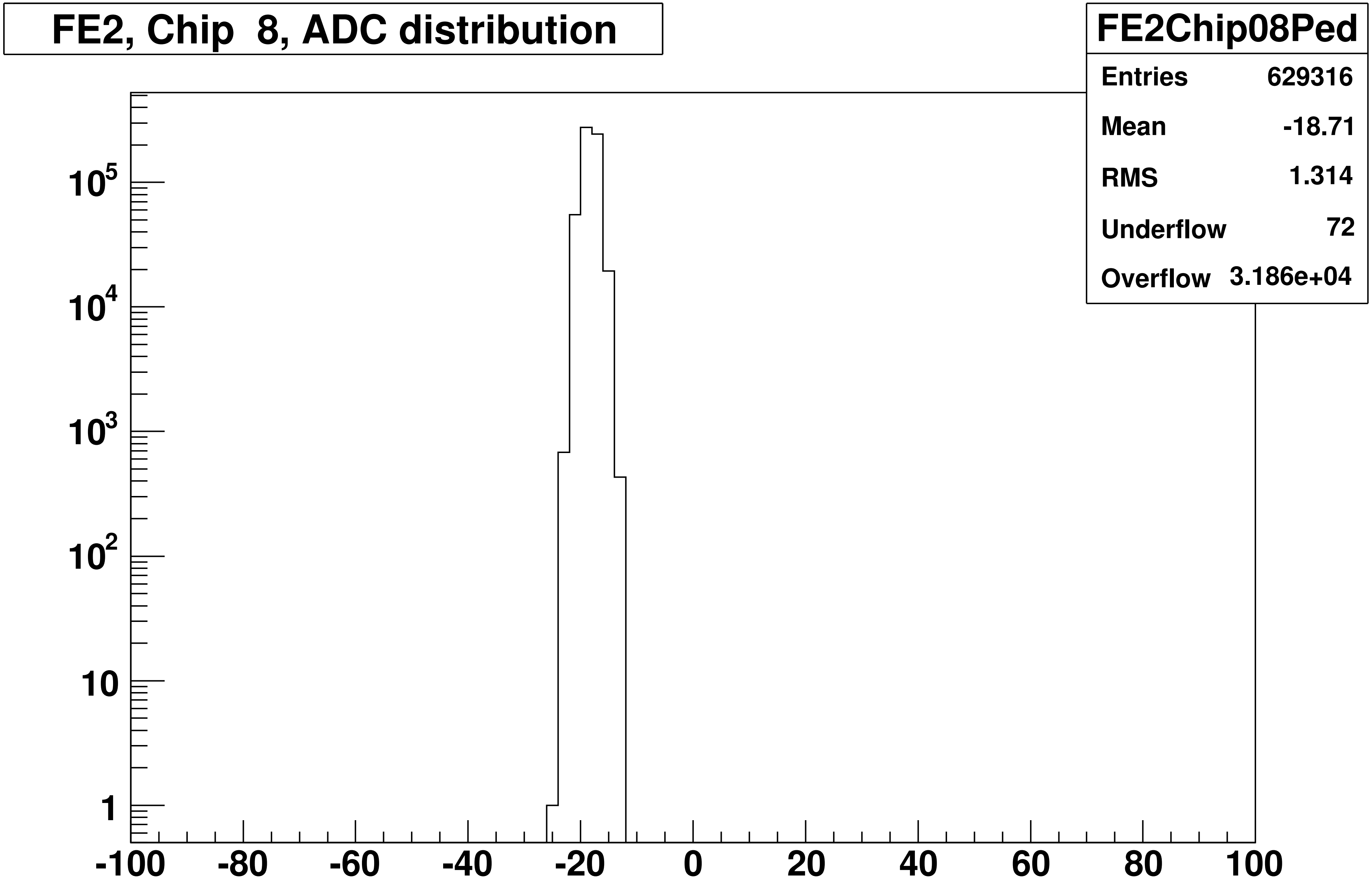

FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 629316
Mean 1.916e+04
RMS 9.78
Underflow 5.973e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

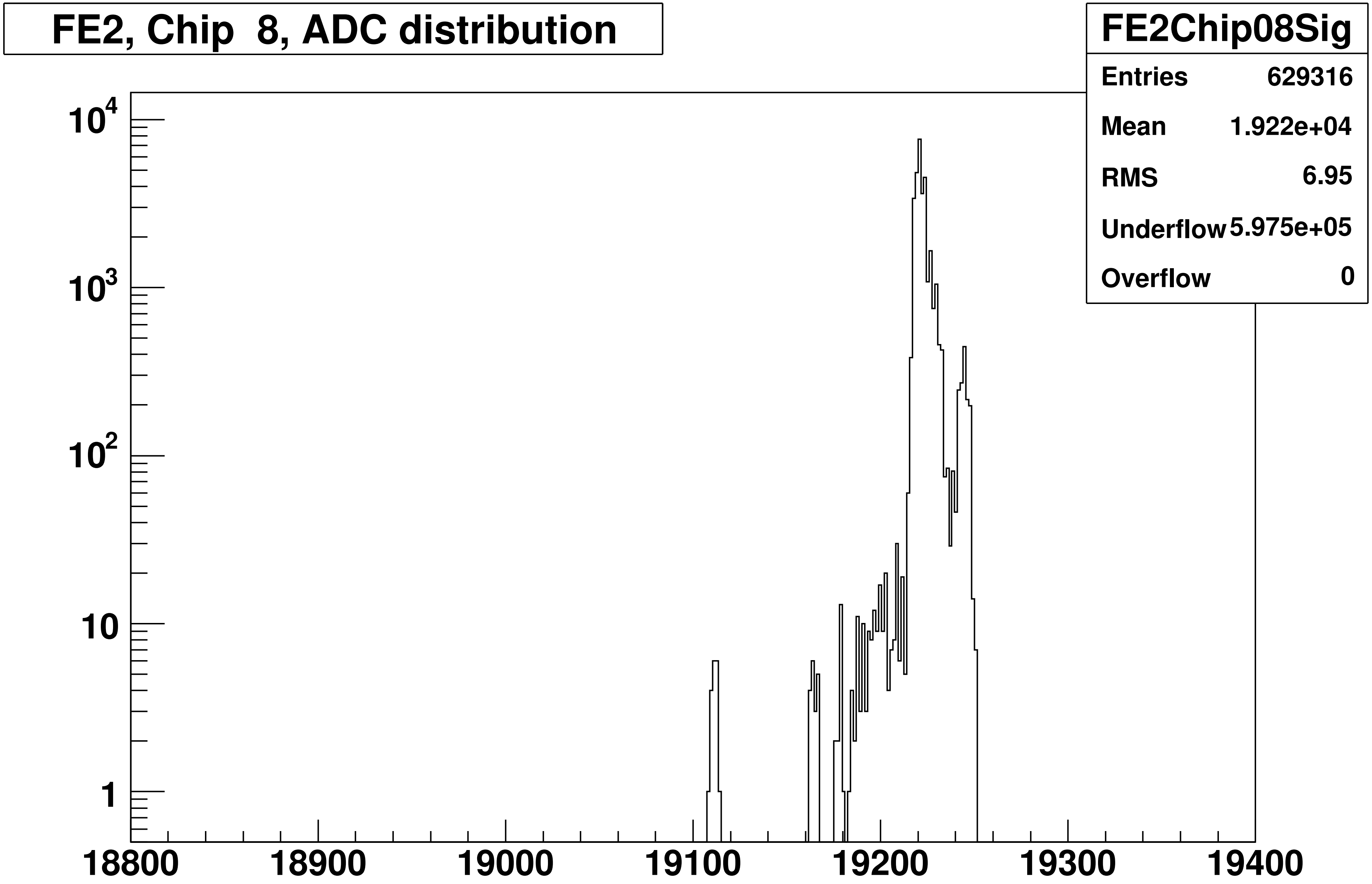

FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 629316
Mean 967.5
RMS 4194
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

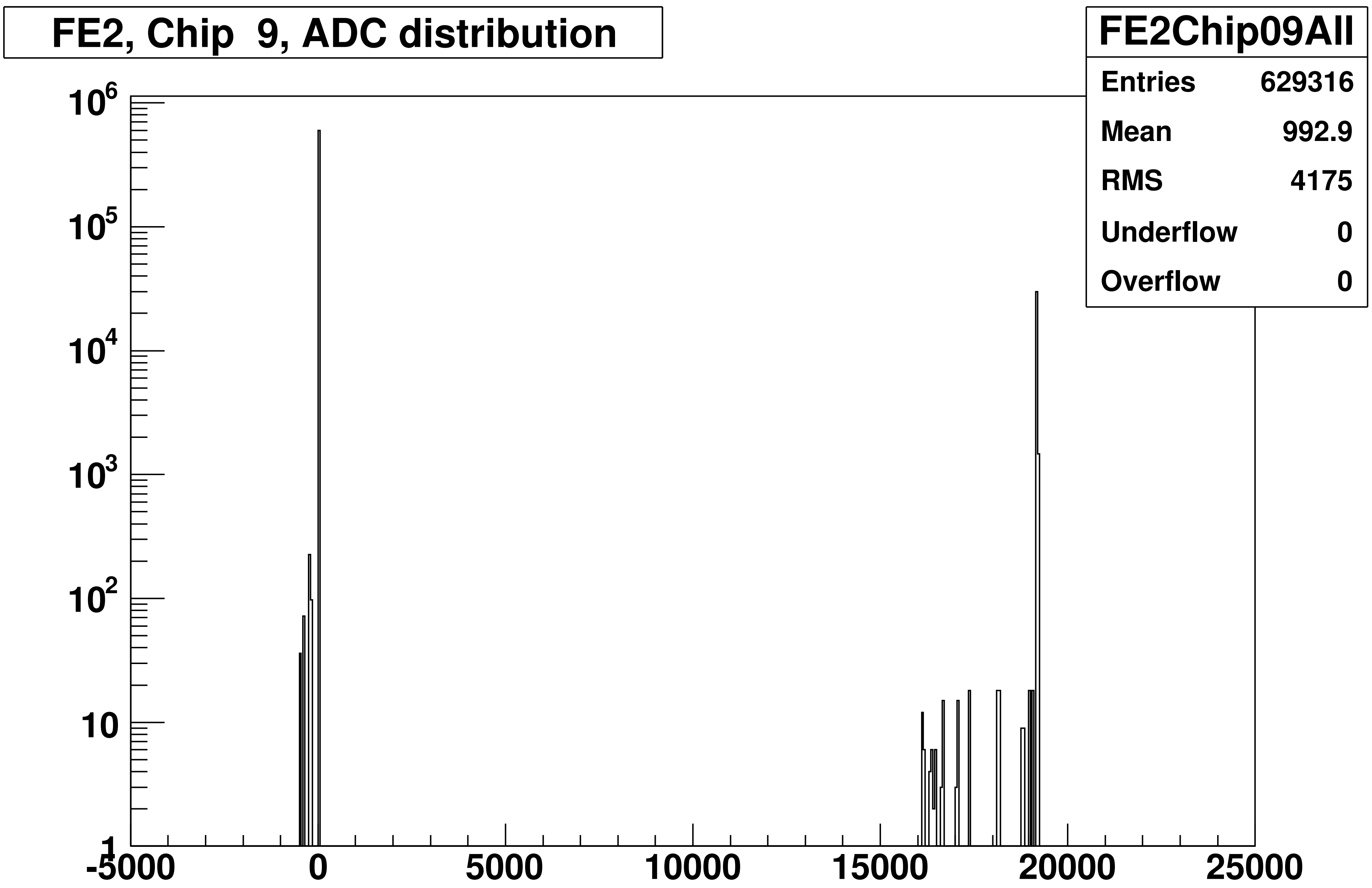

FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 629316
Mean -3.892
RMS 1.357
Underflow 0
Overflow 3.204e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

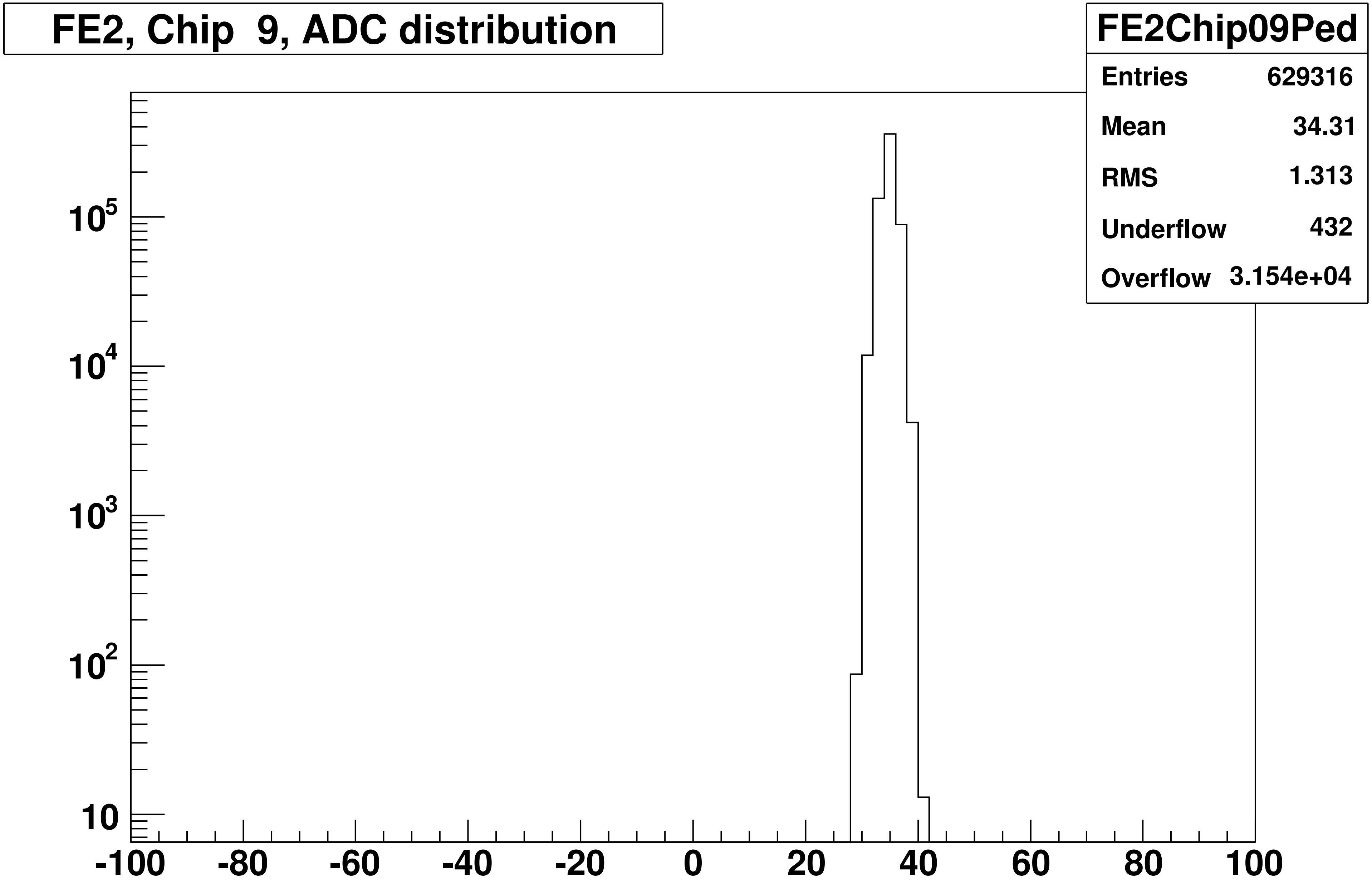

FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 629316
Mean 1.908e+04
RMS 5.752
Underflow 5.973e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 629316
Mean 955.3
RMS 4218
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

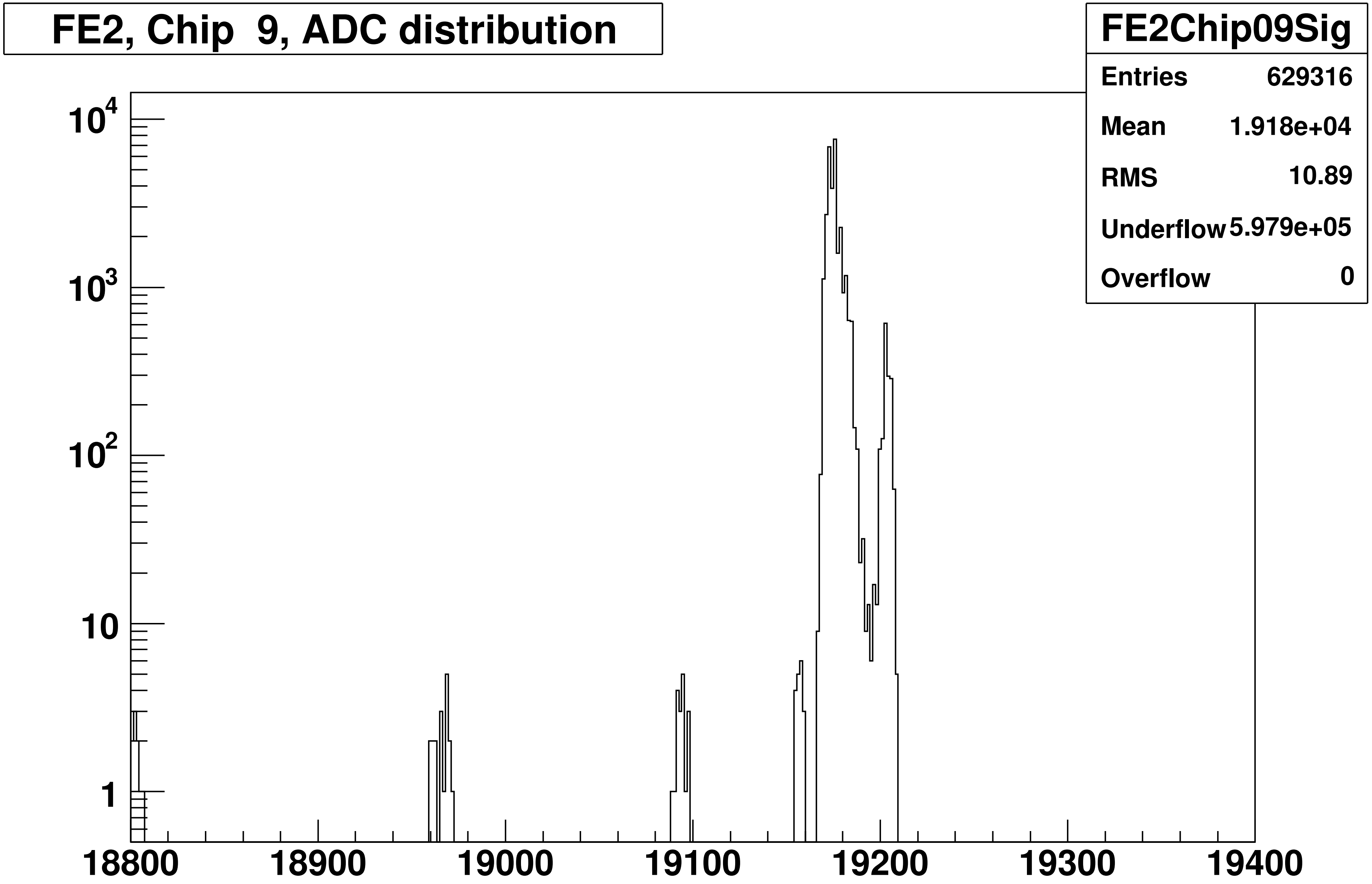

FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 629316
Mean -18.71
RMS 1.314
Underflow 72
Overflow 3.186e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

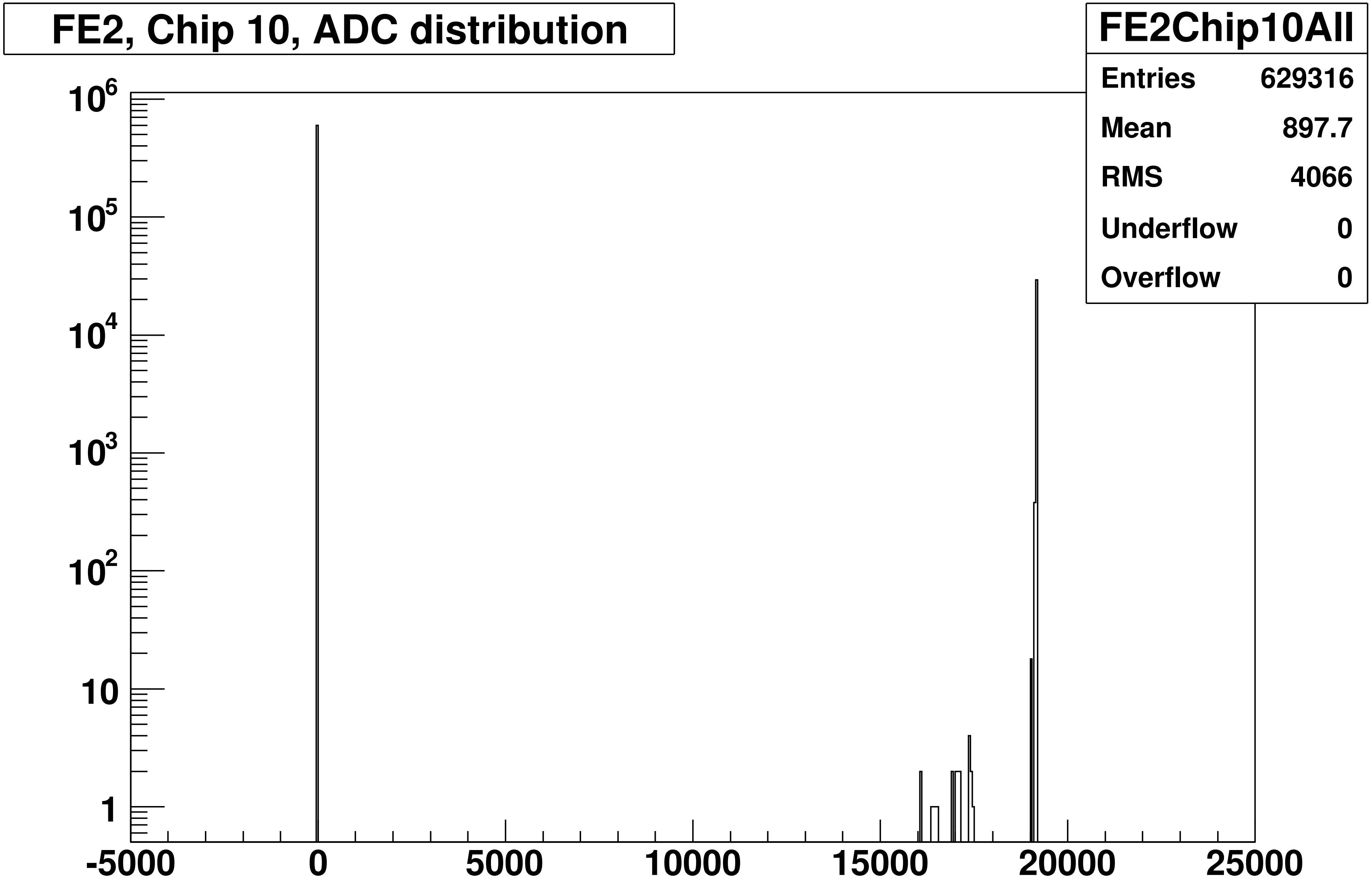

FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 629316
Mean 1.922e+04
RMS 6.95
Underflow 5.975e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

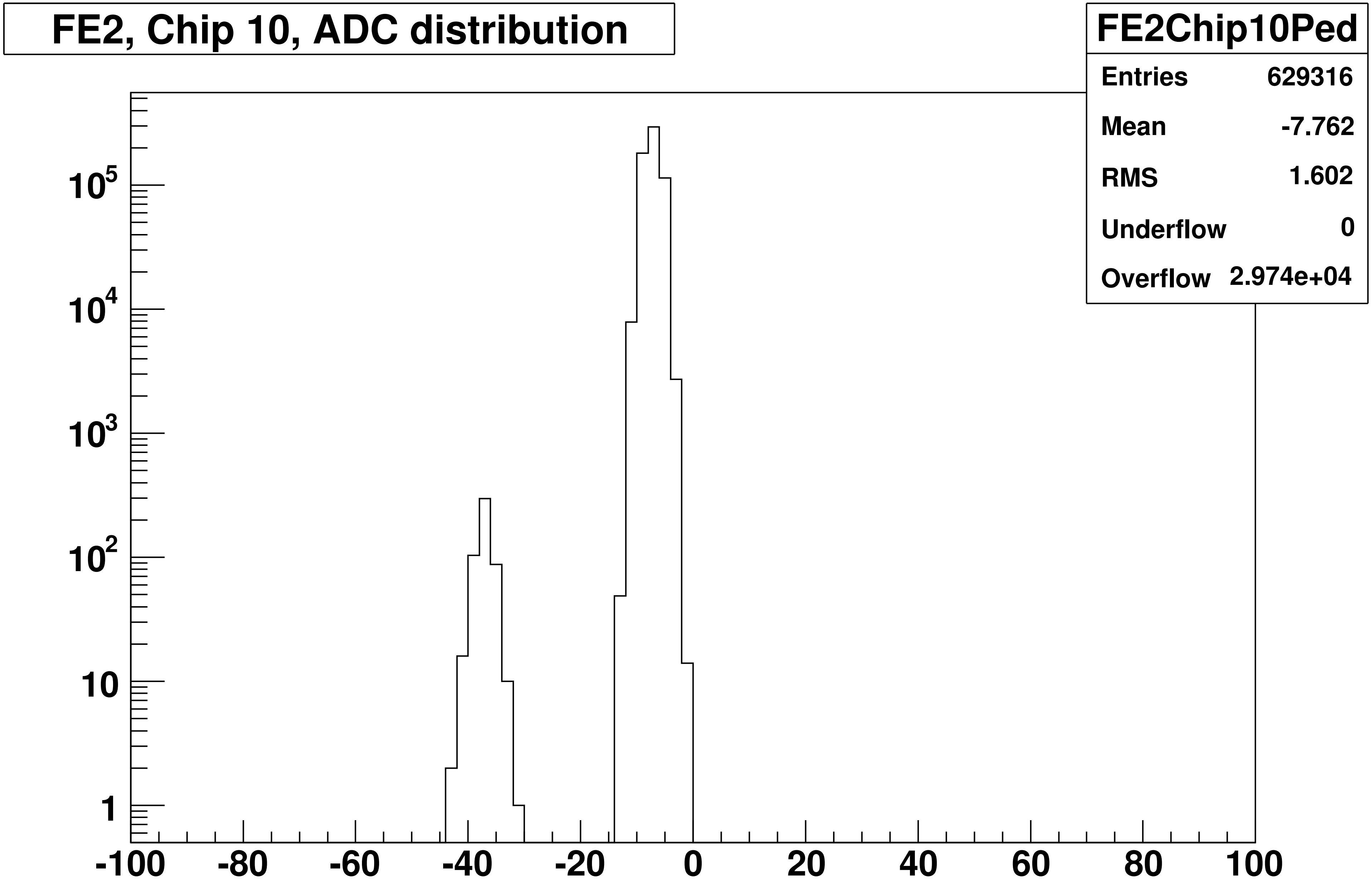

FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 629316
Mean 992.9
RMS 4175
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



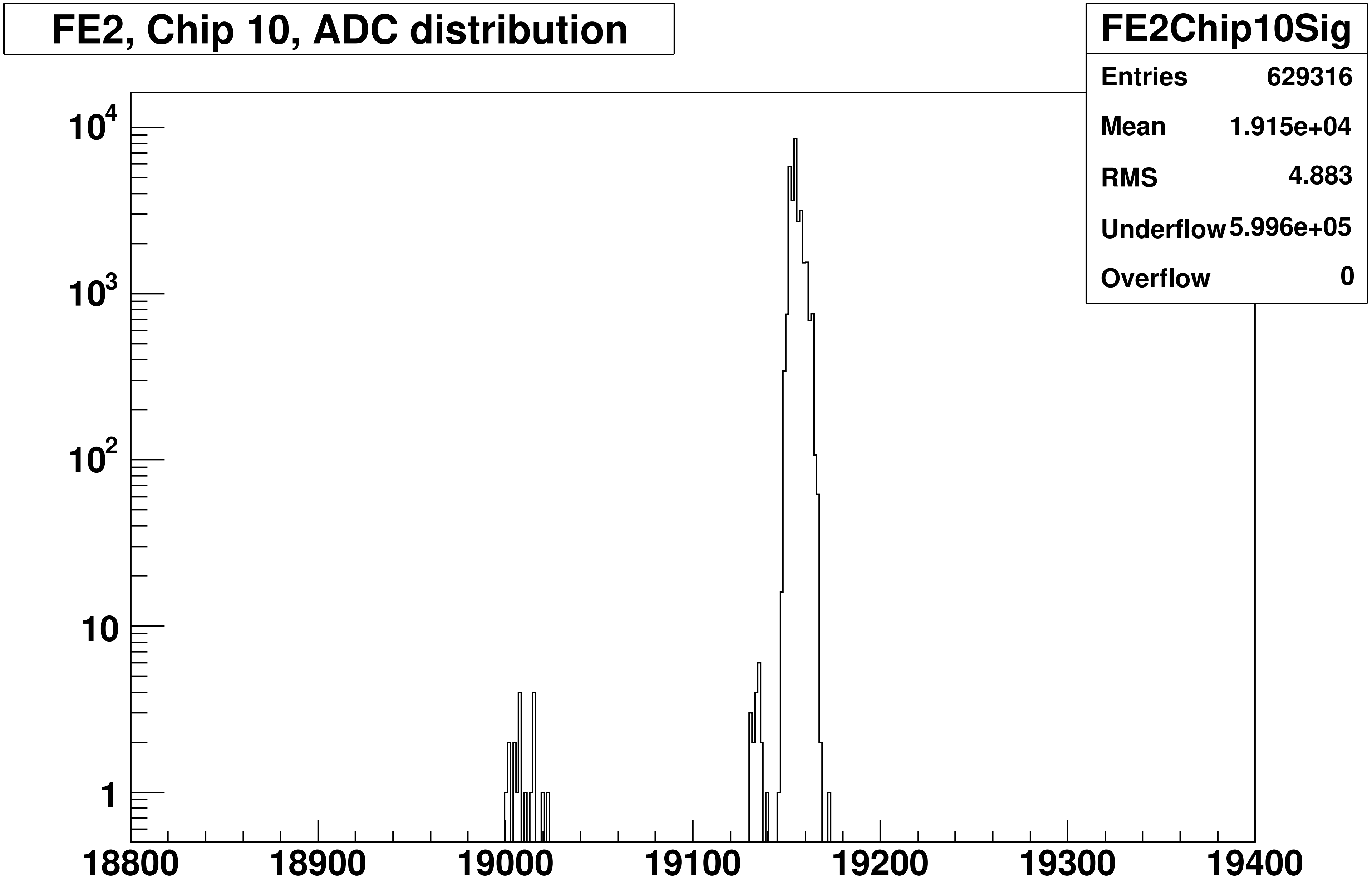
FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 629316
Mean 34.31
RMS 1.313
Underflow 432
Overflow 3.154e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

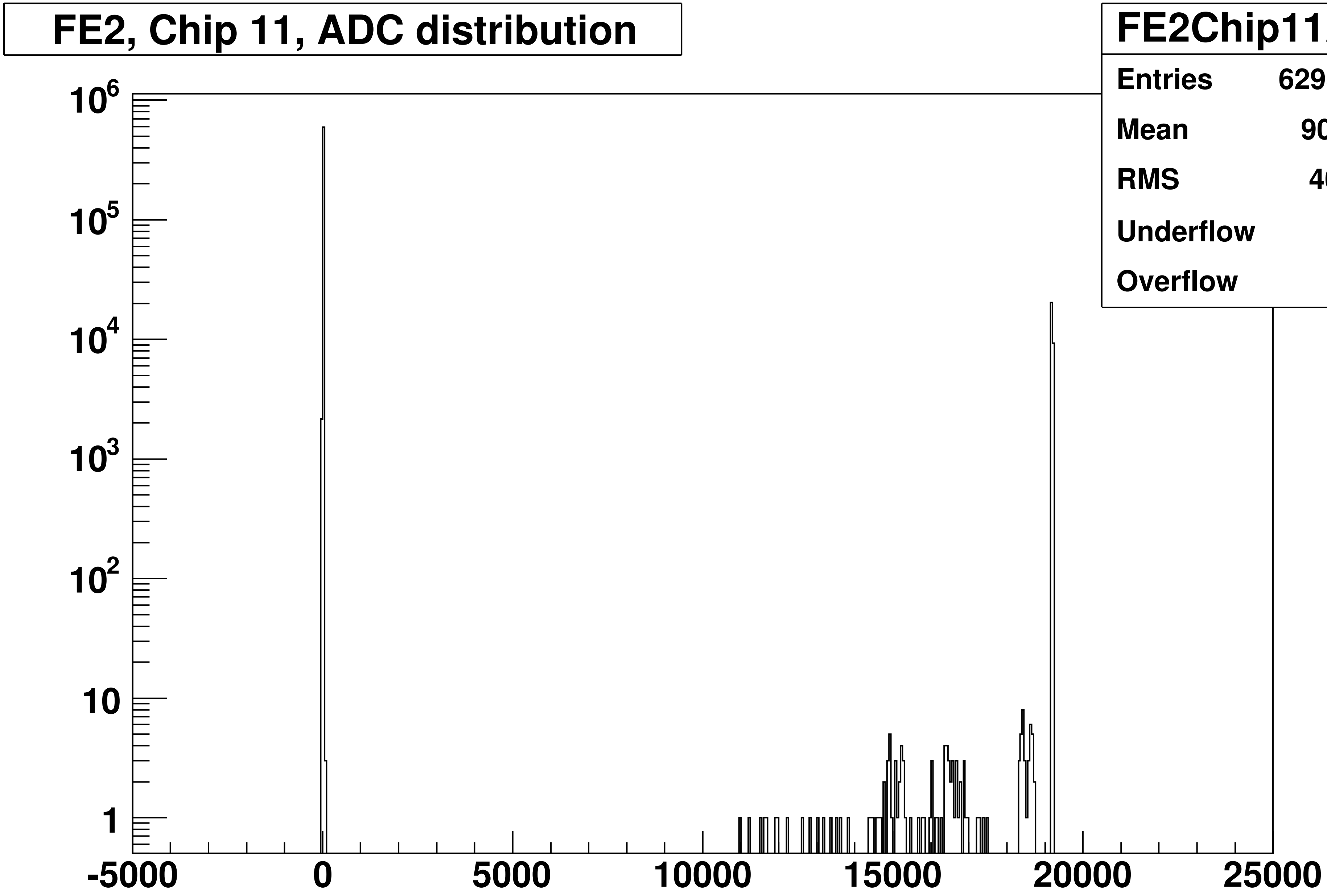

FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 629316
Mean 1.918e+04
RMS 10.89
Underflow 5.979e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 629316
Mean 897.7
RMS 4066
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

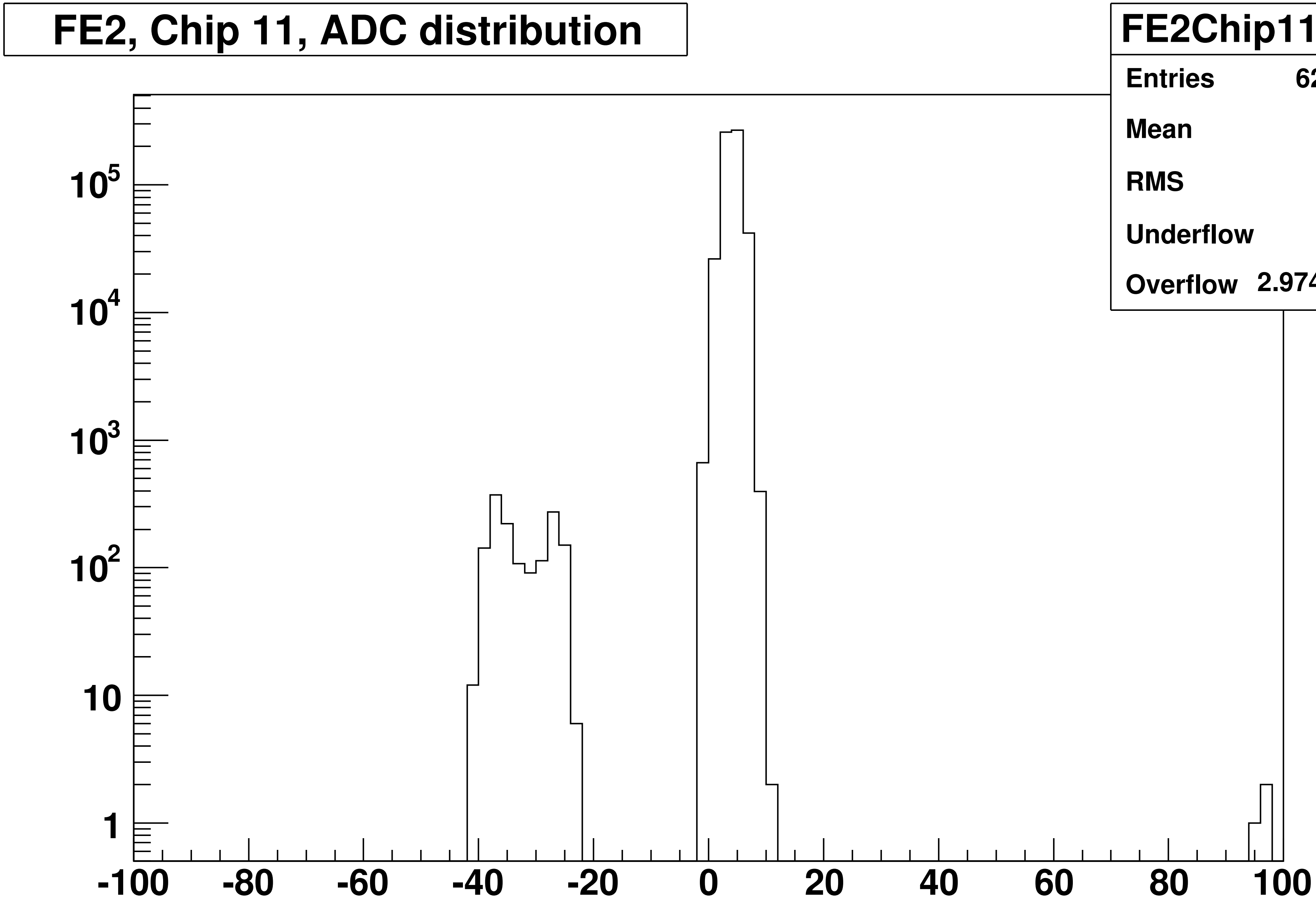


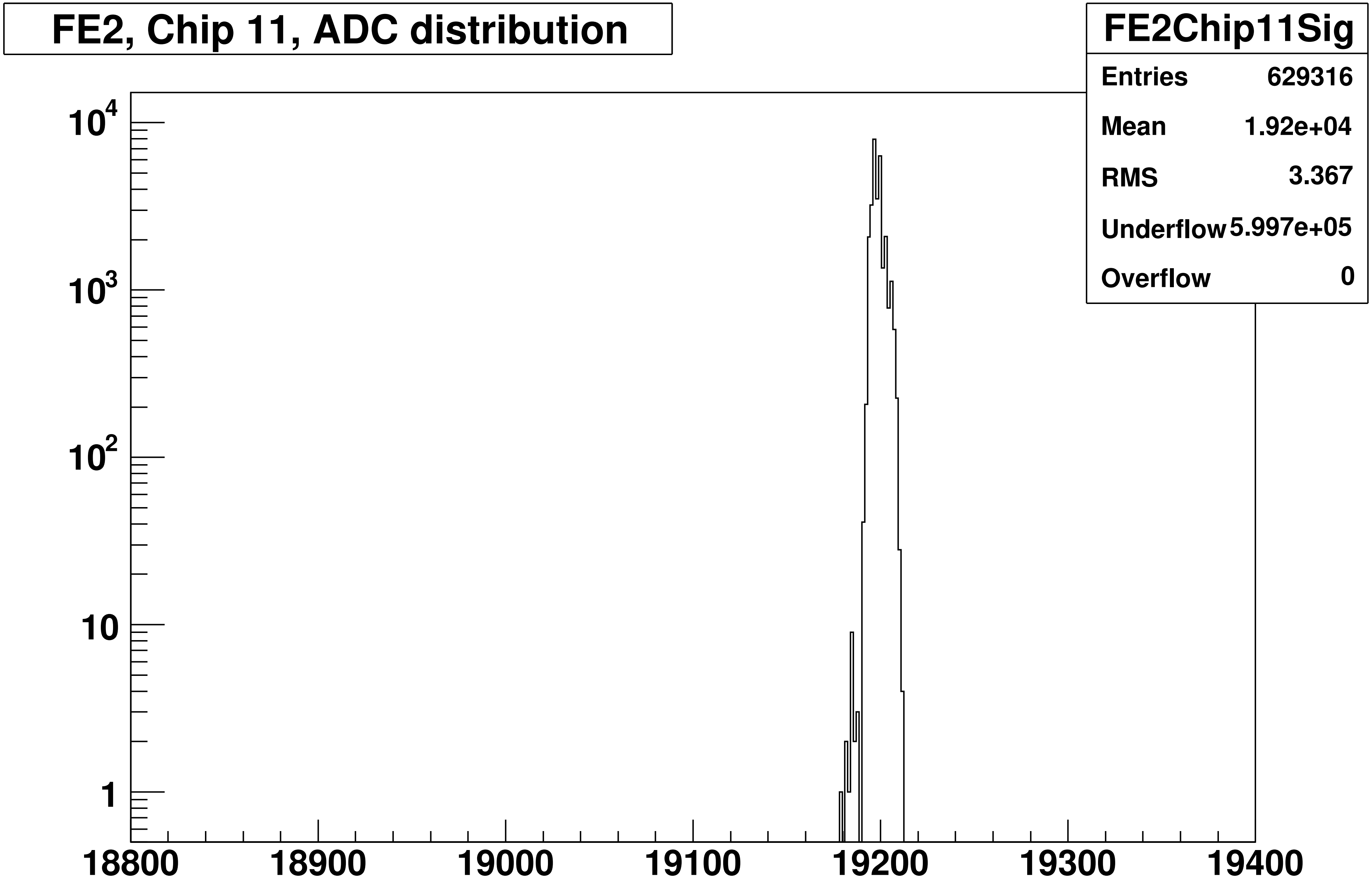
FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 629316
Mean -7.762
RMS 1.602
Underflow 0
Overflow 2.974e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

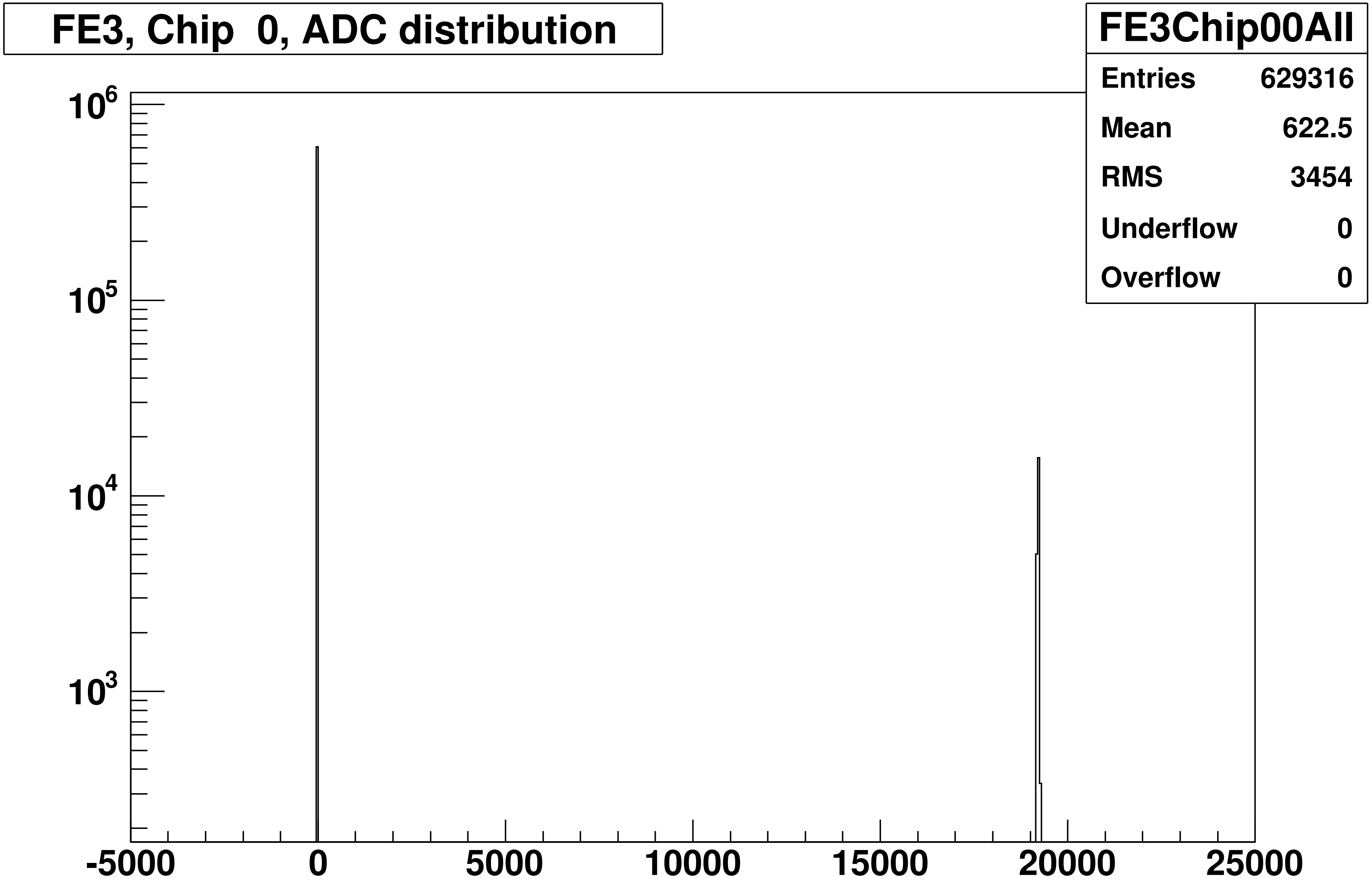

FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 629316
Mean 1.915e+04
RMS 4.883
Underflow 5.996e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



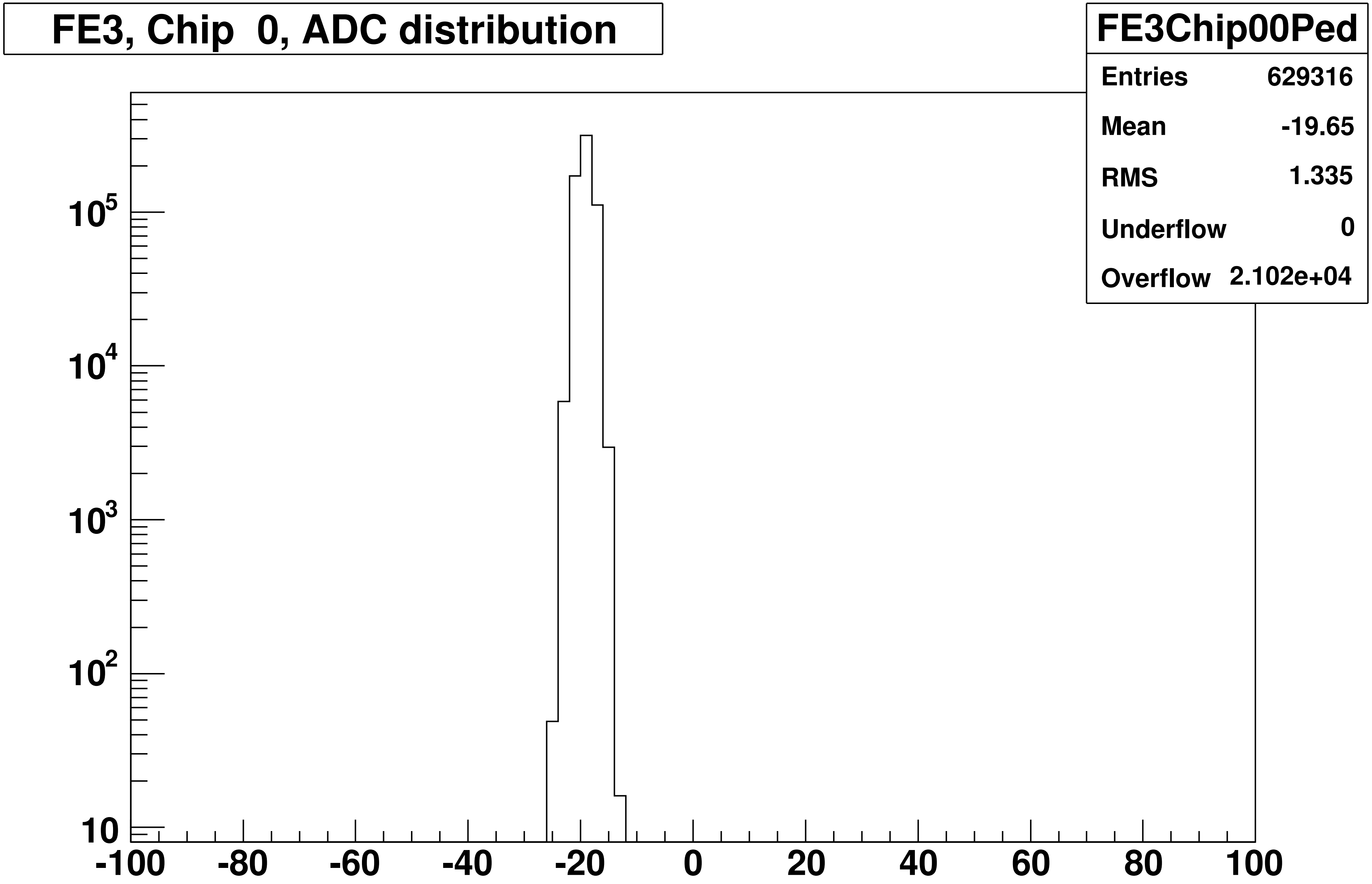
FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 629316
Mean 909.9
RMS 4070
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 629316
Mean 3.507
RMS 2.272
Underflow 0
Overflow 2.974e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

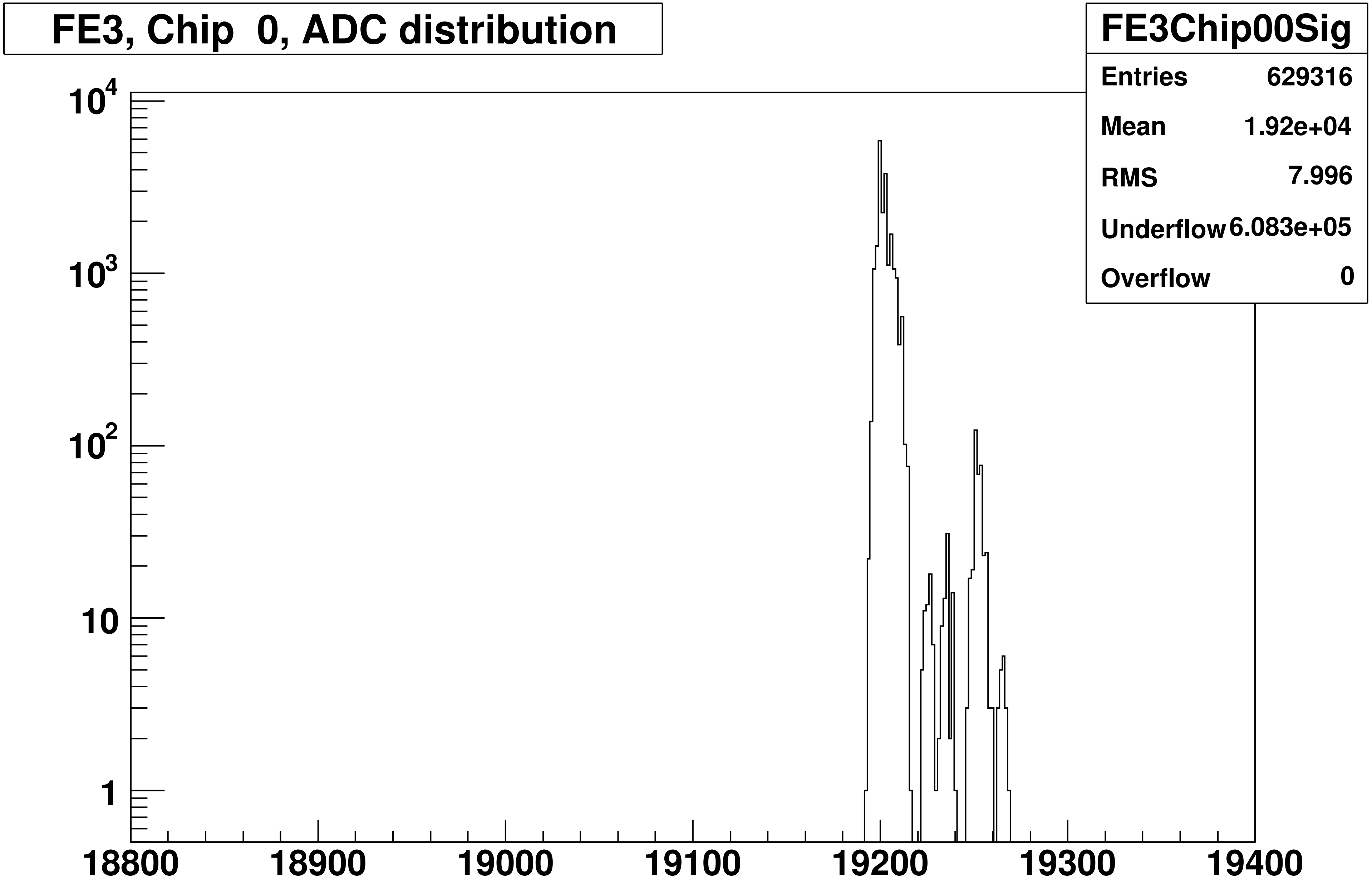

FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 629316
Mean 1.92e+04
RMS 3.367
Underflow 5.997e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

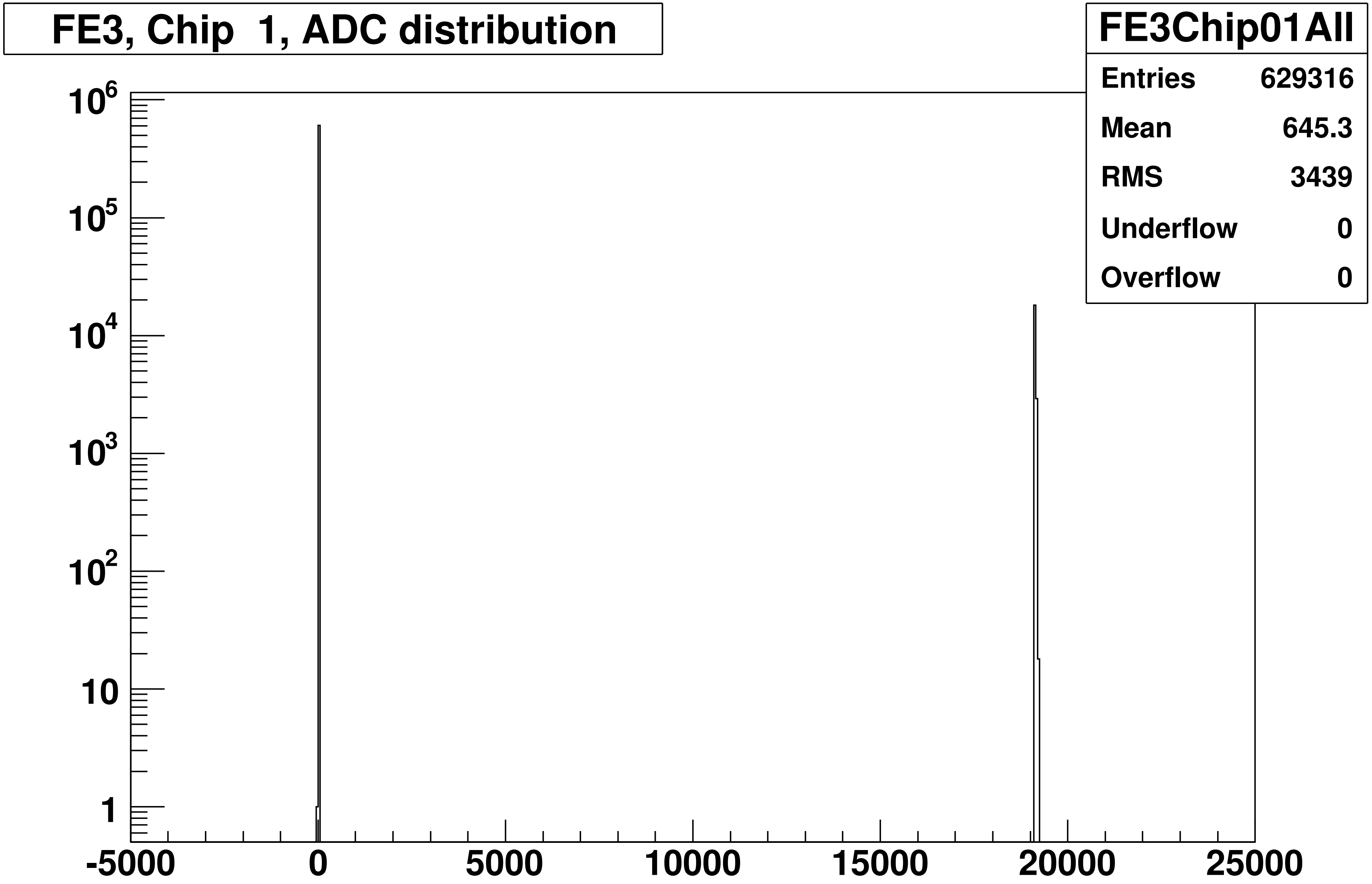


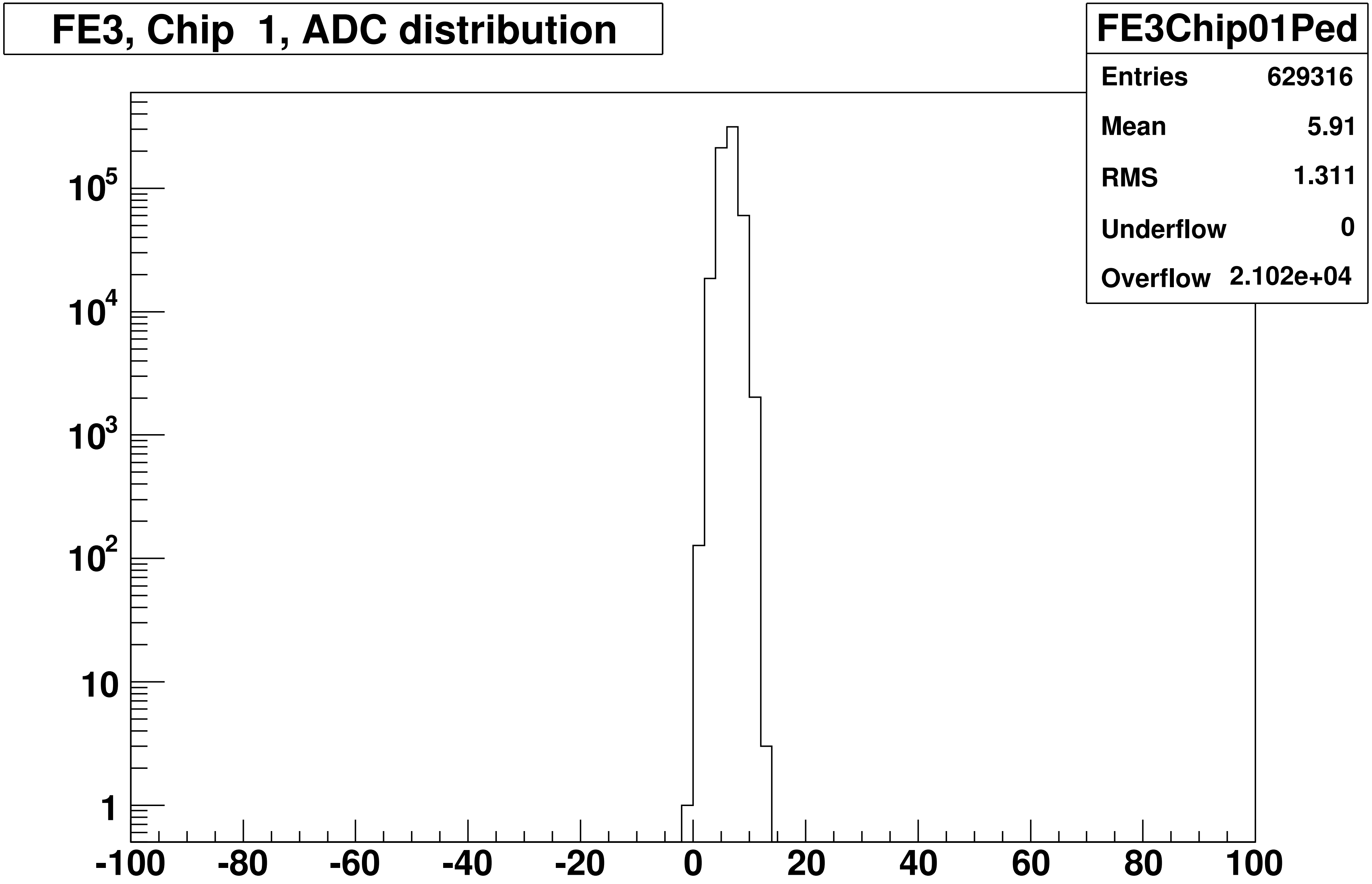
FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 629316
Mean 622.5
RMS 3454
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

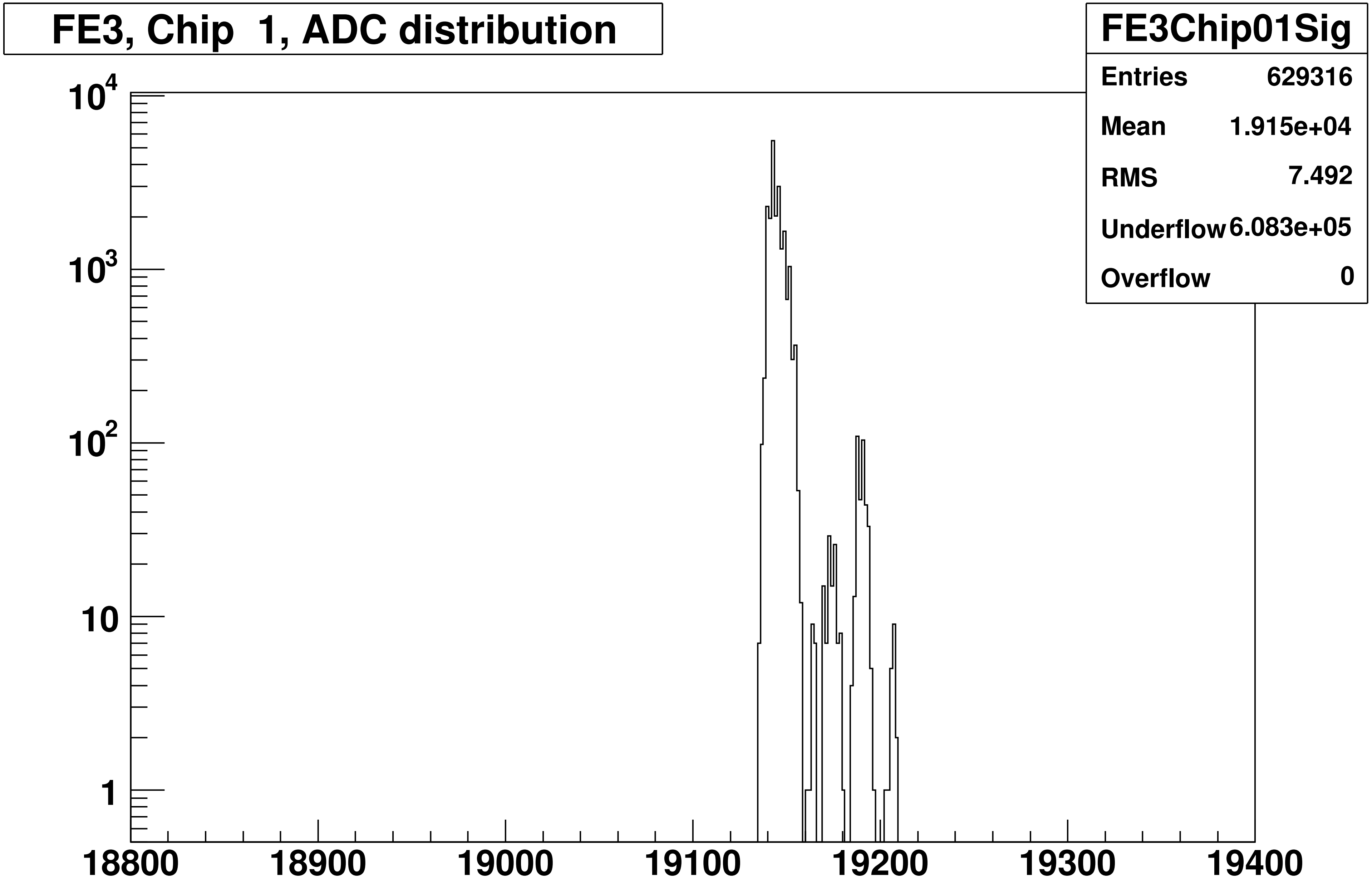


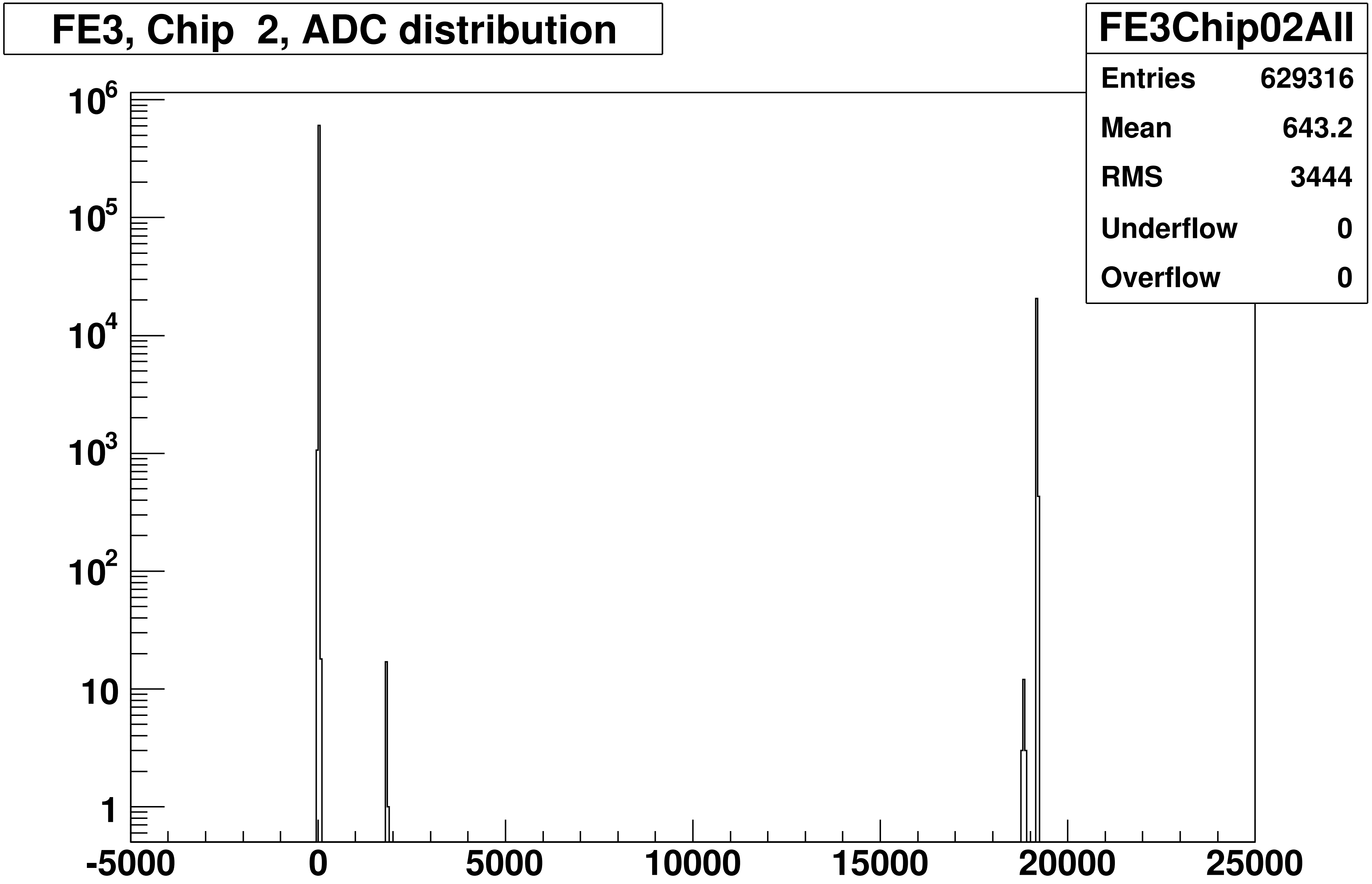
FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 629316
Mean -19.65
RMS 1.335
Underflow 0
Overflow 2.102e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 629316
Mean 1.92e+04
RMS 7.996
Underflow 6.083e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

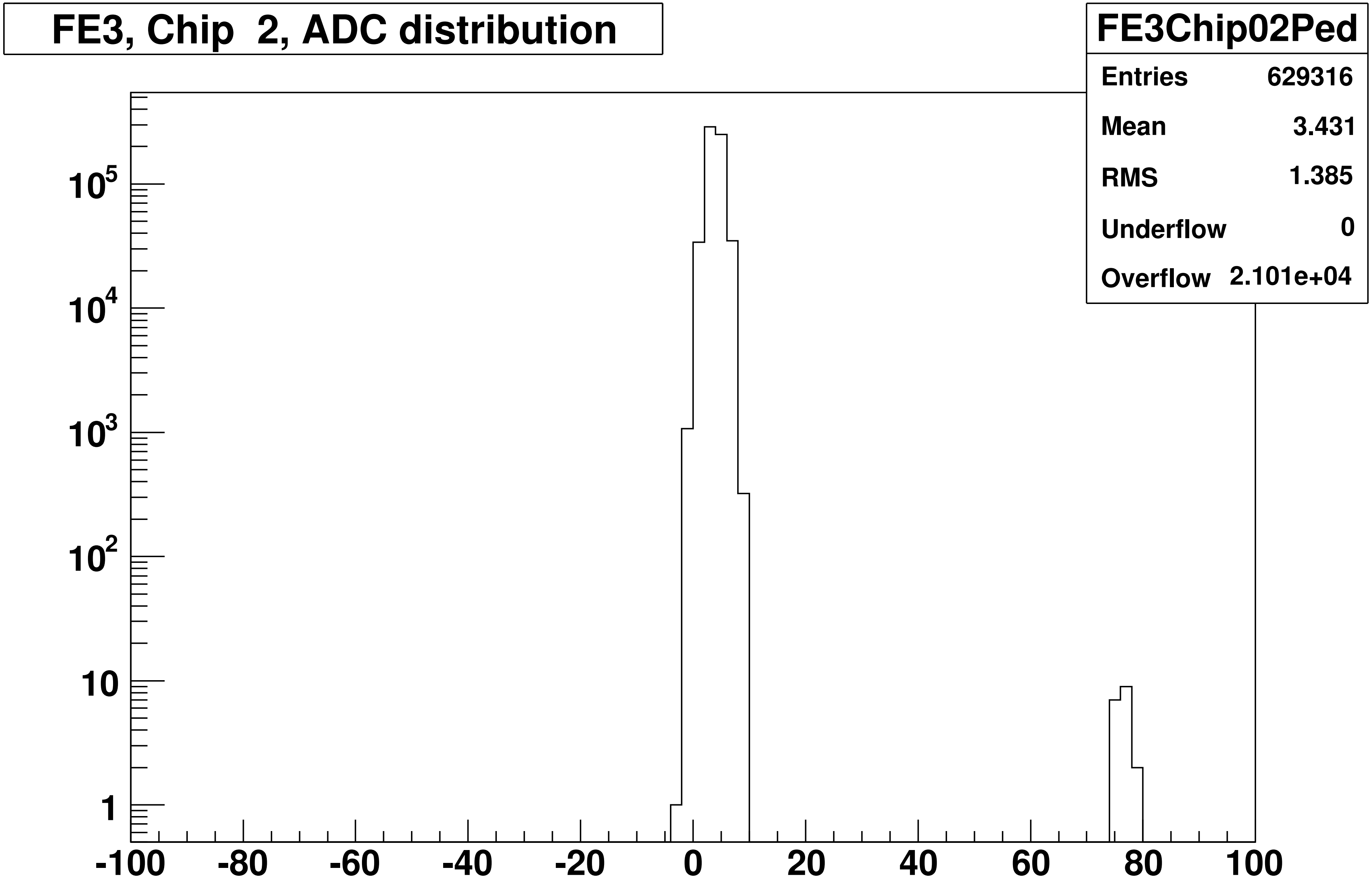

FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 629316
Mean 645.3
RMS 3439
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

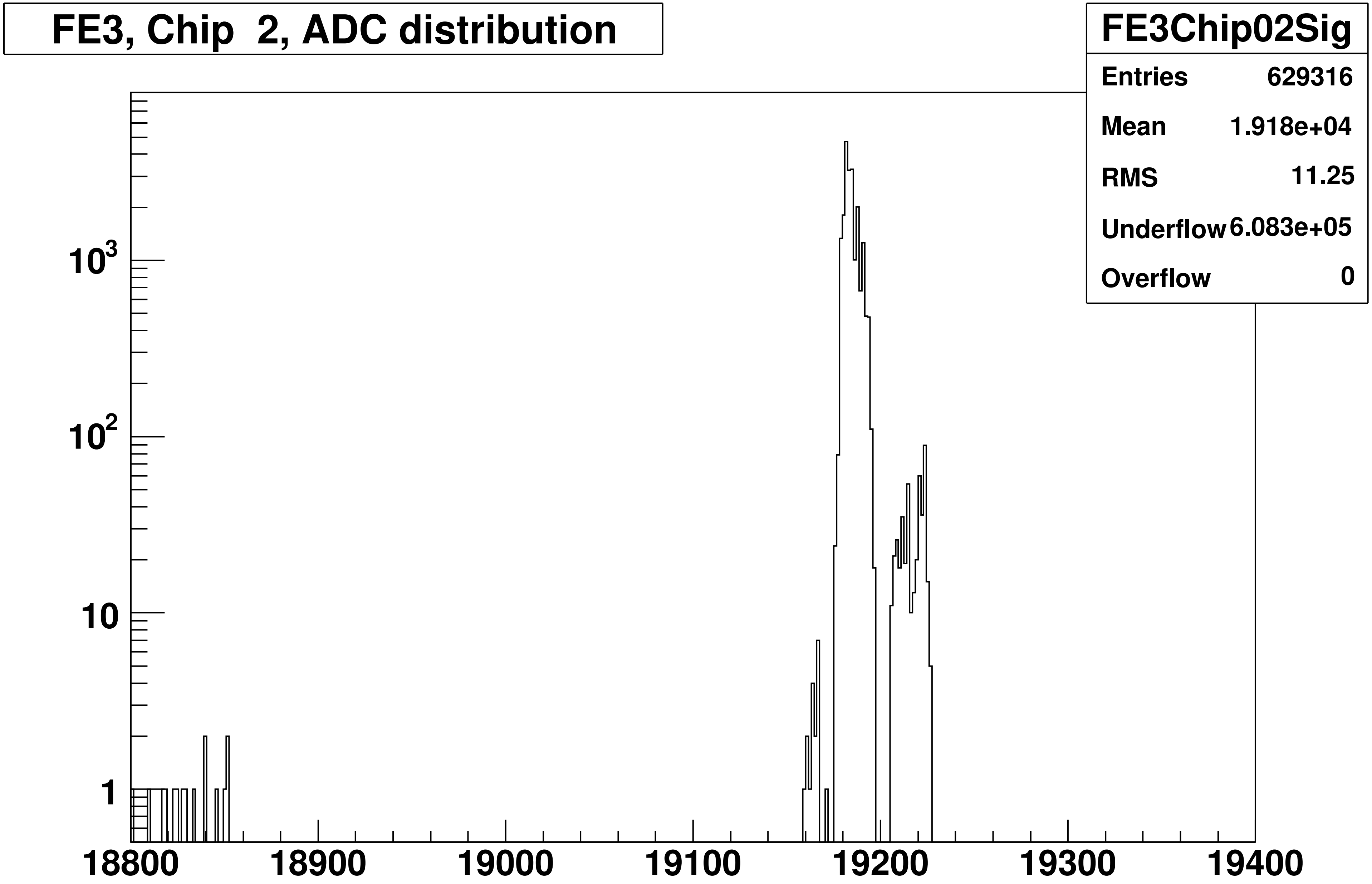

FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 629316
Mean 5.91
RMS 1.311
Underflow 0
Overflow 2.102e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

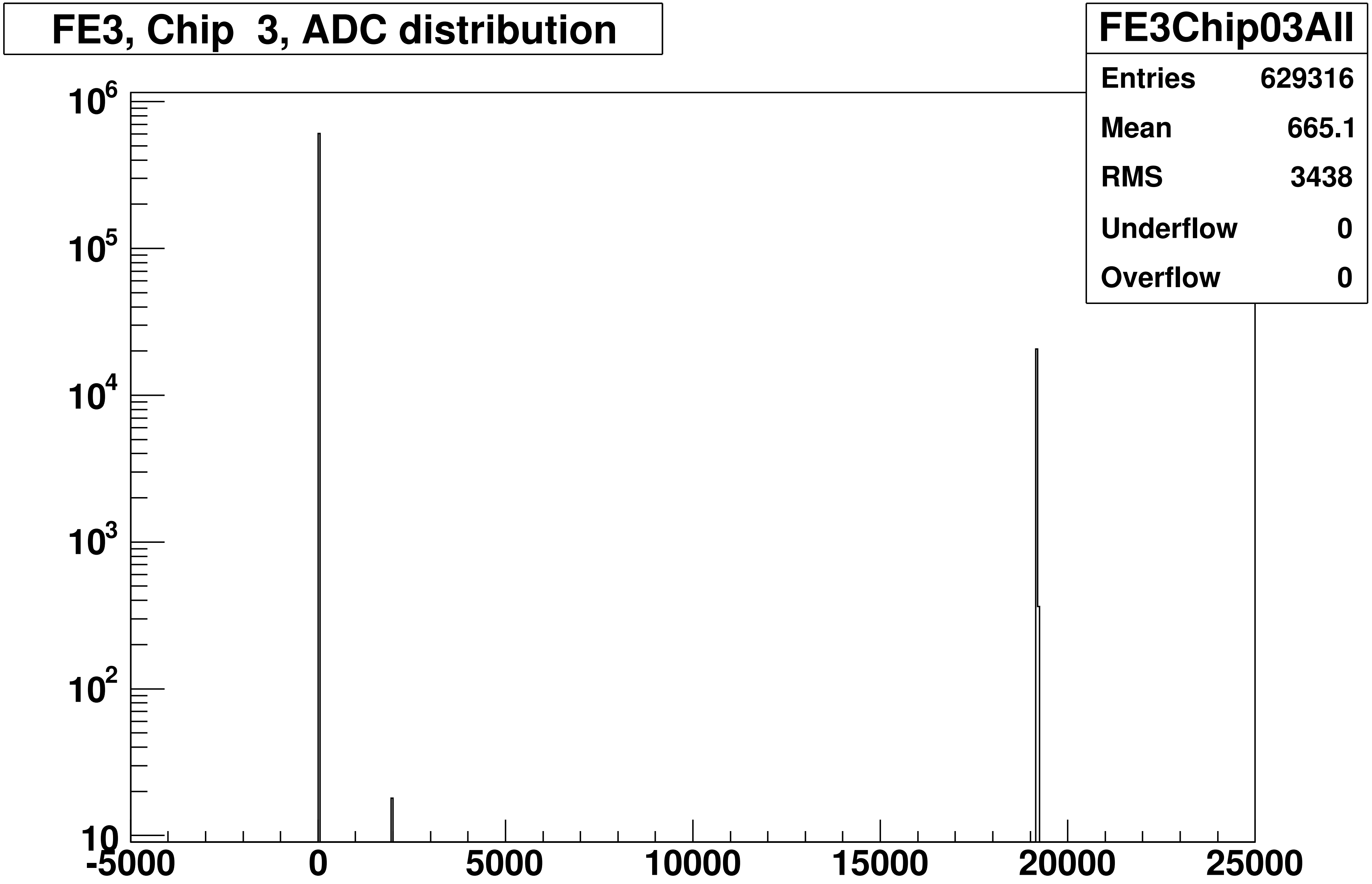

FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 629316
Mean 1.915e+04
RMS 7.492
Underflow 6.083e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

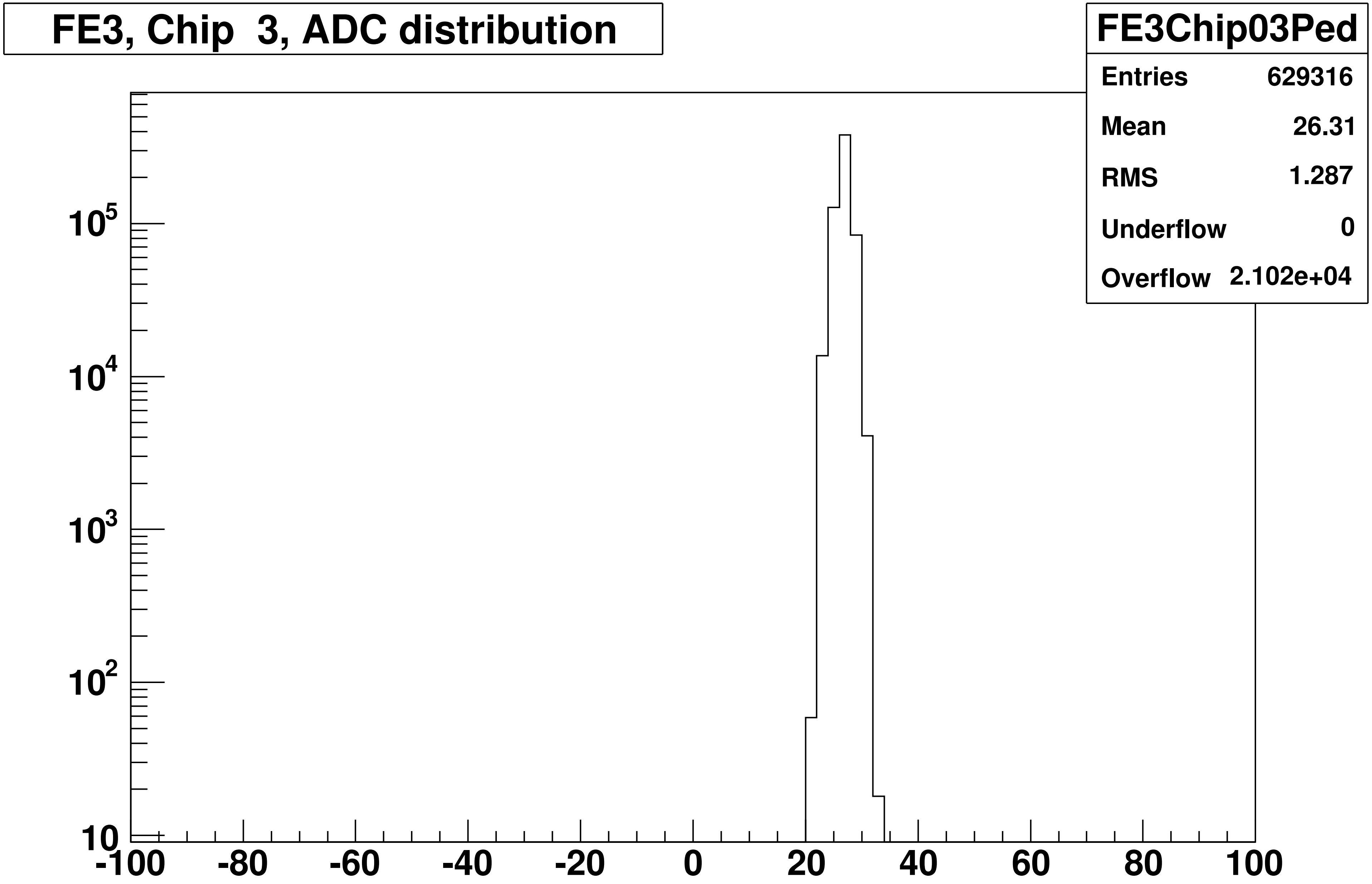


FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 629316
Mean 643.2
RMS 3444
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

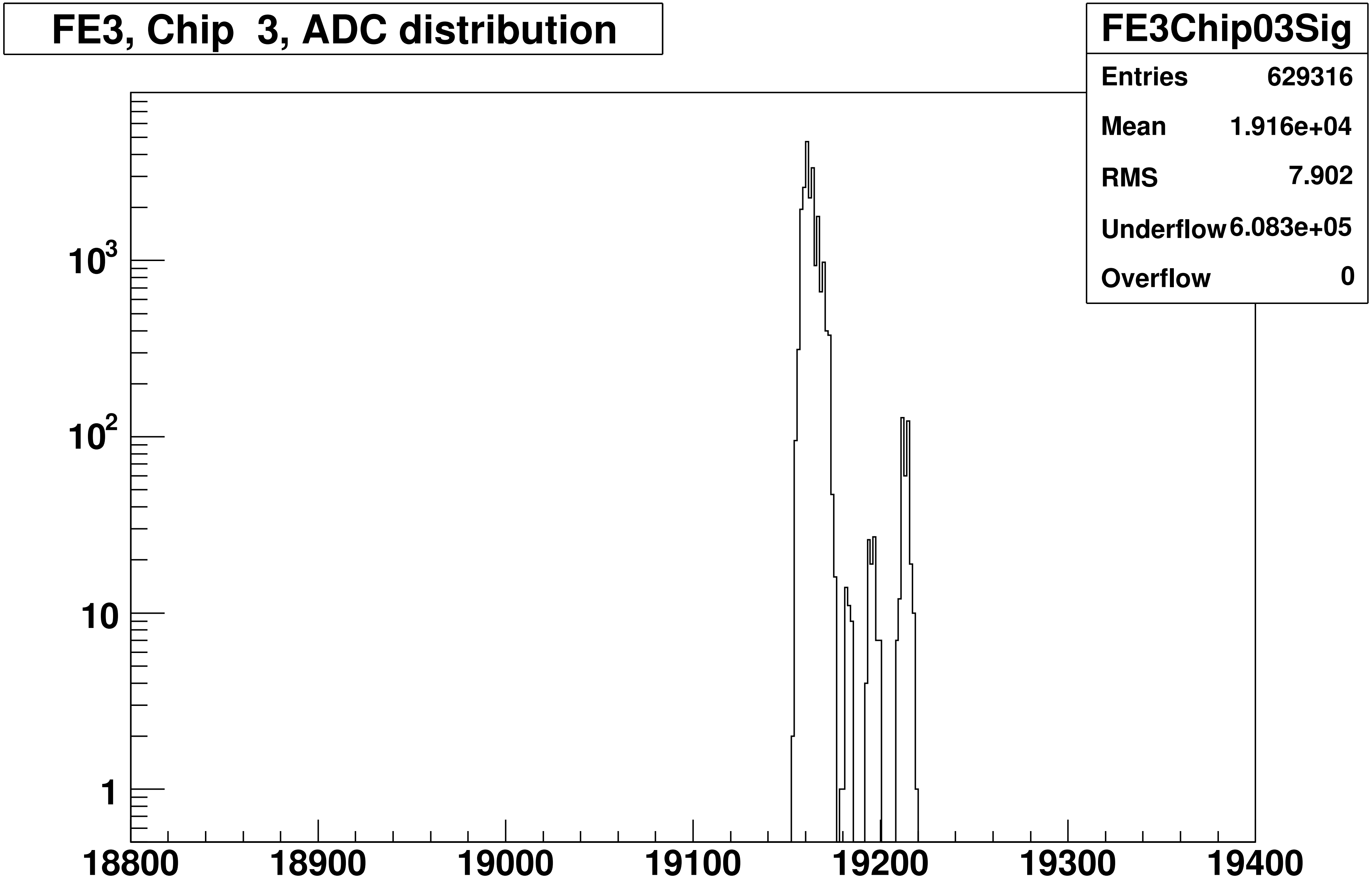

FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 629316
Mean 3.431
RMS 1.385
Underflow 0
Overflow 2.101e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

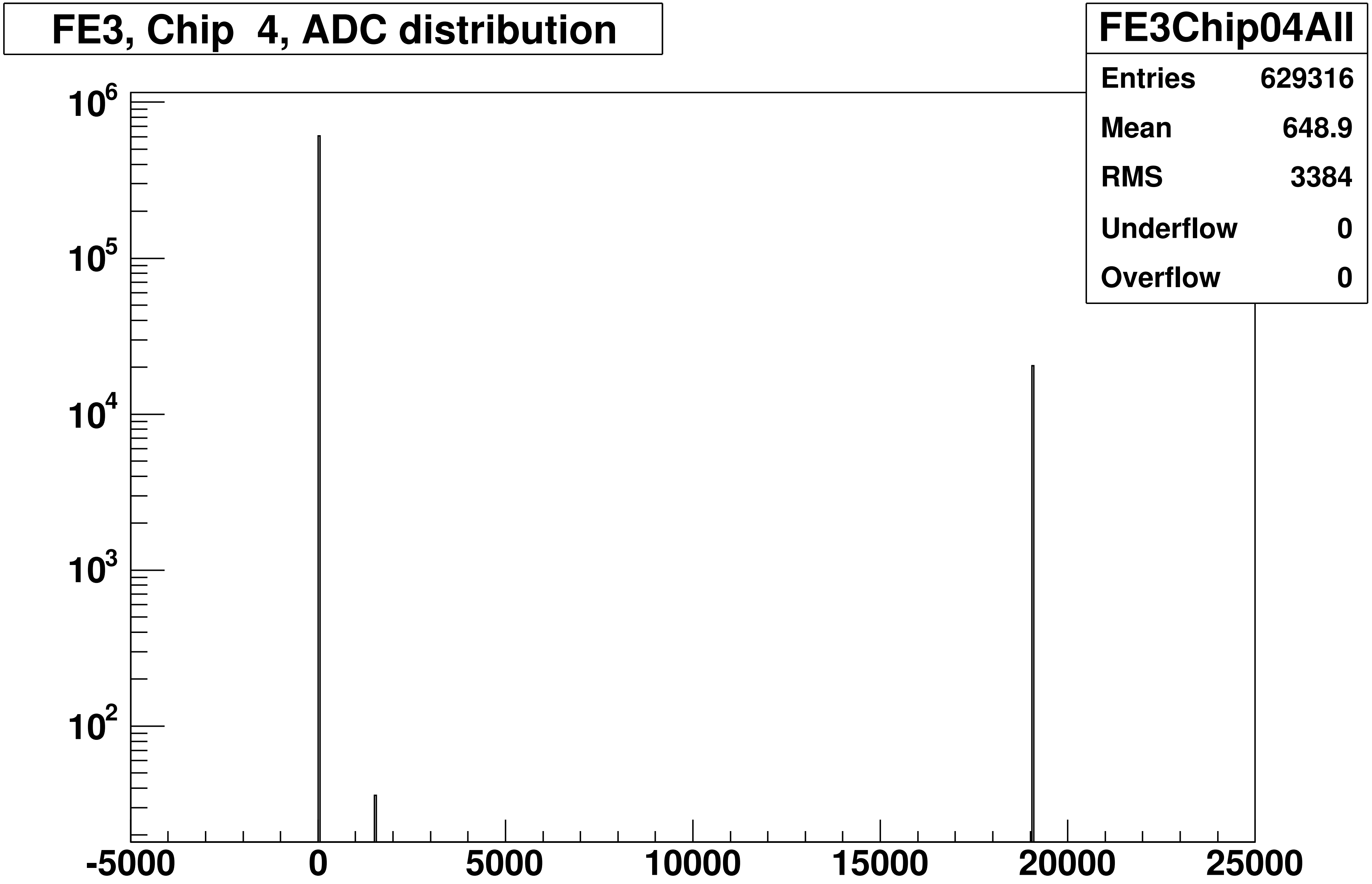

FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 629316
Mean 1.918e+04
RMS 11.25
Underflow 6.083e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

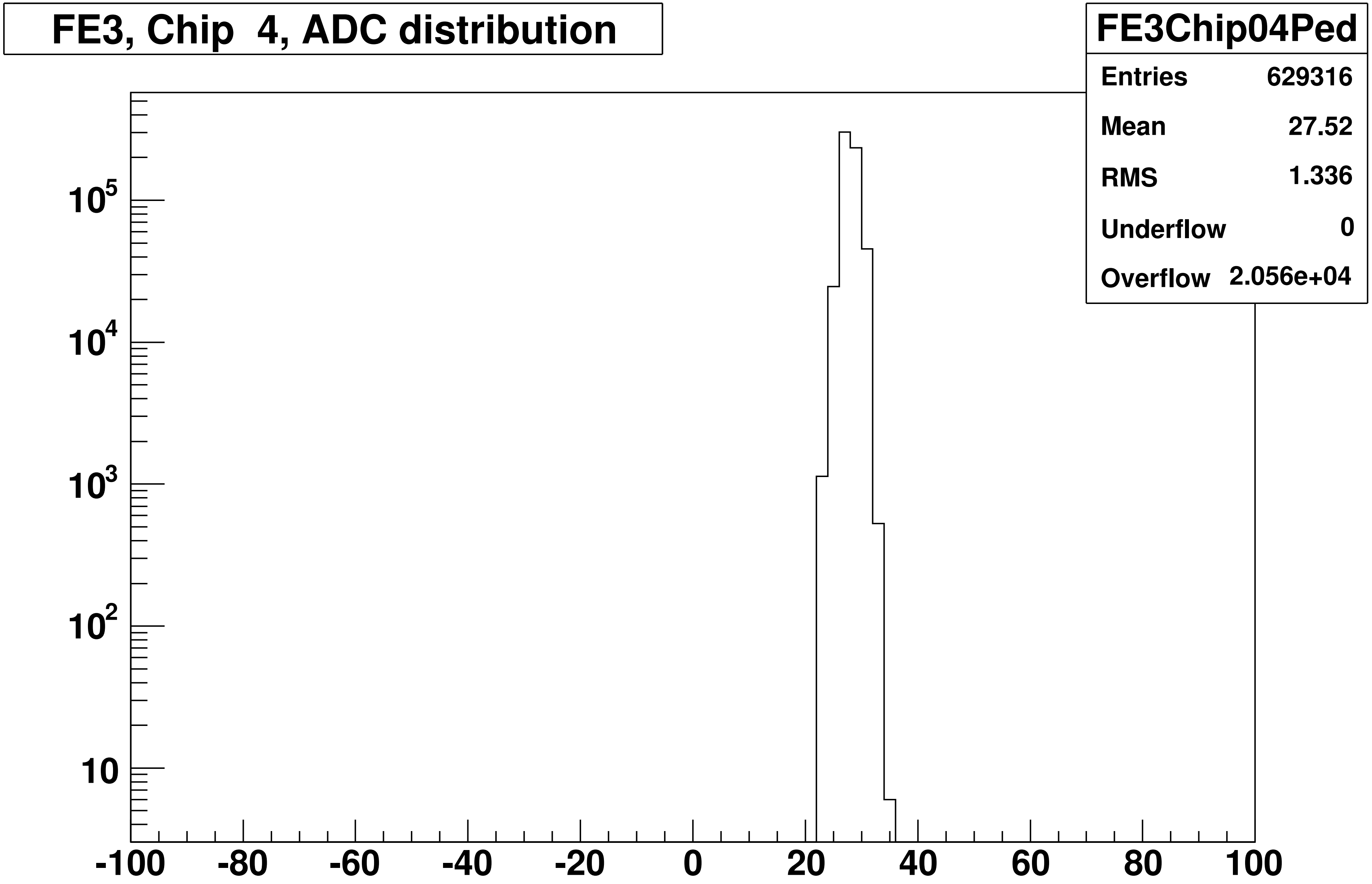

FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 629316
Mean 665.1
RMS 3438
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



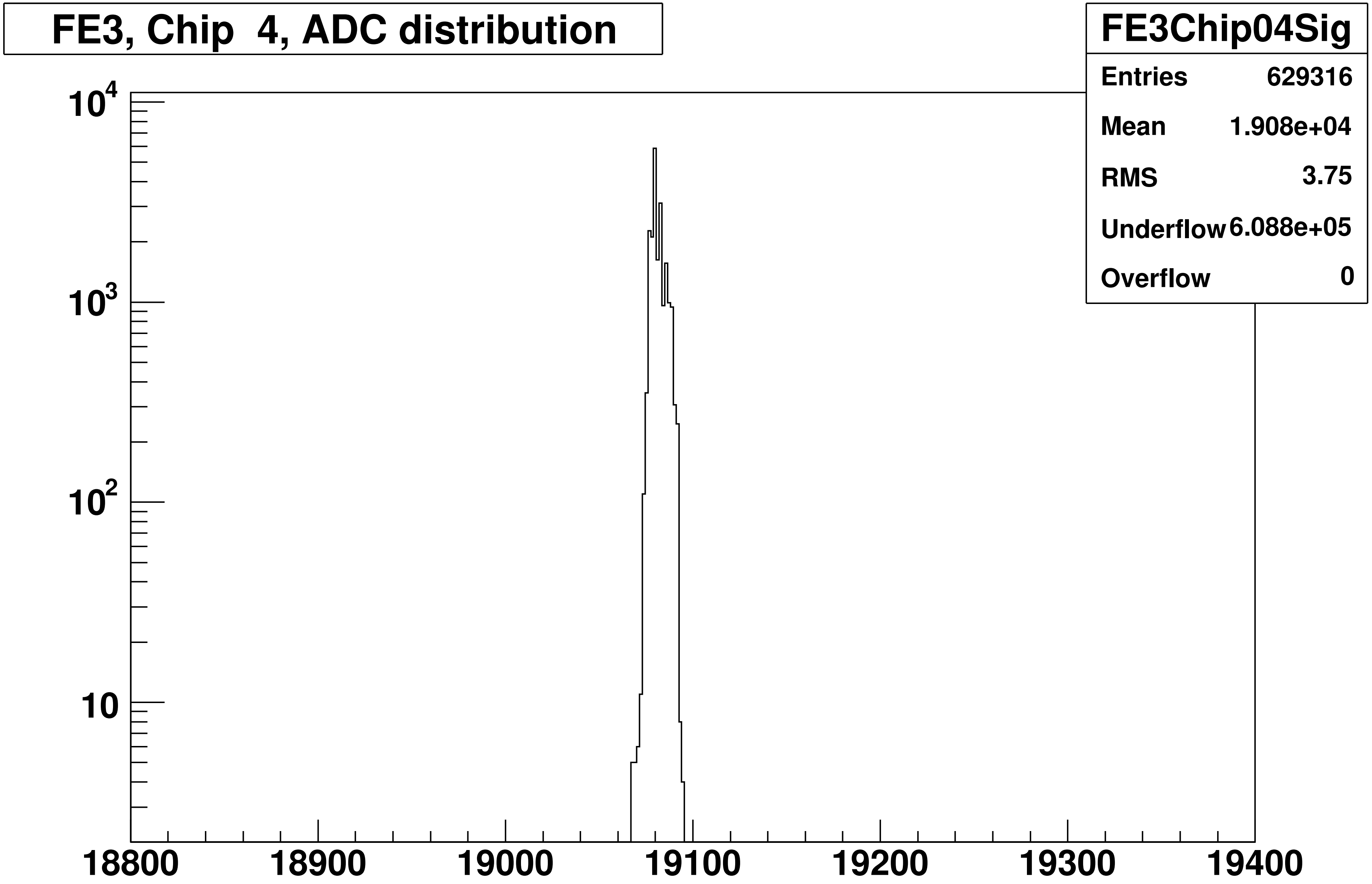
FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 629316
Mean 26.31
RMS 1.287
Underflow 0
Overflow 2.102e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 629316
Mean 1.916e+04
RMS 7.902
Underflow 6.083e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

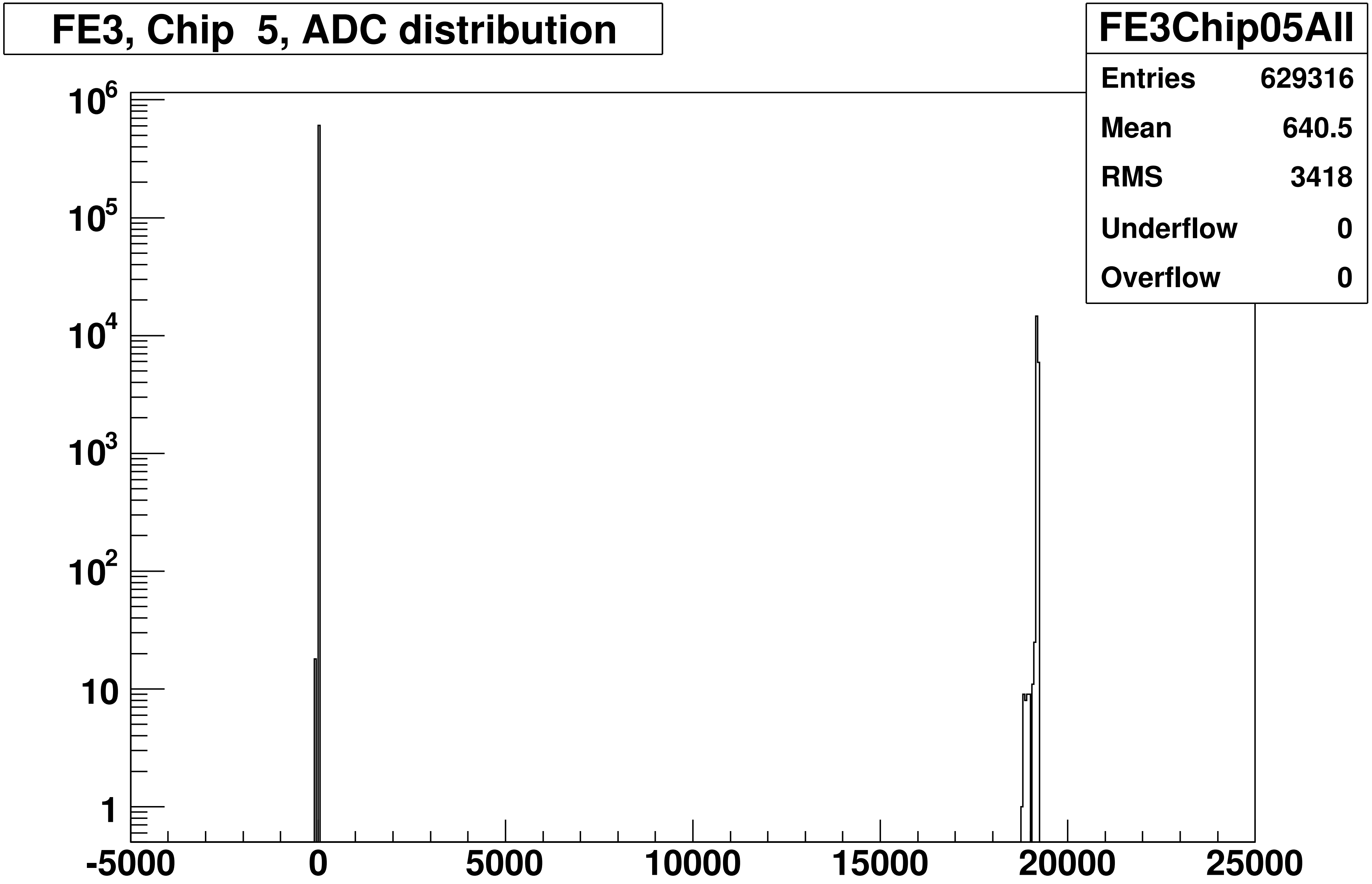

FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 629316
Mean 648.9
RMS 3384
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



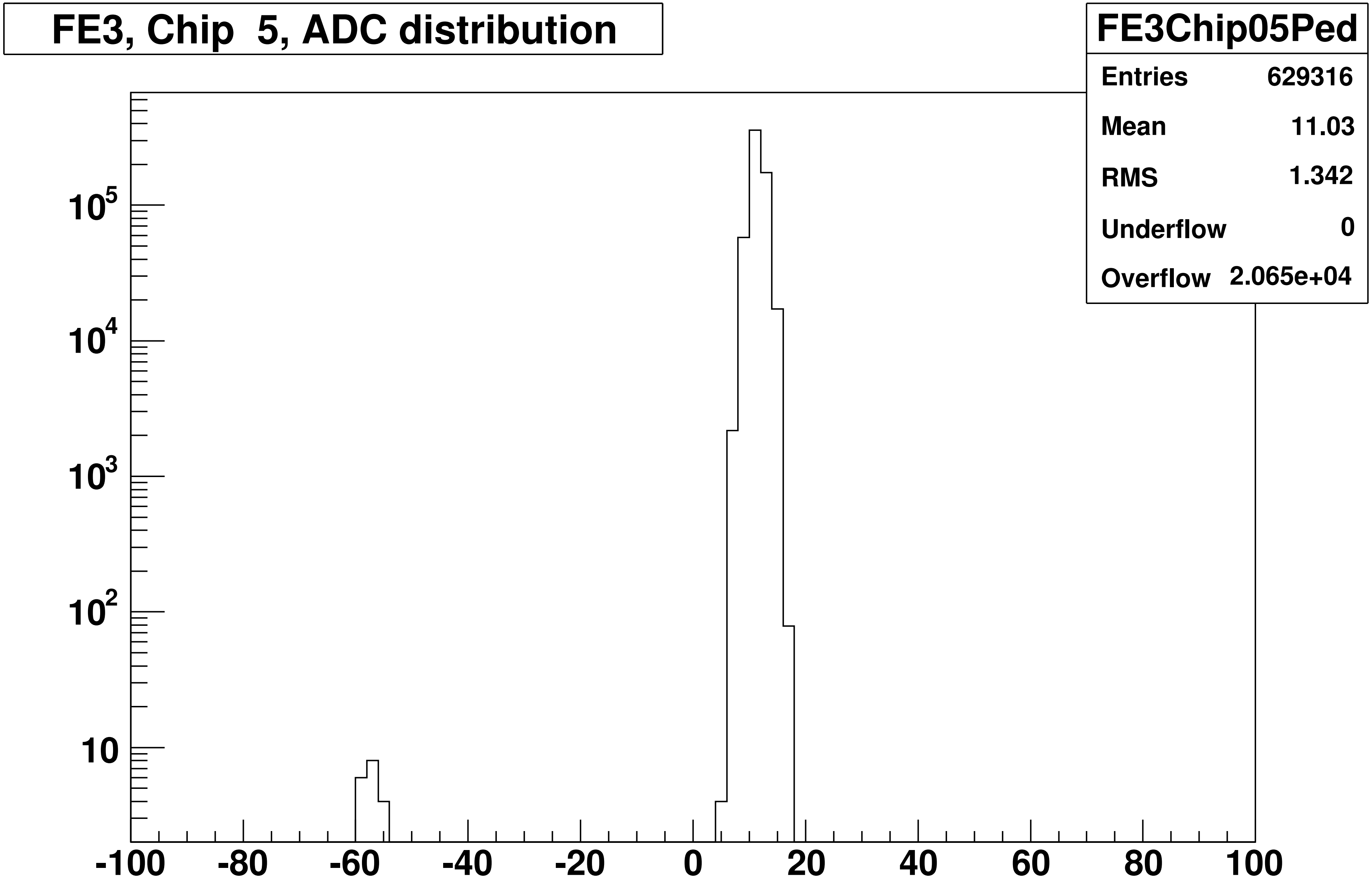
FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 629316
Mean 27.52
RMS 1.336
Underflow 0
Overflow 2.056e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

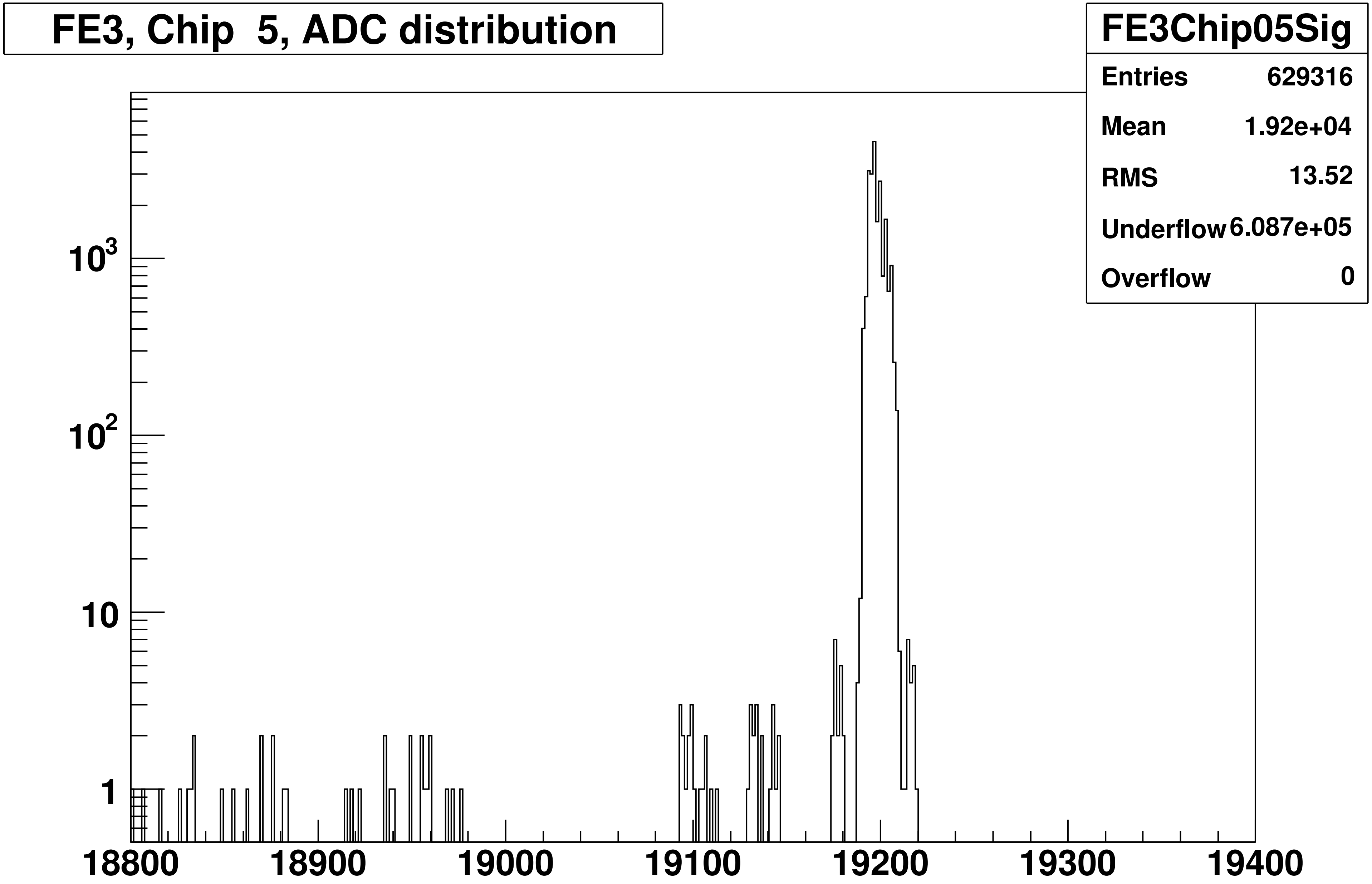


FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 629316
Mean 1.908e+04
RMS 3.75
Underflow 6.088e+05
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400

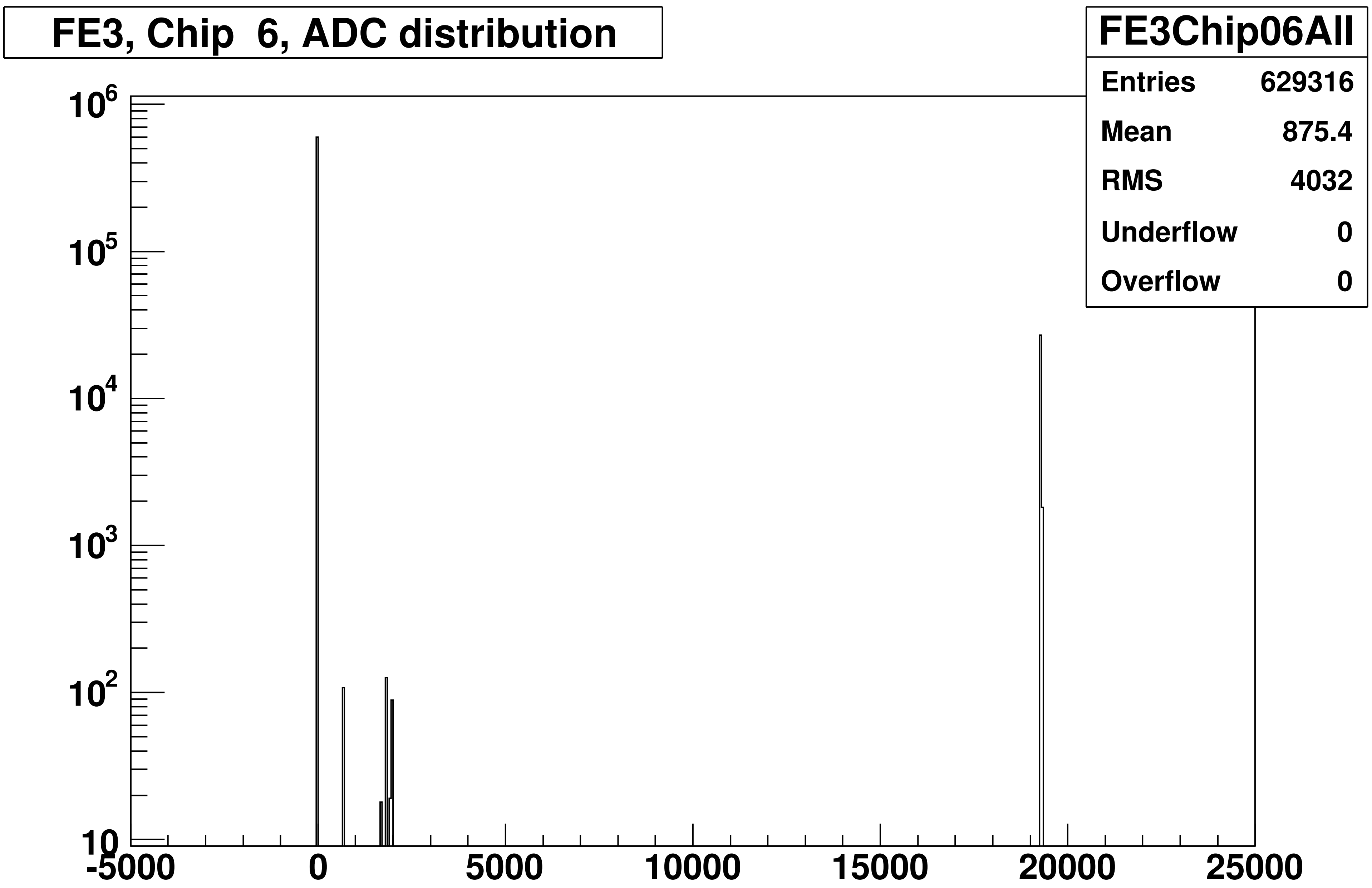


FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 629316
Mean 640.5
RMS 3418
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

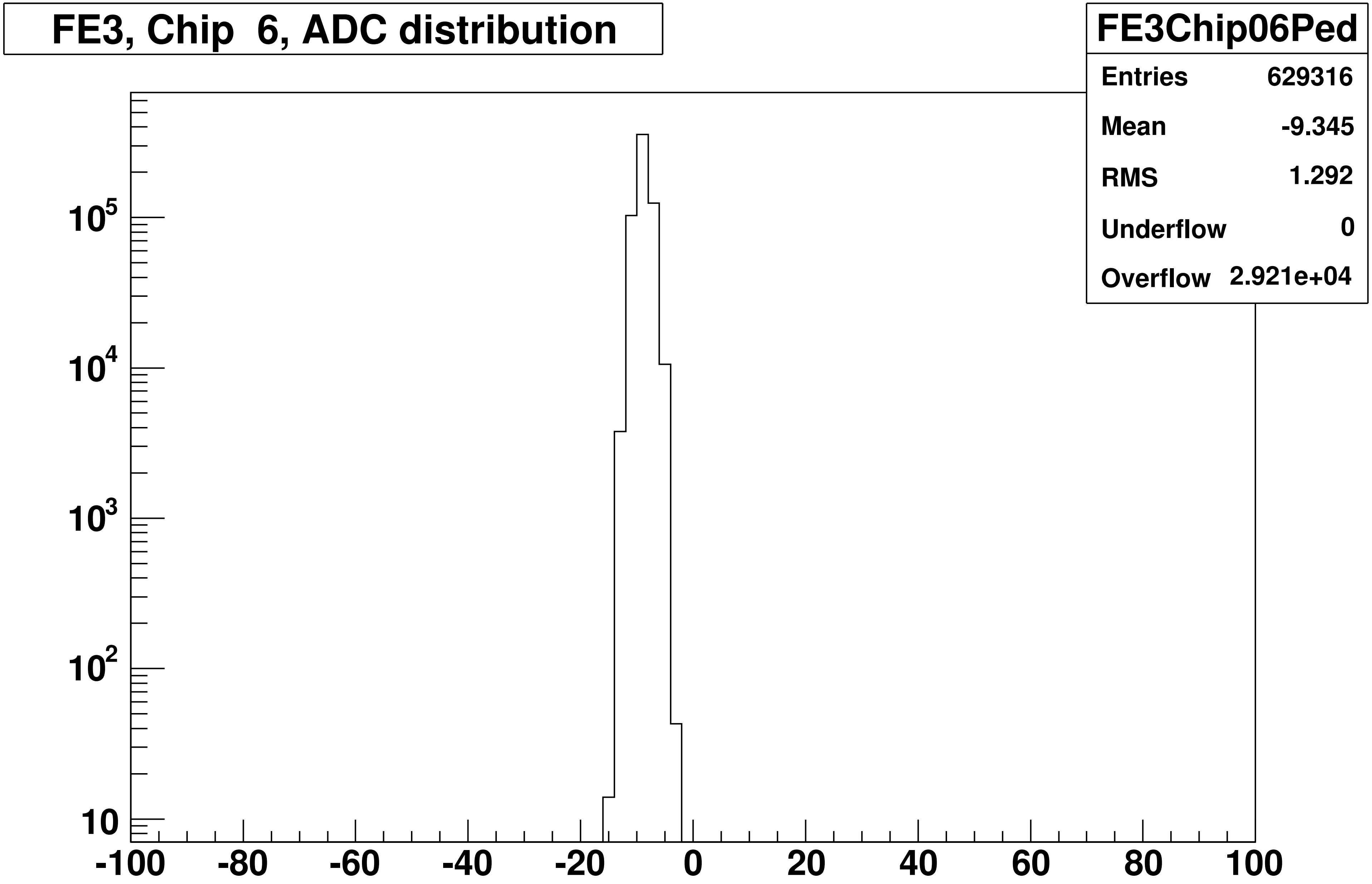

FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 629316
Mean 11.03
RMS 1.342
Underflow 0
Overflow 2.065e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

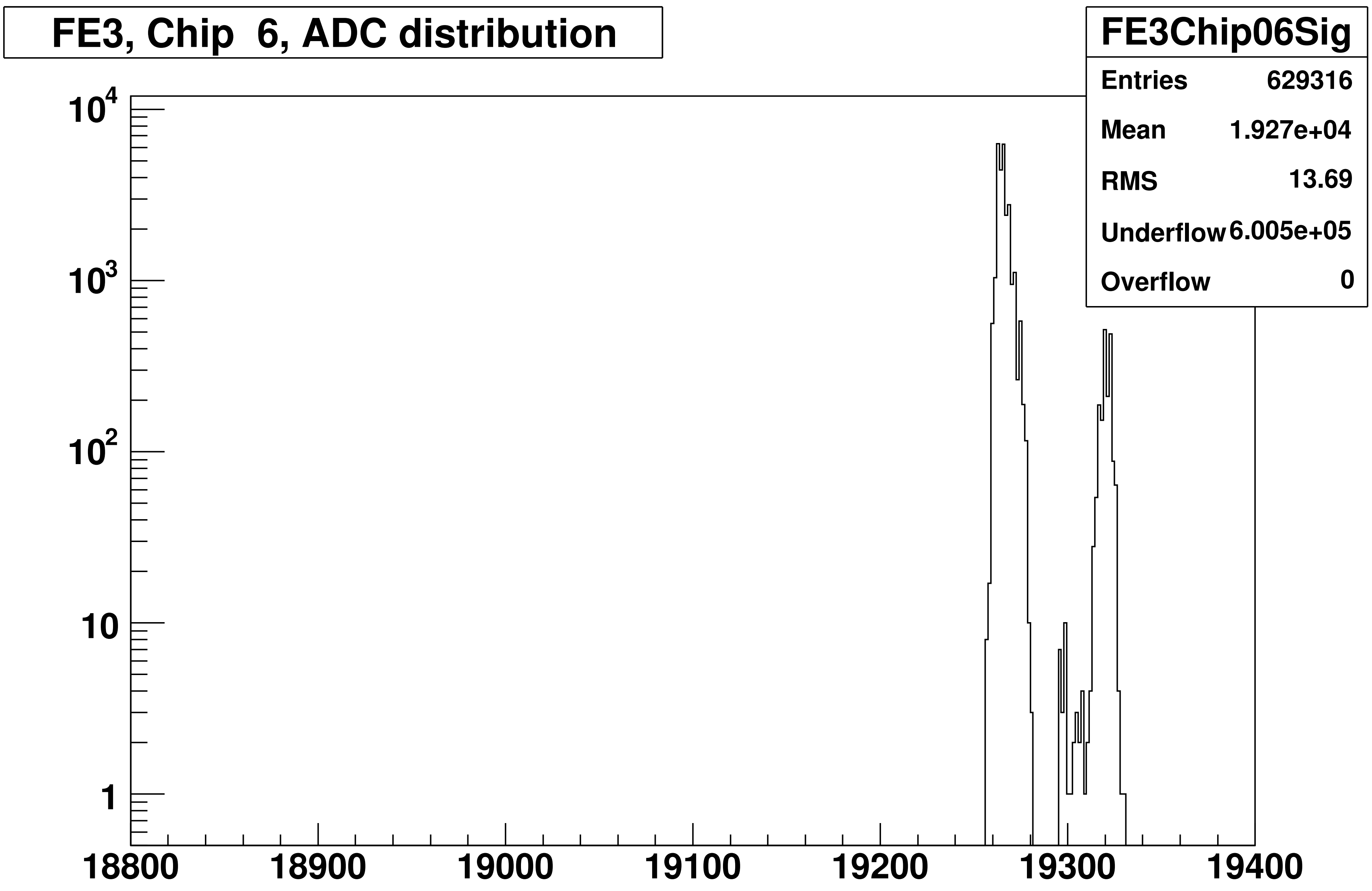


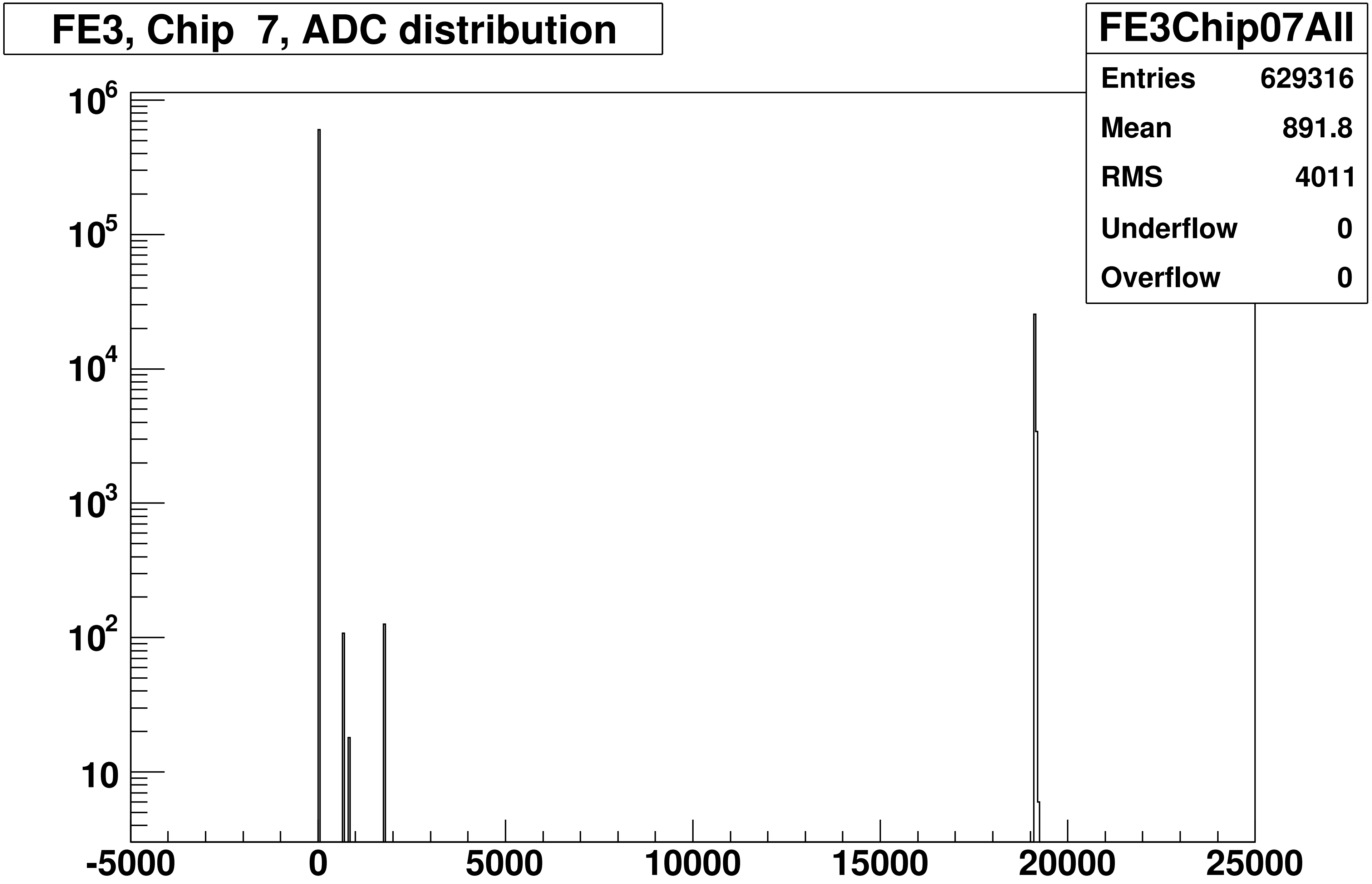
FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 629316
Mean 1.92e+04
RMS 13.52
Underflow 6.087e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

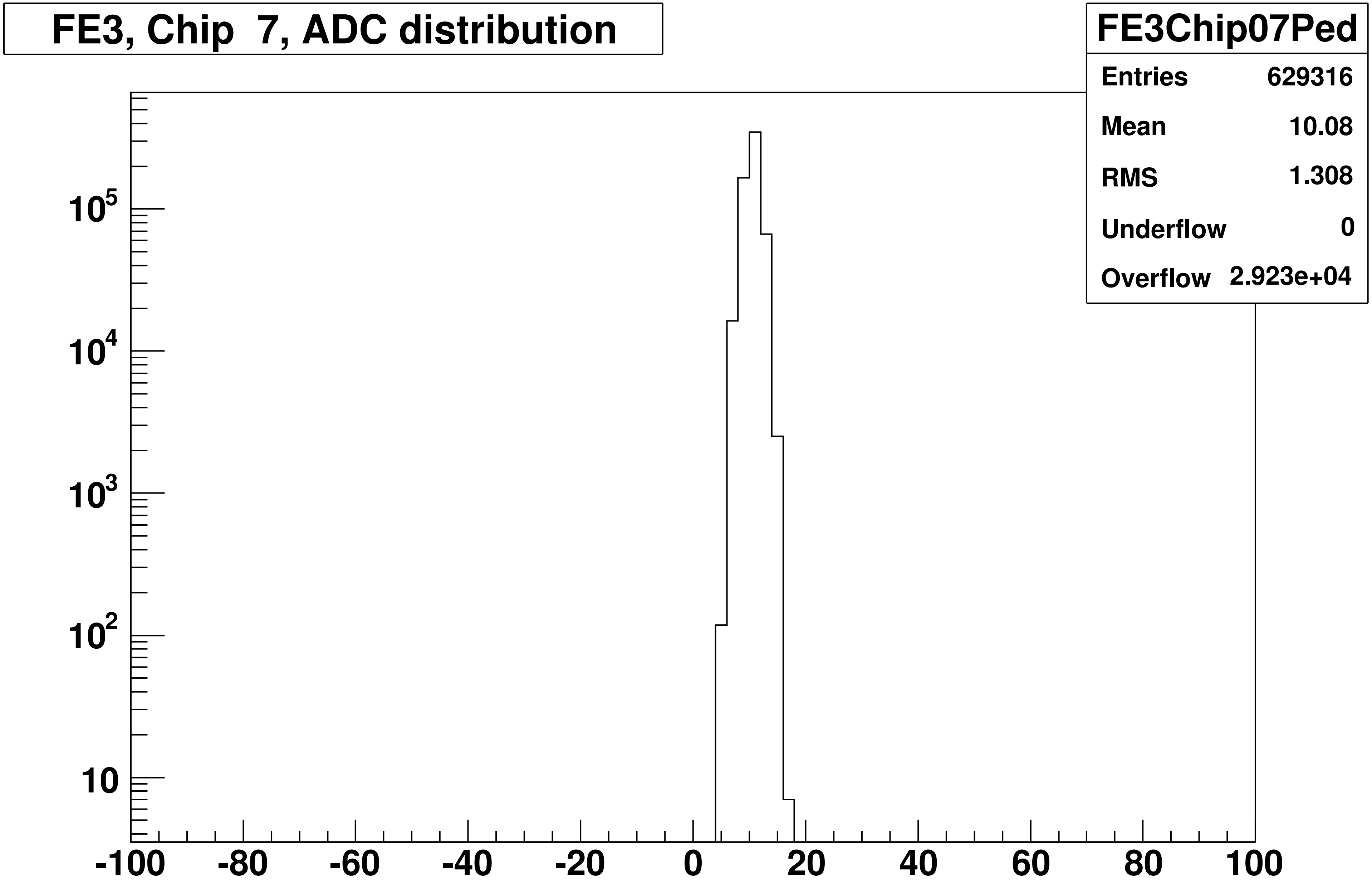


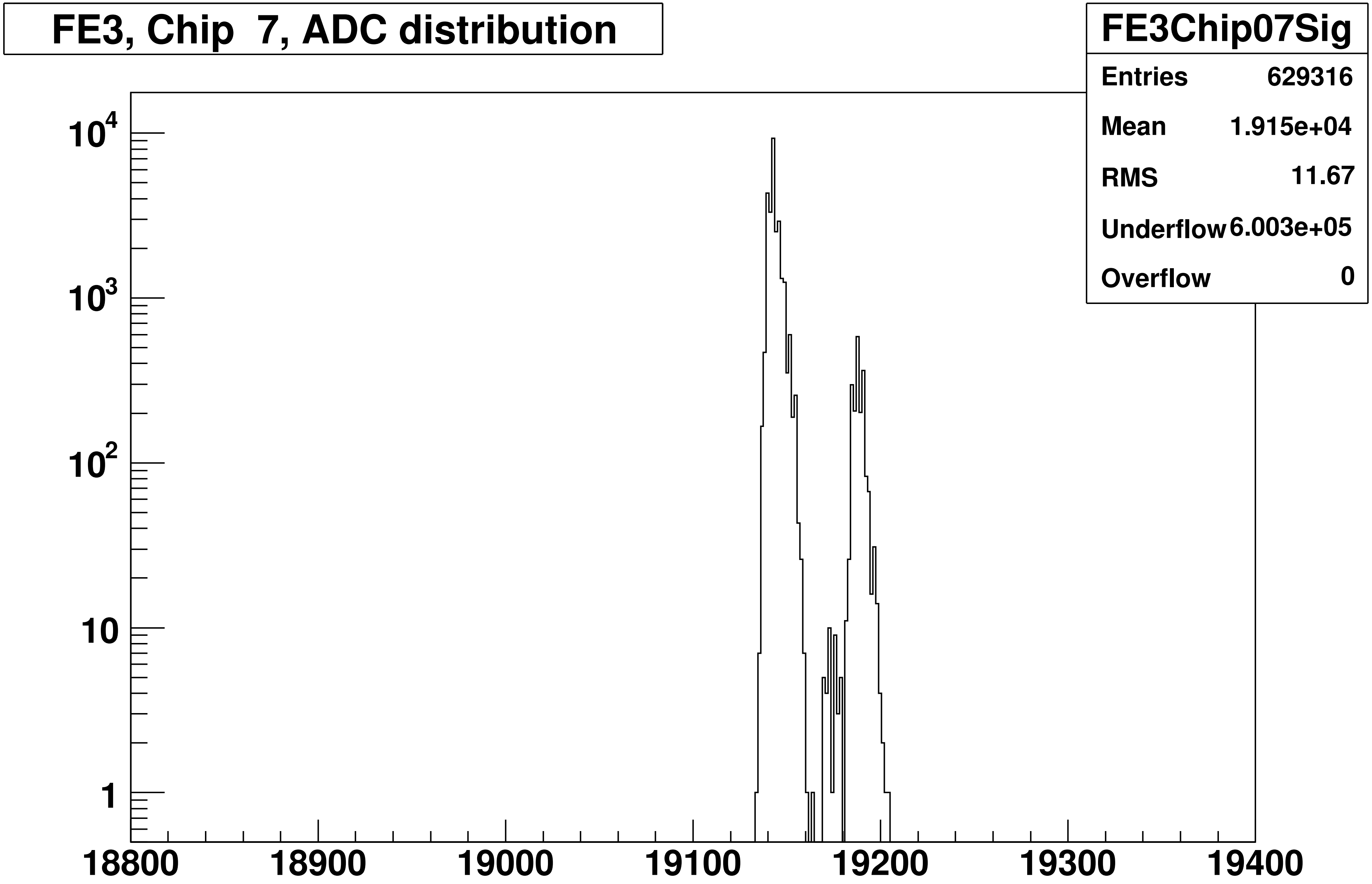
FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 629316
Mean 875.4
RMS 4032
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

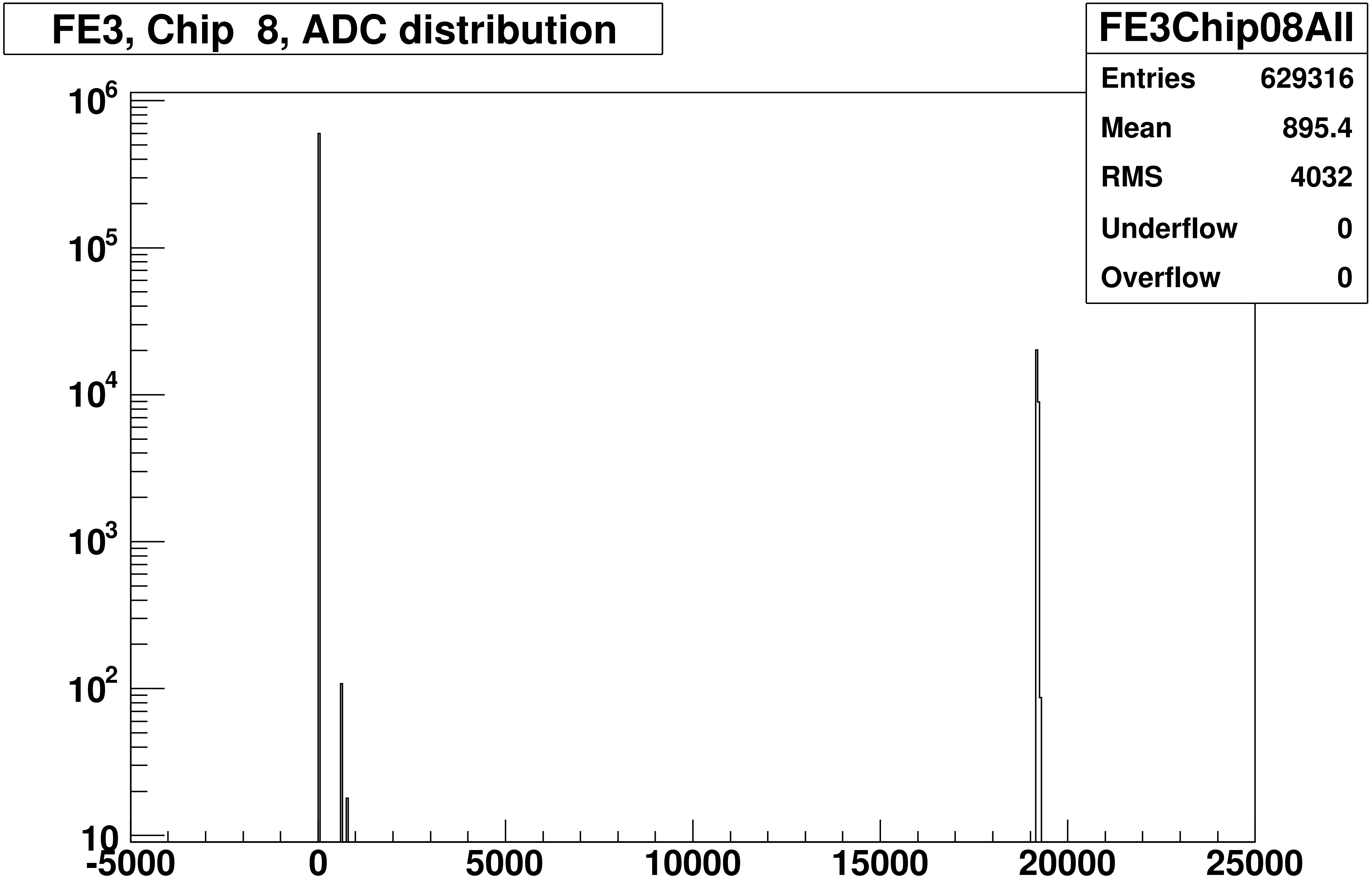

FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 629316
Mean -9.345
RMS 1.292
Underflow 0
Overflow 2.921e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

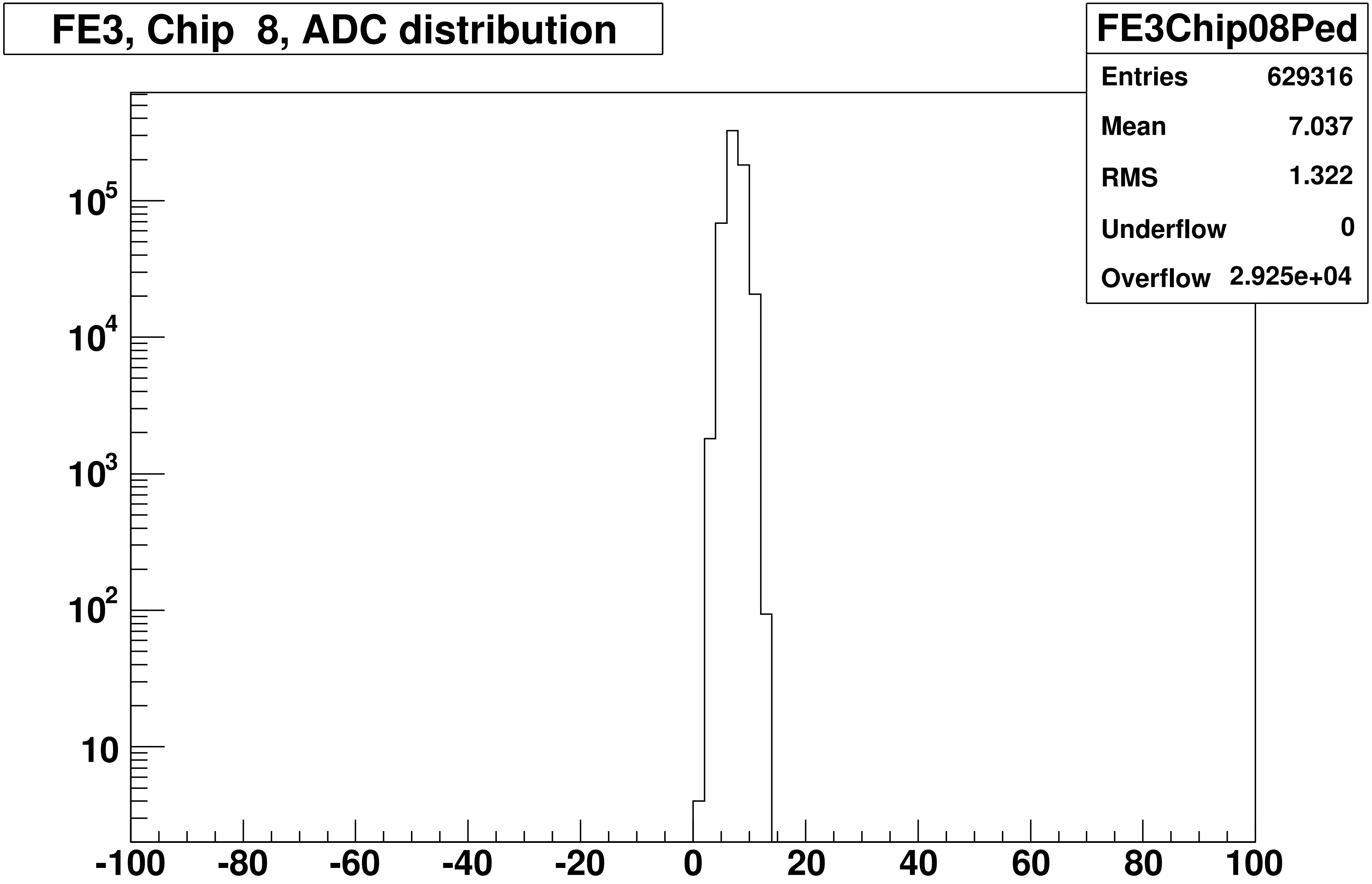

FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 629316
Mean 1.927e+04
RMS 13.69
Underflow 6.005e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 629316
Mean 891.8
RMS 4011
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

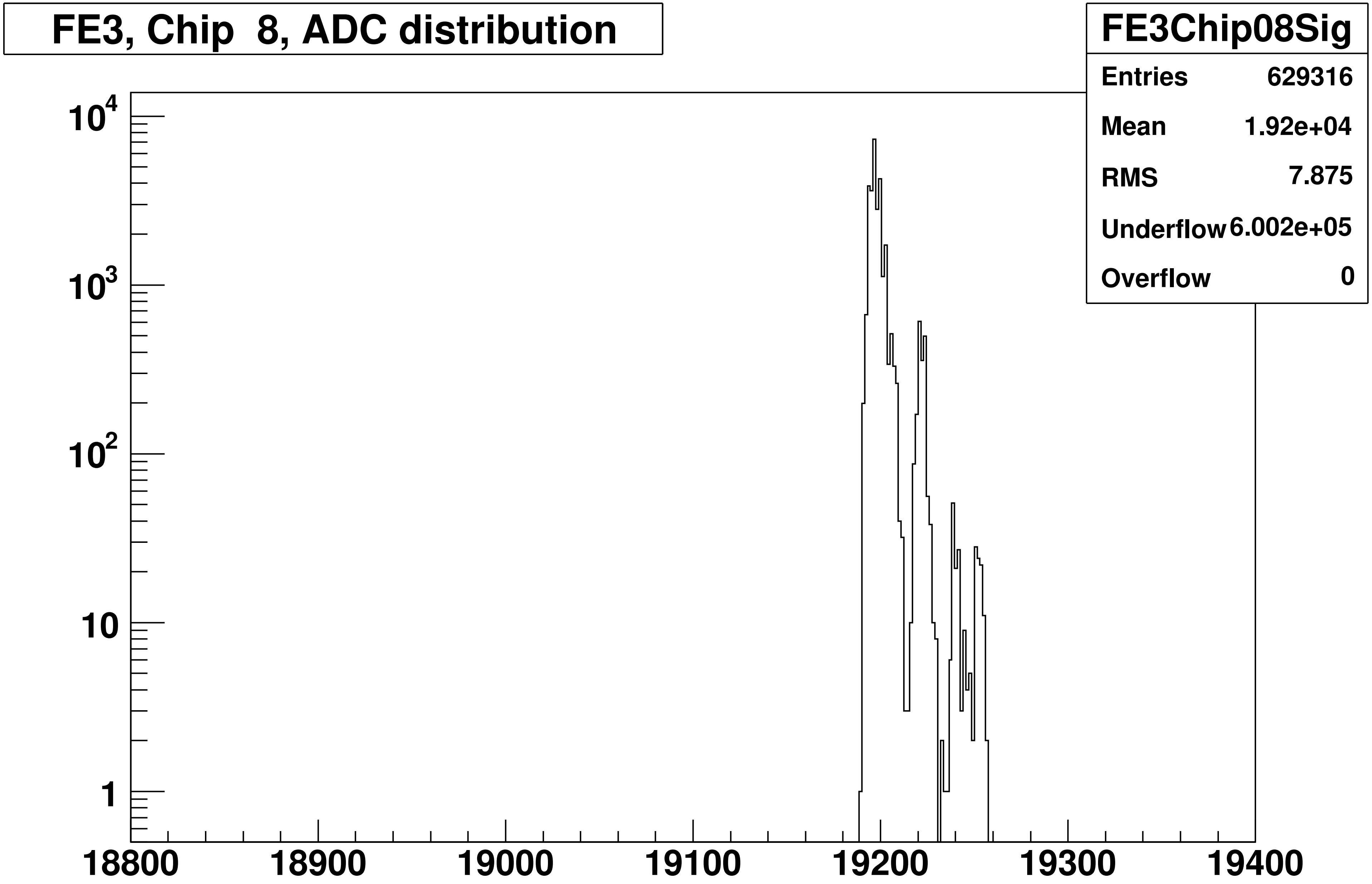

FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 629316
Mean 10.08
RMS 1.308
Underflow 0
Overflow 2.923e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 629316
Mean 1.915e+04
RMS 11.67
Underflow 6.003e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

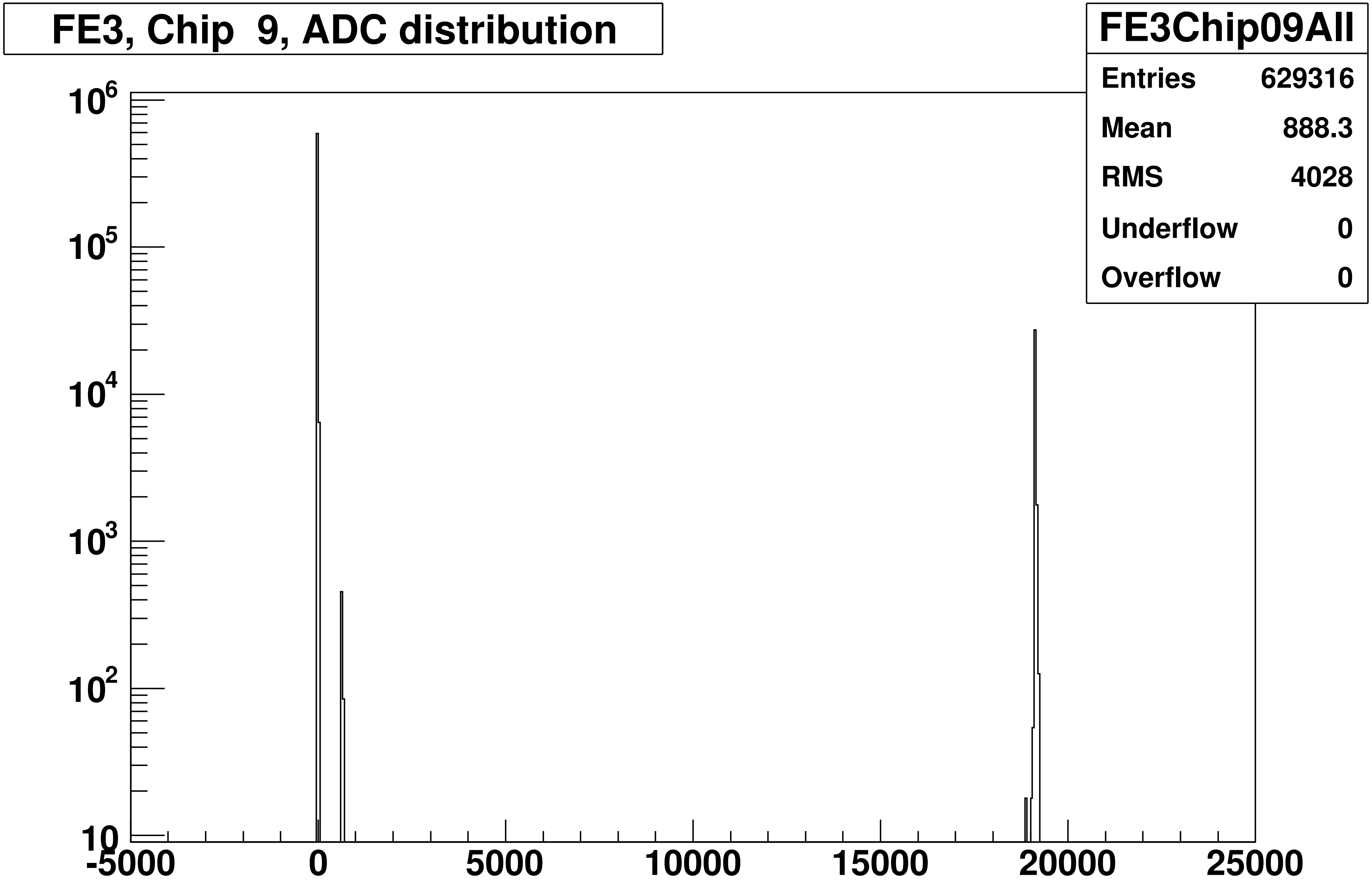

FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 629316
Mean 895.4
RMS 4032
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

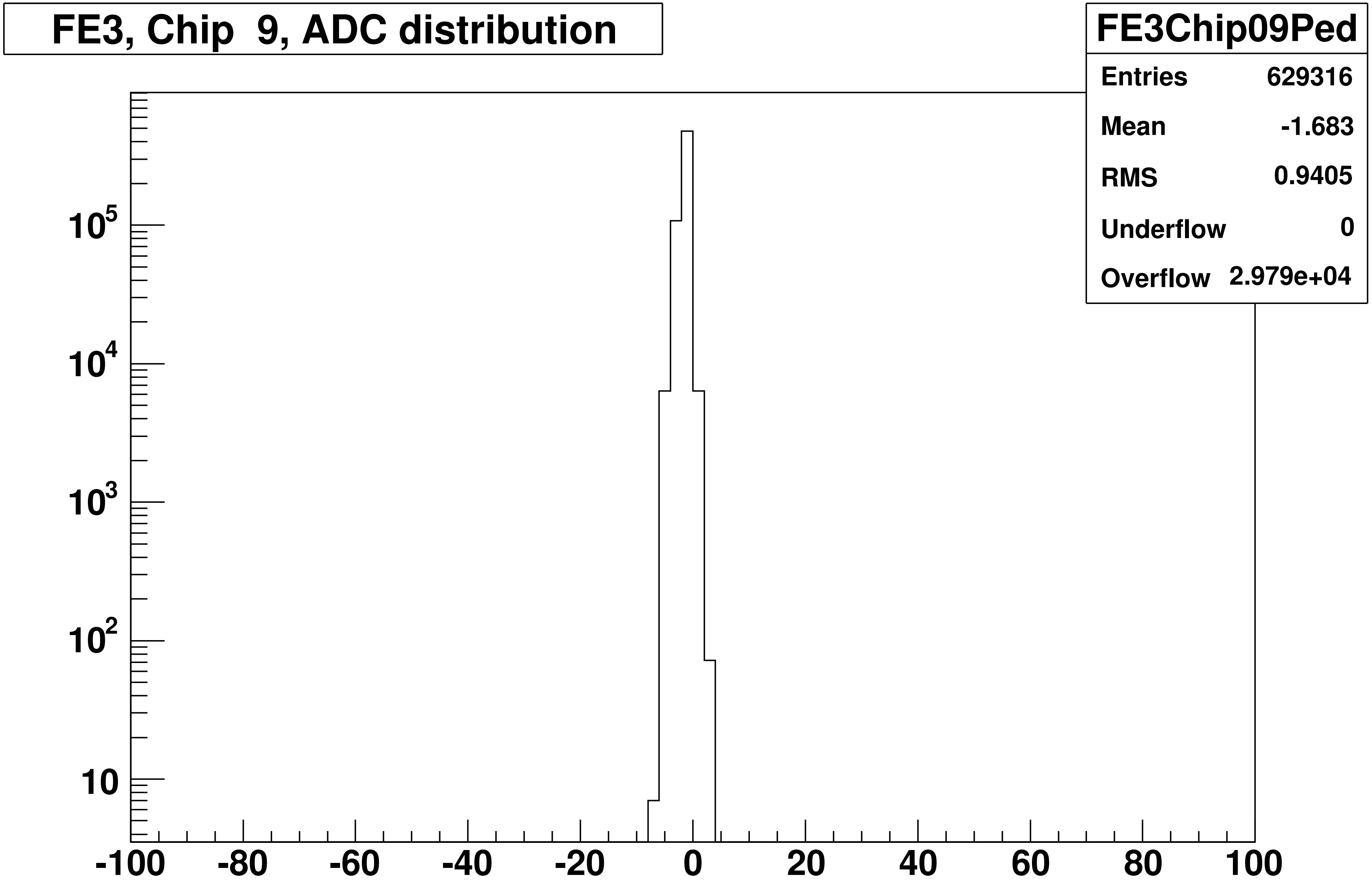


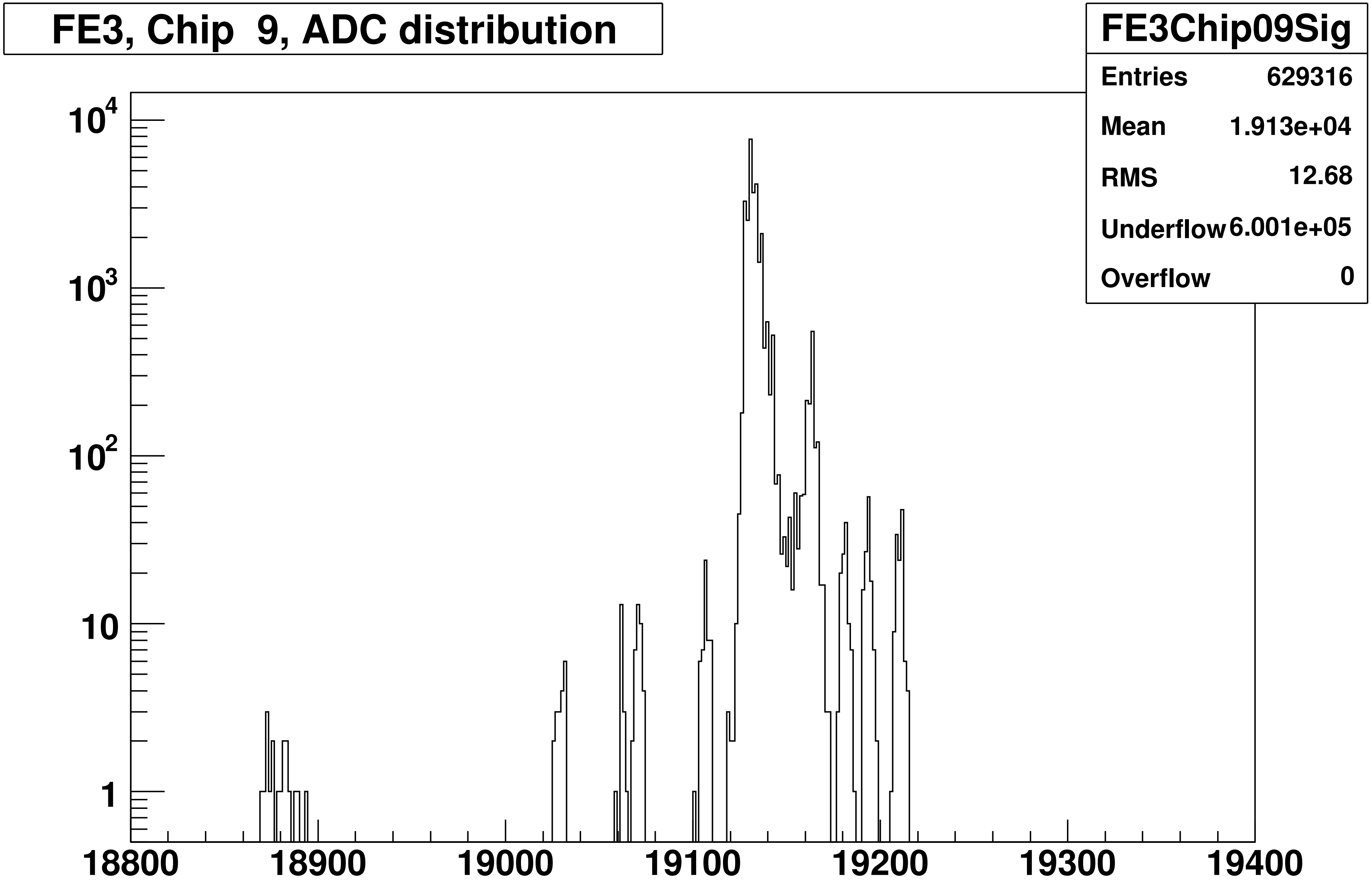
FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 629316
Mean 7.037
RMS 1.322
Underflow 0
Overflow 2.925e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



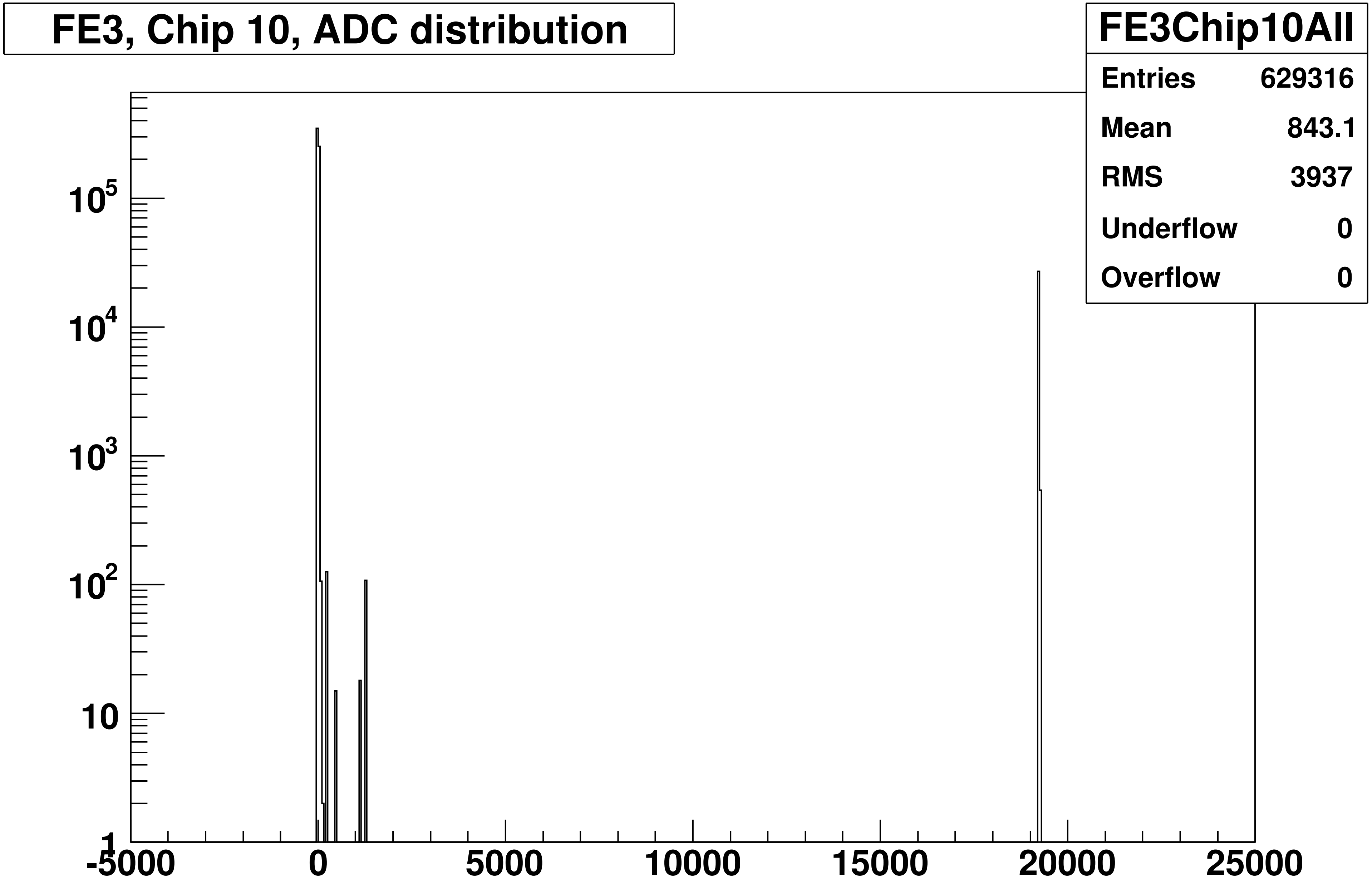
FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 629316
Mean 1.92e+04
RMS 7.875
Underflow 6.002e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 629316
Mean 888.3
RMS 4028
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

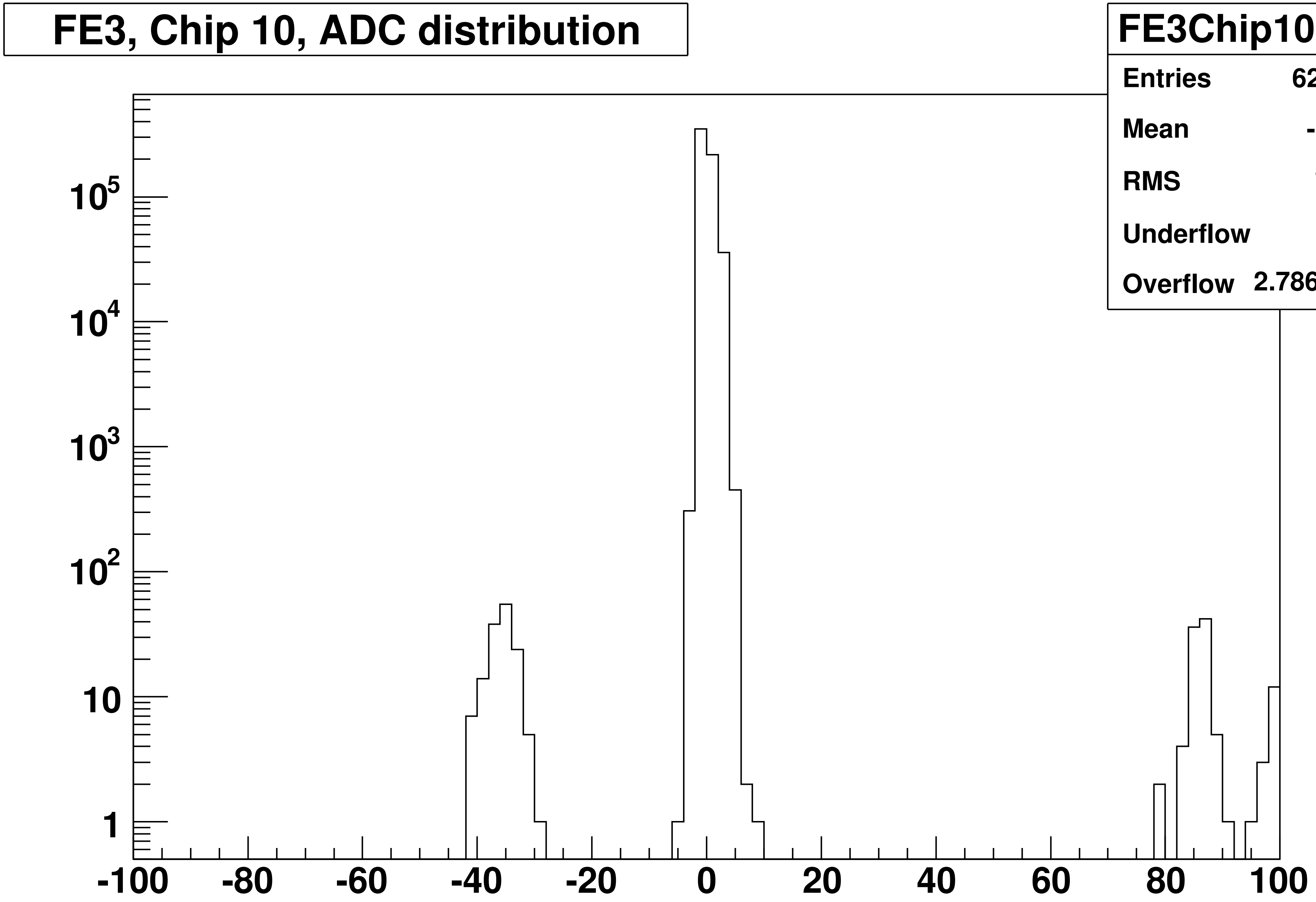

FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 629316
Mean -1.683
RMS 0.9405
Underflow 0
Overflow 2.979e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

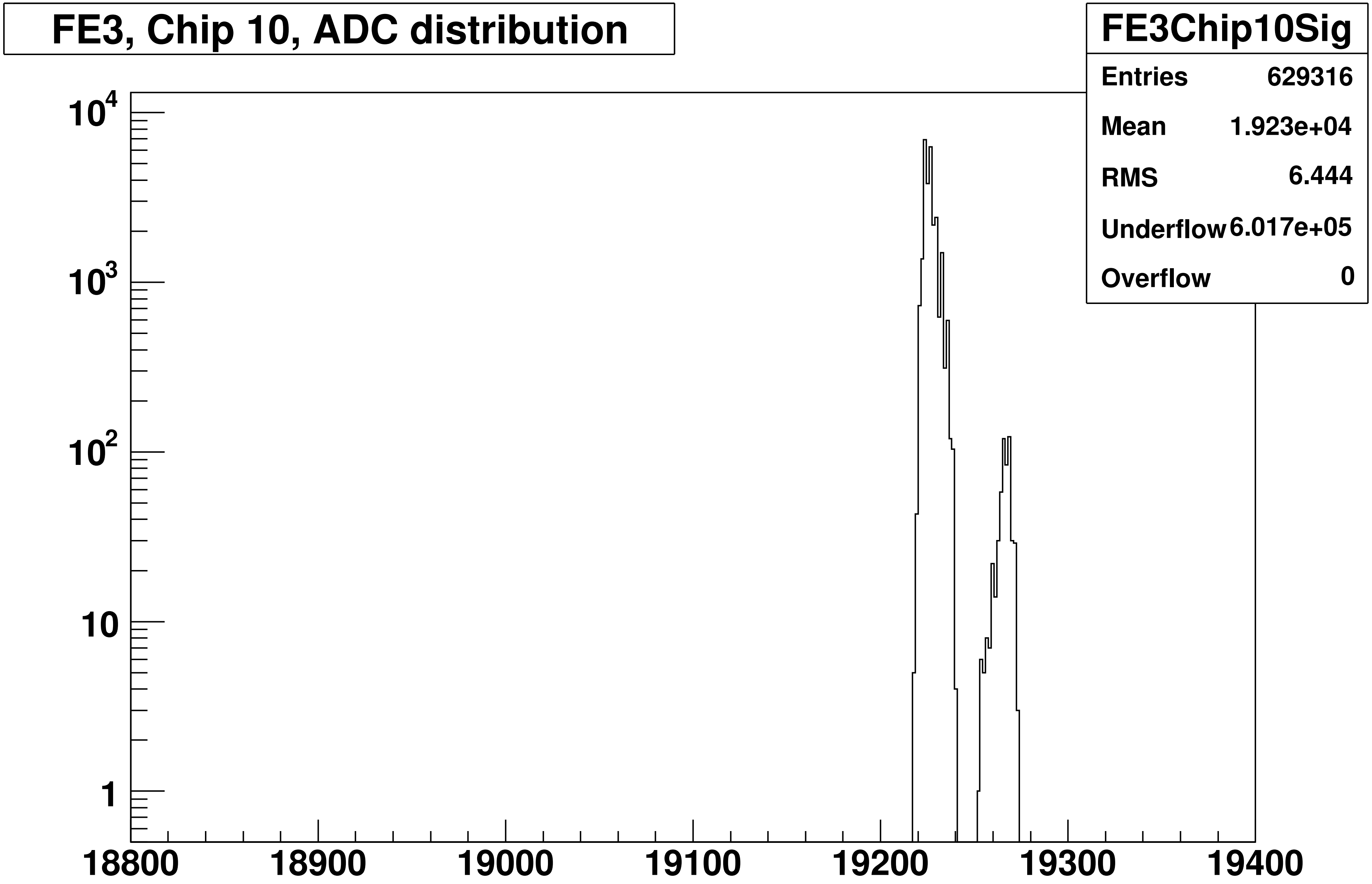


FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 629316
Mean 1.913e+04
RMS 12.68
Underflow 6.001e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

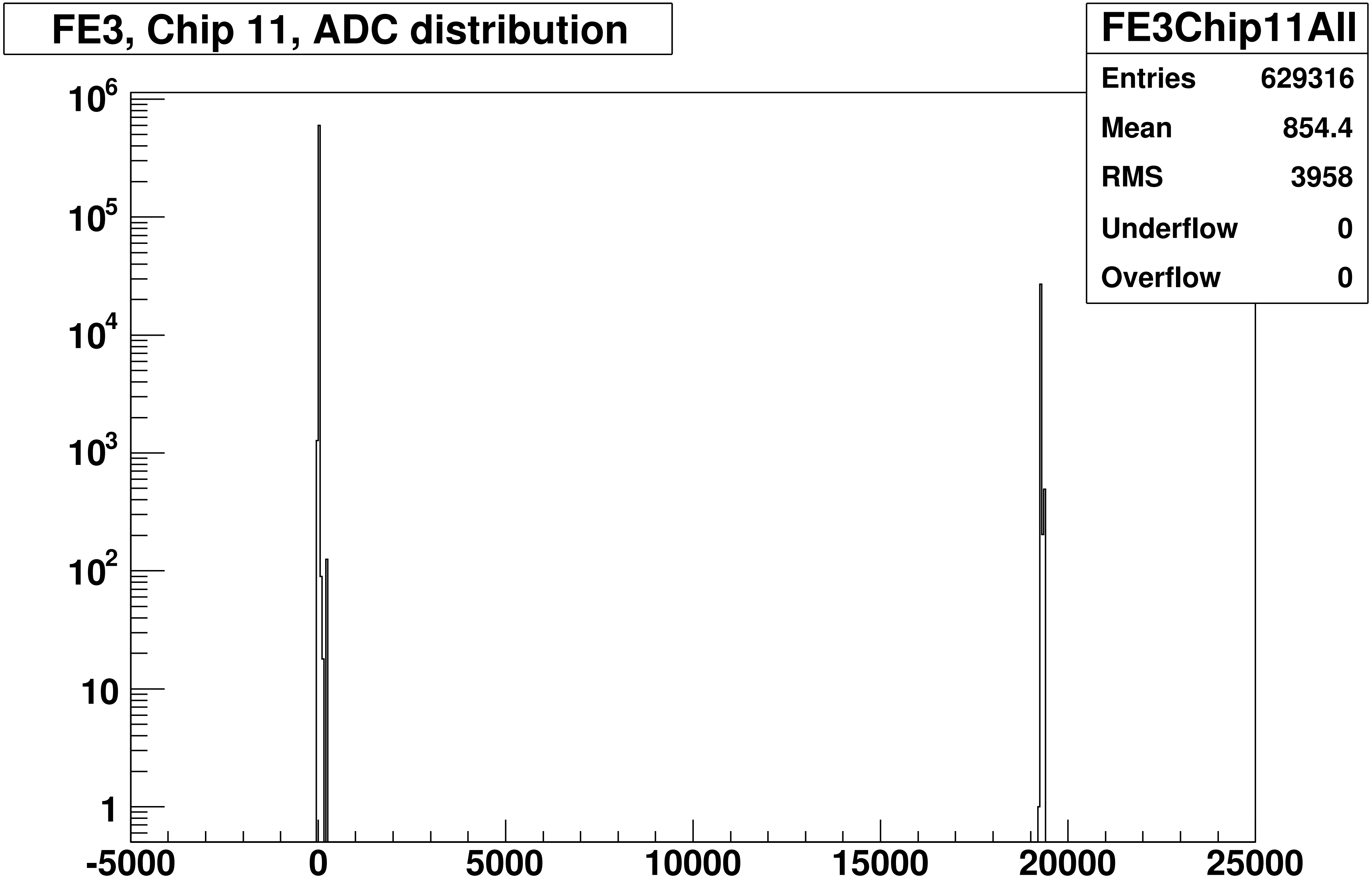


FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 629316
Mean 843.1
RMS 3937
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

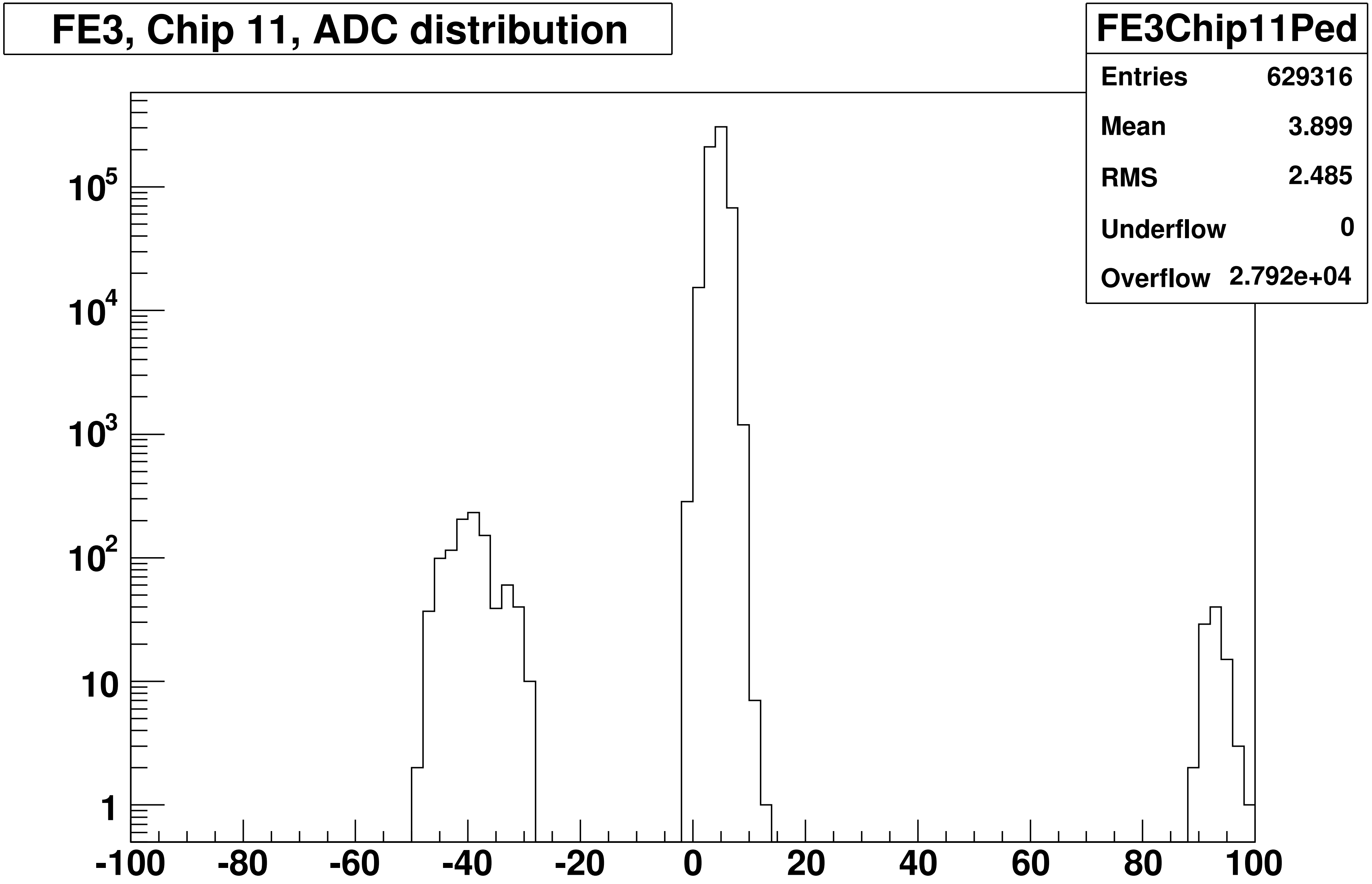

FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 629316
Mean -0.318
RMS 1.607
Underflow 0
Overflow 2.786e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

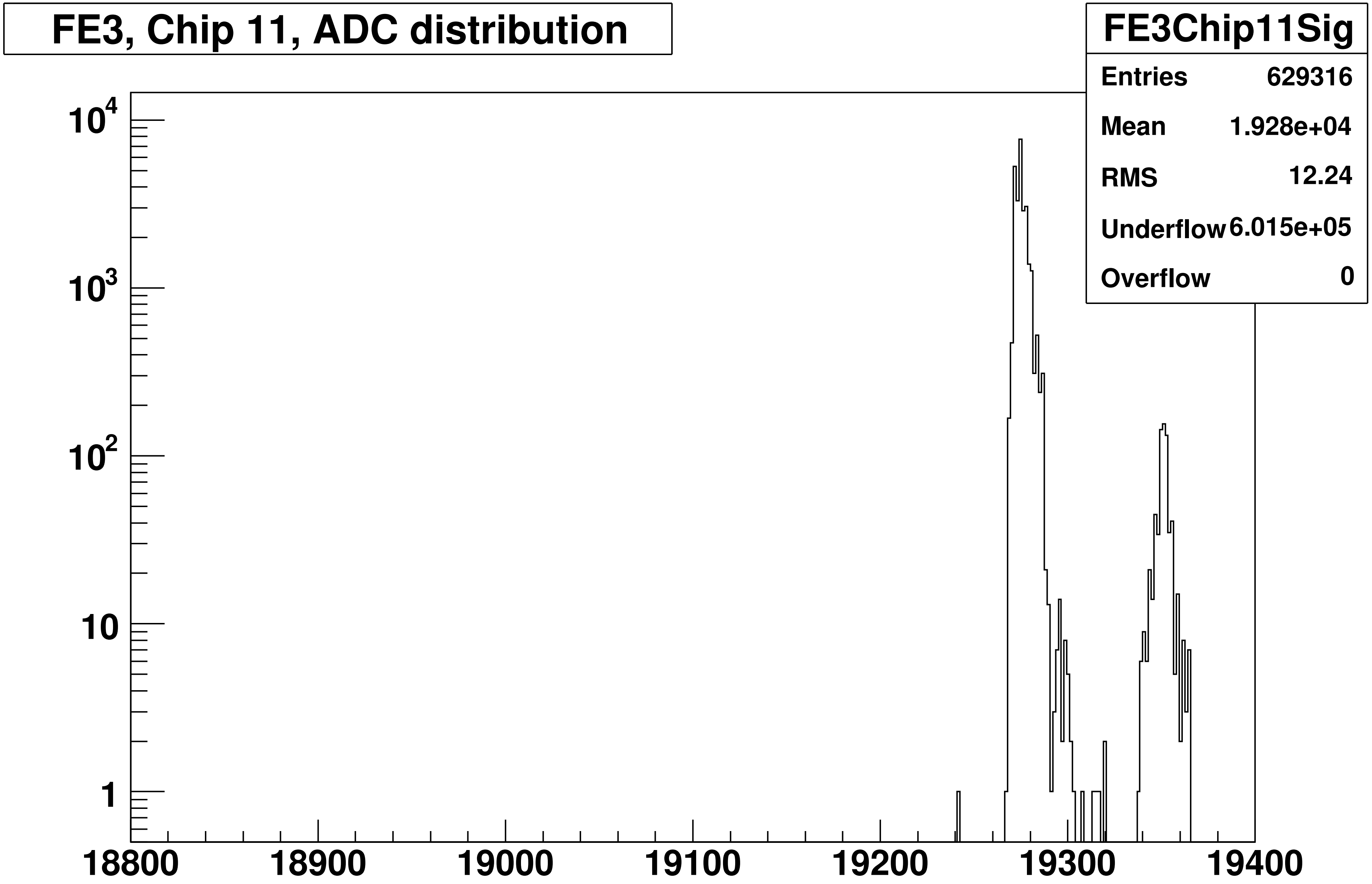


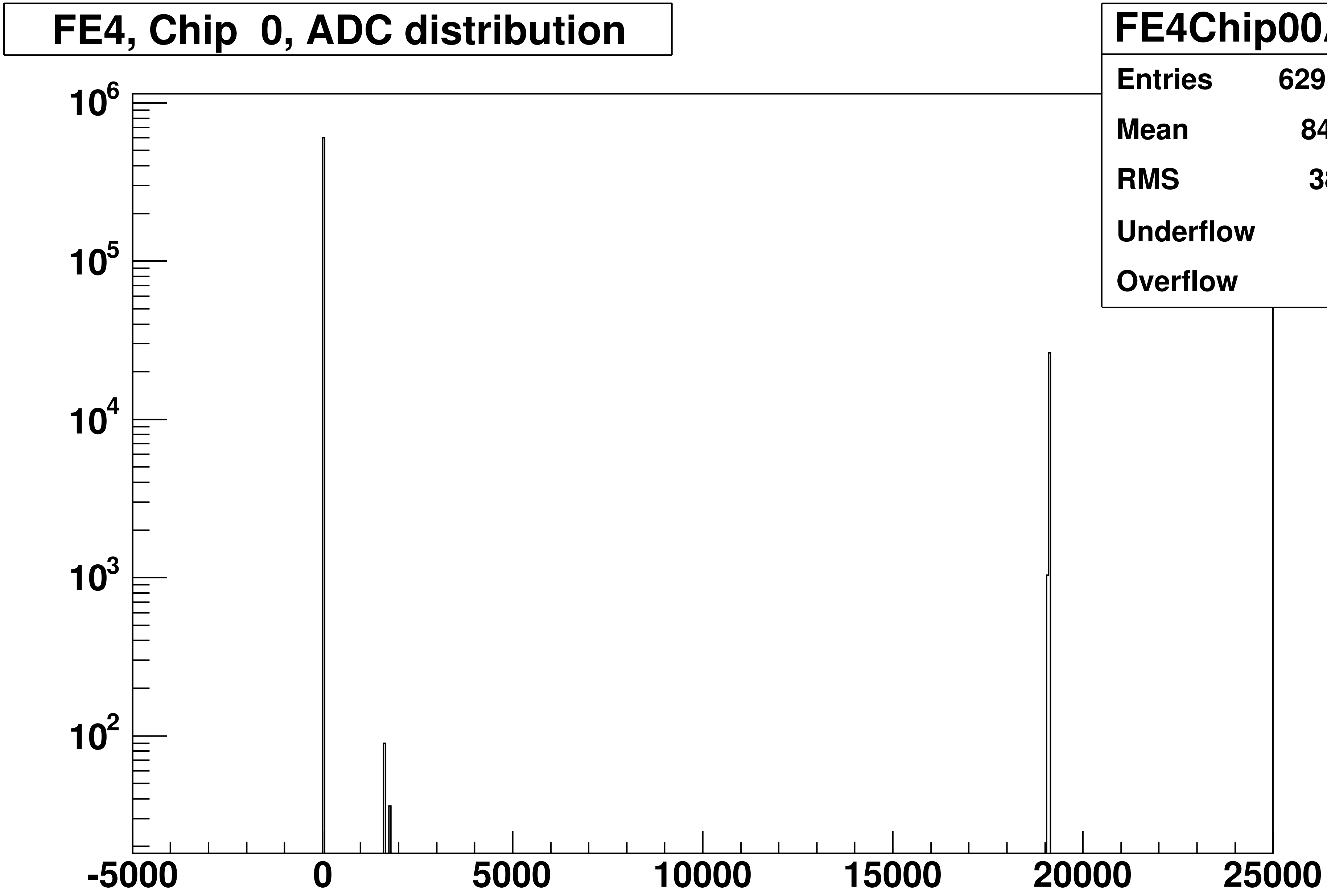
FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 629316
Mean 1.923e+04
RMS 6.444
Underflow 6.017e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

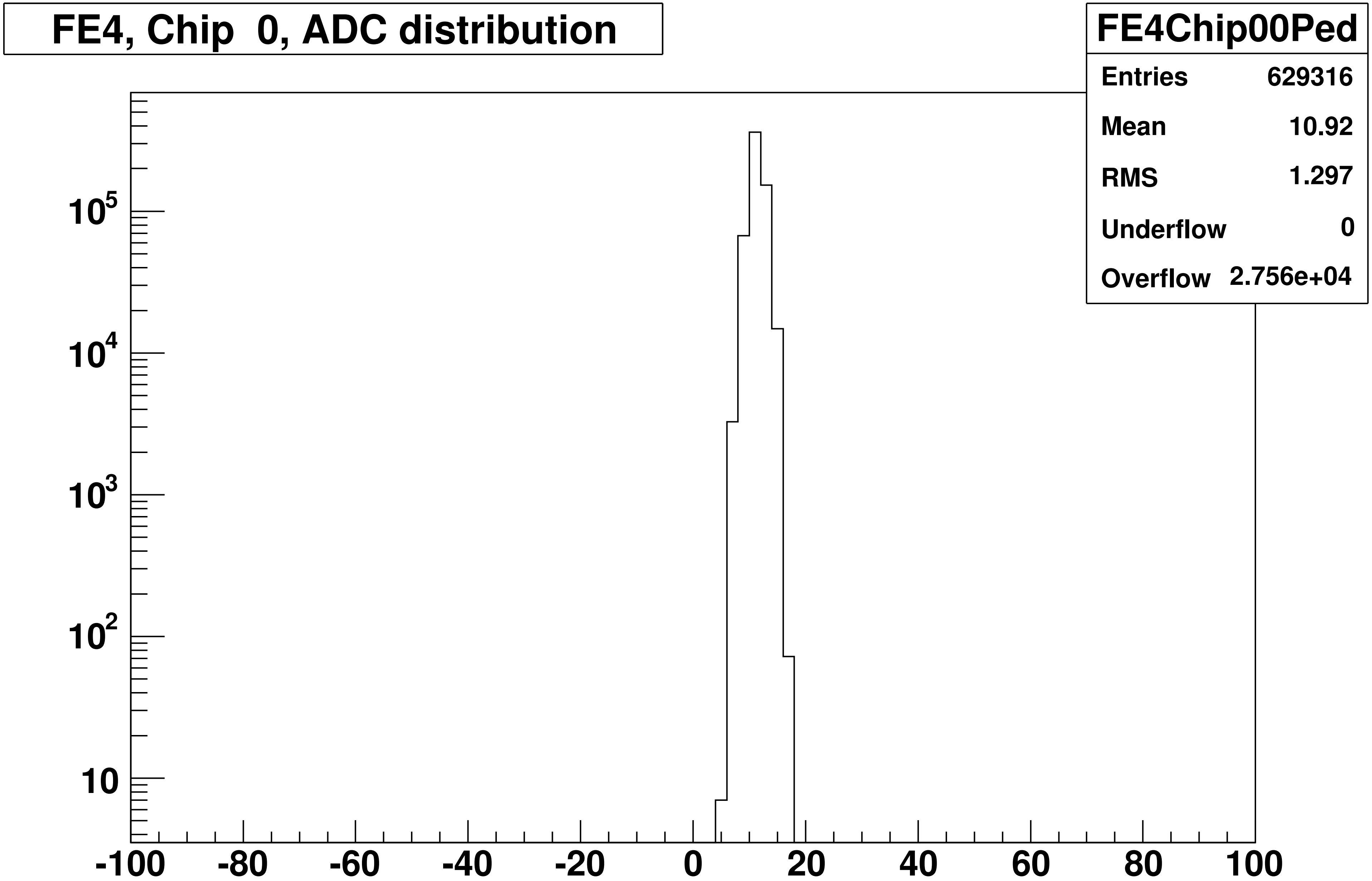


FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 629316
Mean 854.4
RMS 3958
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

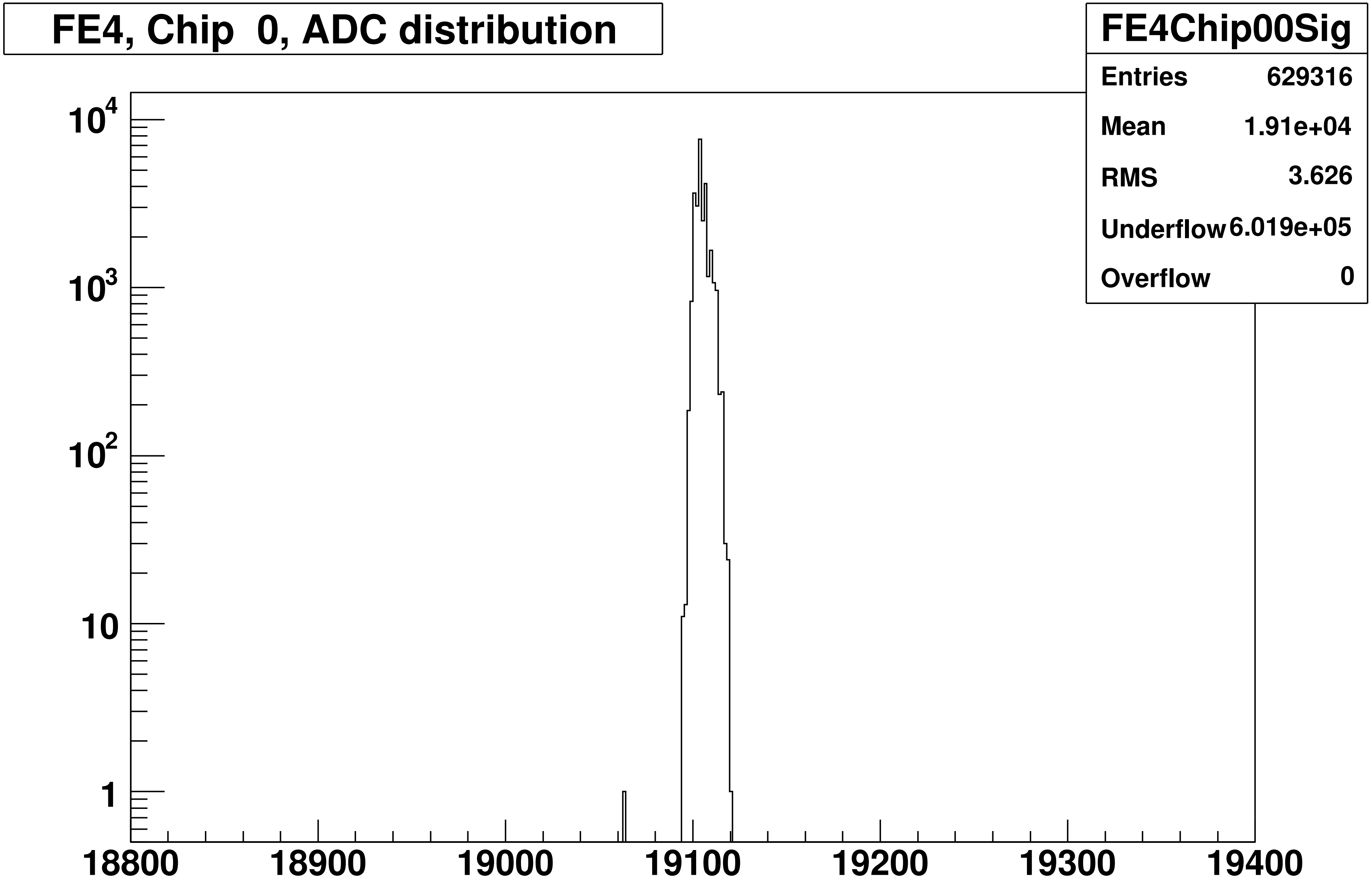

FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 629316
Mean 3.899
RMS 2.485
Underflow 0
Overflow 2.792e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 629316
Mean 1.928e+04
RMS 12.24
Underflow 6.015e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

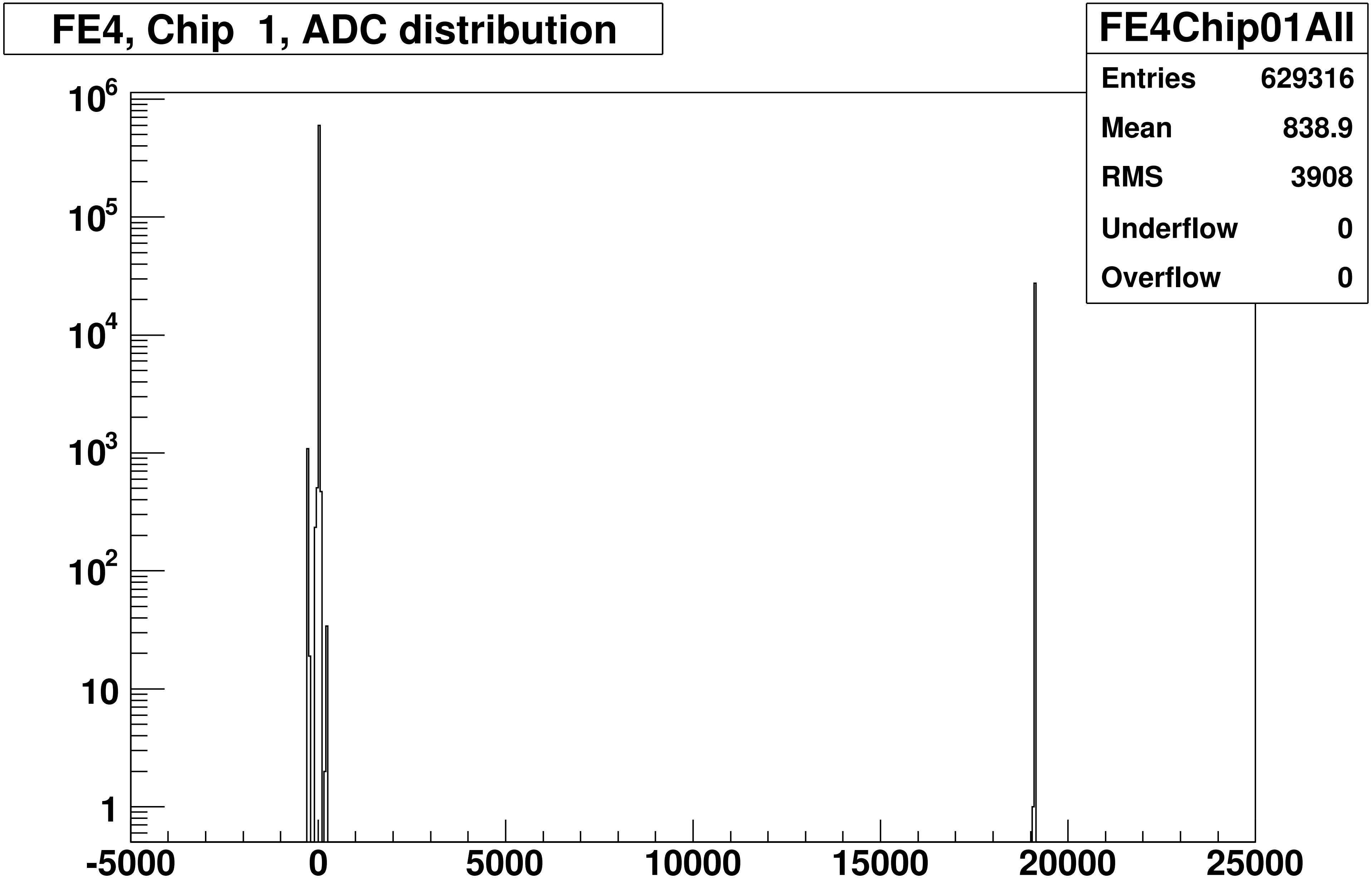

FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 629316
Mean 843.6
RMS 3899
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

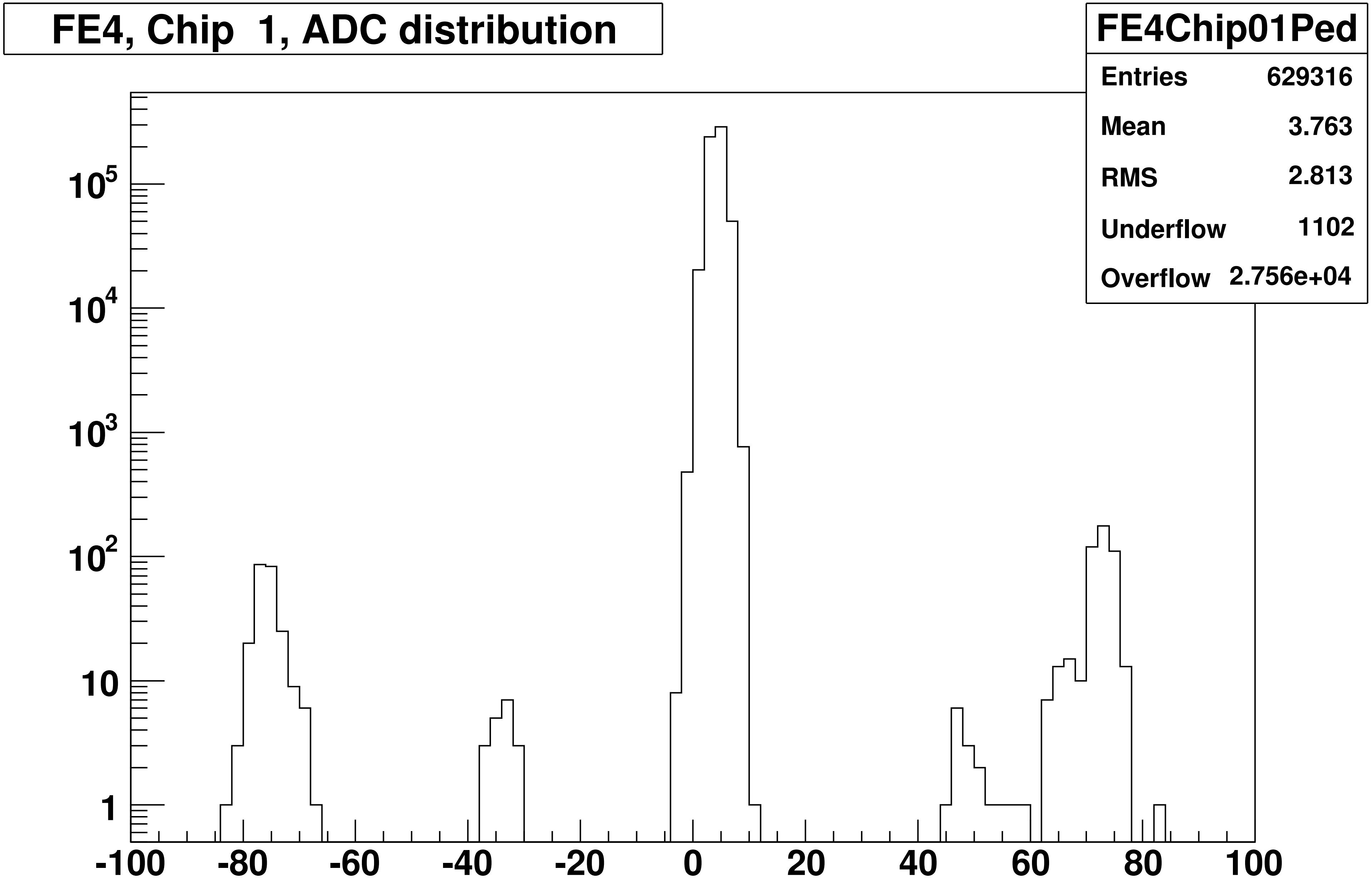


FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 629316
Mean 10.92
RMS 1.297
Underflow 0
Overflow 2.756e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

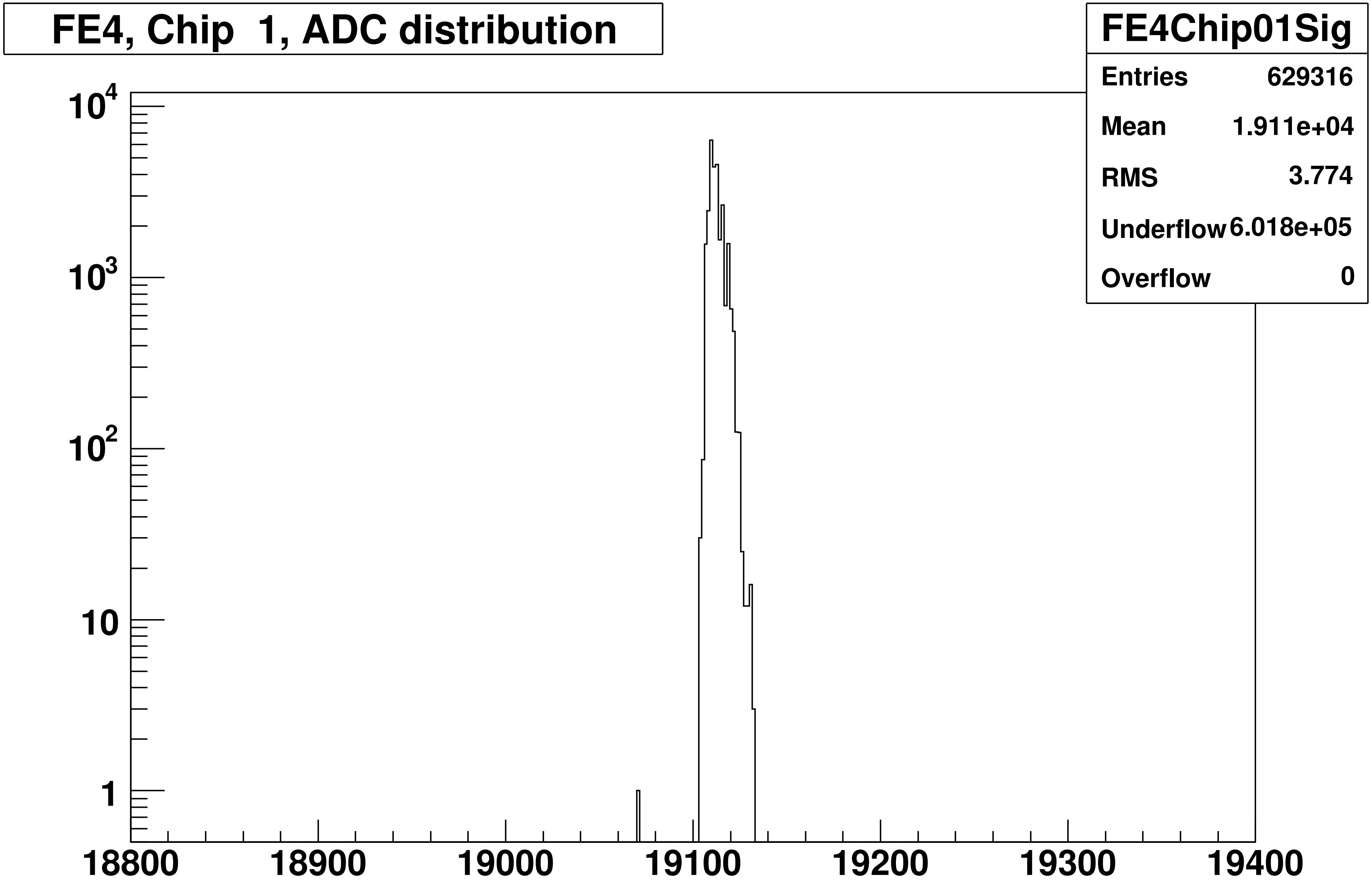


FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 629316
Mean 1.91e+04
RMS 3.626
Underflow 6.019e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

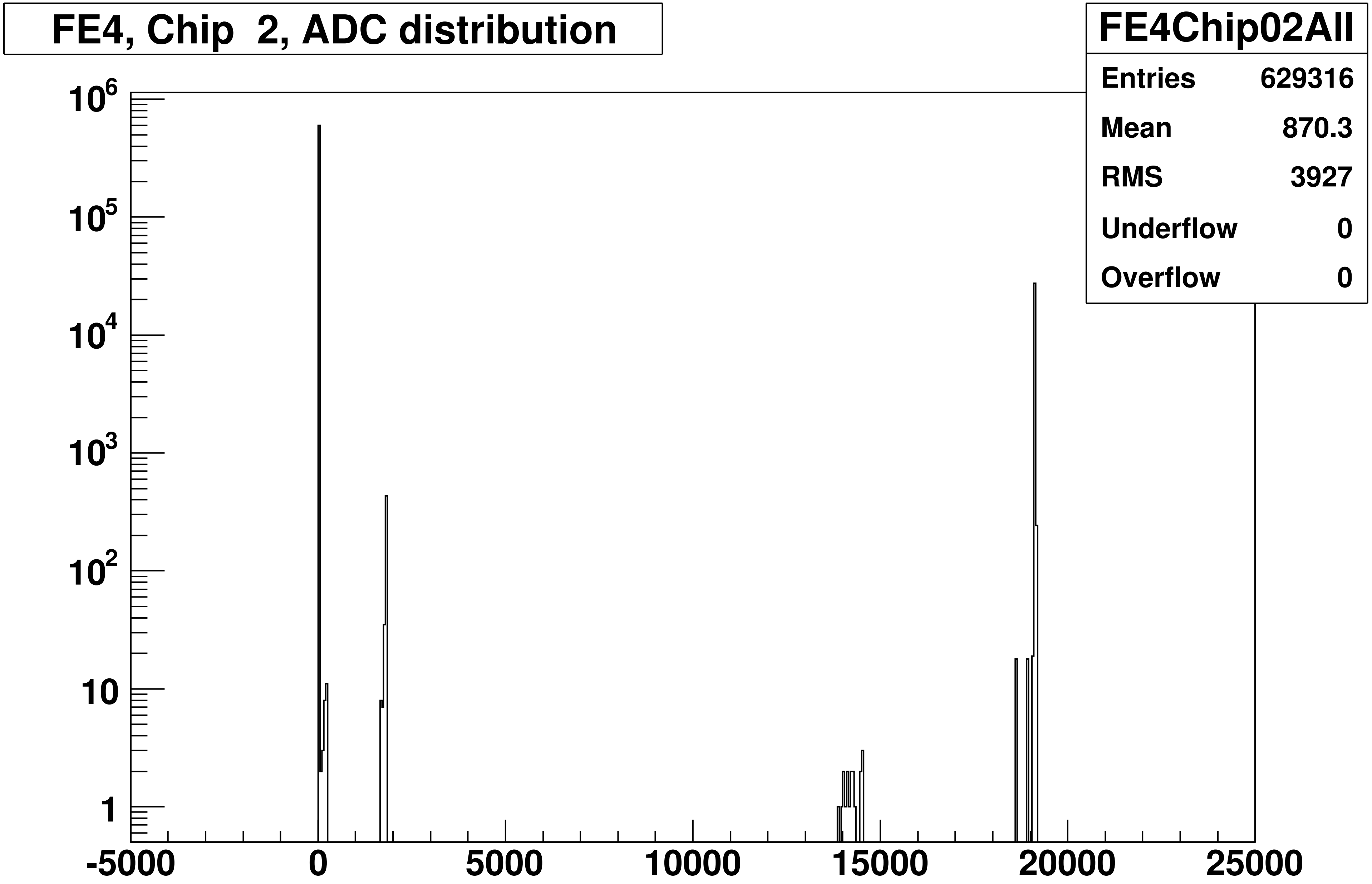

FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 629316
Mean 838.9
RMS 3908
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

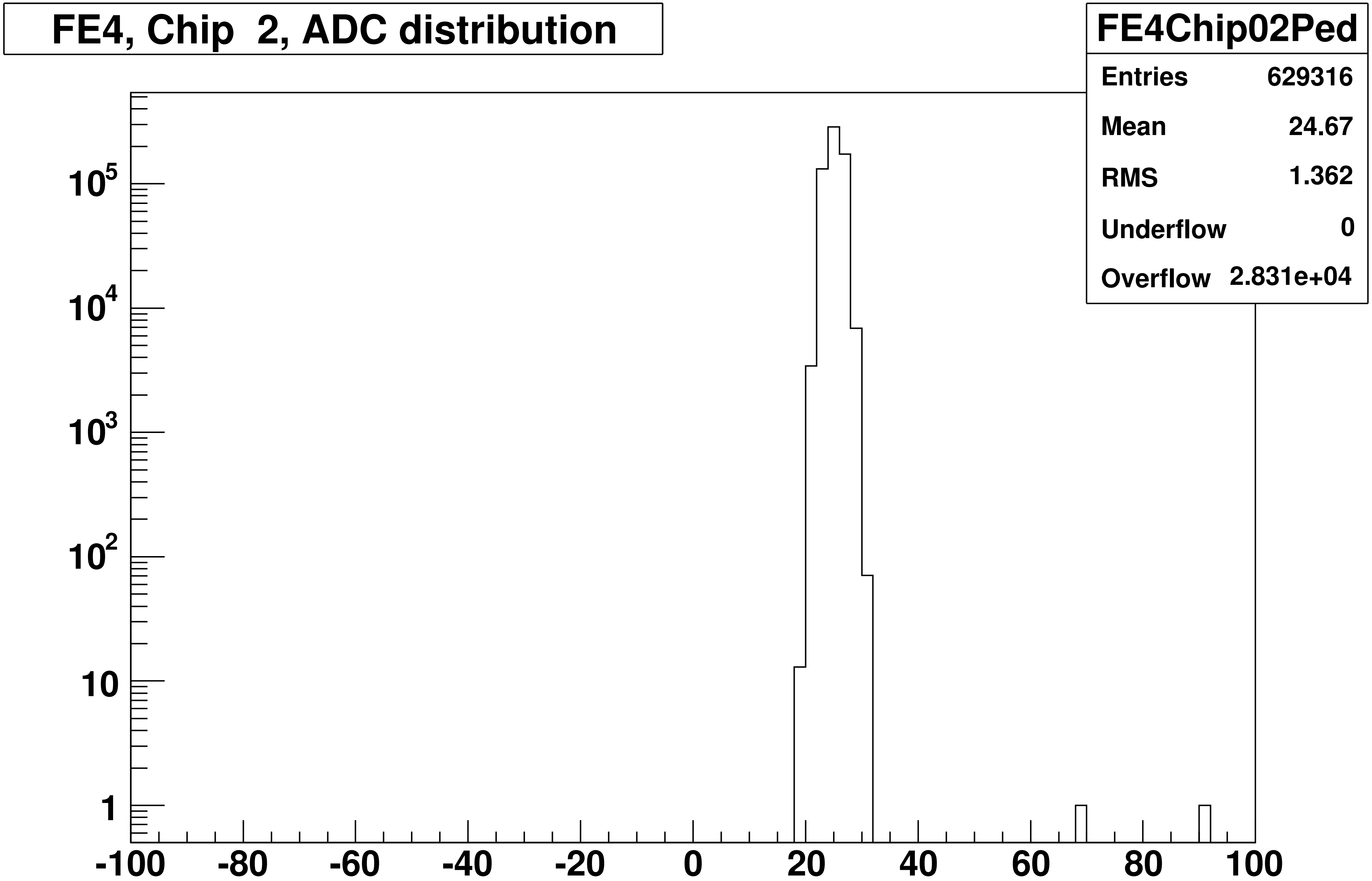


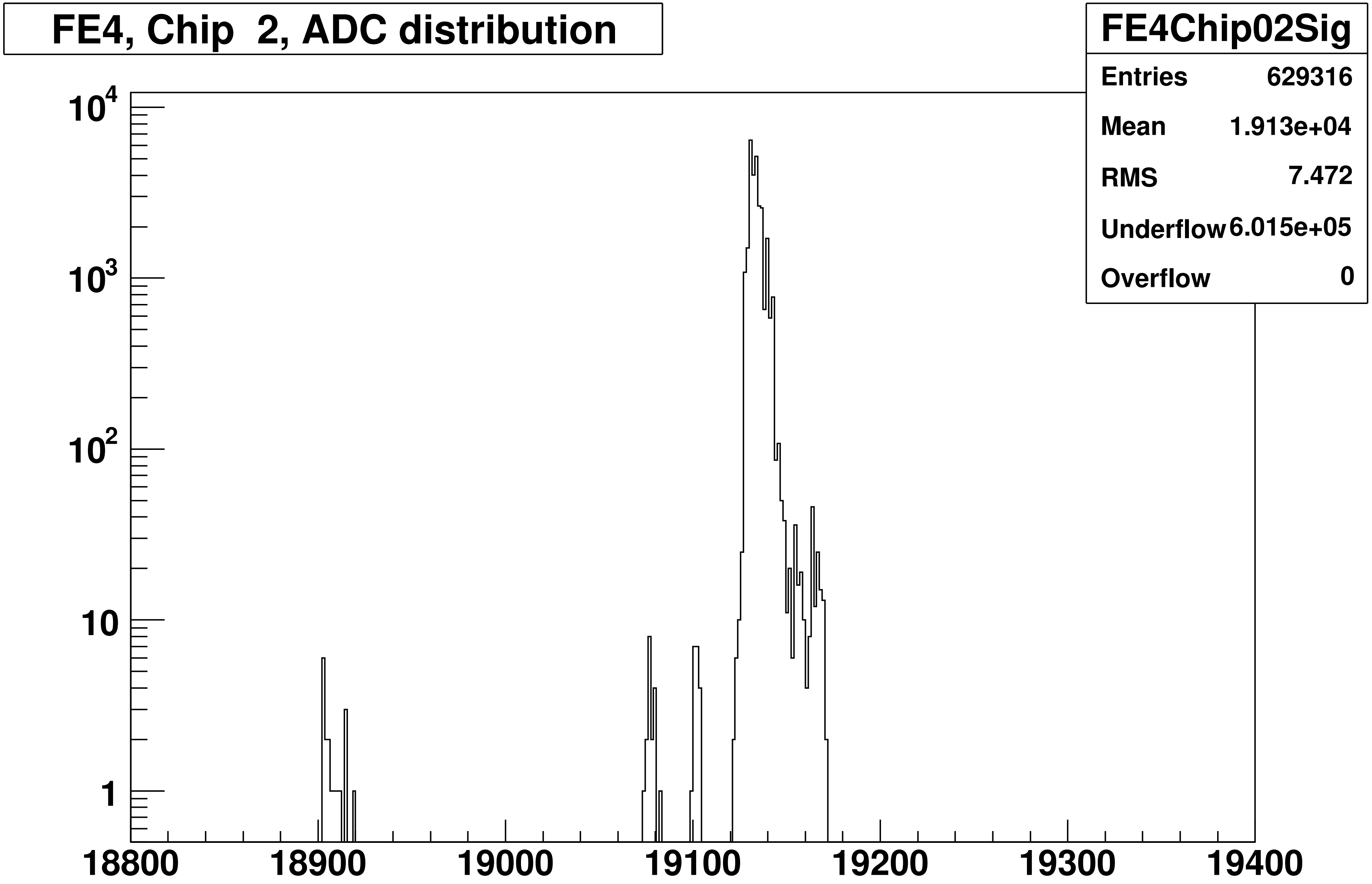
FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 629316
Mean 3.763
RMS 2.813
Underflow 1102
Overflow 2.756e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

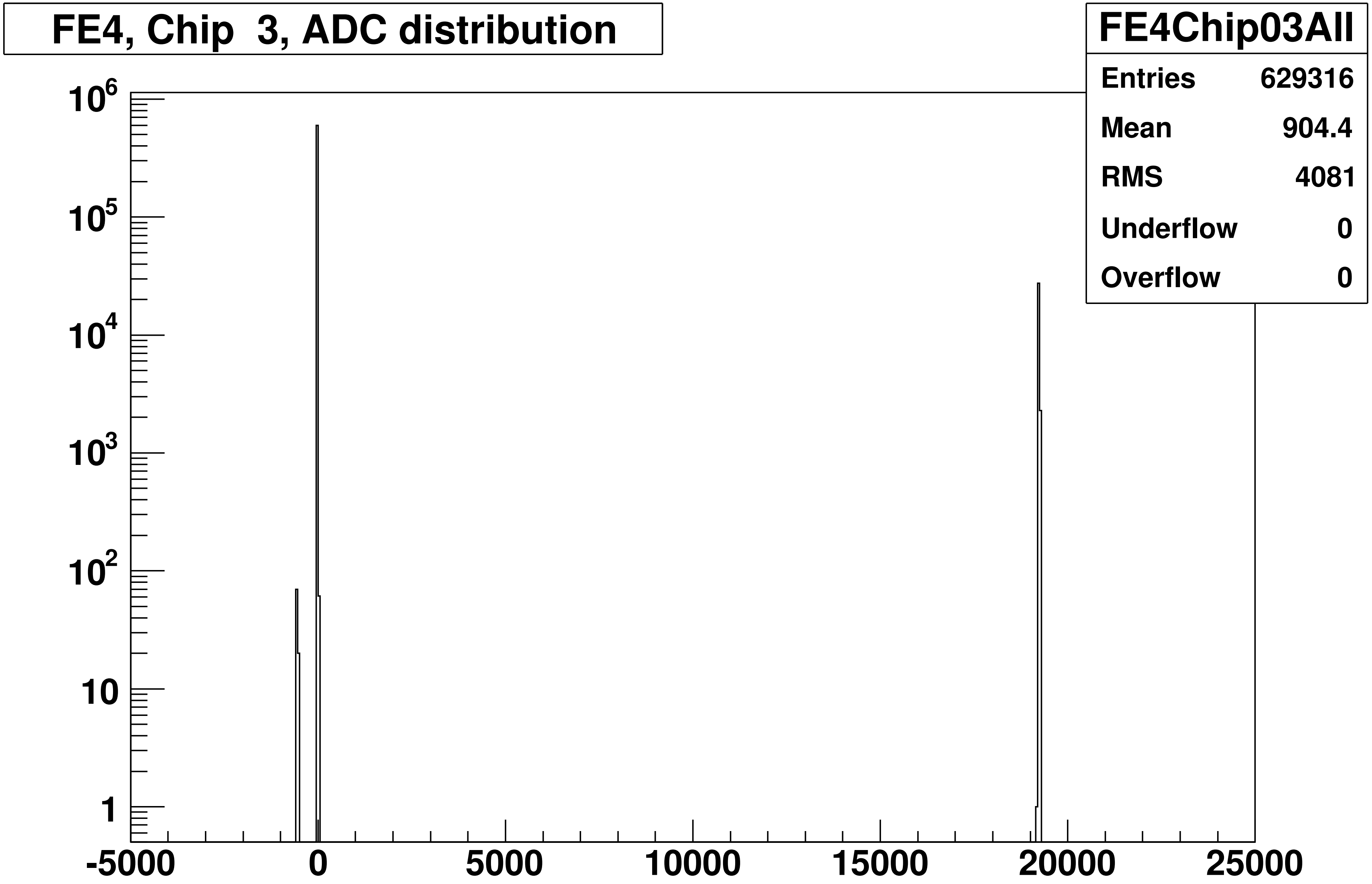


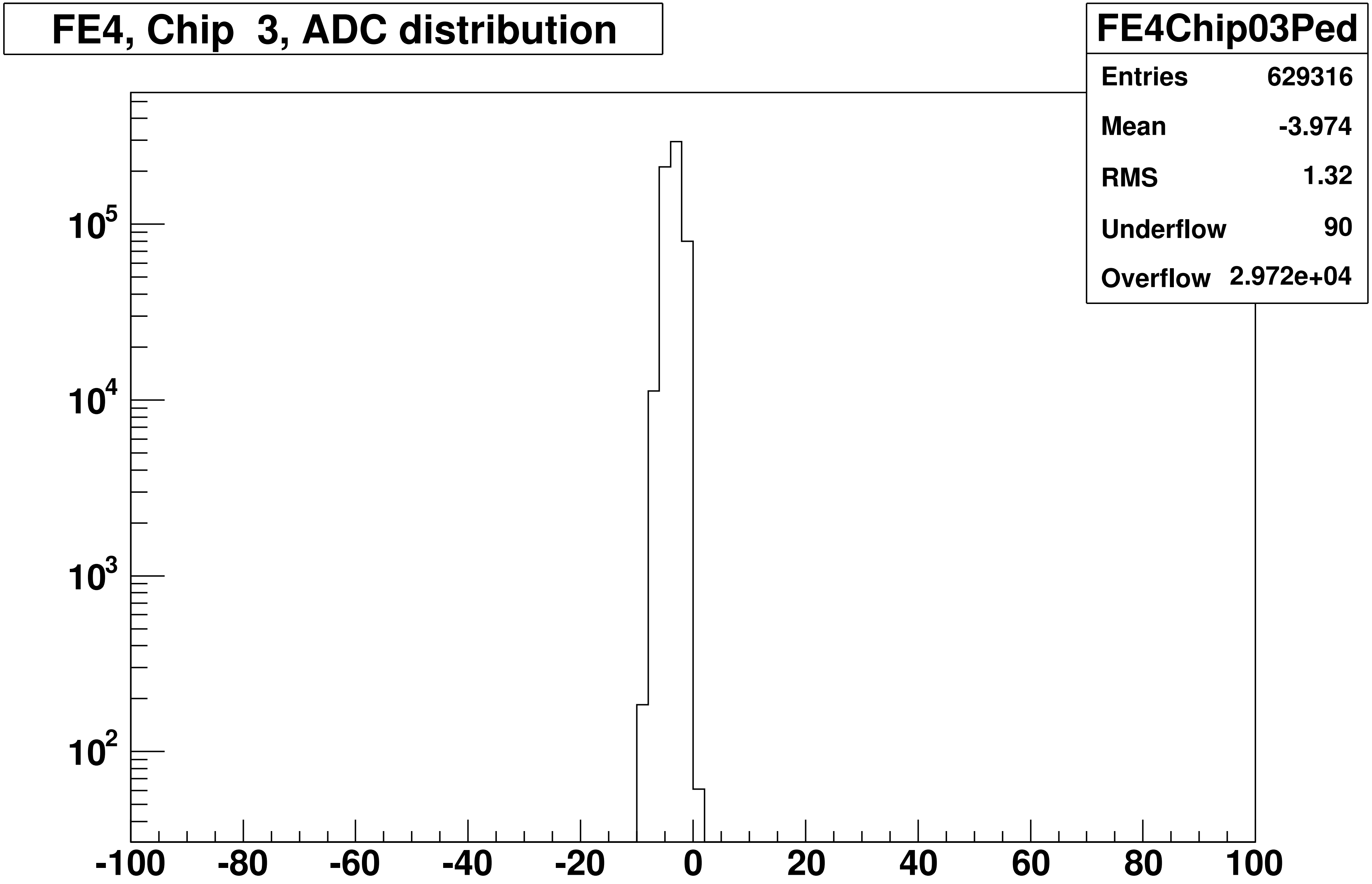
FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 629316
Mean 1.911e+04
RMS 3.774
Underflow 6.018e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 629316
Mean 870.3
RMS 3927
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

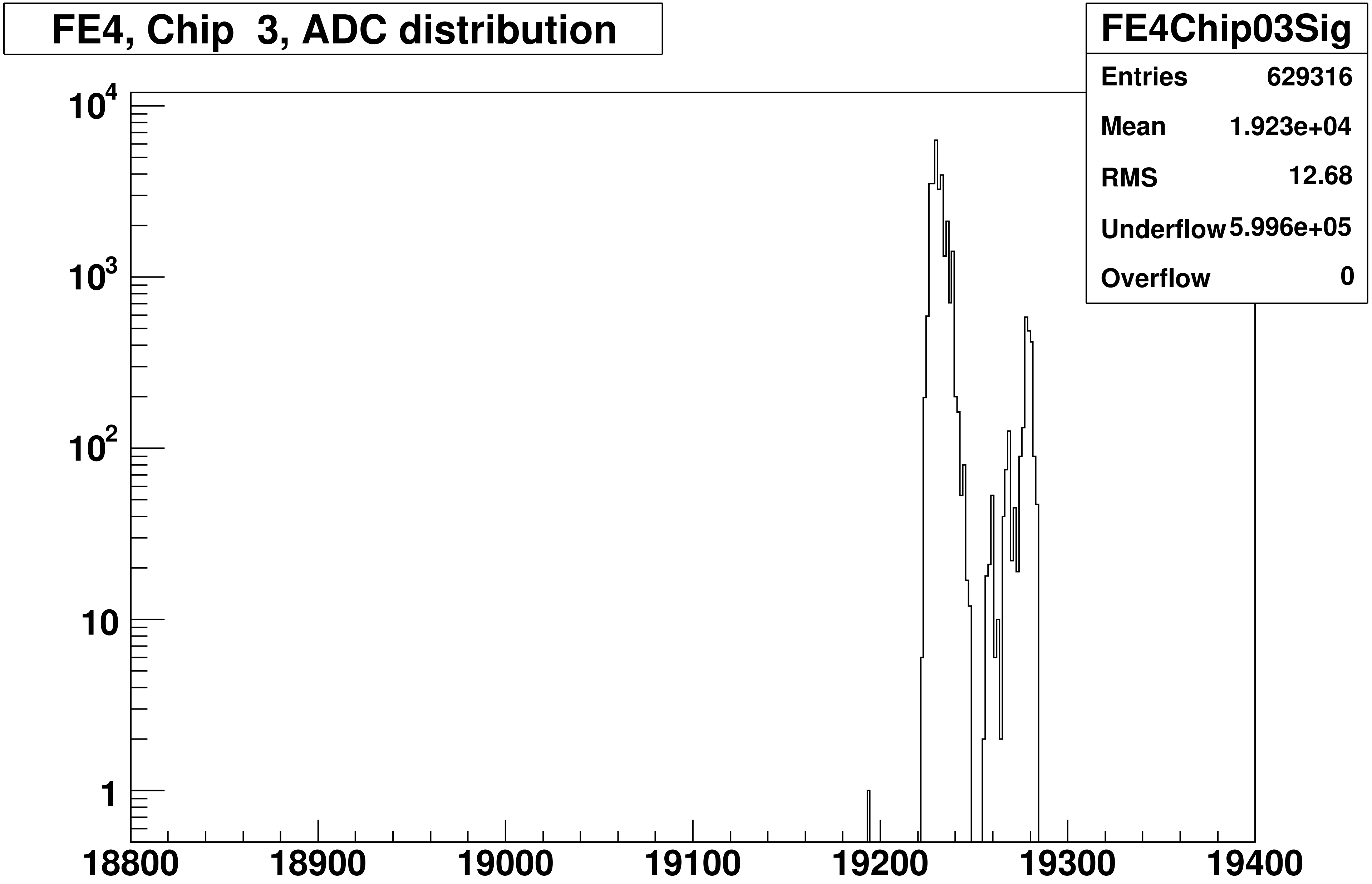

FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 629316
Mean 24.67
RMS 1.362
Underflow 0
Overflow 2.831e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

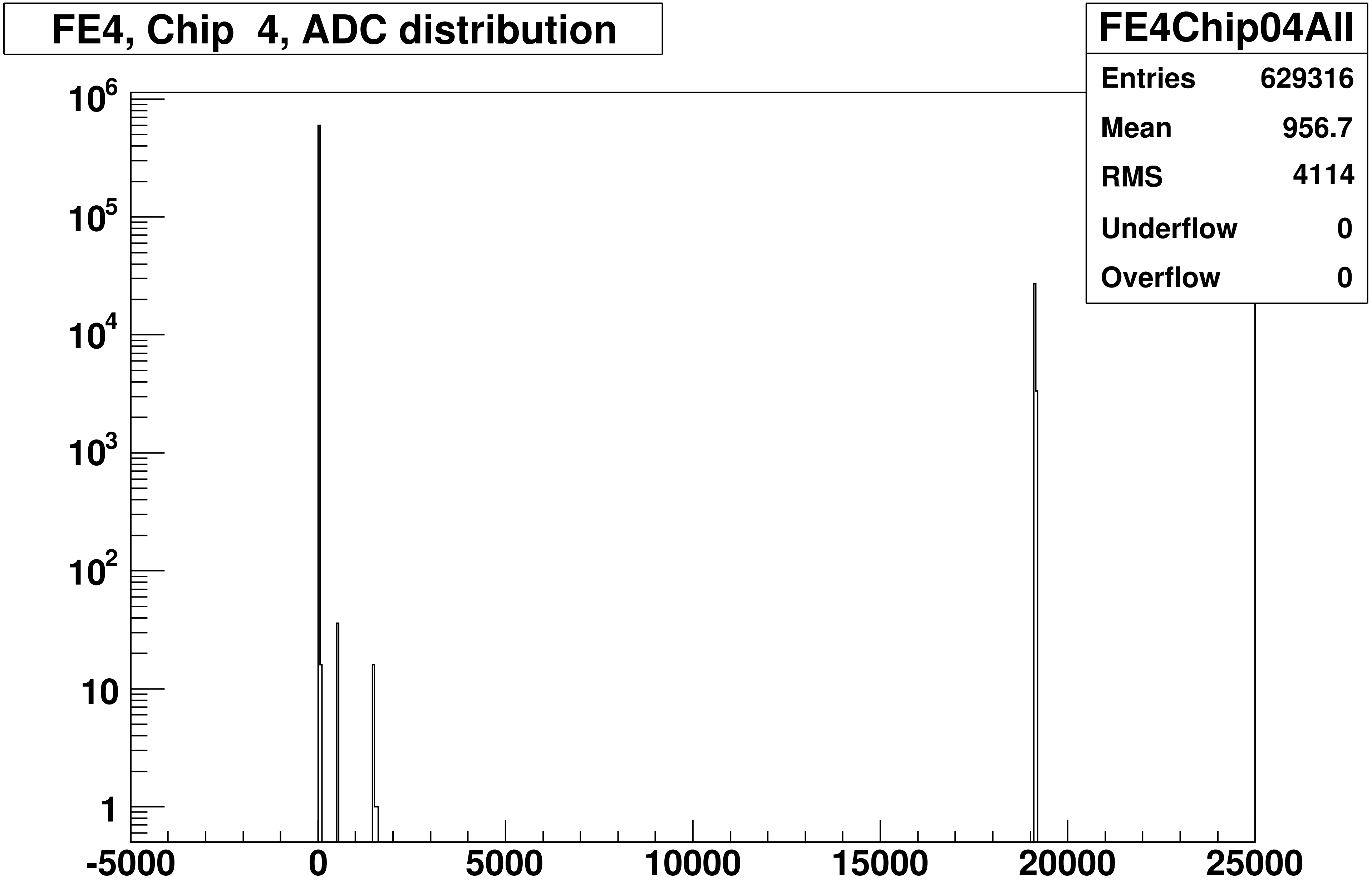

FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 629316
Mean 1.913e+04
RMS 7.472
Underflow 6.015e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

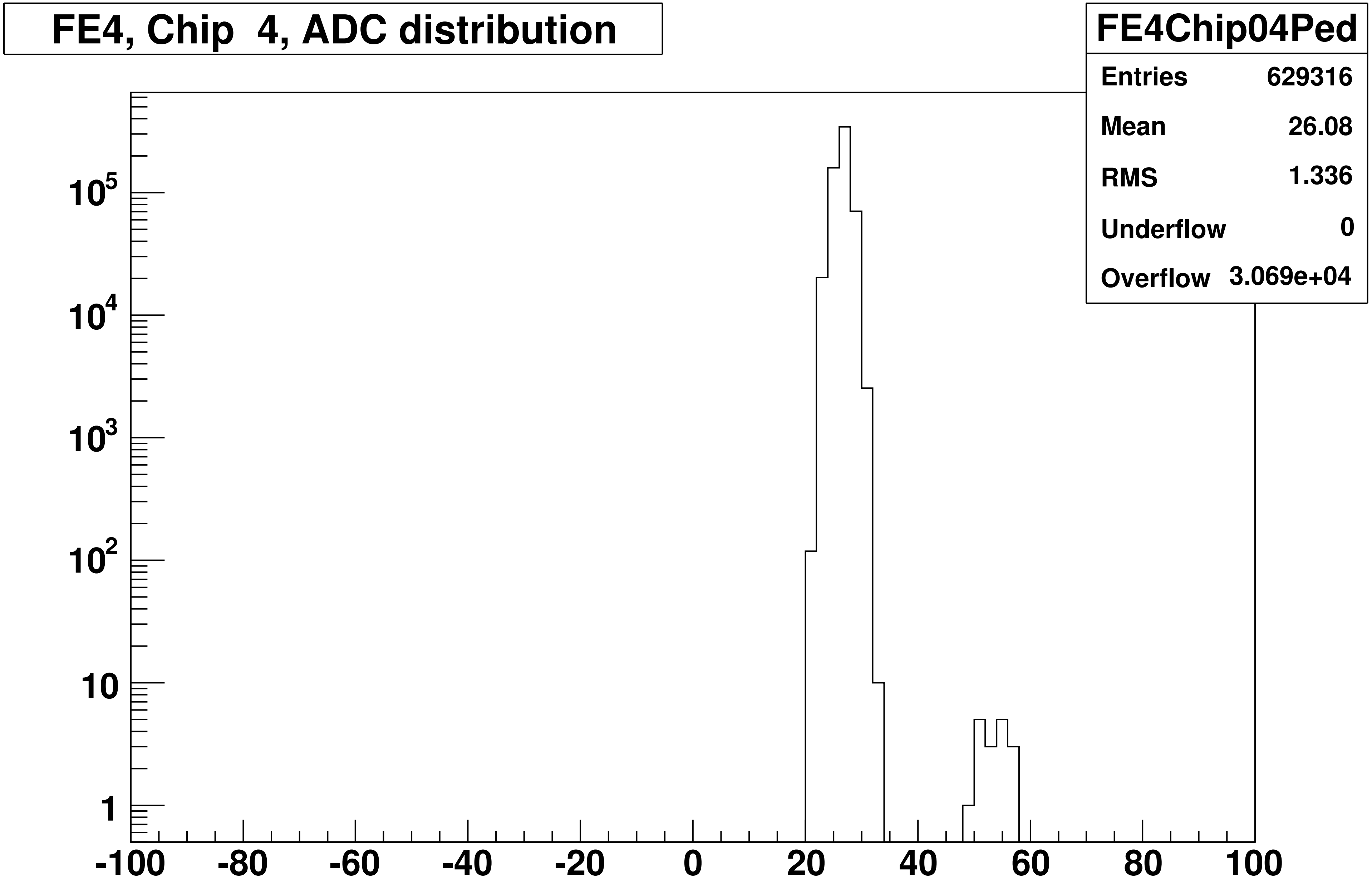

FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 629316
Mean 904.4
RMS 4081
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



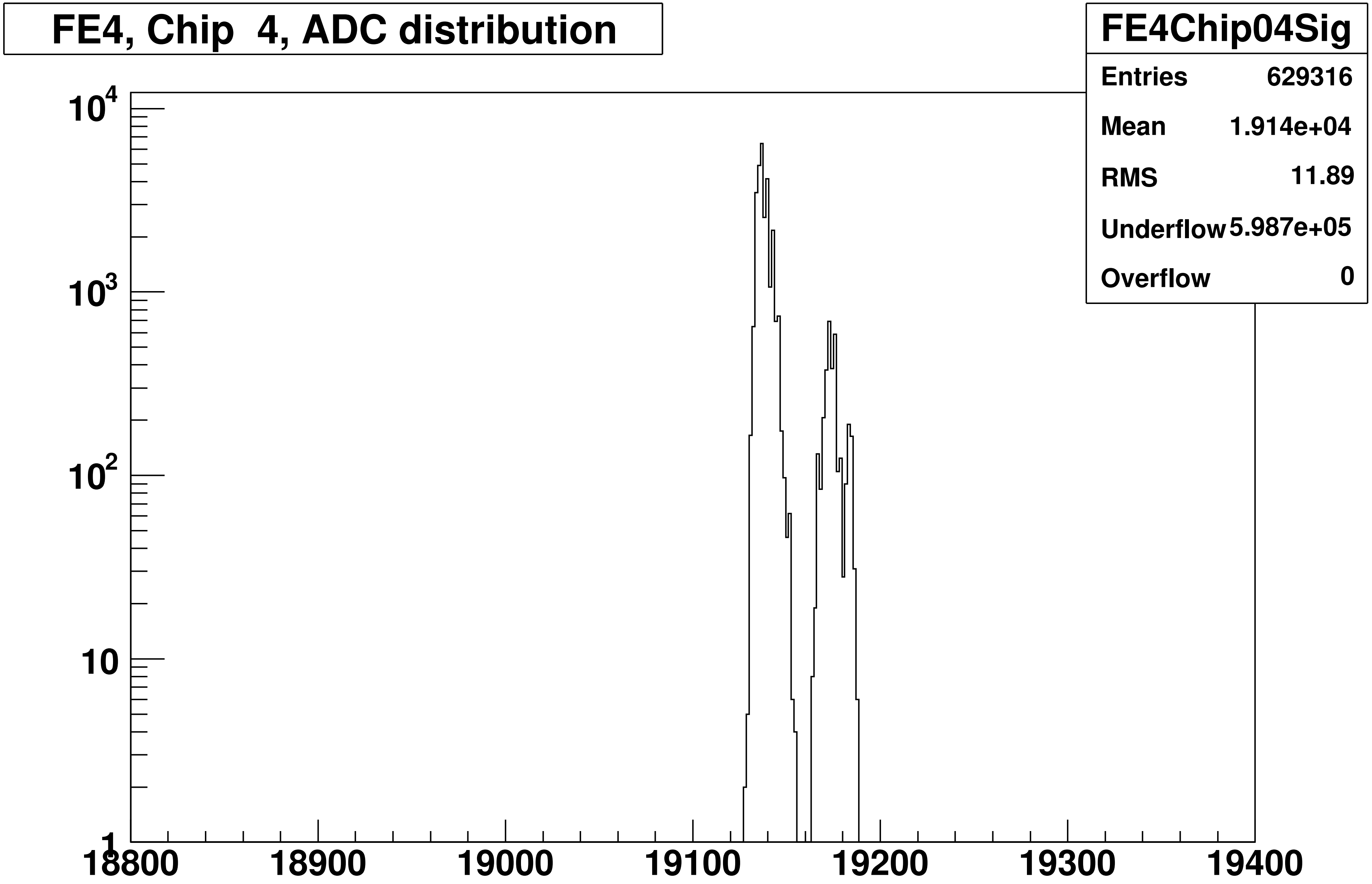
FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 629316
Mean -3.974
RMS 1.32
Underflow 90
Overflow 2.972e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 629316
Mean 1.923e+04
RMS 12.68
Underflow 5.996e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

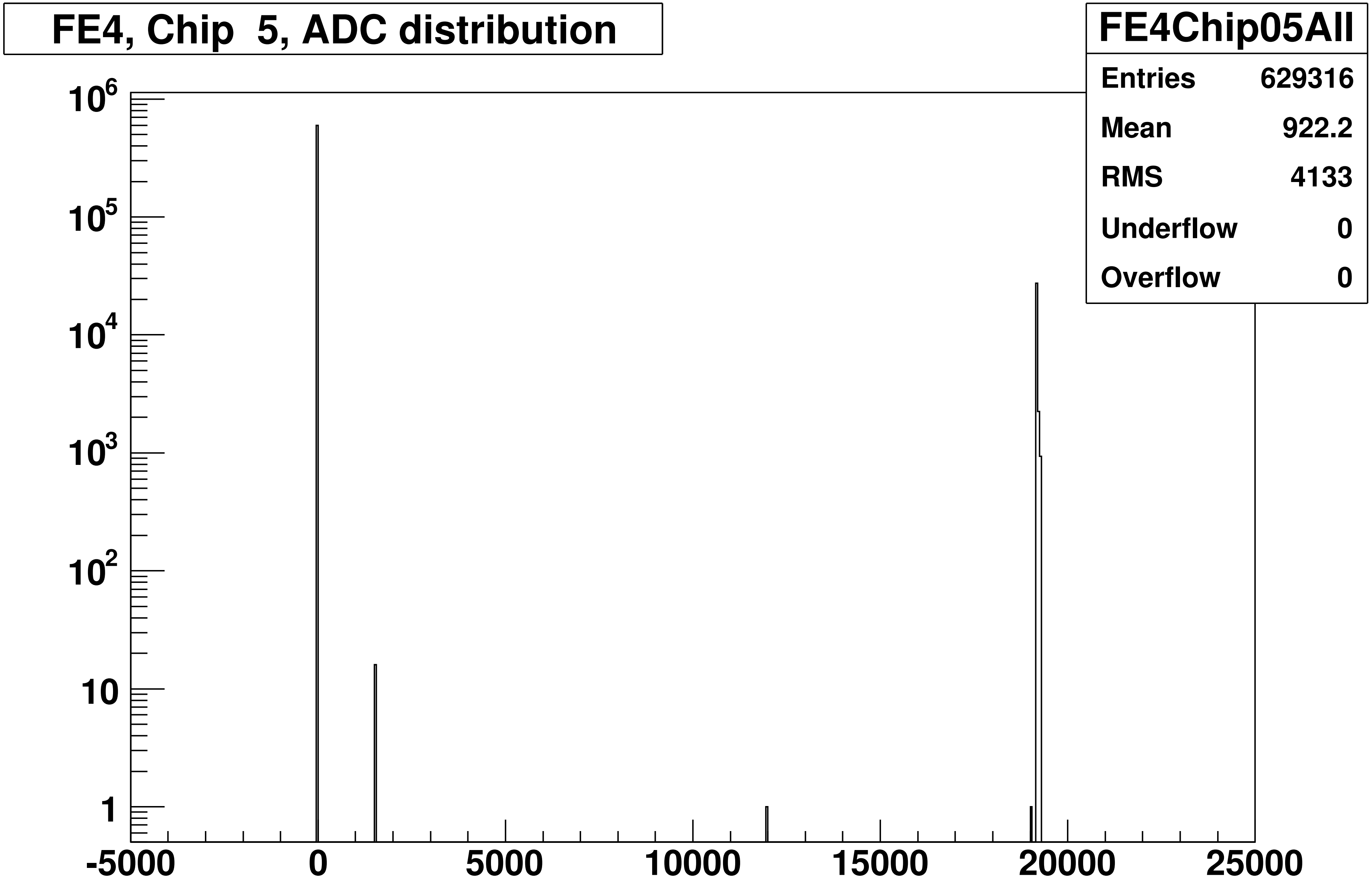

FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 629316
Mean 956.7
RMS 4114
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



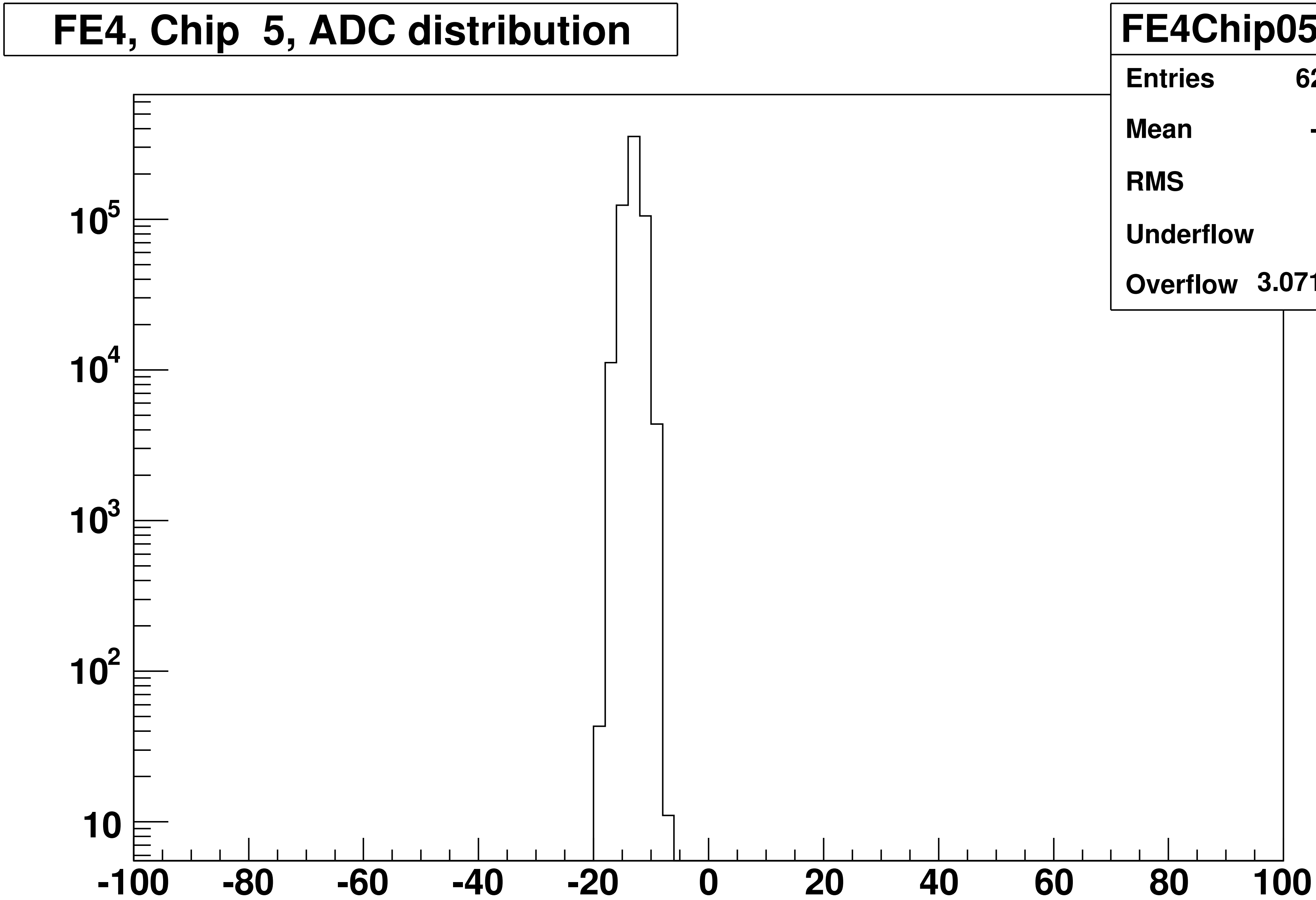
FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 629316
Mean 26.08
RMS 1.336
Underflow 0
Overflow 3.069e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



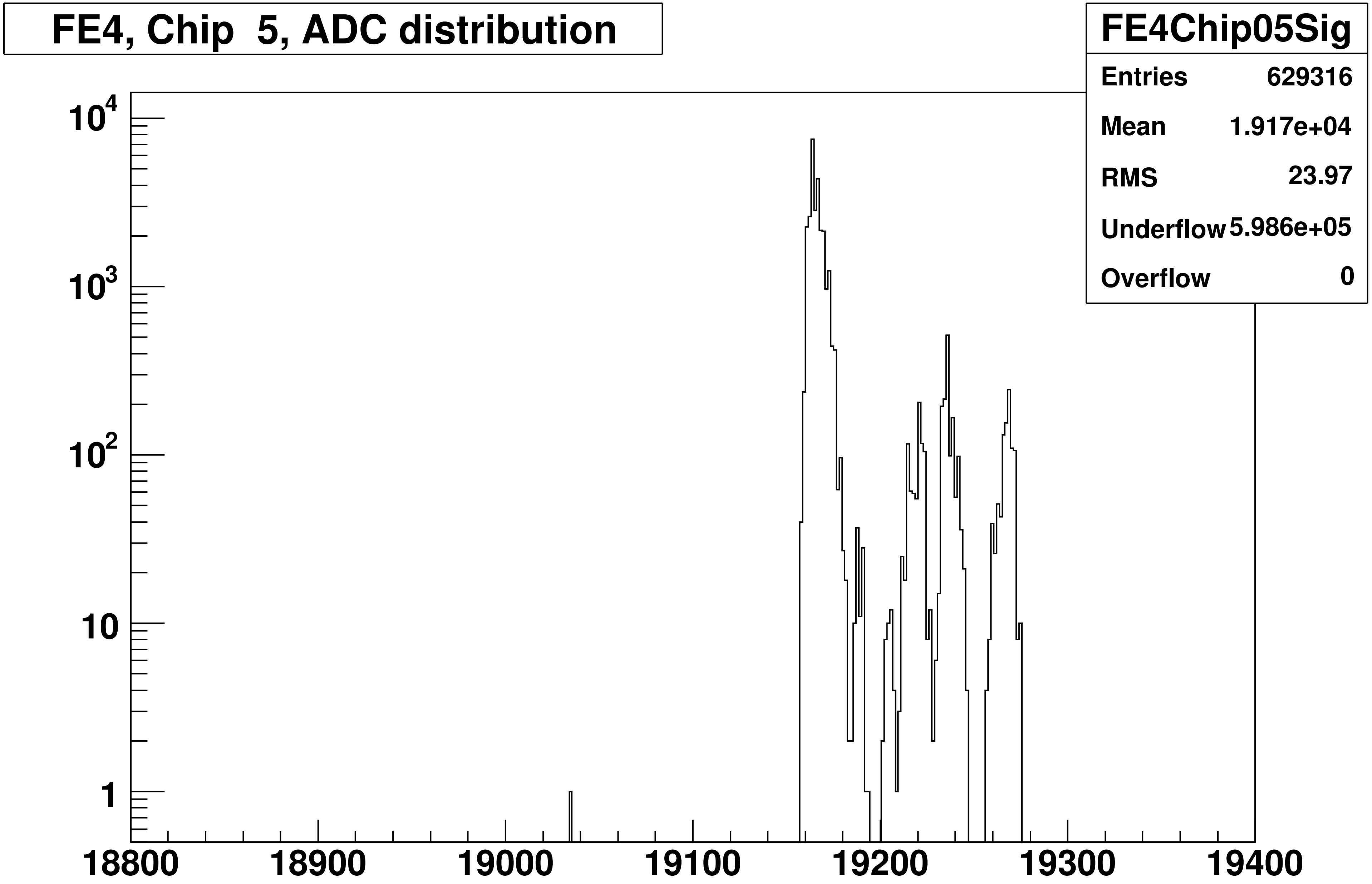
FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 629316
Mean 1.914e+04
RMS 11.89
Underflow 5.987e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

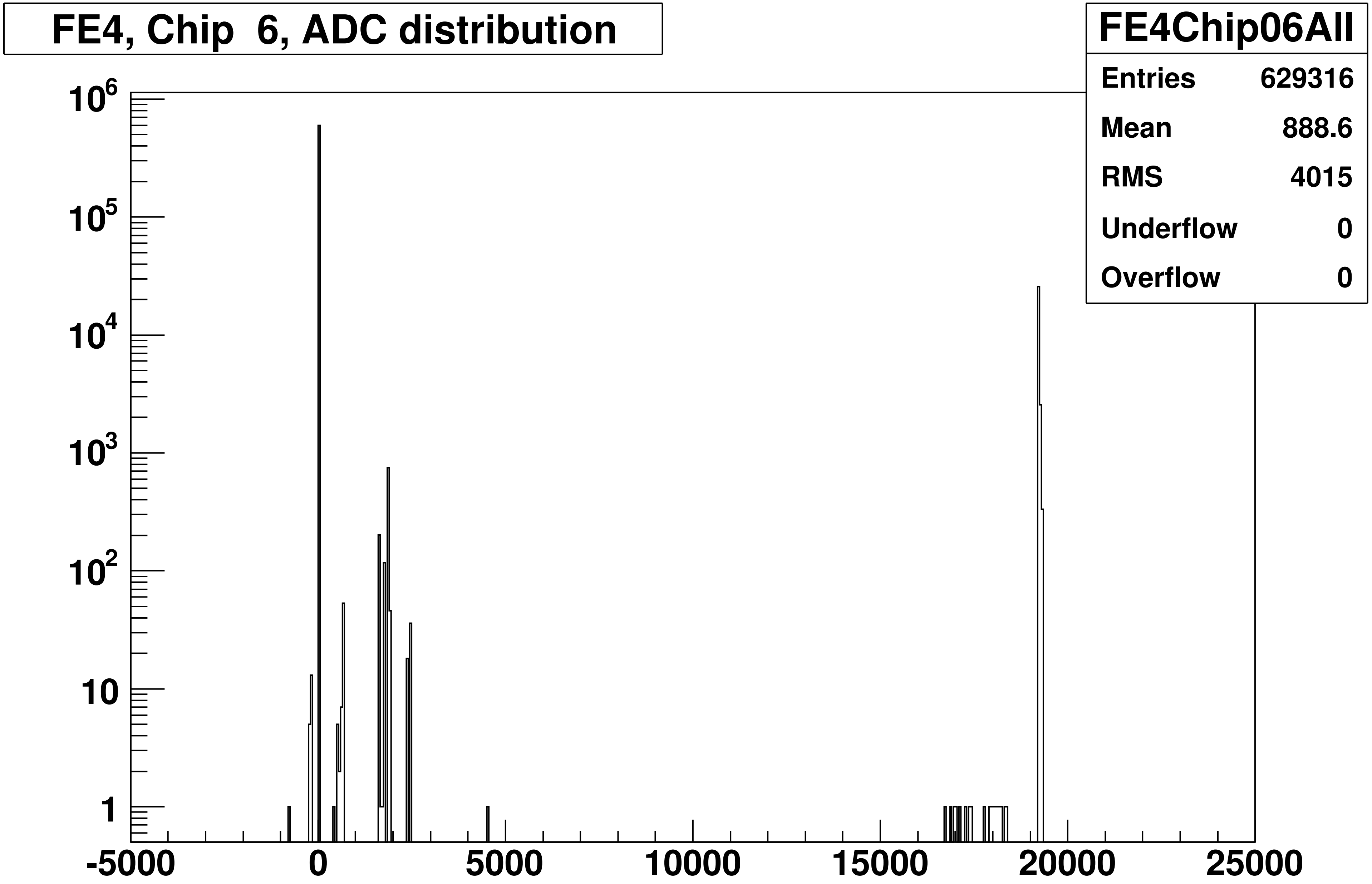


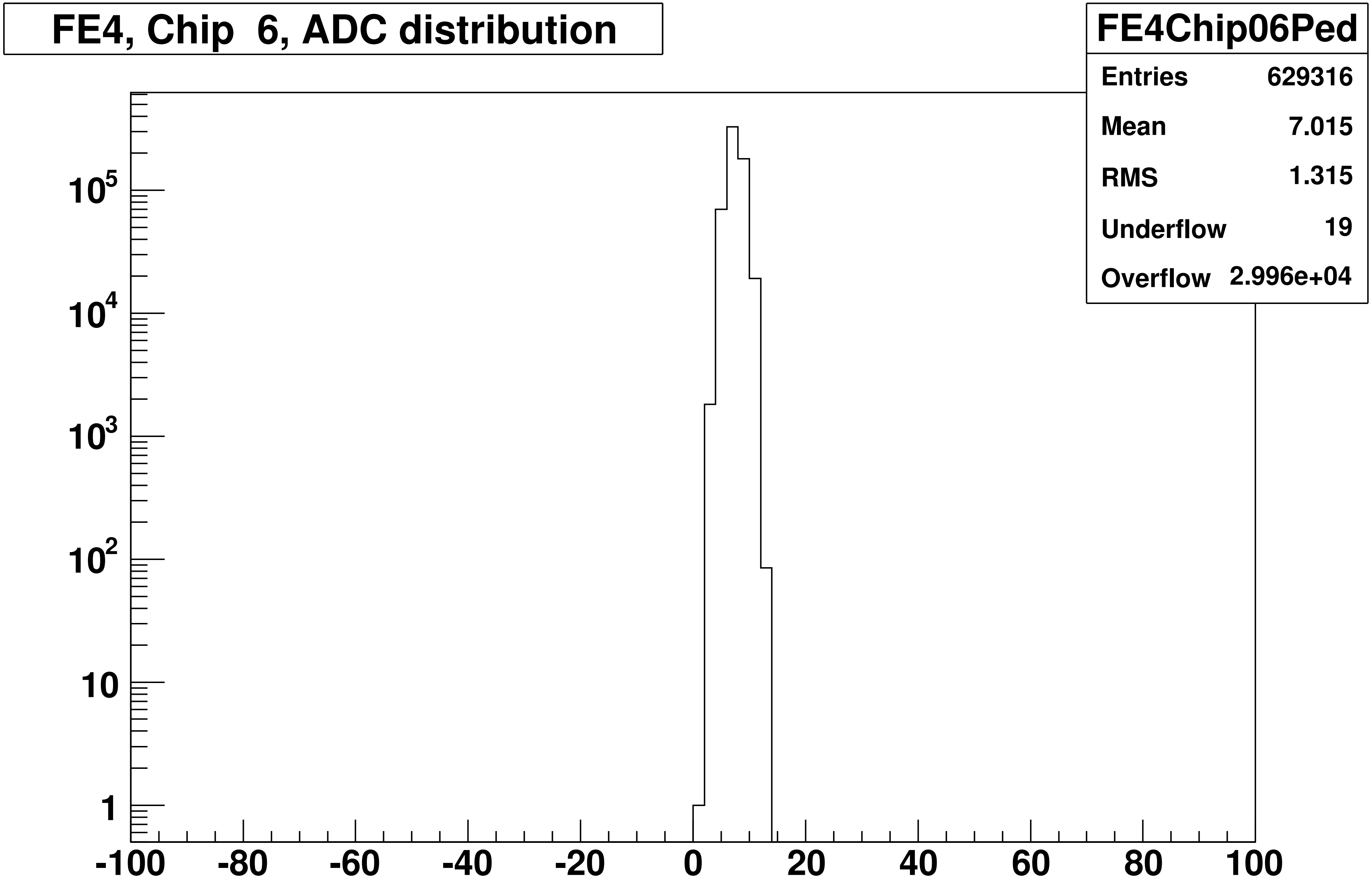
FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 629316
Mean 922.2
RMS 4133
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



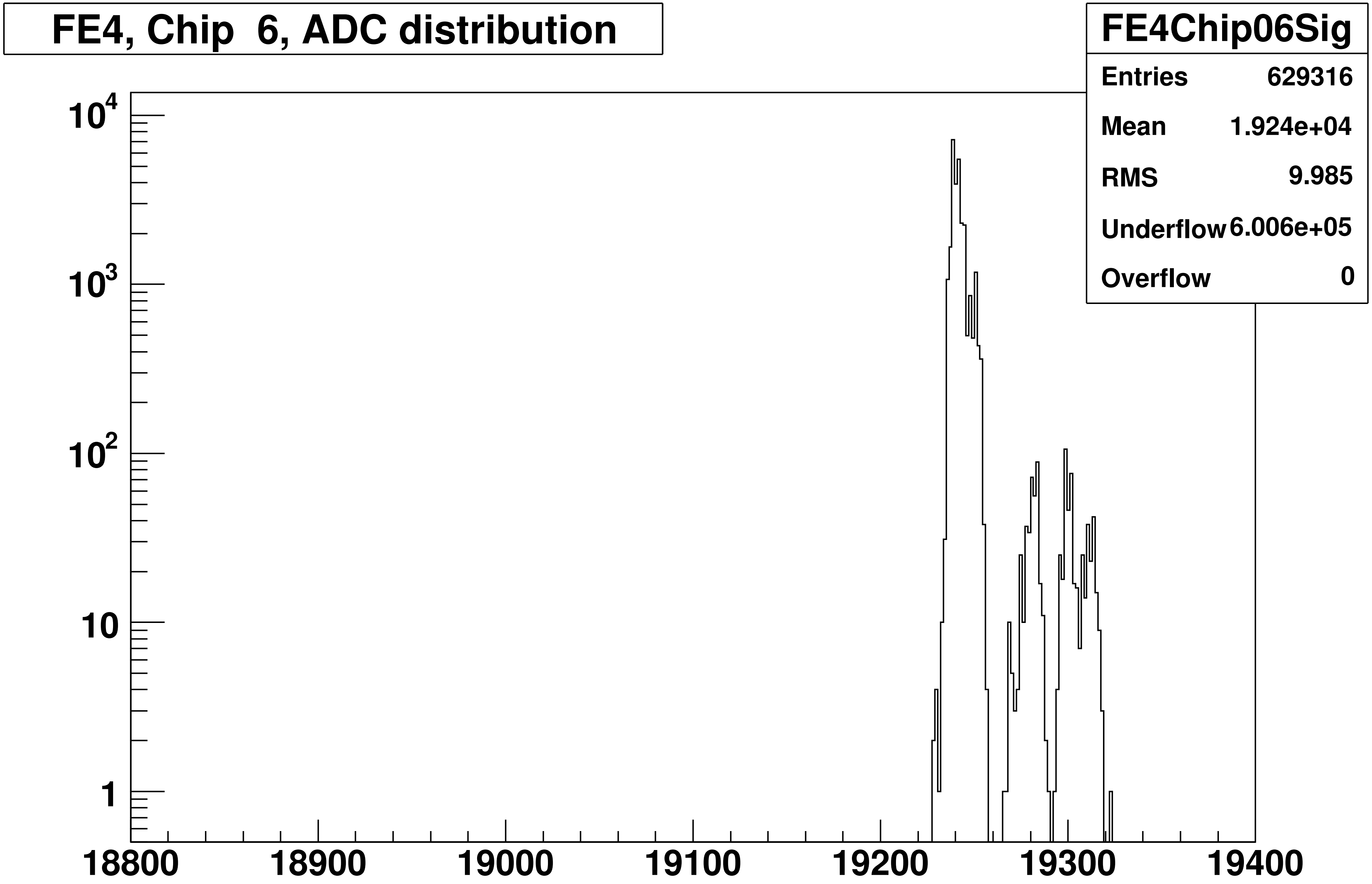
FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 629316
Mean -13.63
RMS 1.311
Underflow 0
Overflow 3.071e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

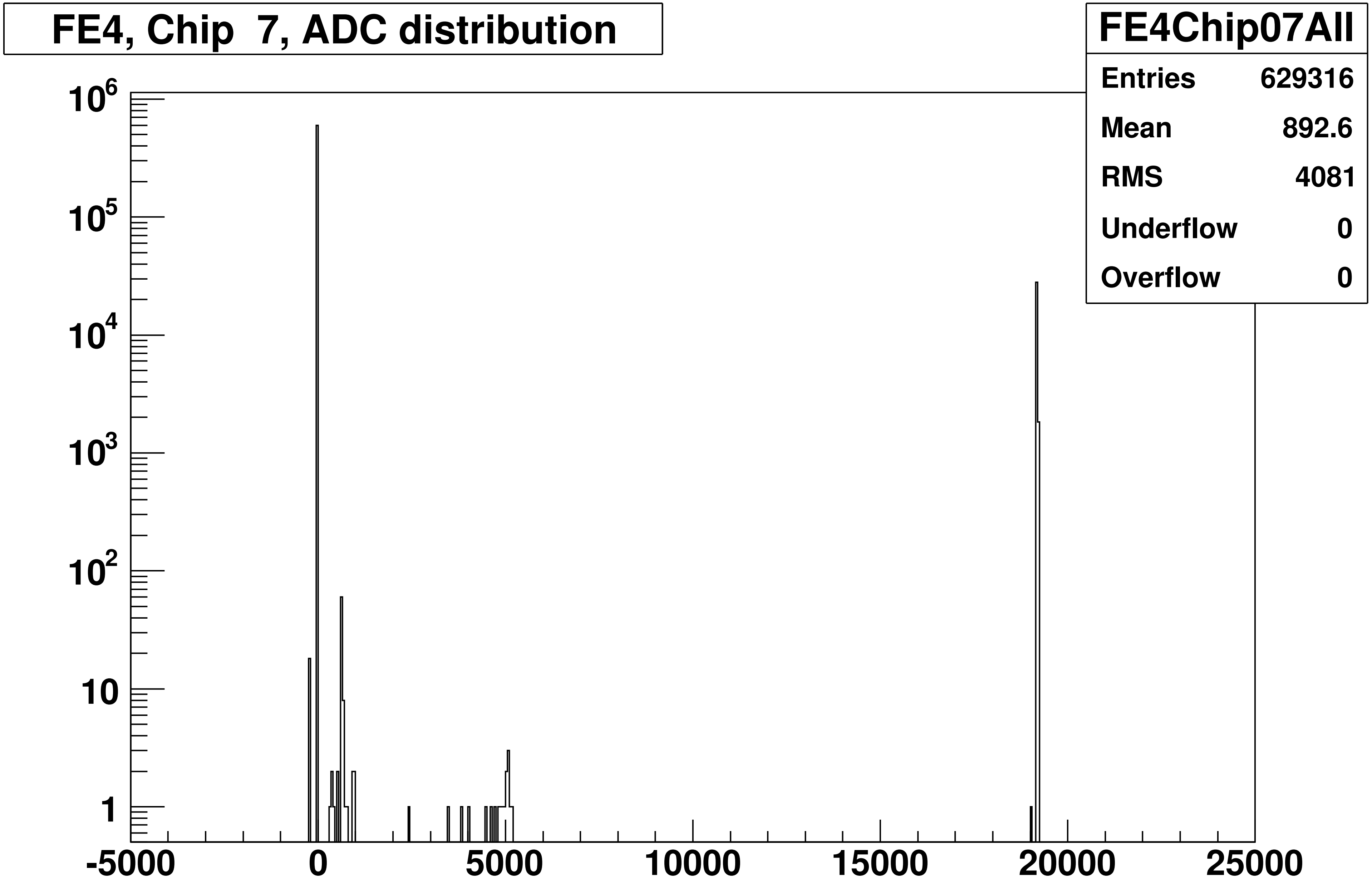

FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 629316
Mean 1.917e+04
RMS 23.97
Underflow 5.986e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 629316
Mean 888.6
RMS 4015
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

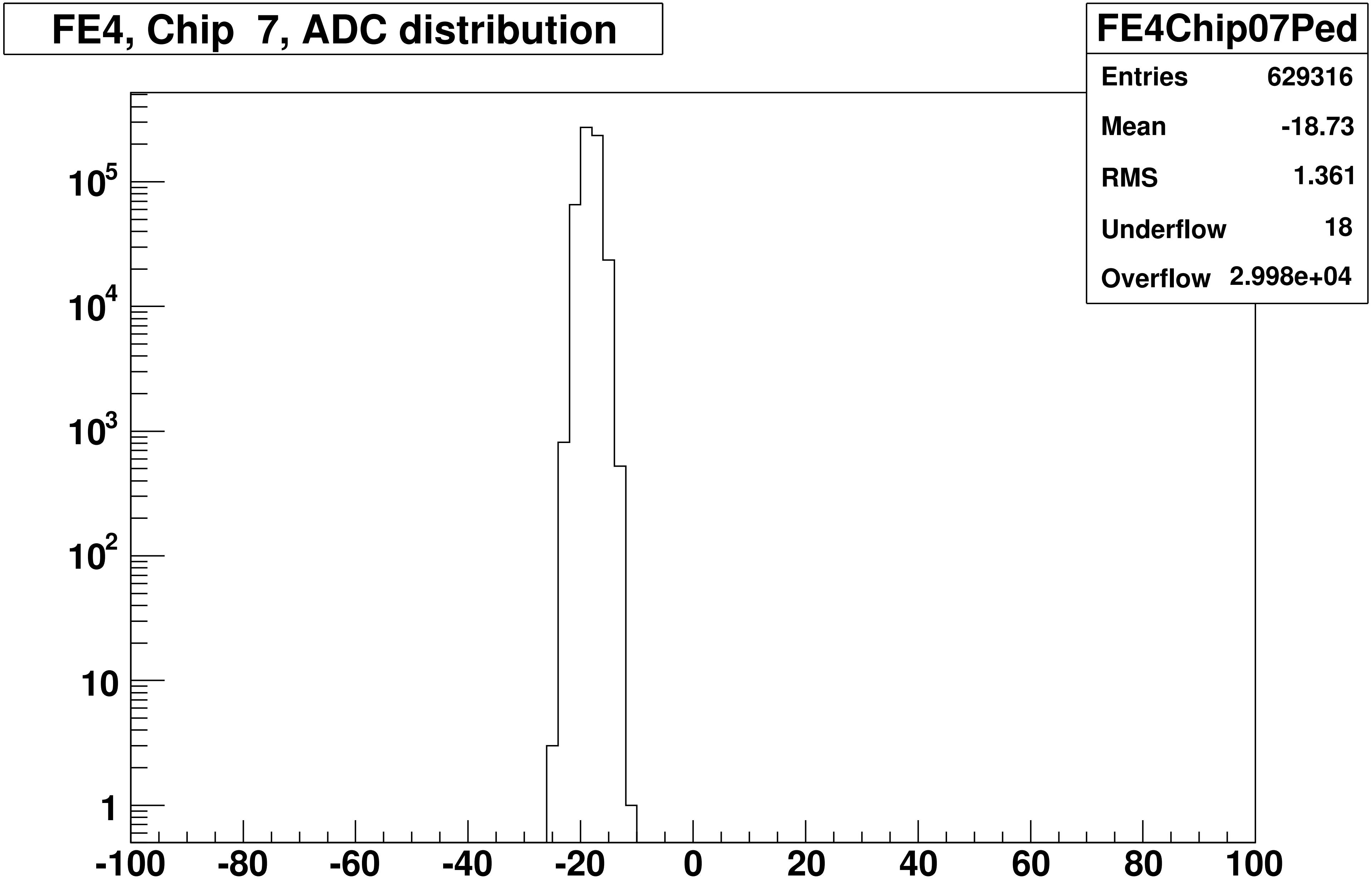


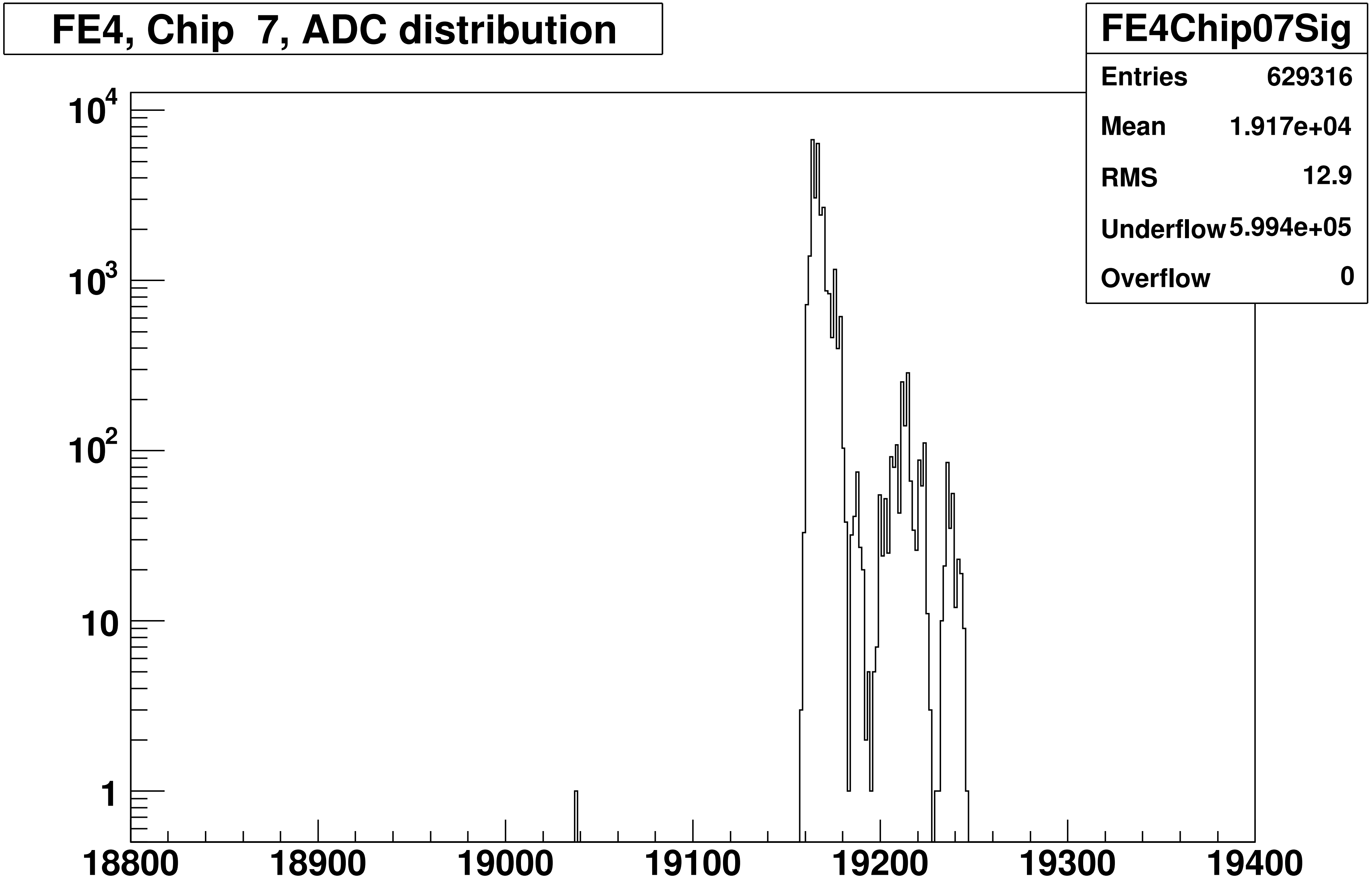
FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 629316
Mean 7.015
RMS 1.315
Underflow 19
Overflow 2.996e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

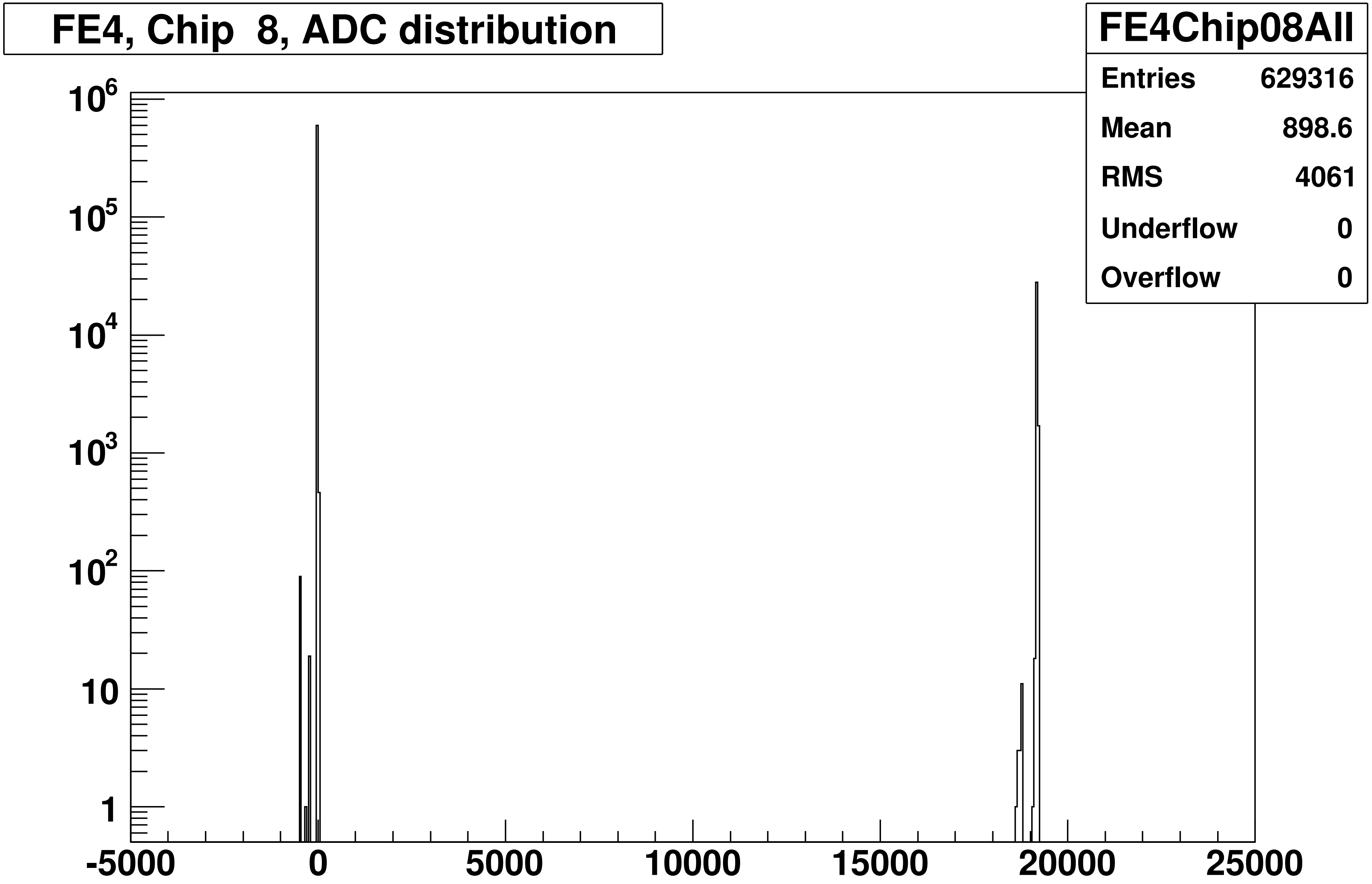

FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 629316
Mean 1.924e+04
RMS 9.985
Underflow 6.006e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

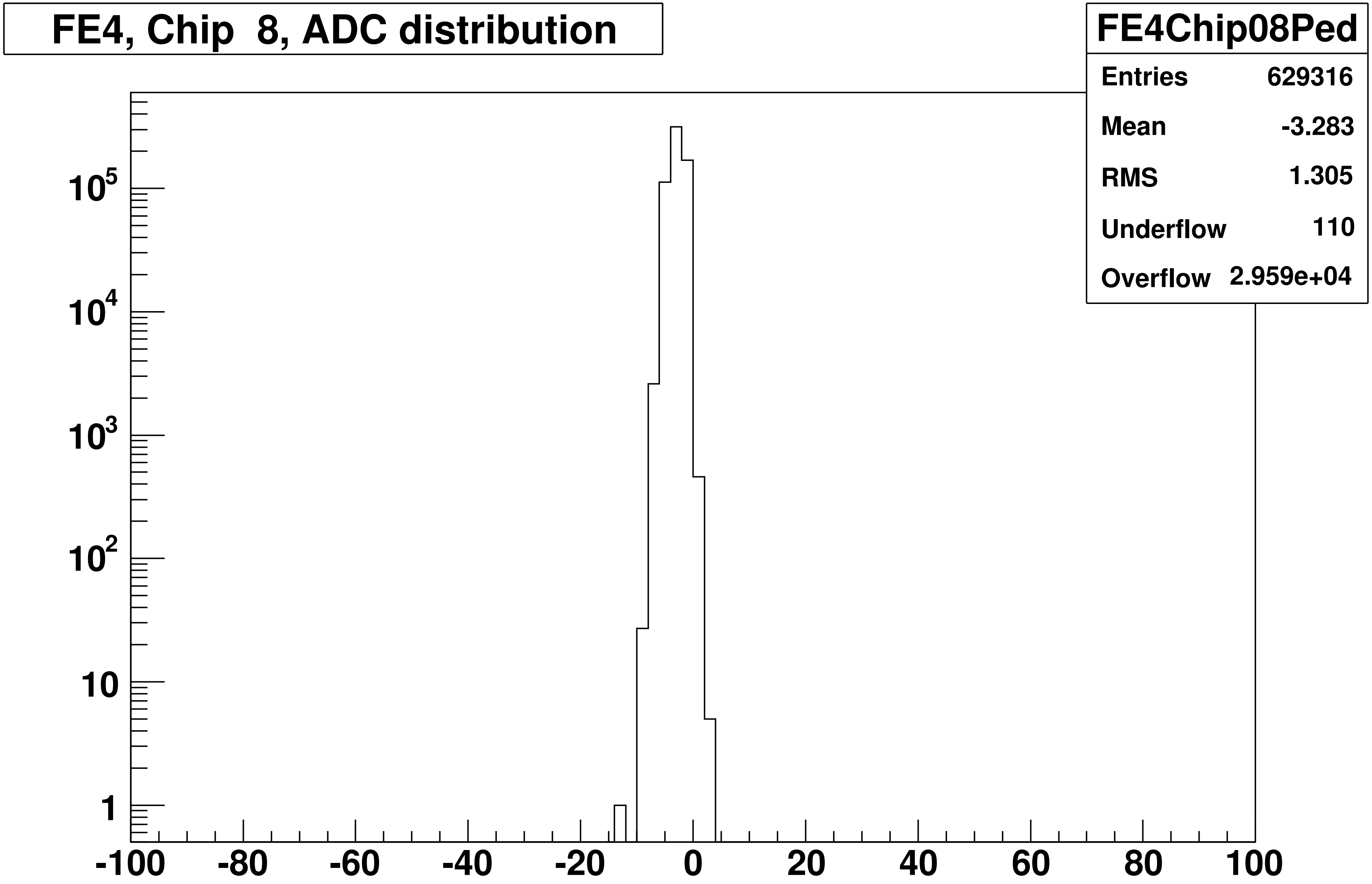


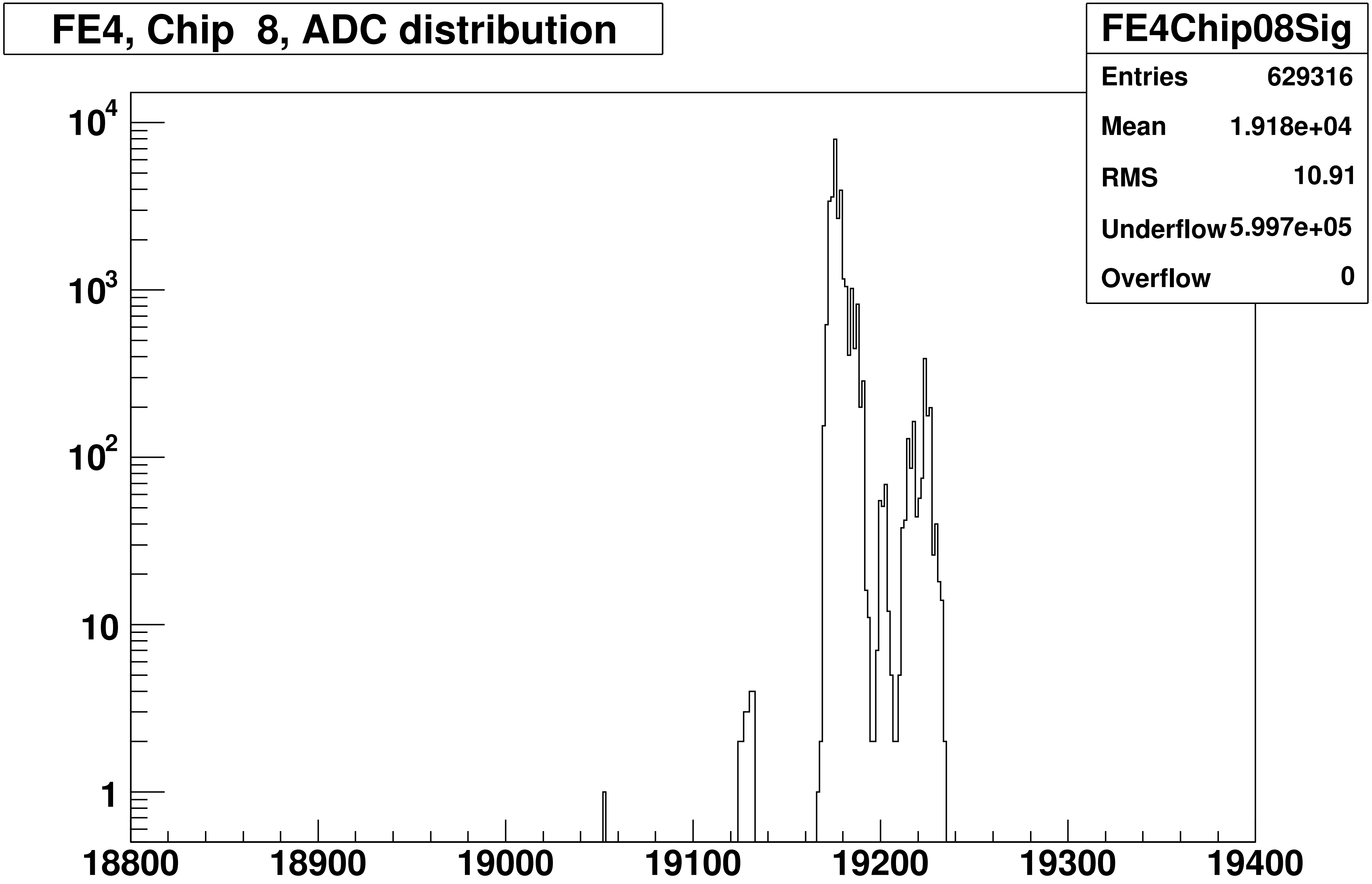
FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 629316
Mean 892.6
RMS 4081
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

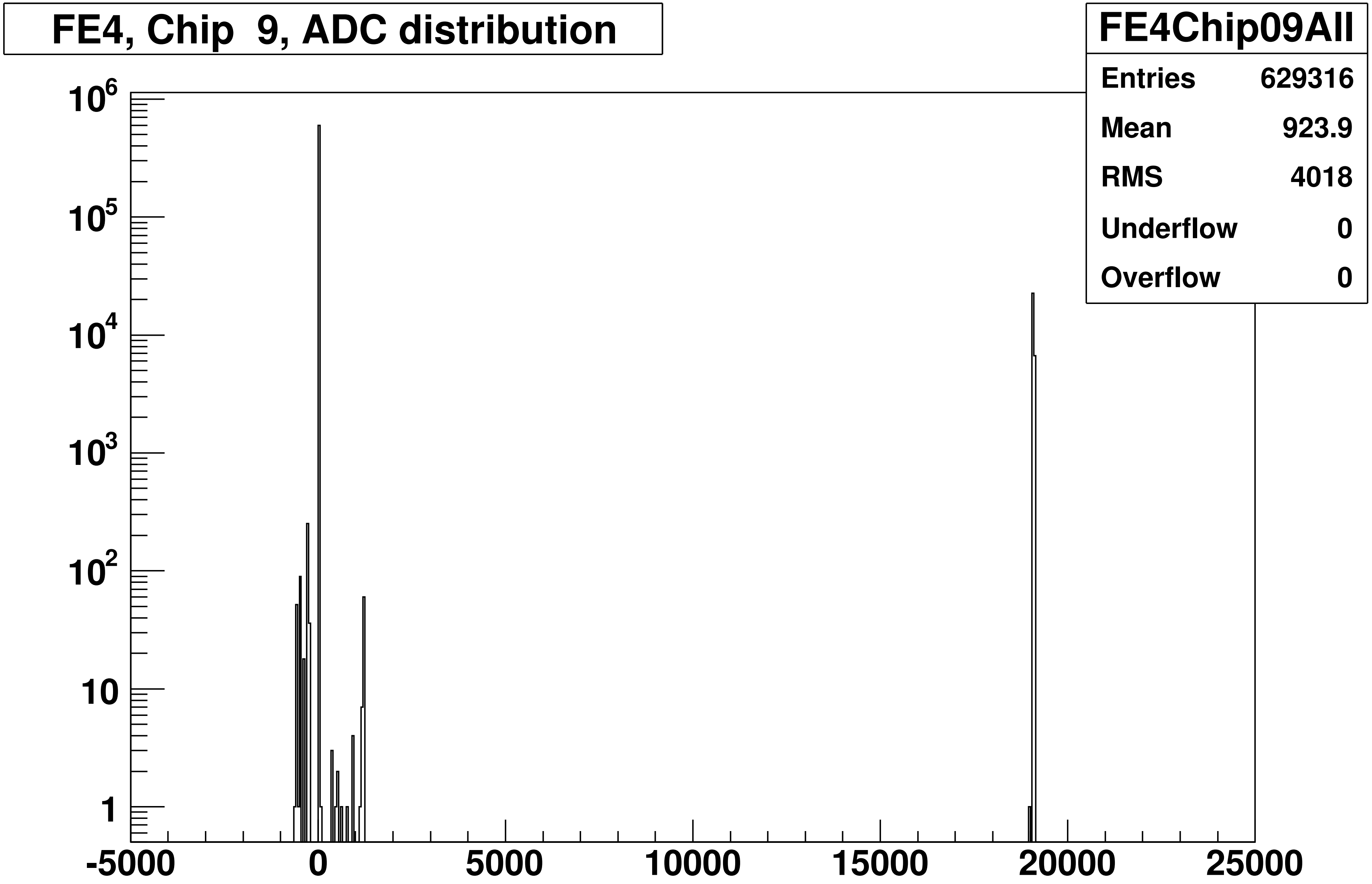

FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 629316
Mean -18.73
RMS 1.361
Underflow 18
Overflow 2.998e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 629316
Mean 1.917e+04
RMS 12.9
Underflow 5.994e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

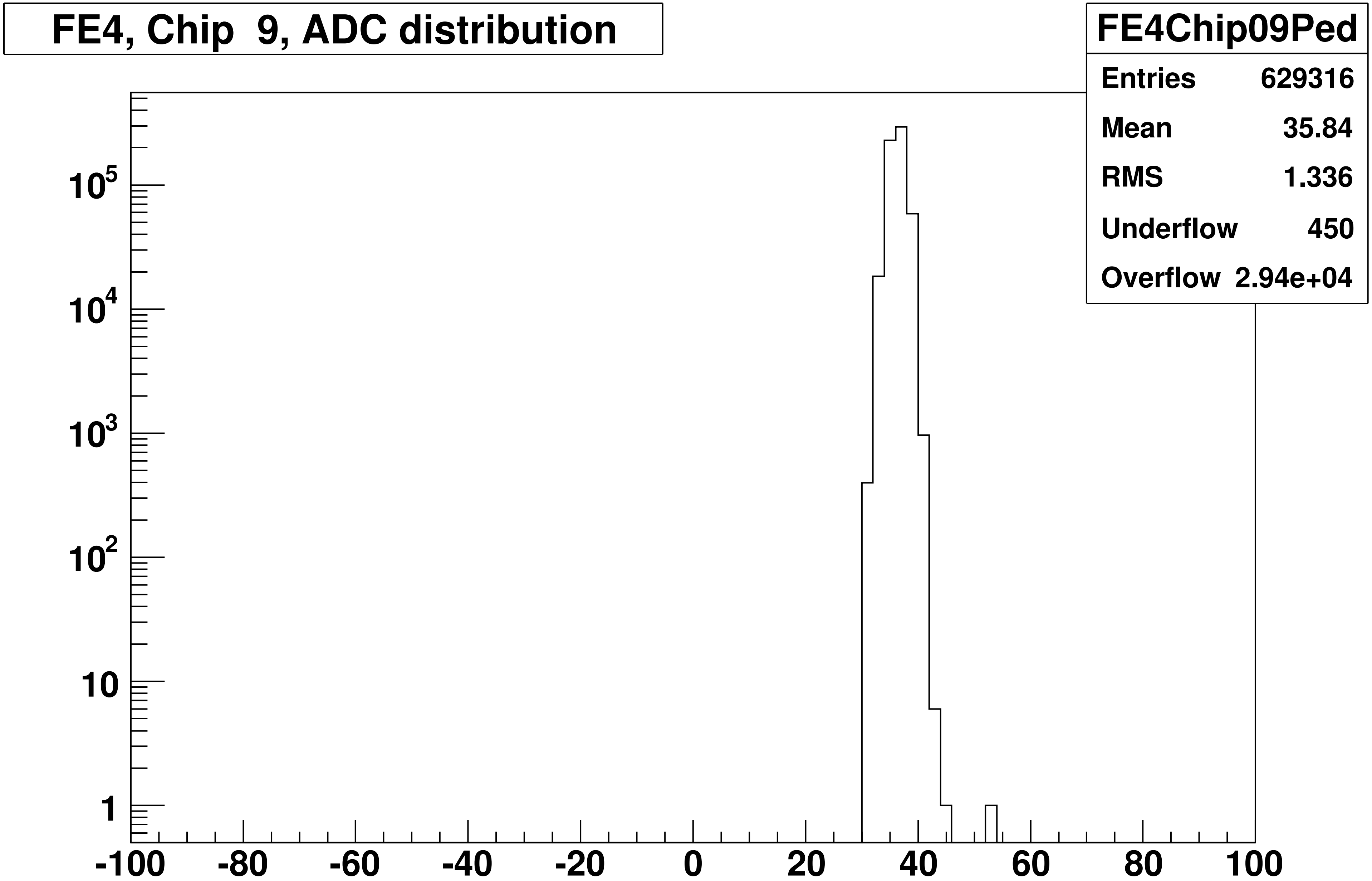

FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 629316
Mean 898.6
RMS 4061
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

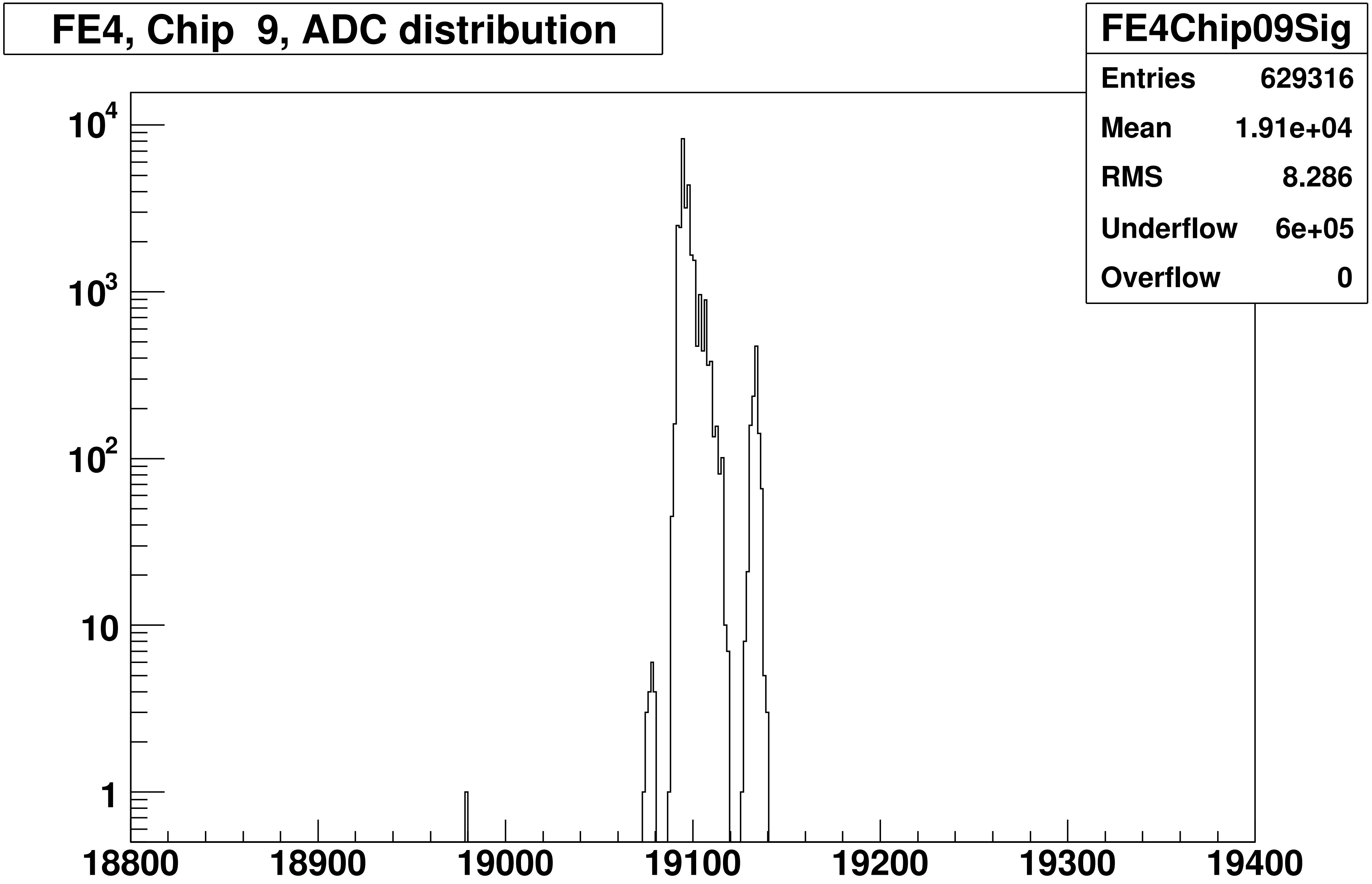

FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 629316
Mean -3.283
RMS 1.305
Underflow 110
Overflow 2.959e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

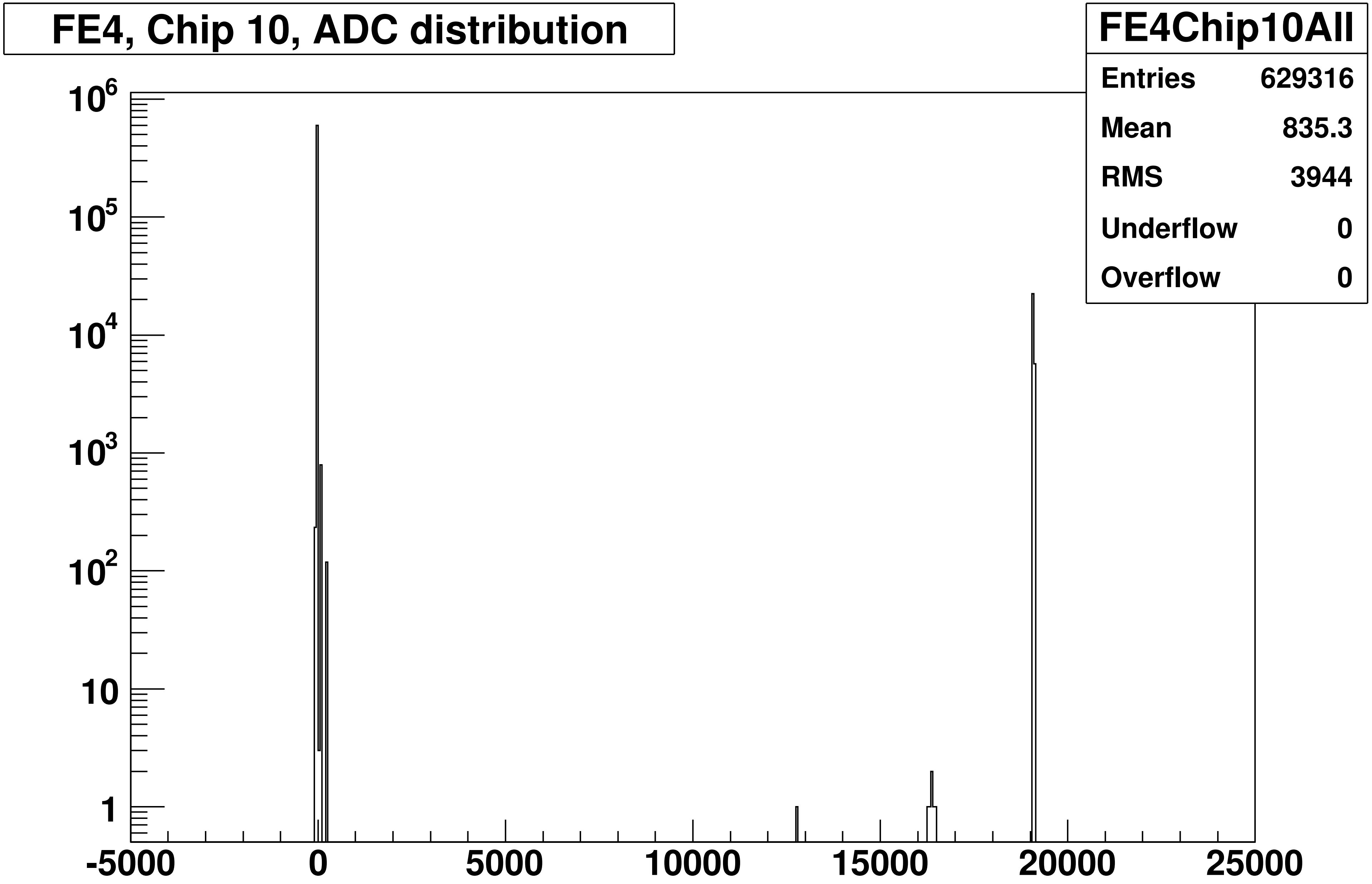

FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 629316
Mean 1.918e+04
RMS 10.91
Underflow 5.997e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



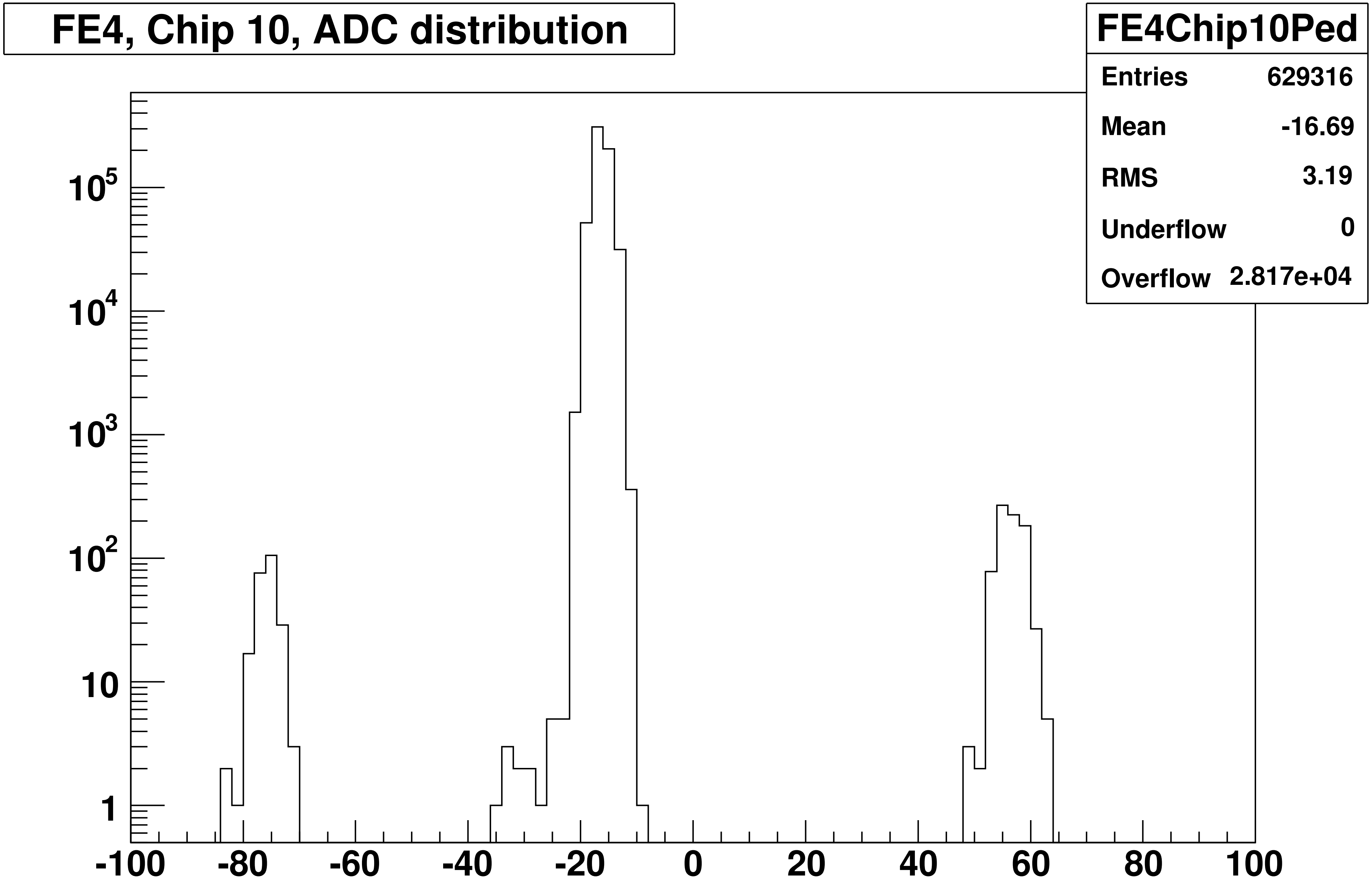
FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 629316
Mean 923.9
RMS 4018
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

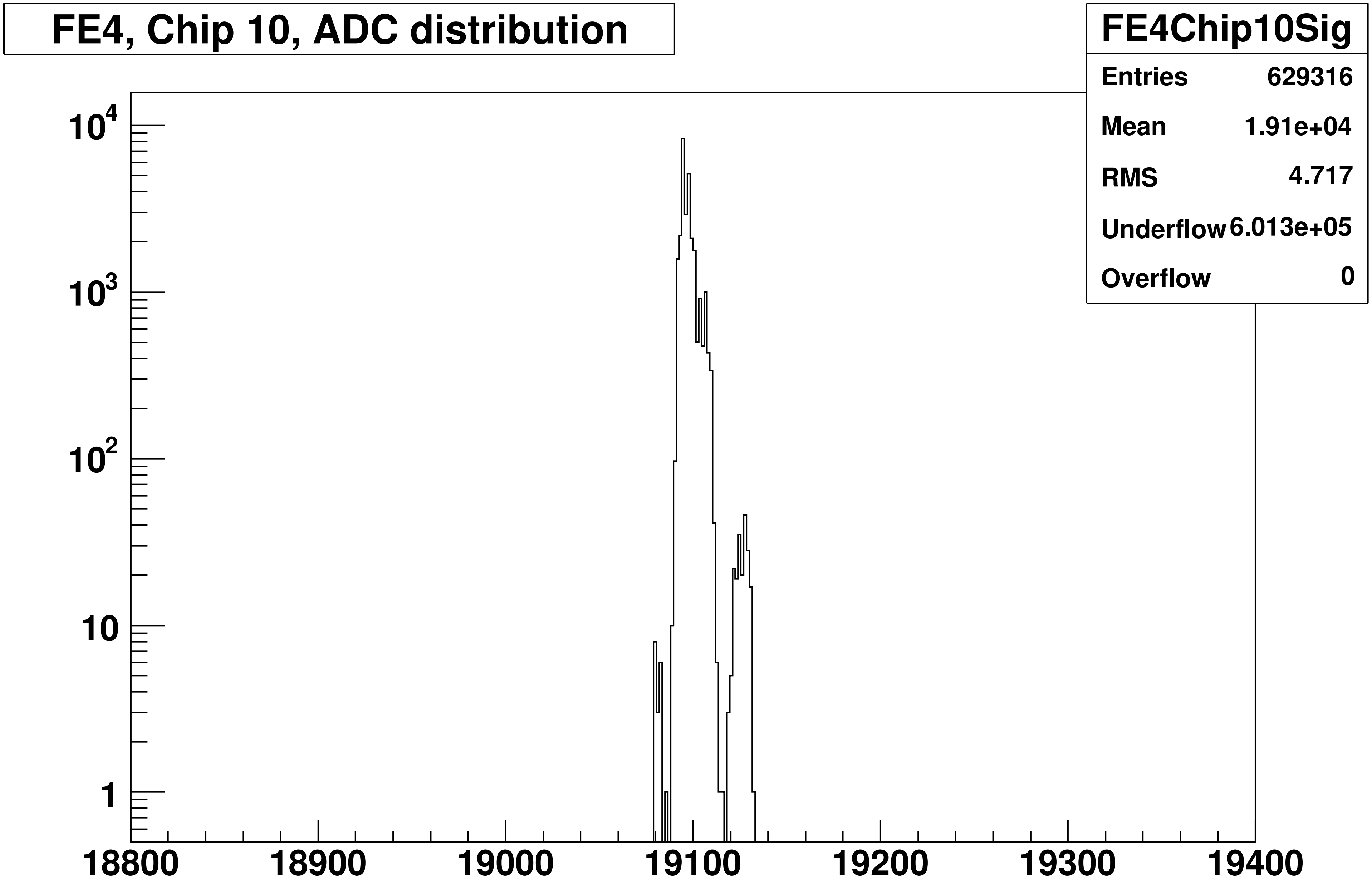

FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 629316
Mean 35.84
RMS 1.336
Underflow 450
Overflow 2.94e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

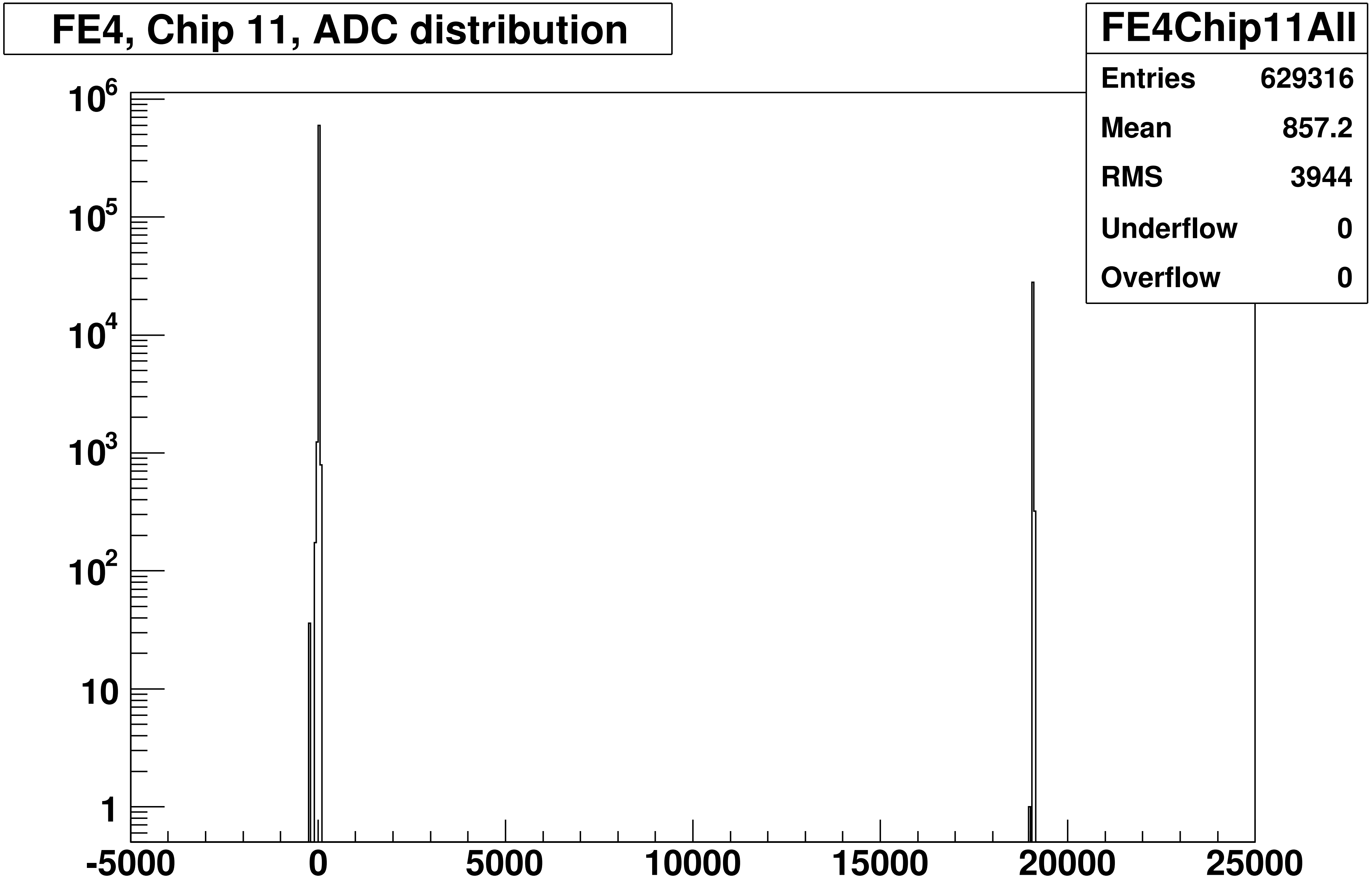

FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 629316
Mean 1.91e+04
RMS 8.286
Underflow 6e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

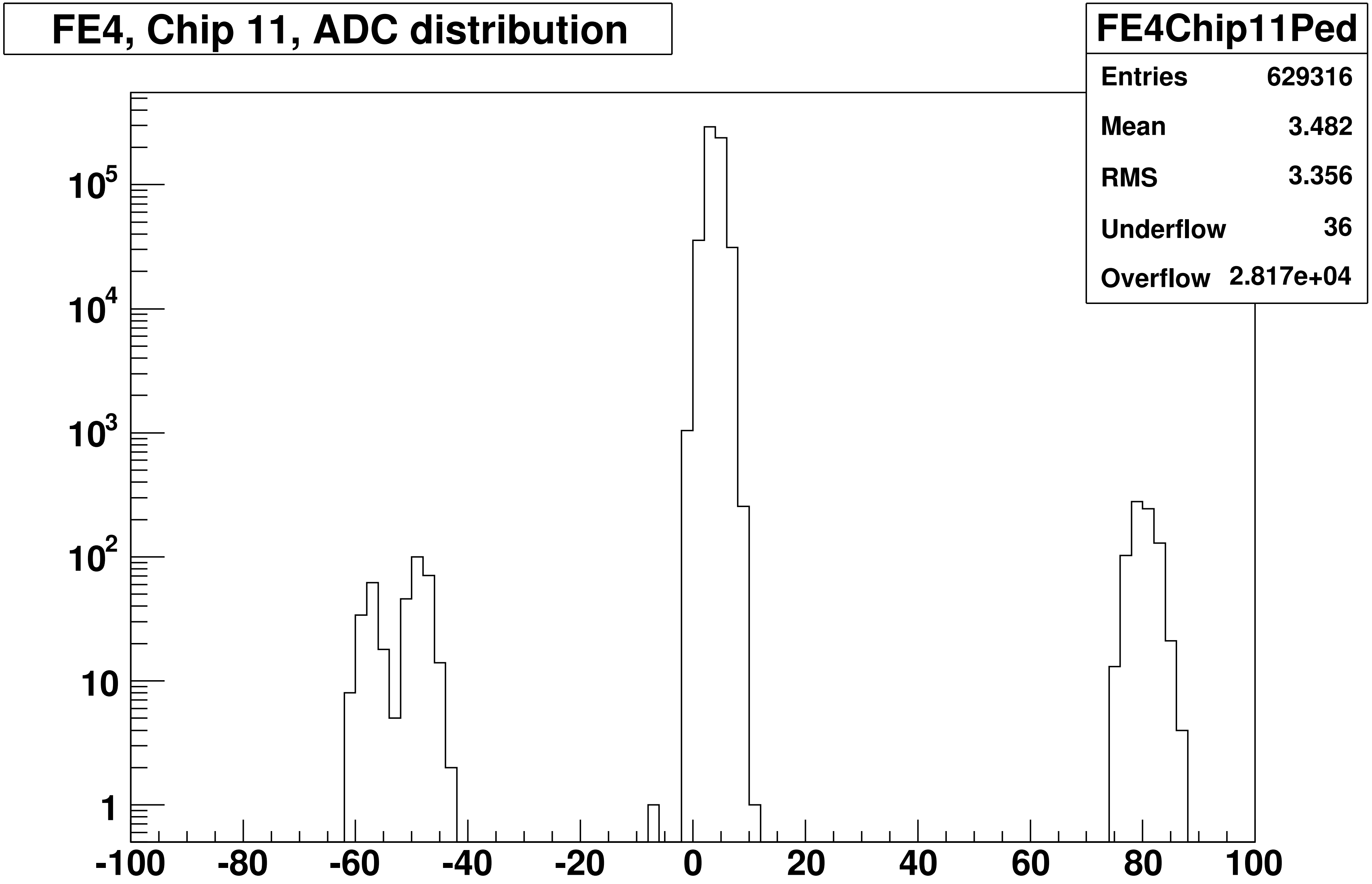


FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 629316
Mean 835.3
RMS 3944
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

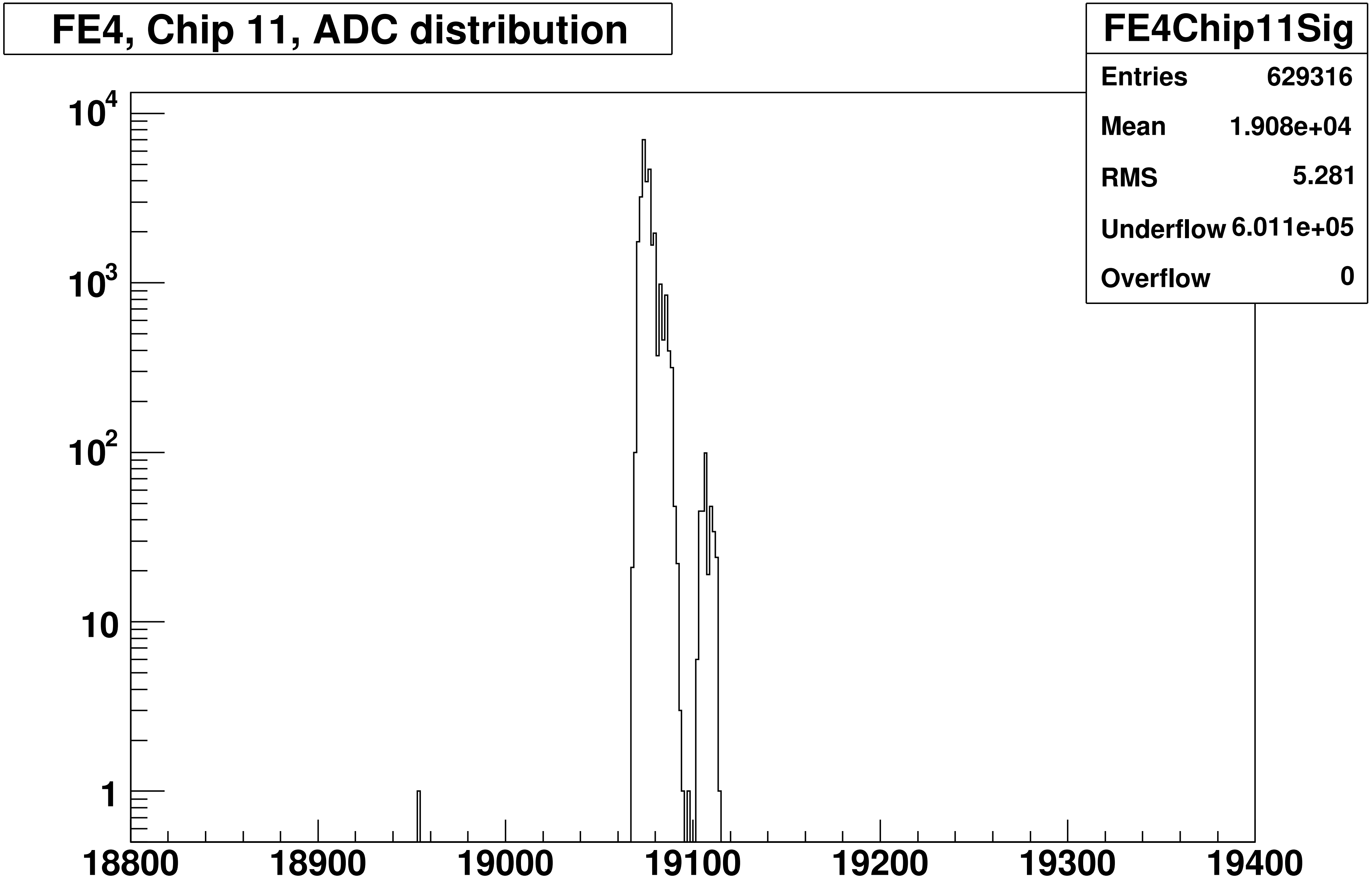

FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 629316
Mean -16.69
RMS 3.19
Underflow 0
Overflow 2.817e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 629316
Mean 1.91e+04
RMS 4.717
Underflow 6.013e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

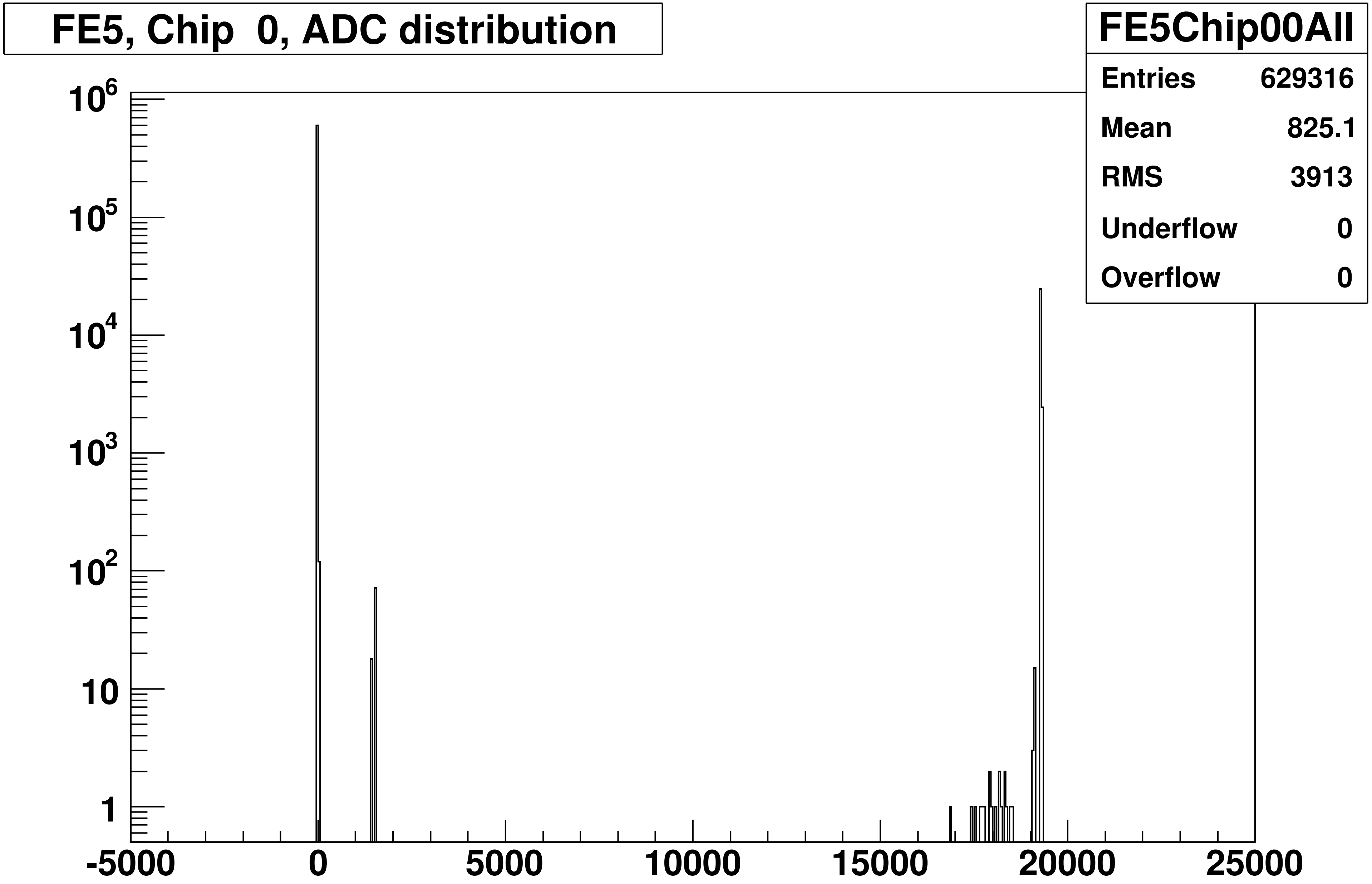

FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 629316
Mean 857.2
RMS 3944
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

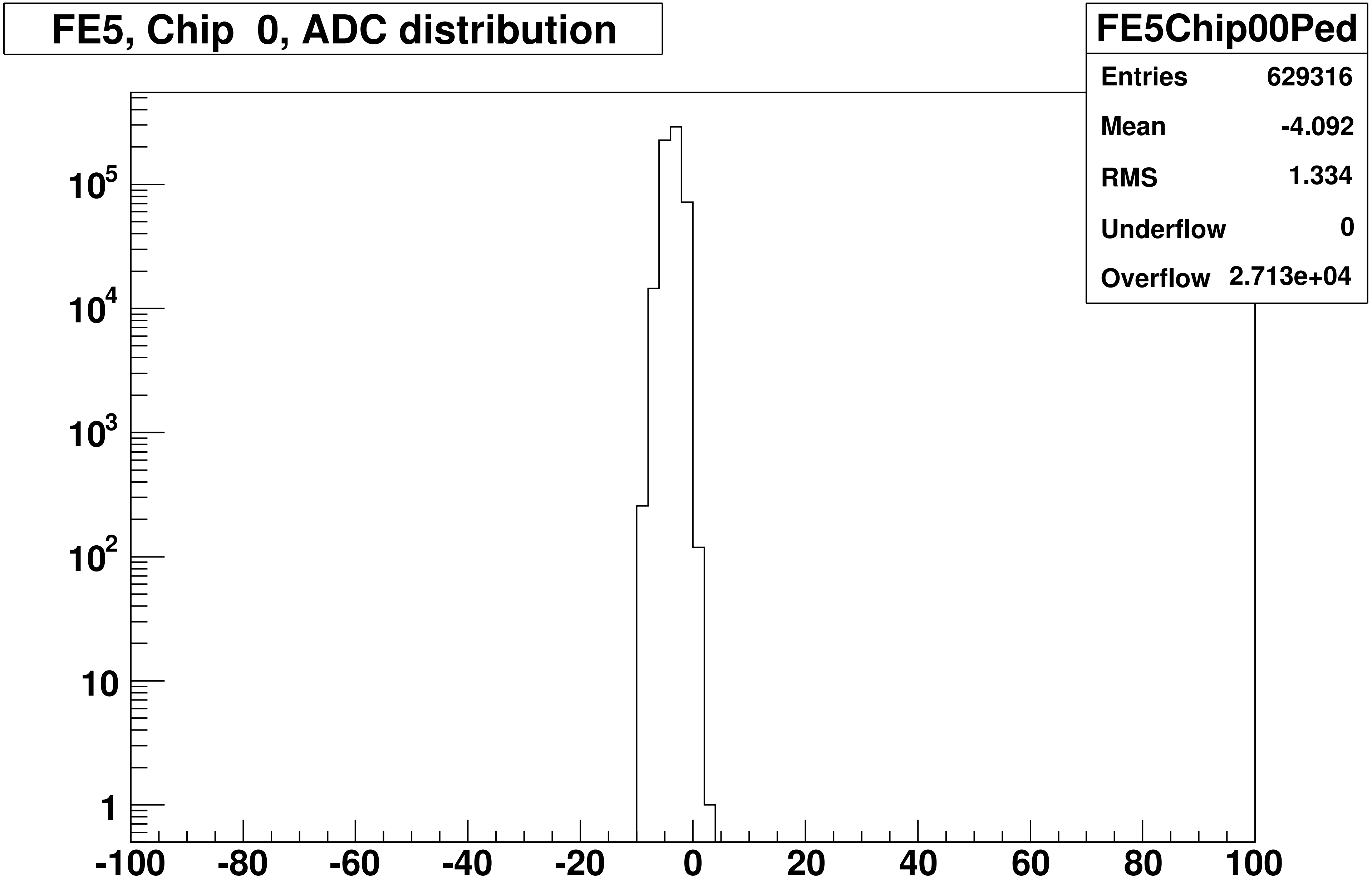


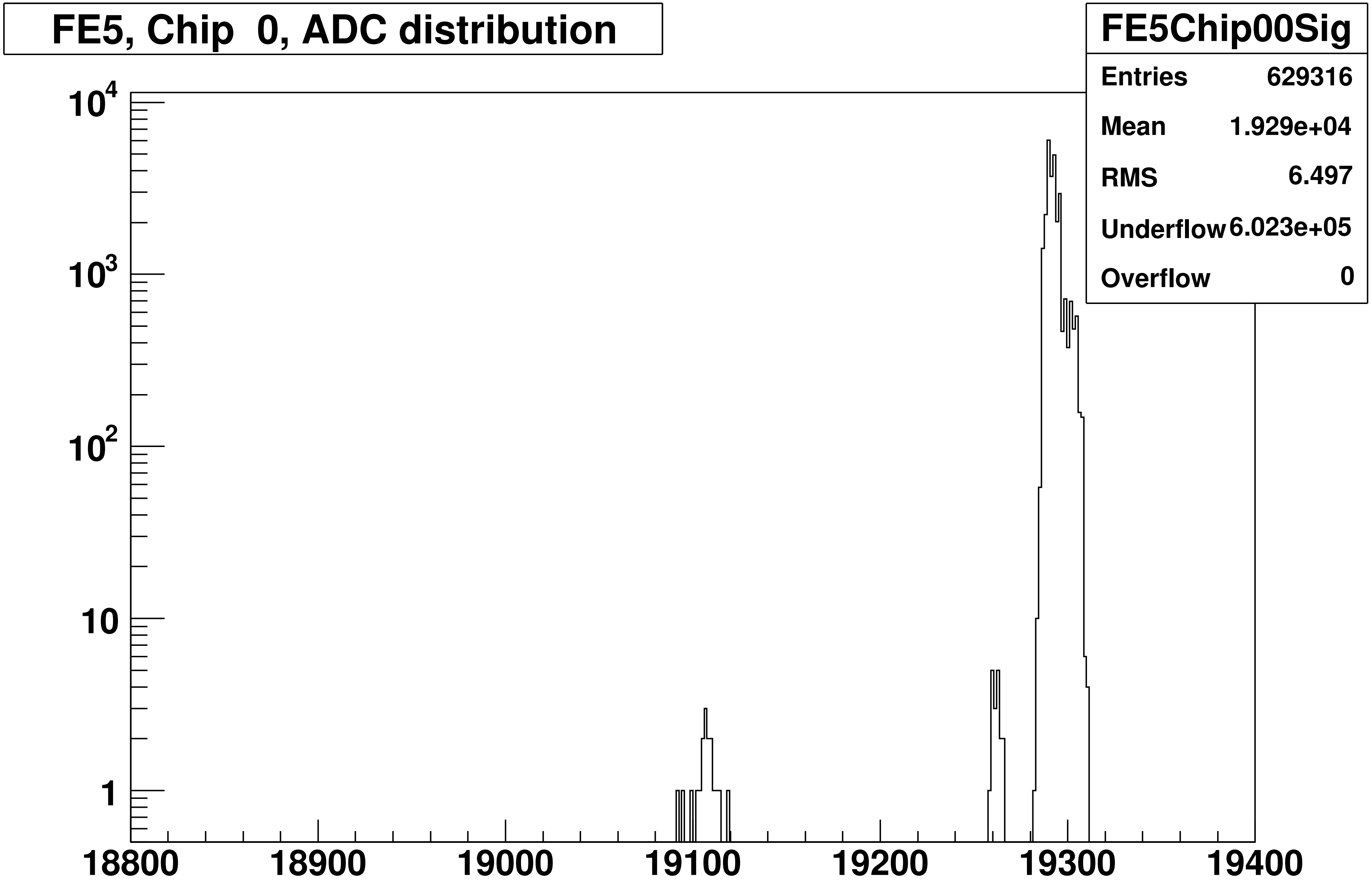
FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 629316
Mean 3.482
RMS 3.356
Underflow 36
Overflow 2.817e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

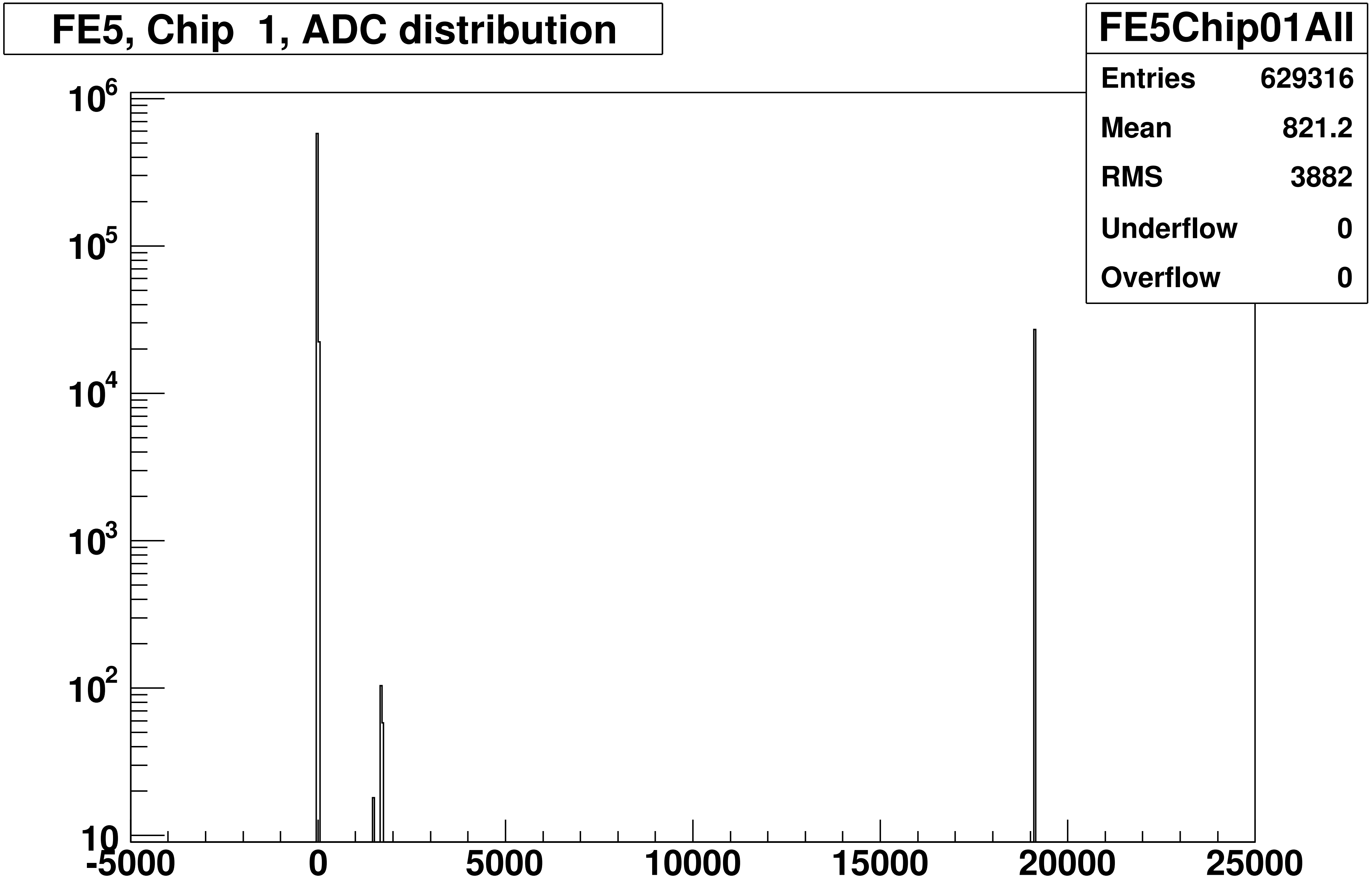


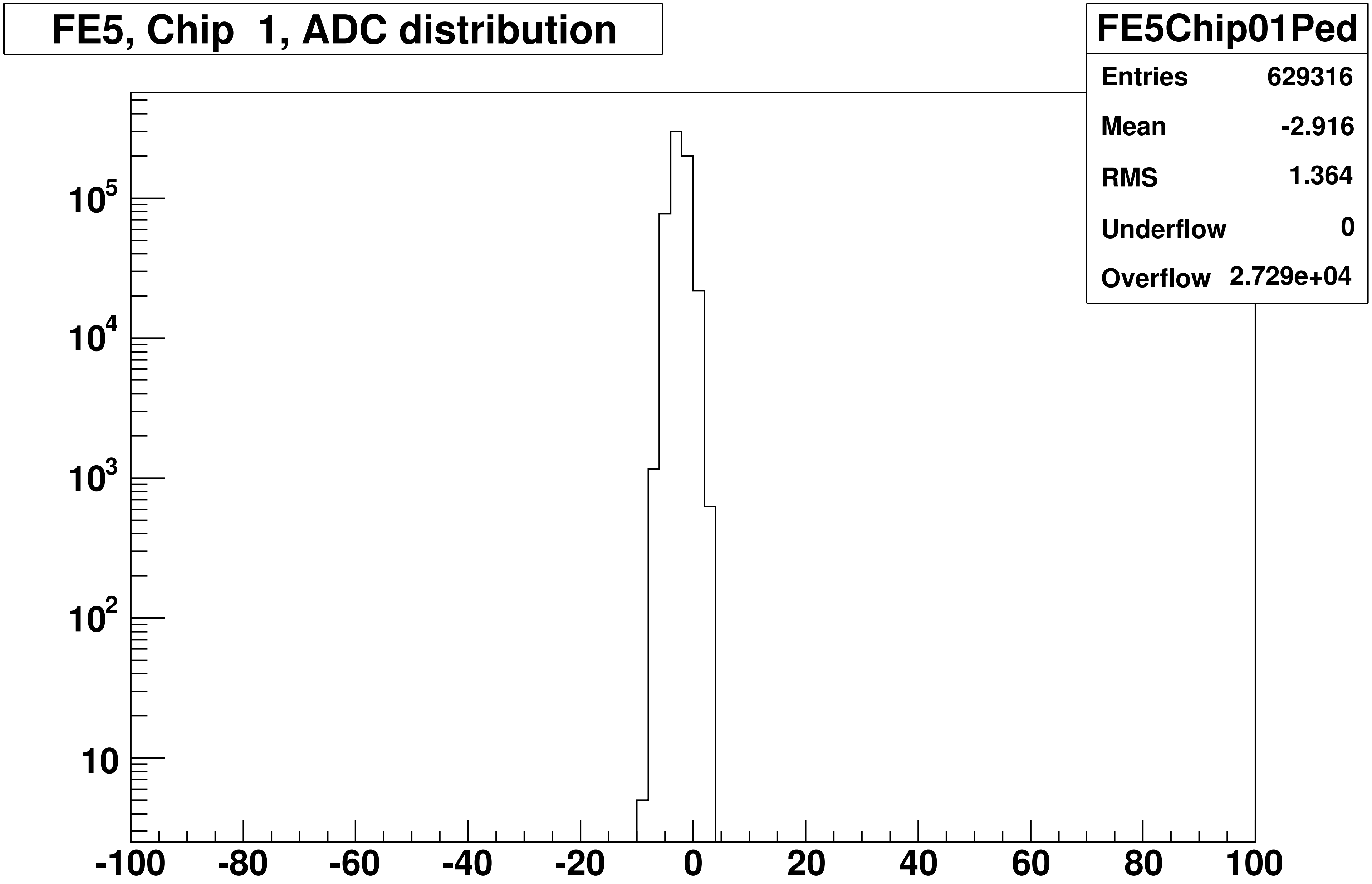
FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 629316
Mean 1.908e+04
RMS 5.281
Underflow 6.011e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

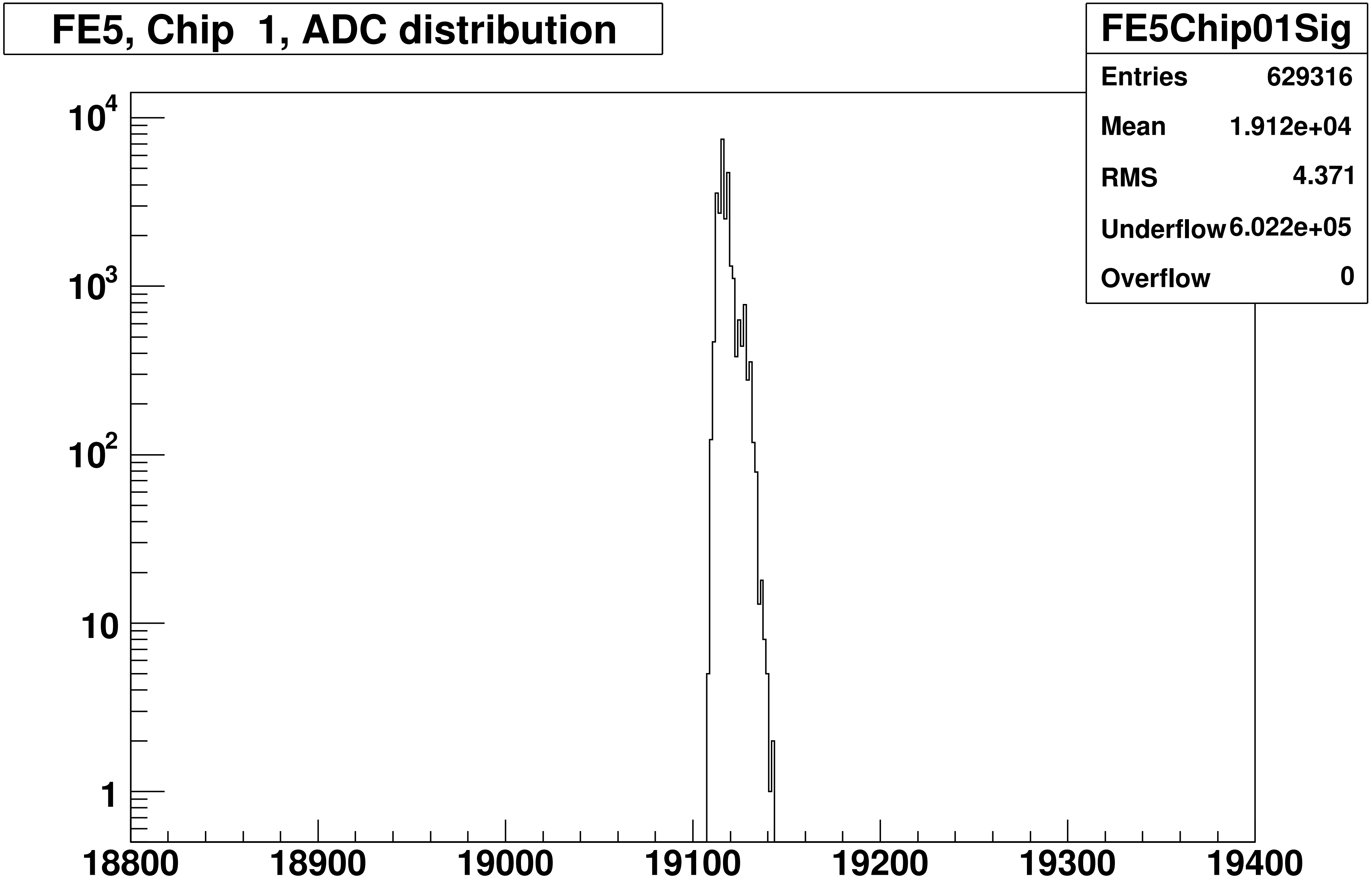

FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 629316
Mean 825.1
RMS 3913
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

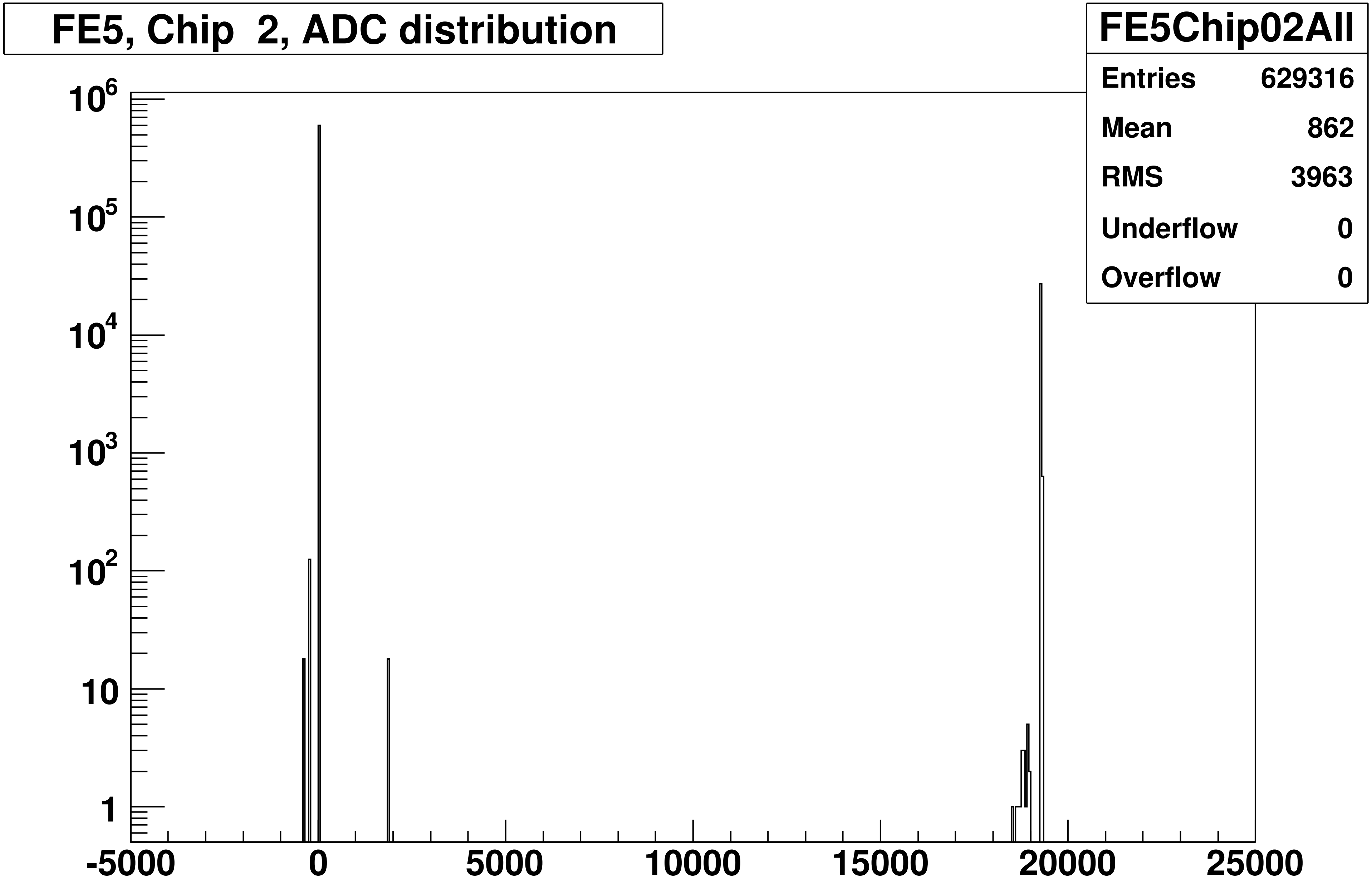

FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 629316
Mean -4.092
RMS 1.334
Underflow 0
Overflow 2.713e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

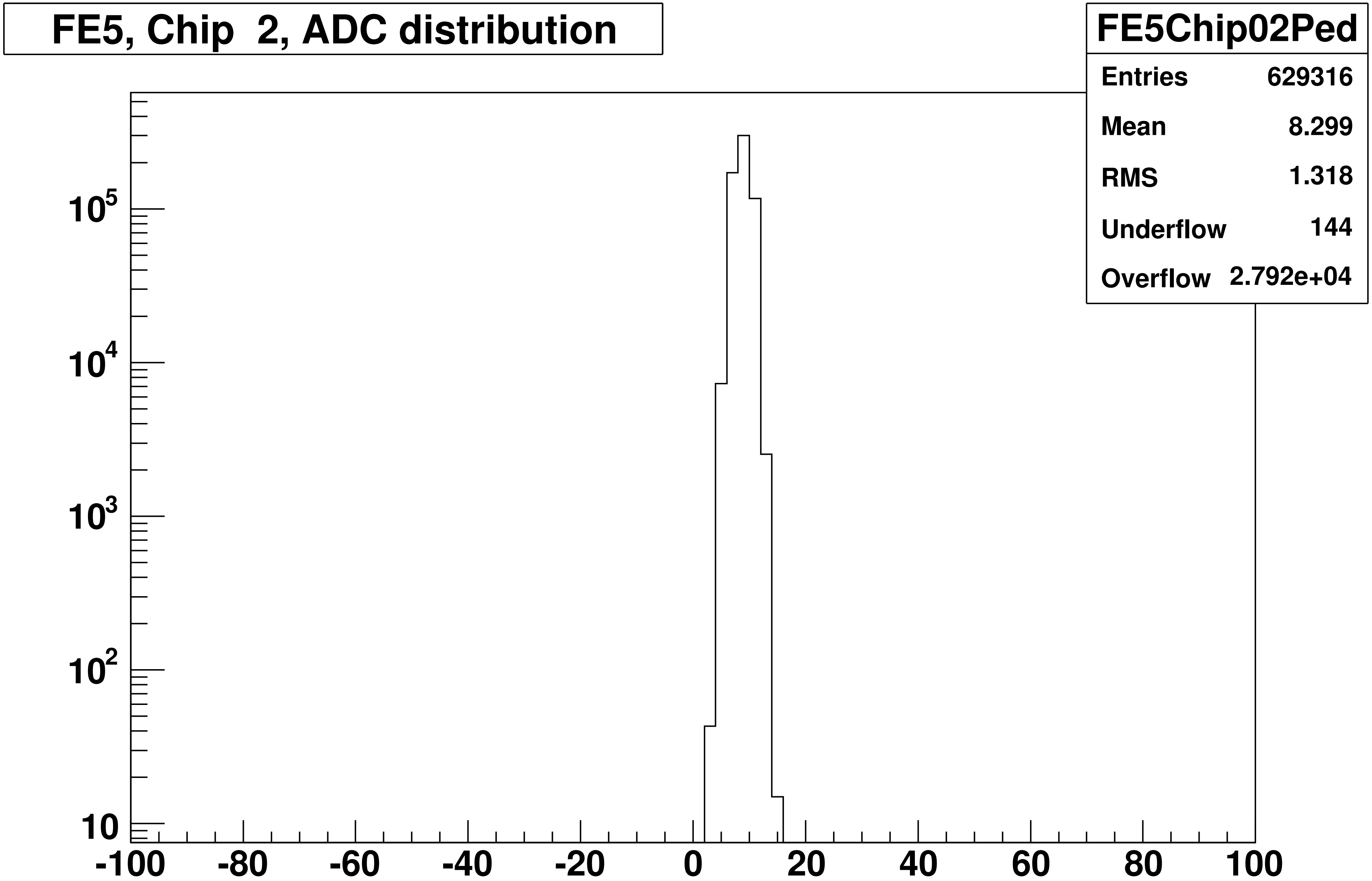

FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 629316
Mean 1.929e+04
RMS 6.497
Underflow 6.023e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

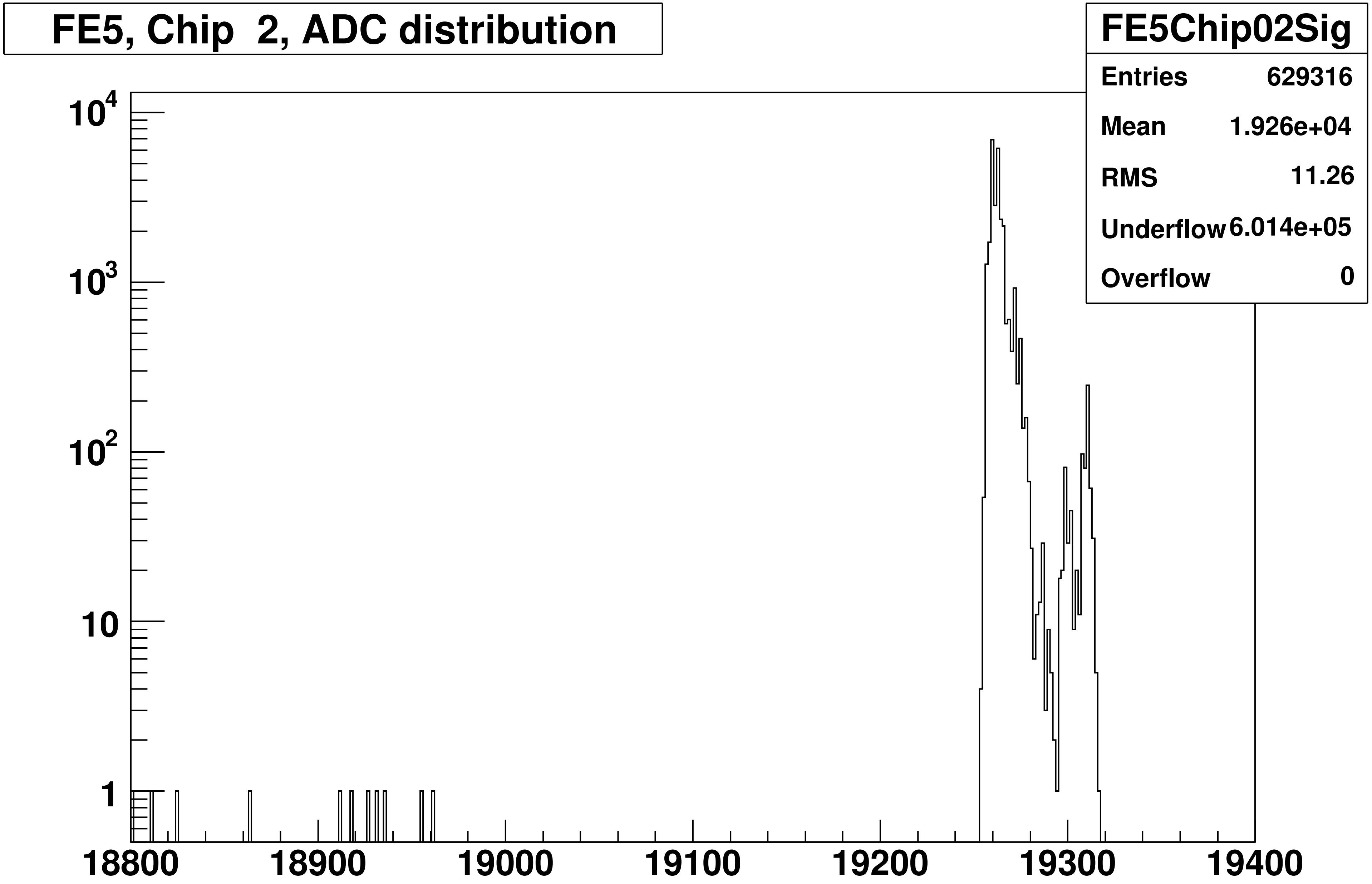

FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 629316
Mean 821.2
RMS 3882
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

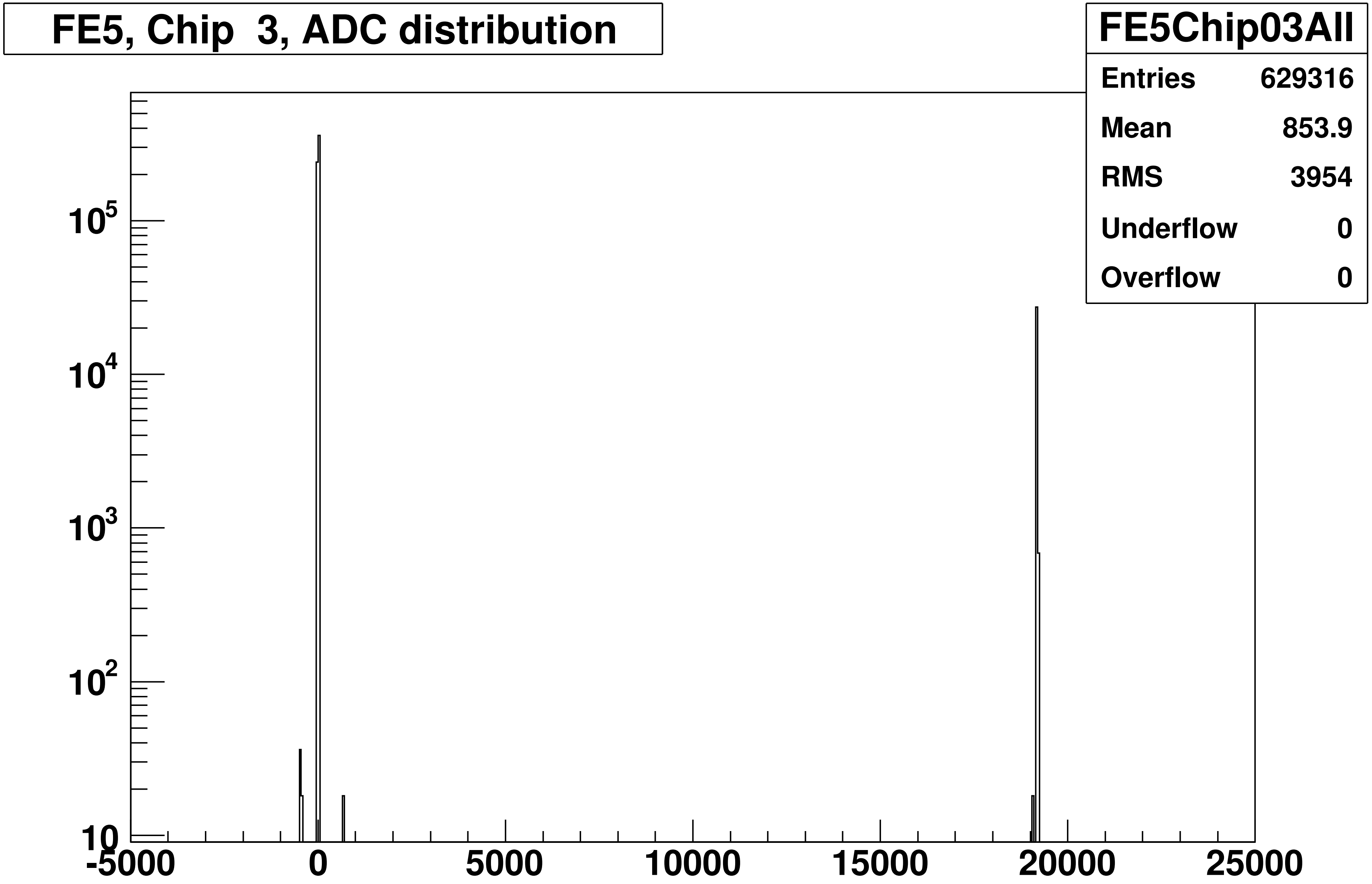

FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 629316
Mean -2.916
RMS 1.364
Underflow 0
Overflow 2.729e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 629316
Mean 1.912e+04
RMS 4.371
Underflow 6.022e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

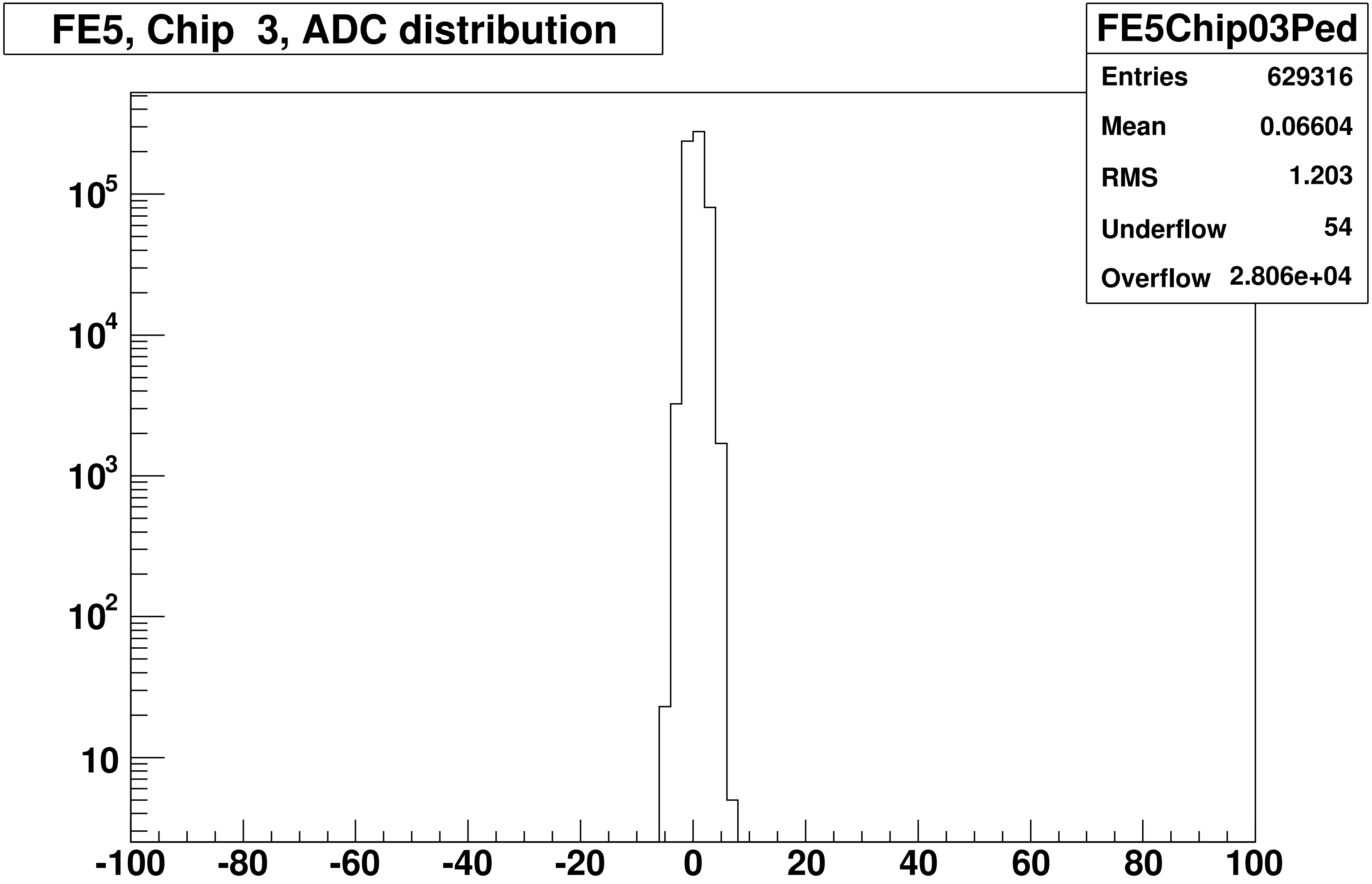

FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 629316
Mean 862
RMS 3963
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 629316
Mean 8.299
RMS 1.318
Underflow 144
Overflow 2.792e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

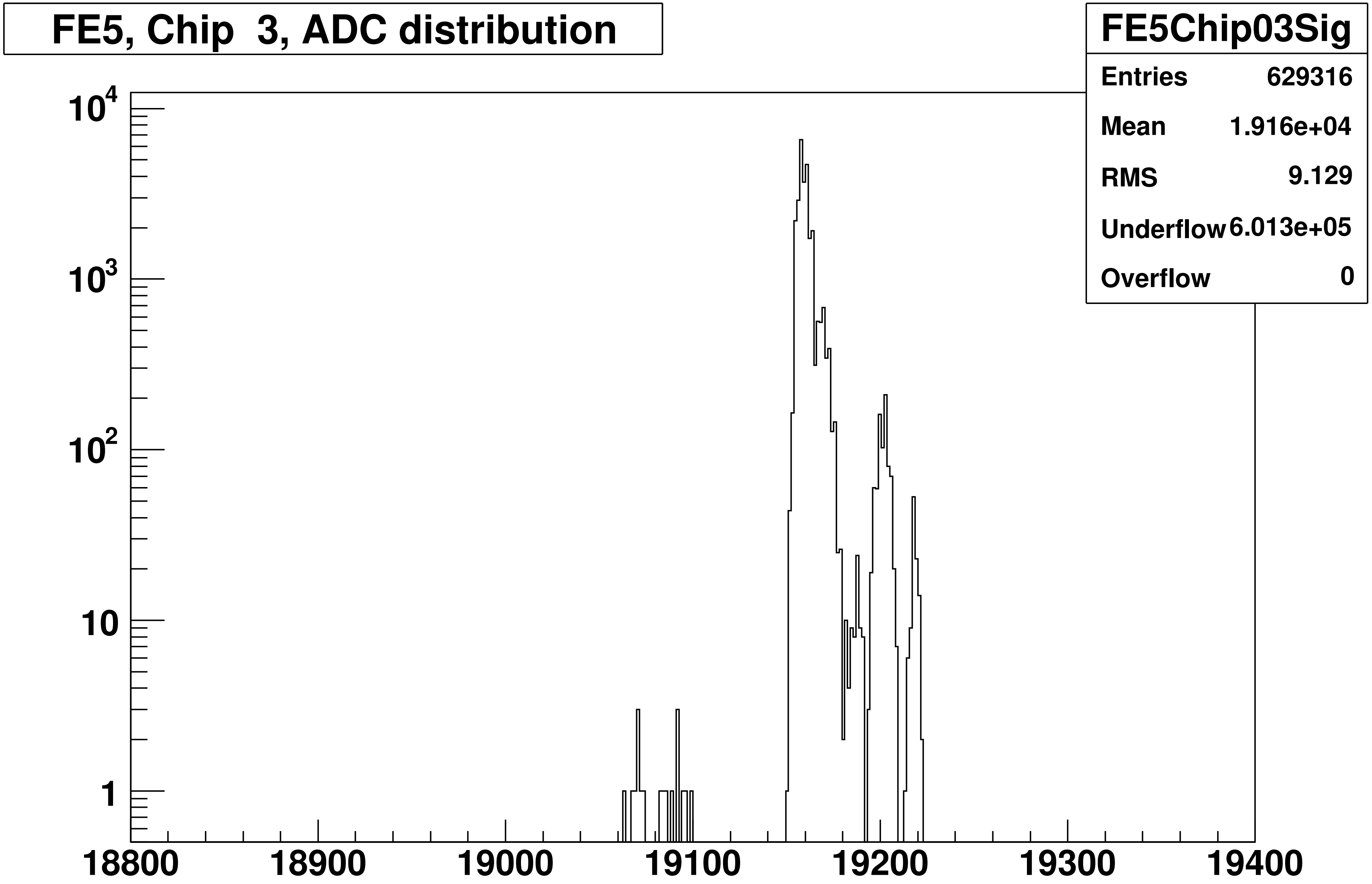

FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 629316
Mean 1.926e+04
RMS 11.26
Underflow 6.014e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

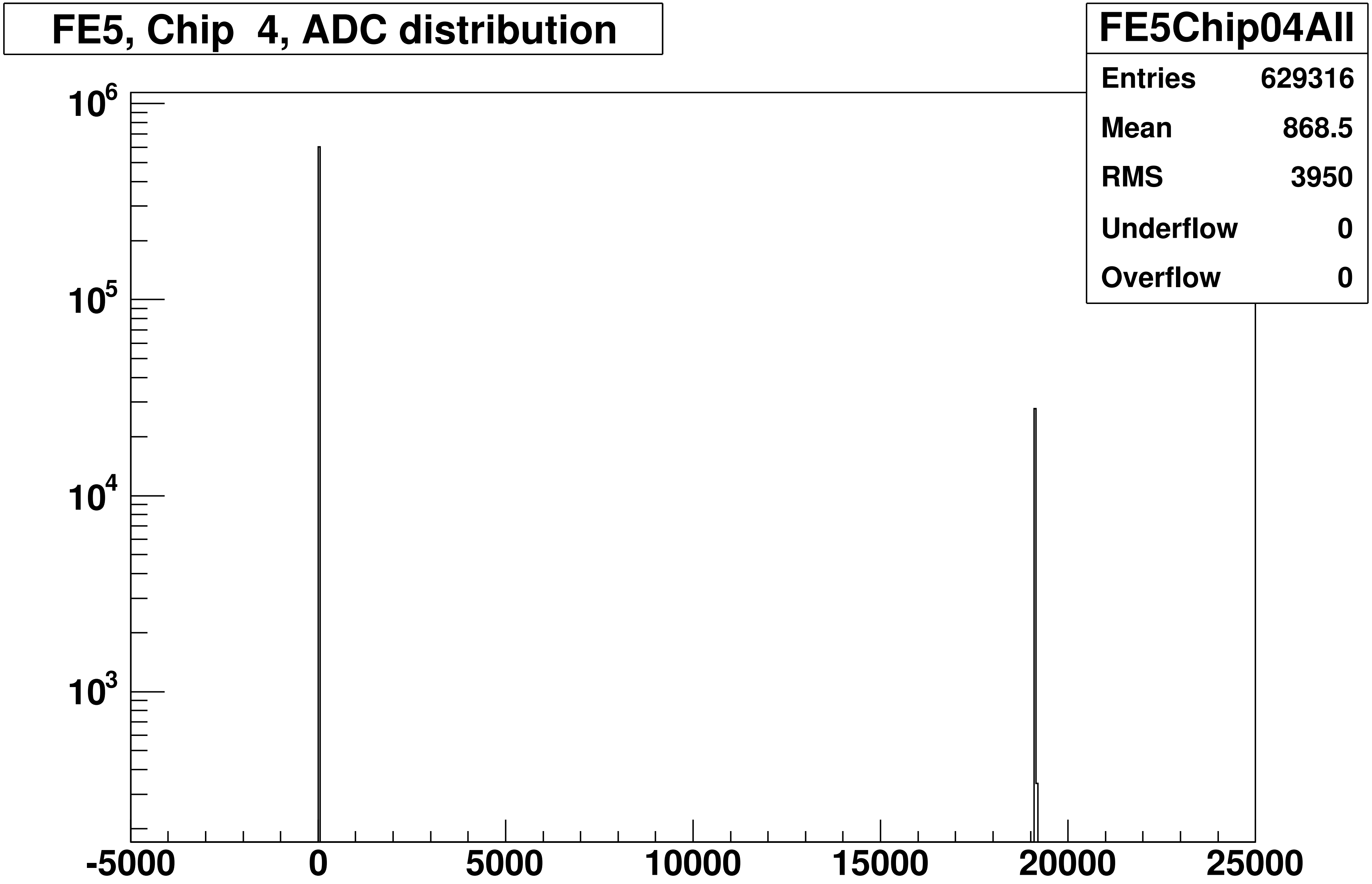


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 629316
Mean 853.9
RMS 3954
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

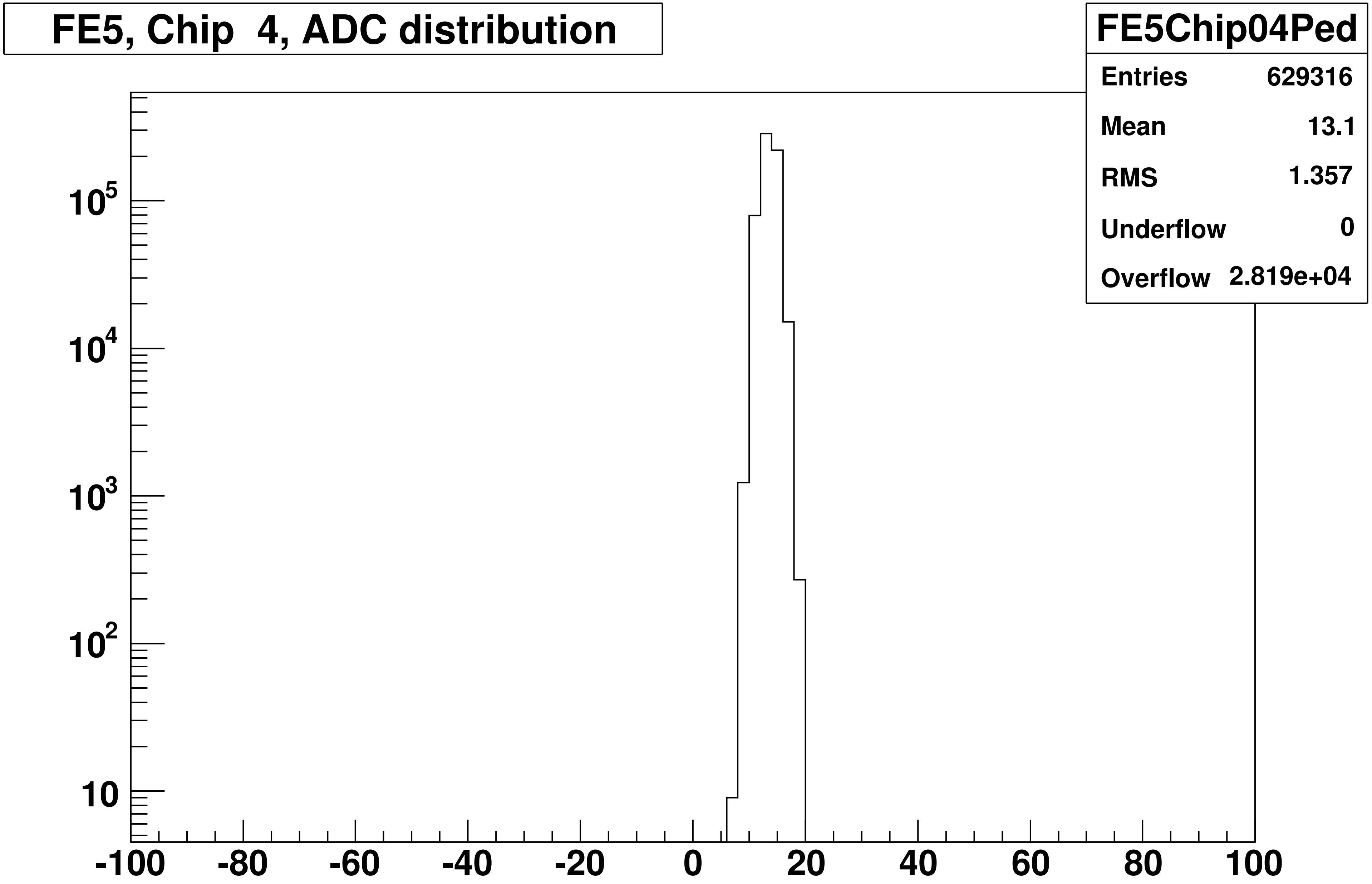


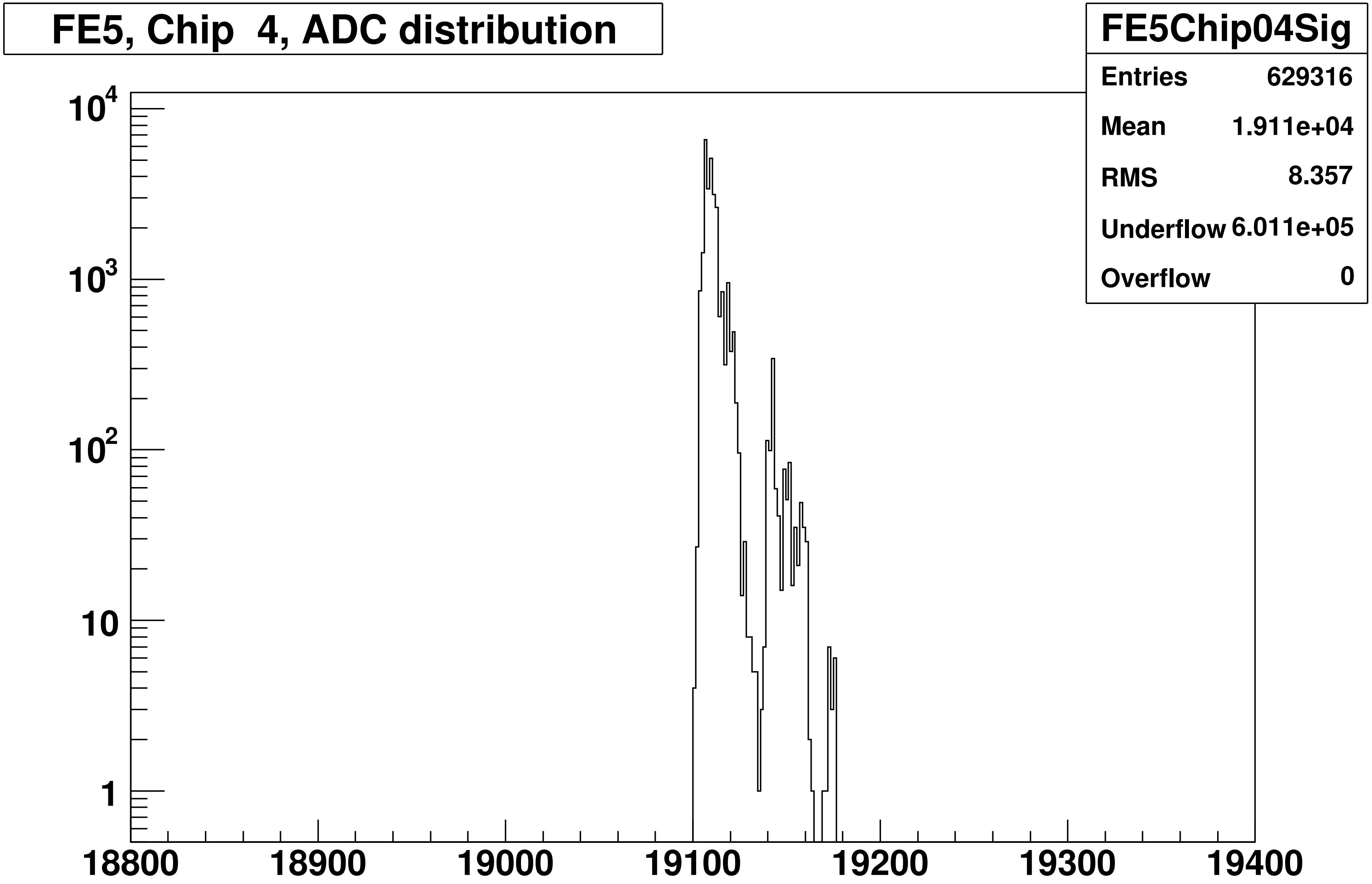
FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 629316
Mean 0.06604
RMS 1.203
Underflow 54
Overflow 2.806e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

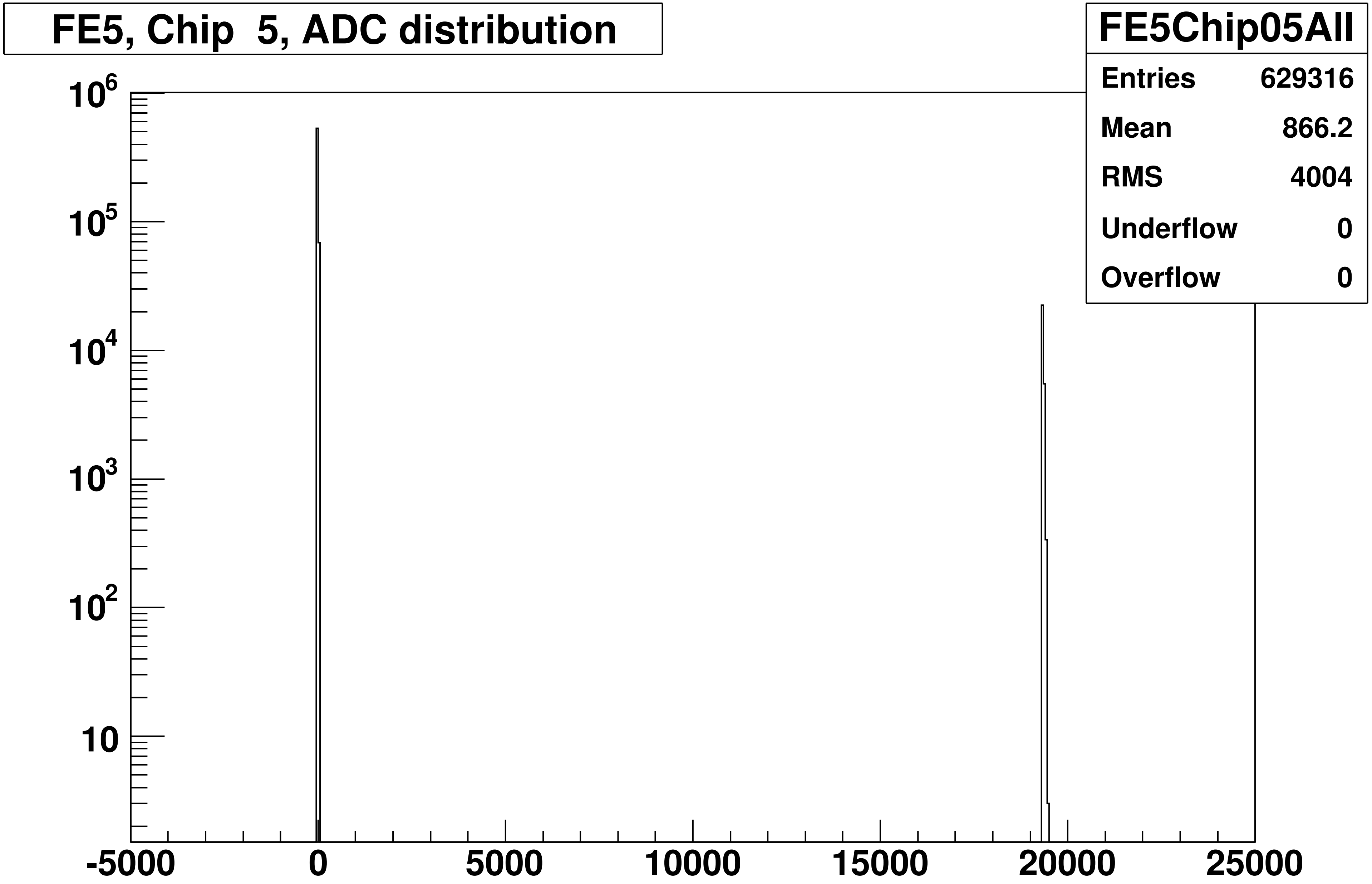

FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 629316
Mean 1.916e+04
RMS 9.129
Underflow 6.013e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

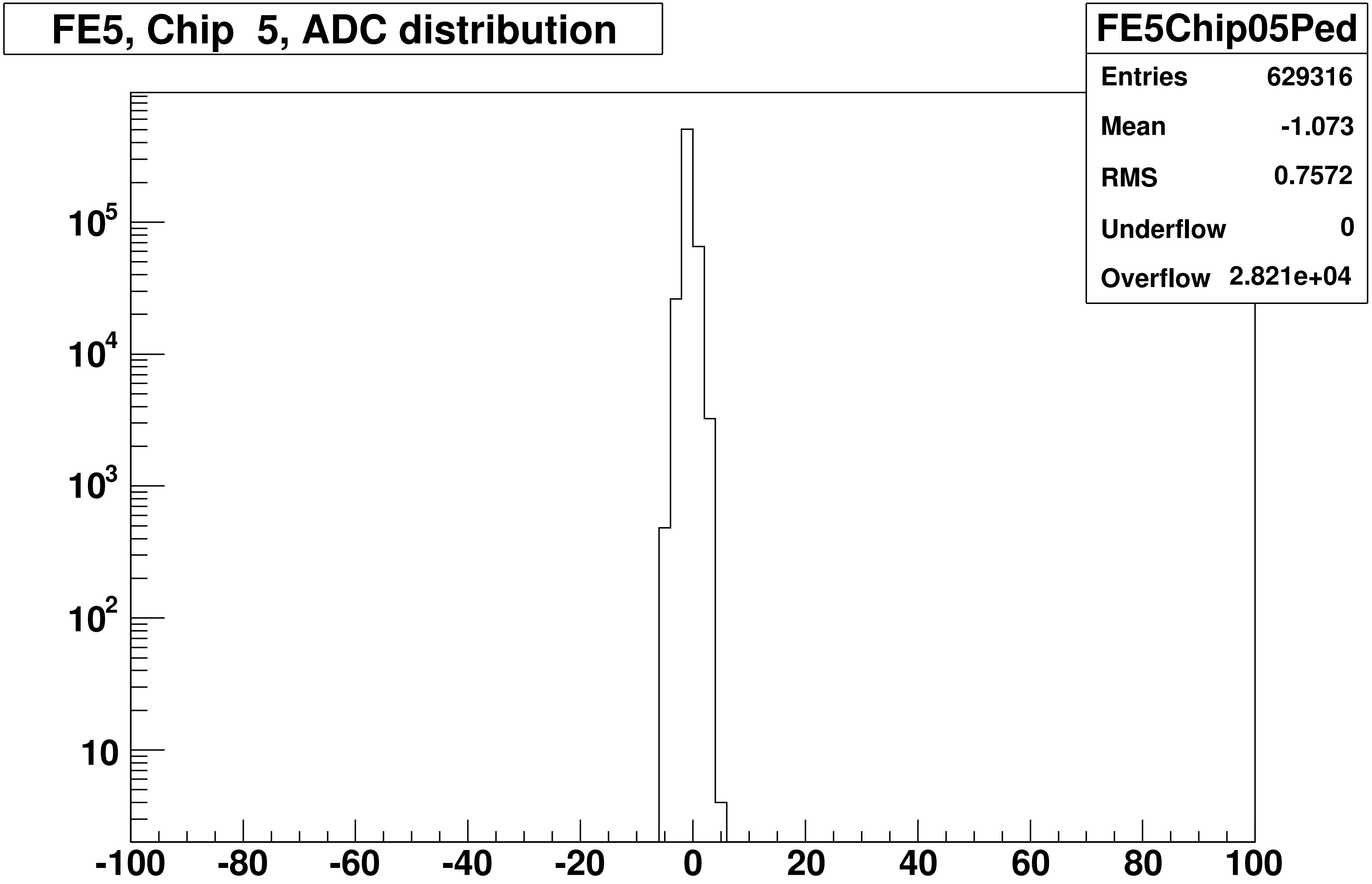


FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 629316
Mean 868.5
RMS 3950
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

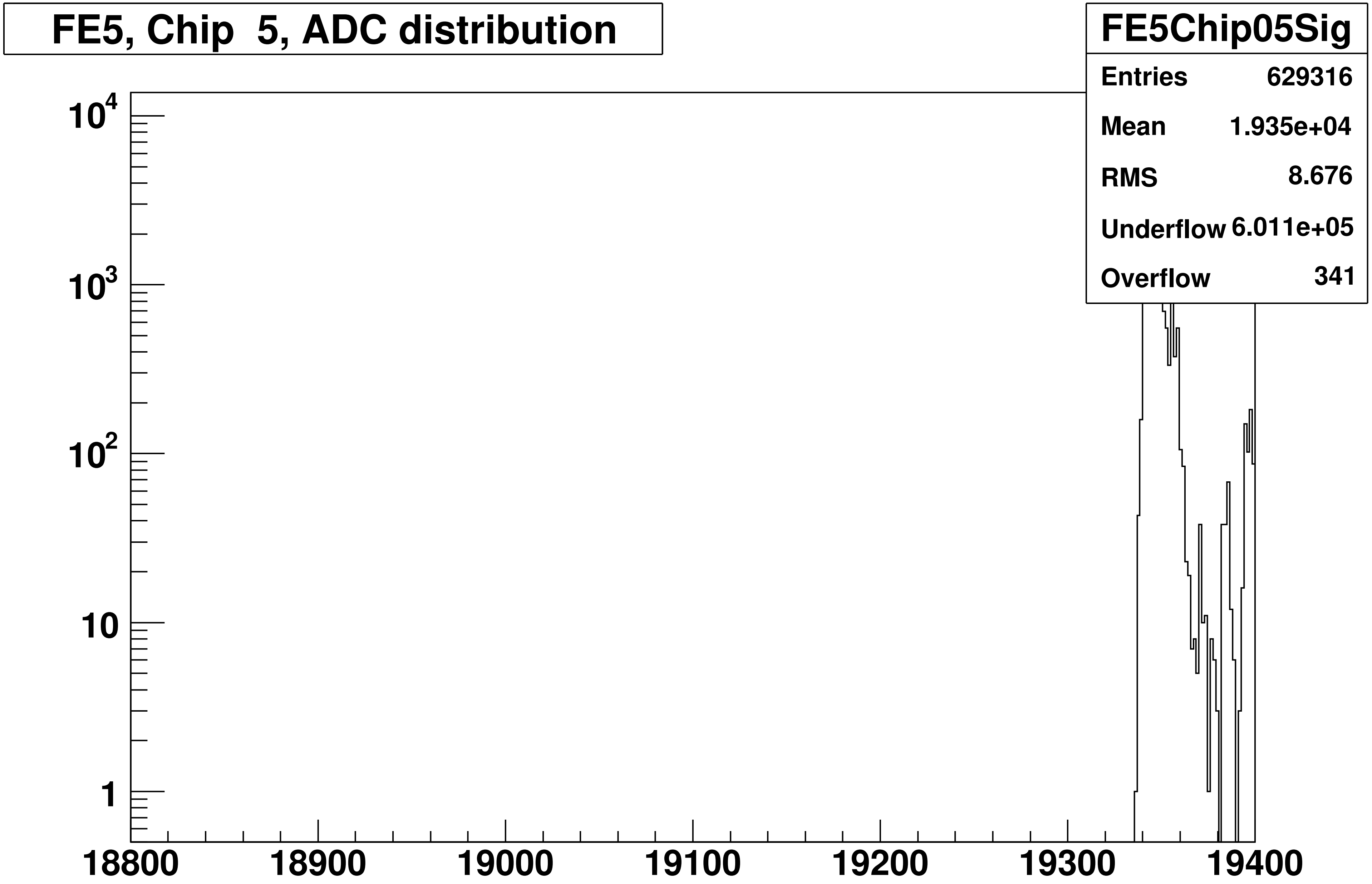

FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 629316
Mean 13.1
RMS 1.357
Underflow 0
Overflow 2.819e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

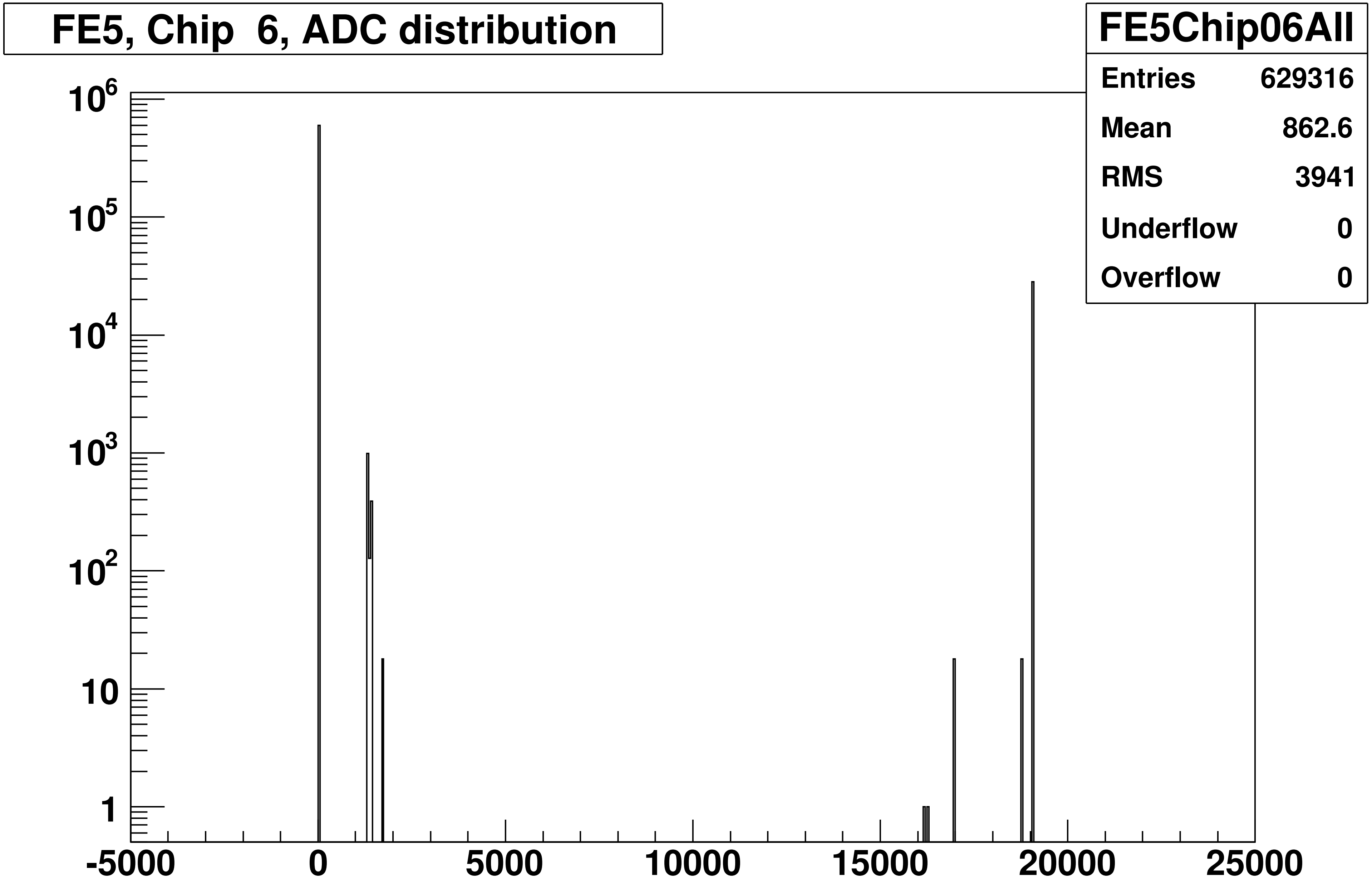

FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 629316
Mean 1.911e+04
RMS 8.357
Underflow 6.011e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

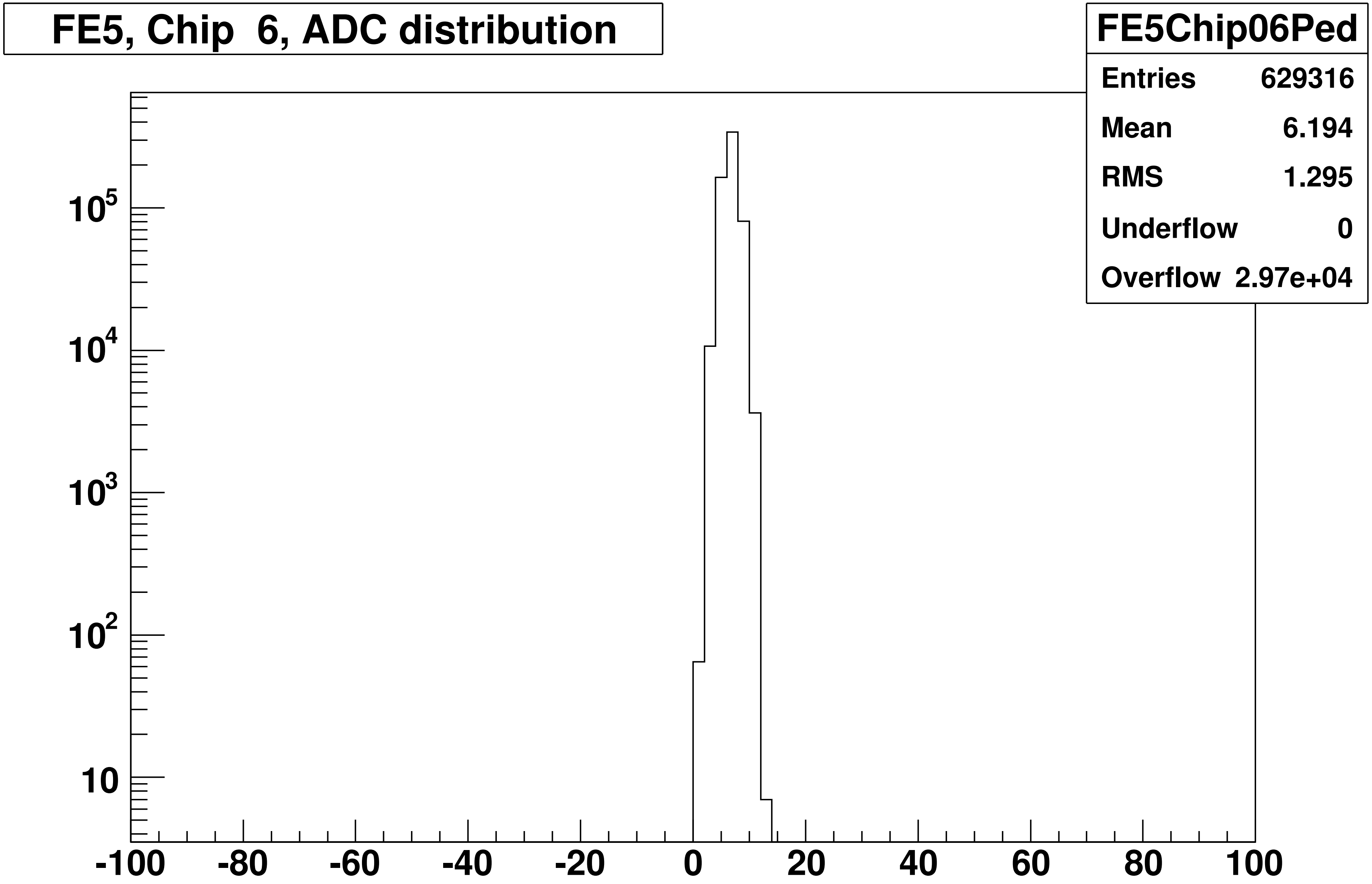

FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 629316
Mean 866.2
RMS 4004
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

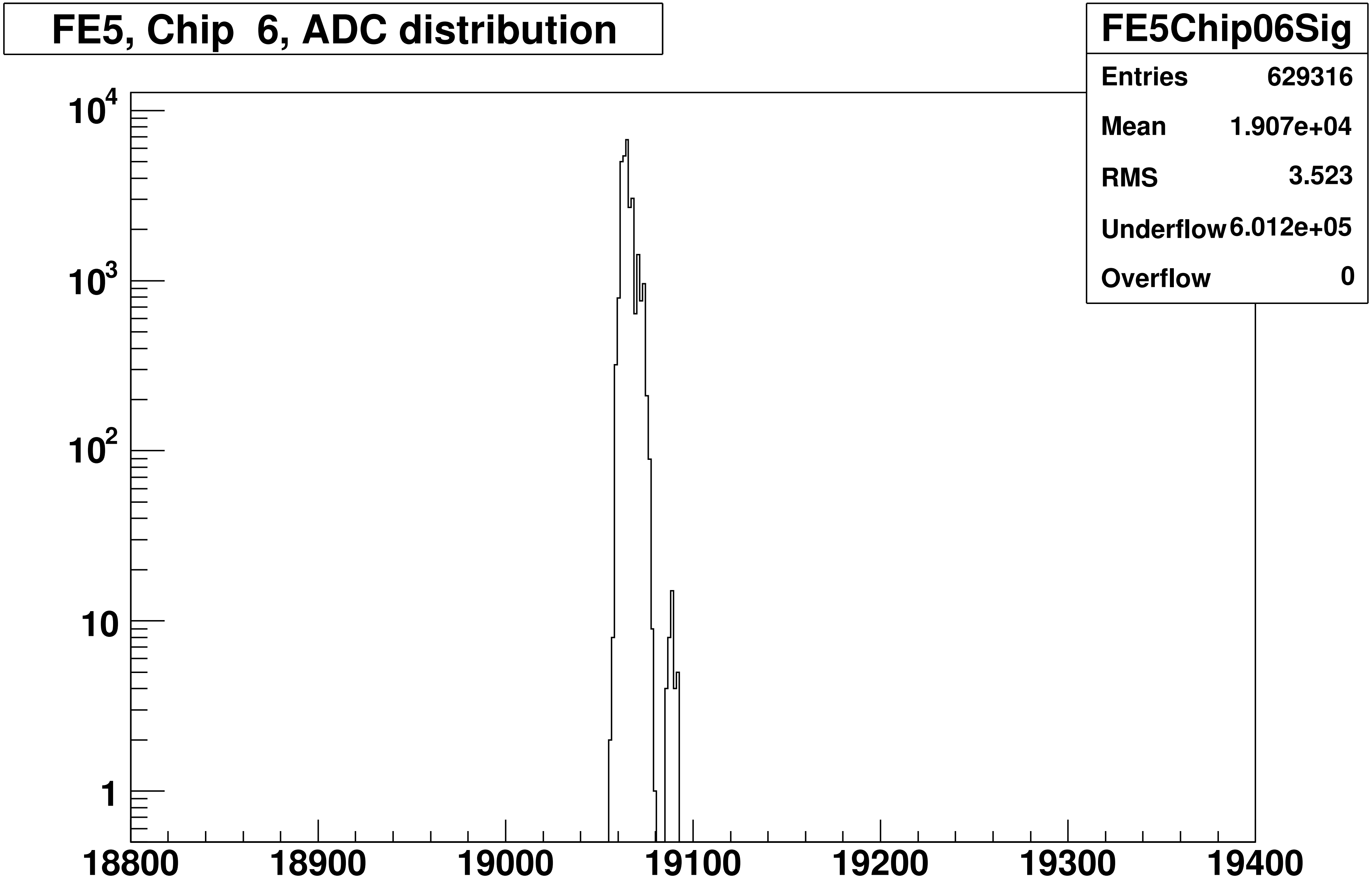


FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 629316
Mean -1.073
RMS 0.7572
Underflow 0
Overflow 2.821e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

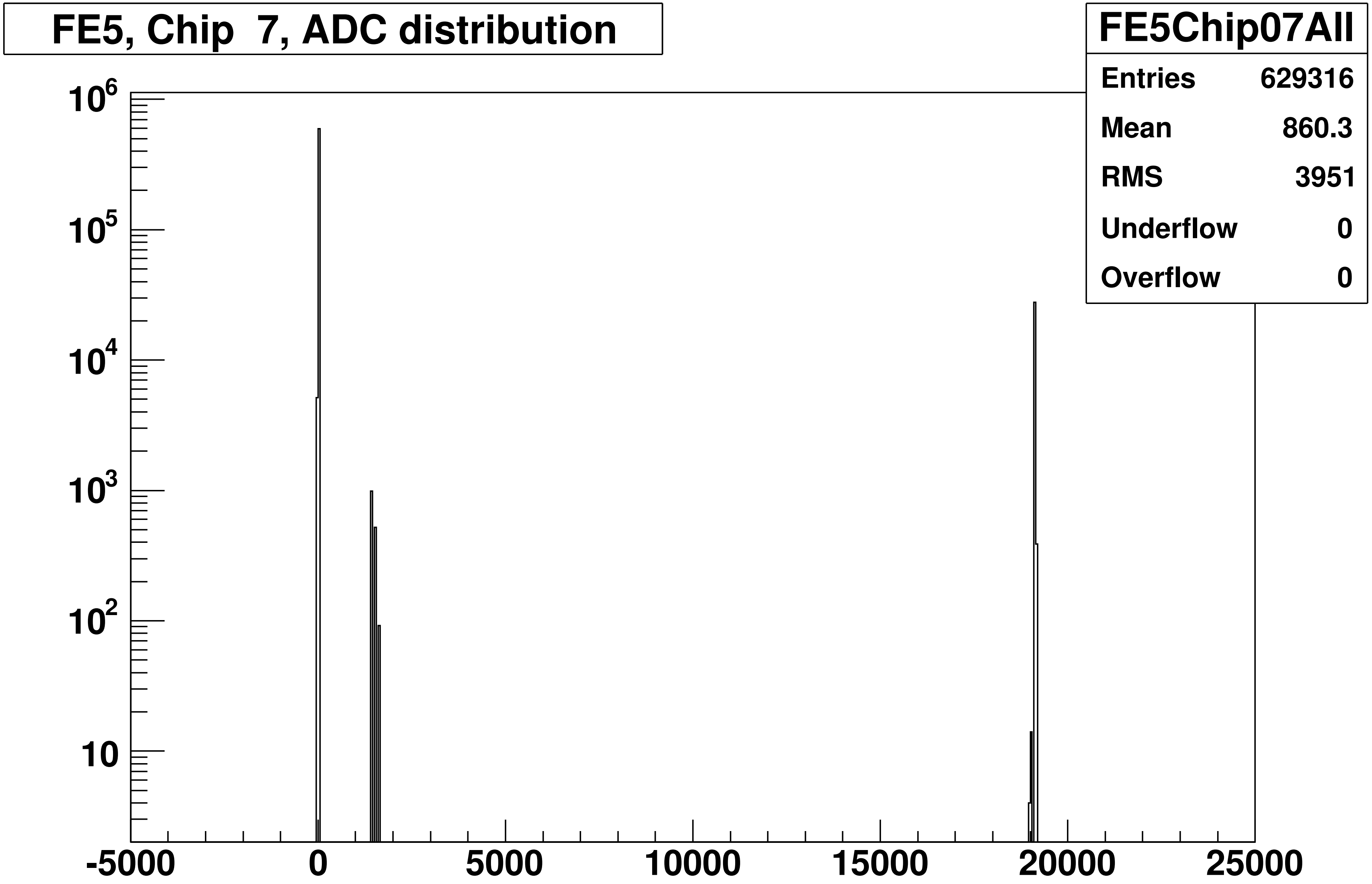

FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 629316
Mean 1.935e+04
RMS 8.676
Underflow 6.011e+05
Overflow 341
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

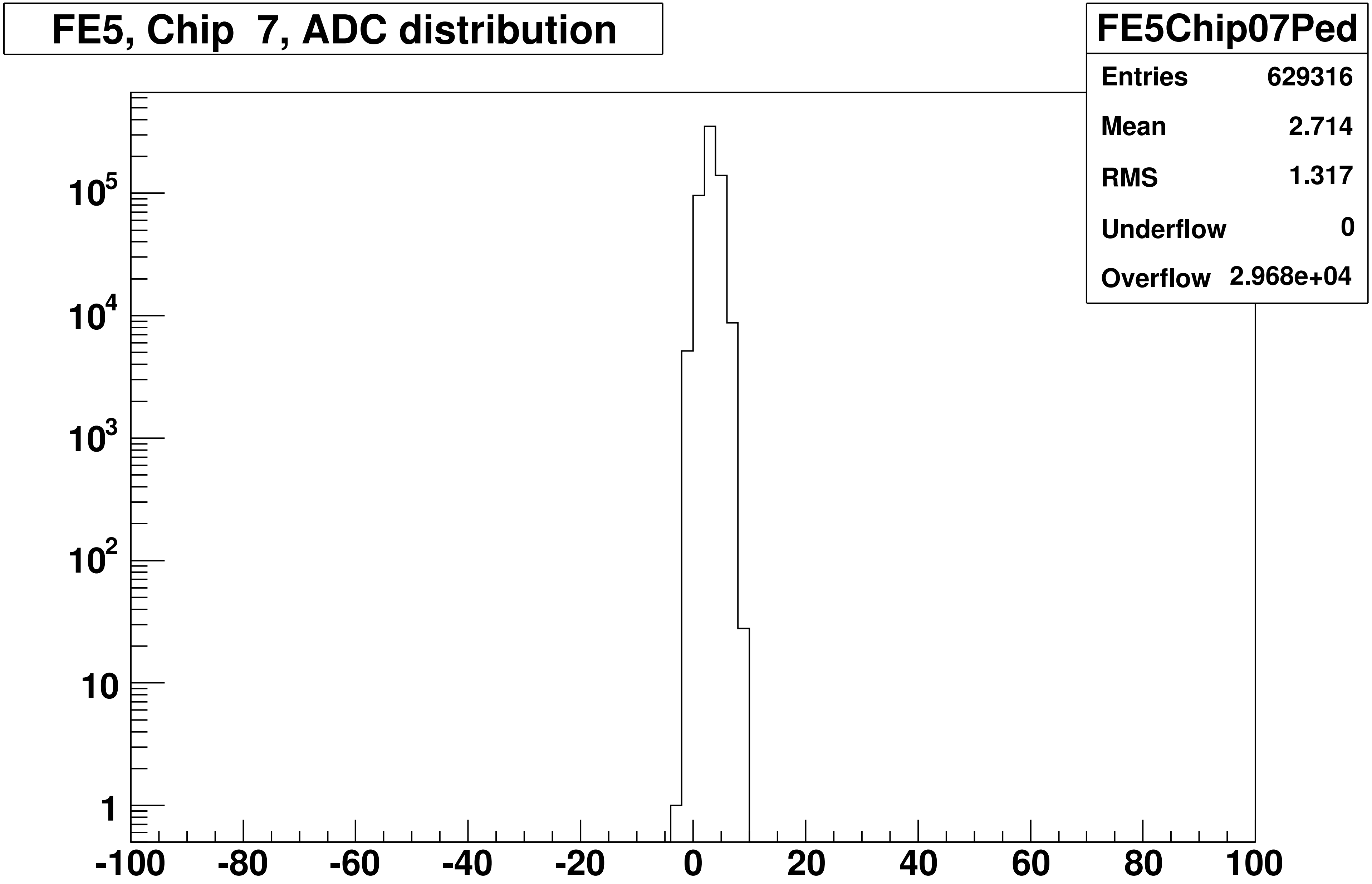

FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 629316
Mean 862.6
RMS 3941
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 629316
Mean 6.194
RMS 1.295
Underflow 0
Overflow 2.97e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

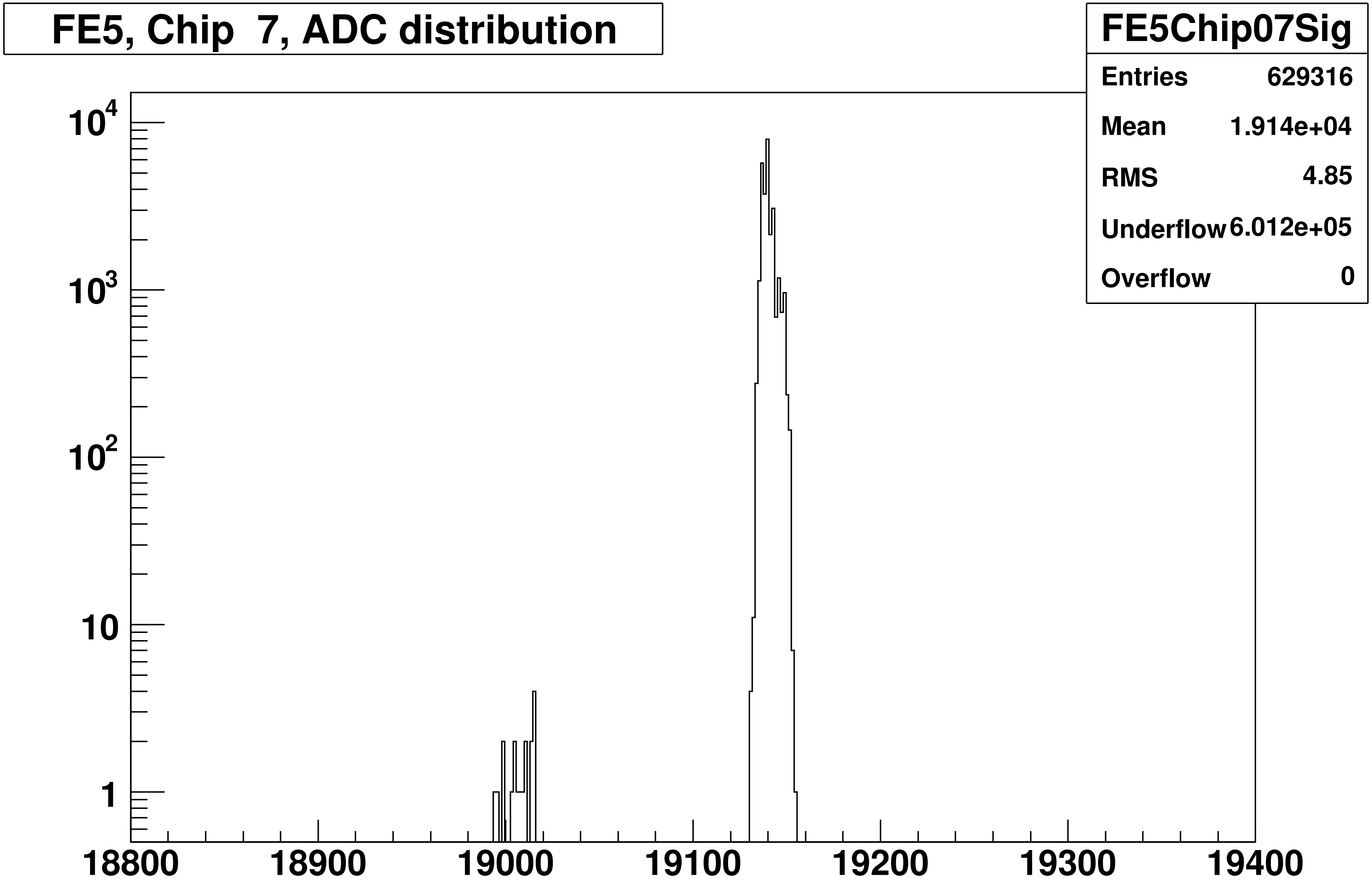


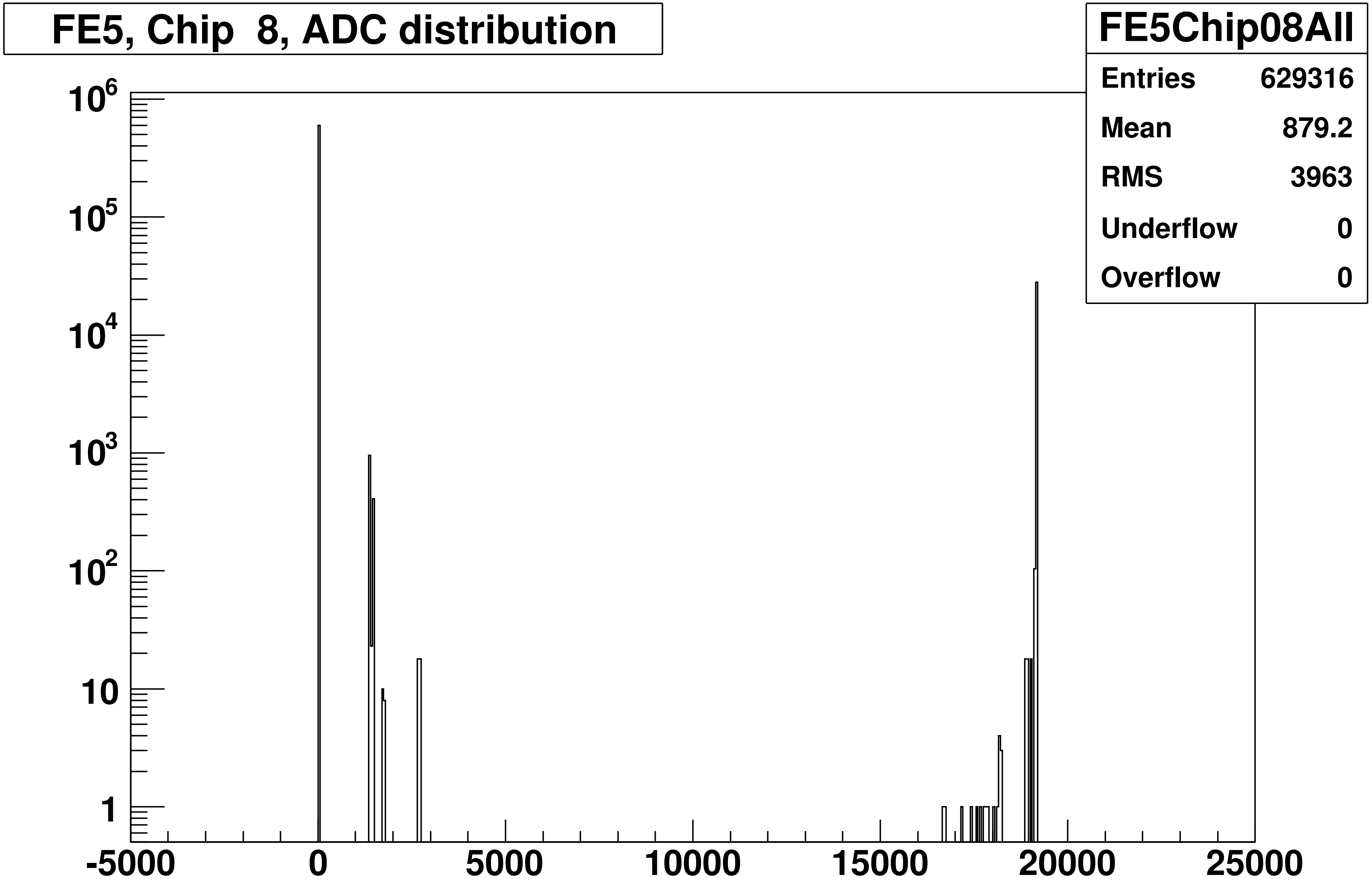
FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 629316
Mean 1.907e+04
RMS 3.523
Underflow 6.012e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

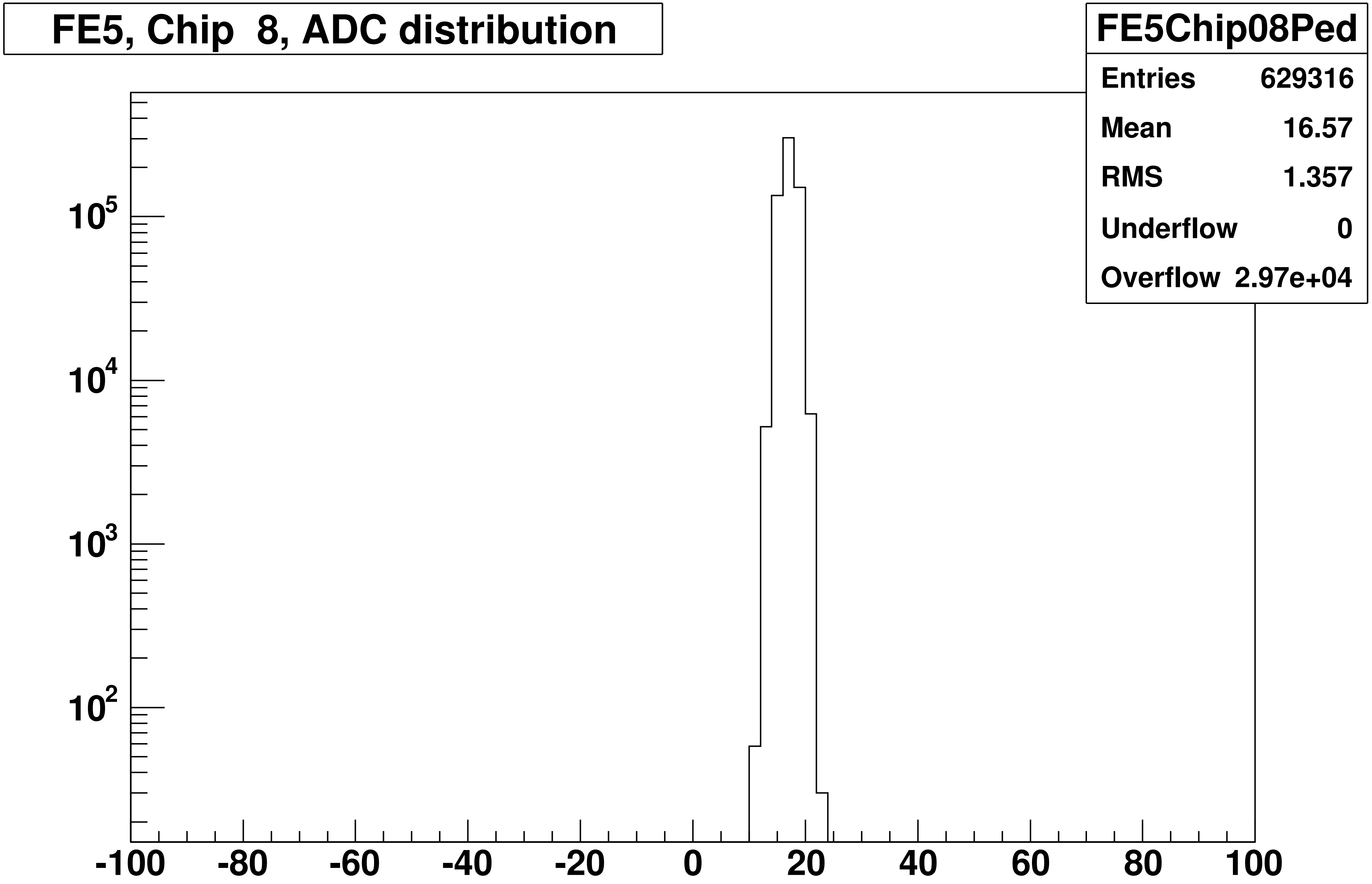

FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 629316
Mean 860.3
RMS 3951
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



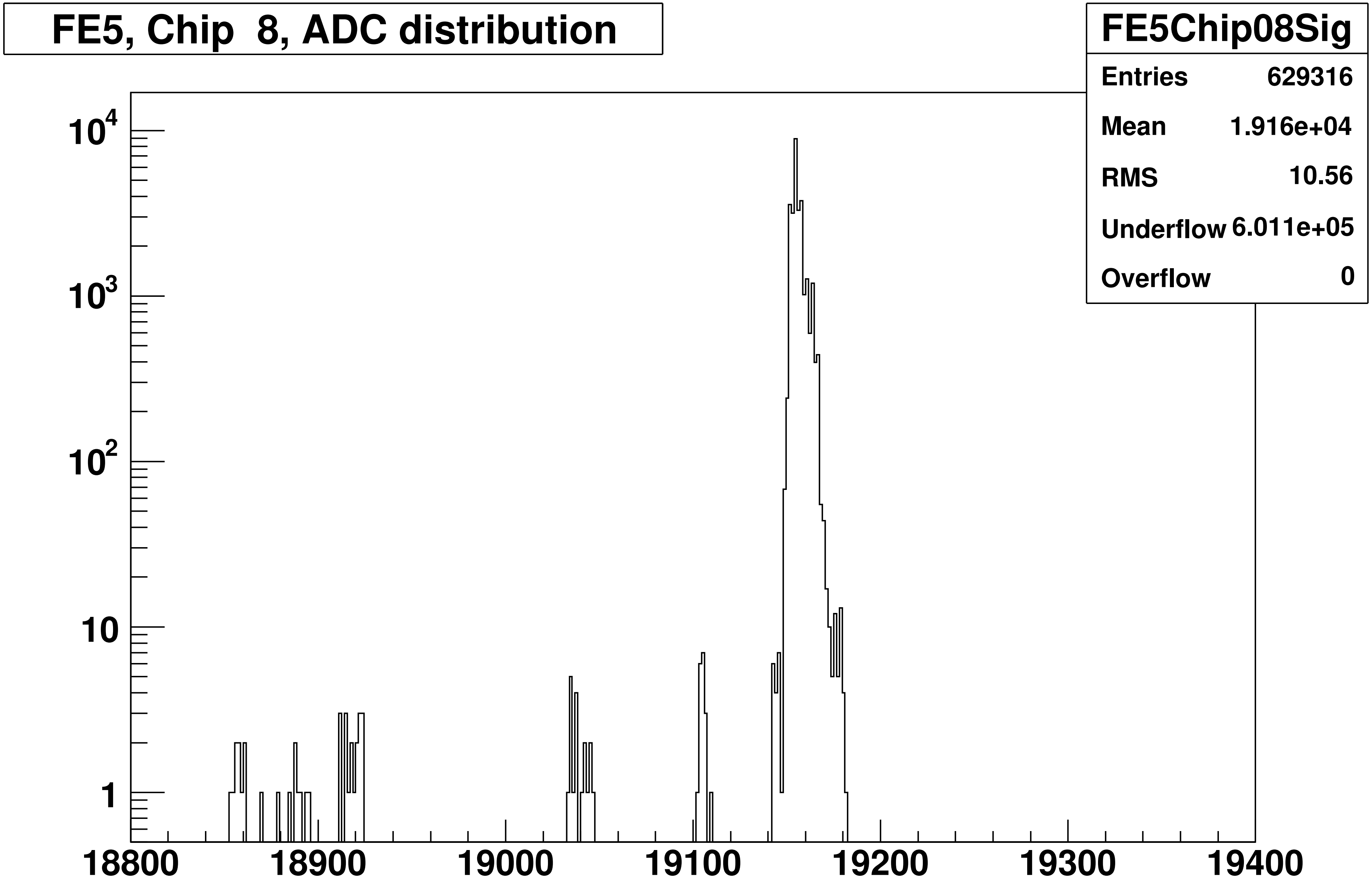
FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 629316
Mean 2.714
RMS 1.317
Underflow 0
Overflow 2.968e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 629316
Mean 1.914e+04
RMS 4.85
Underflow 6.012e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

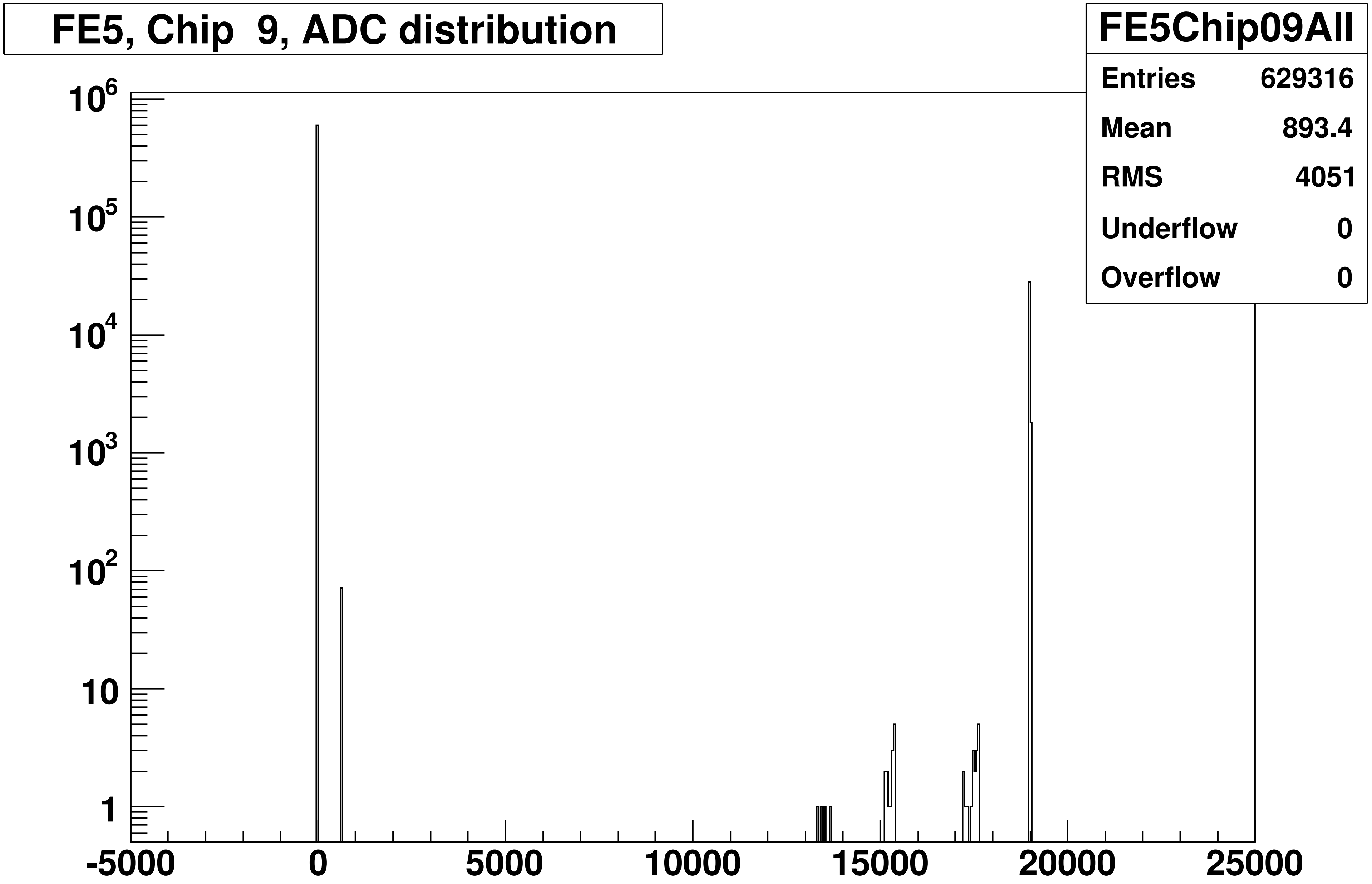

FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 629316
Mean 879.2
RMS 3963
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



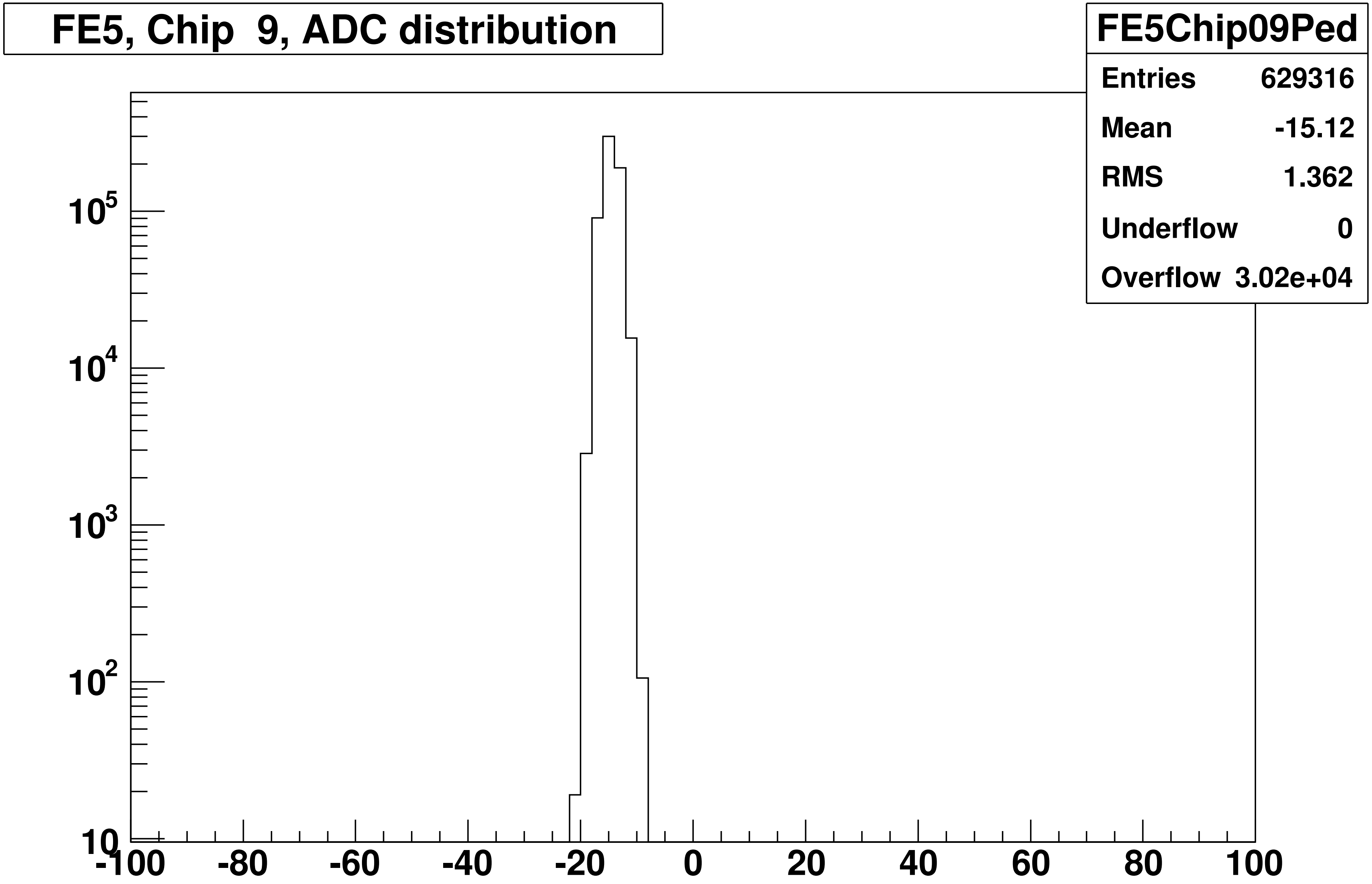
FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 629316
Mean 16.57
RMS 1.357
Underflow 0
Overflow 2.97e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



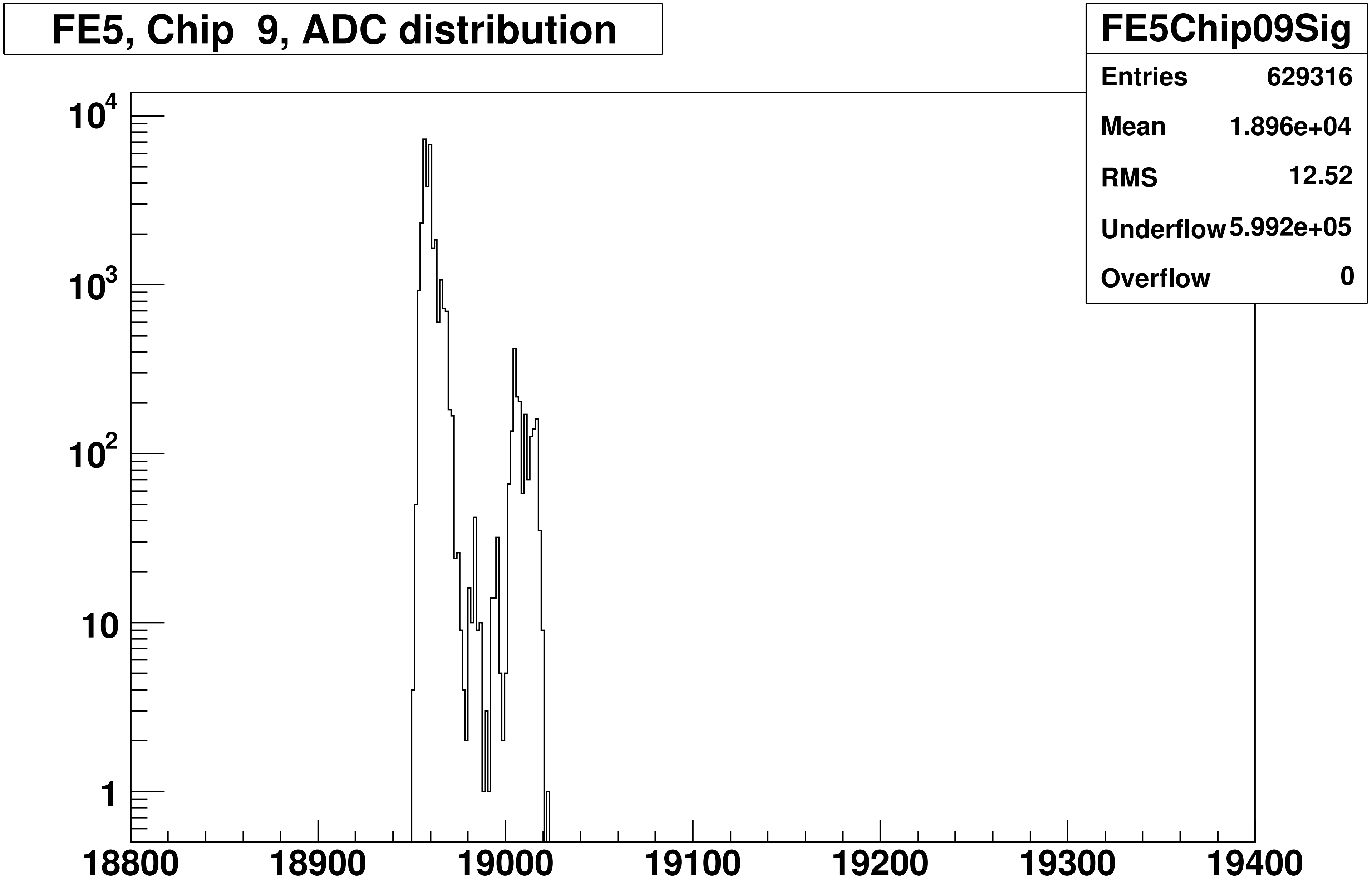
FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 629316
Mean 1.916e+04
RMS 10.56
Underflow 6.011e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



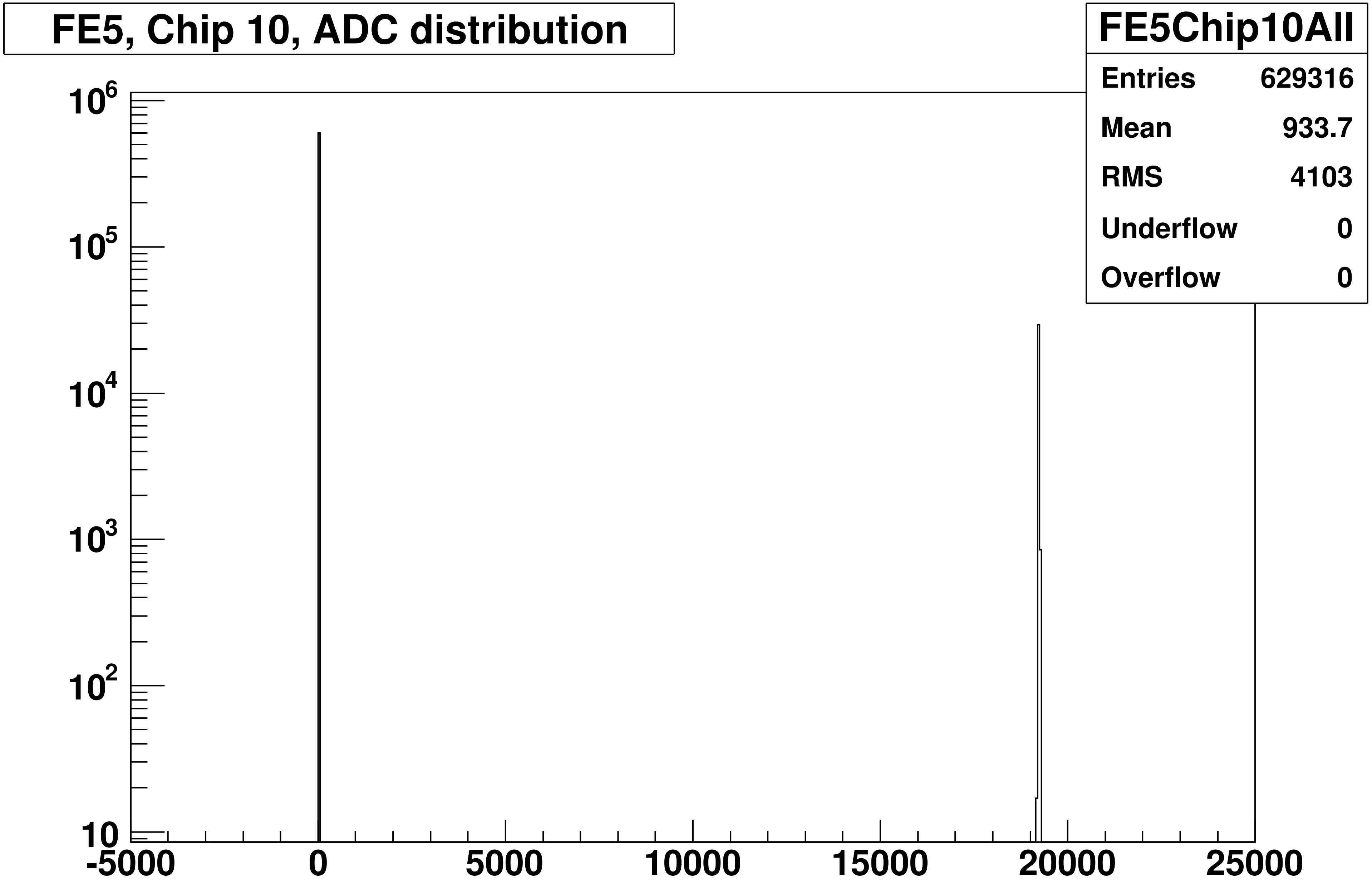
FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 629316
Mean 893.4
RMS 4051
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



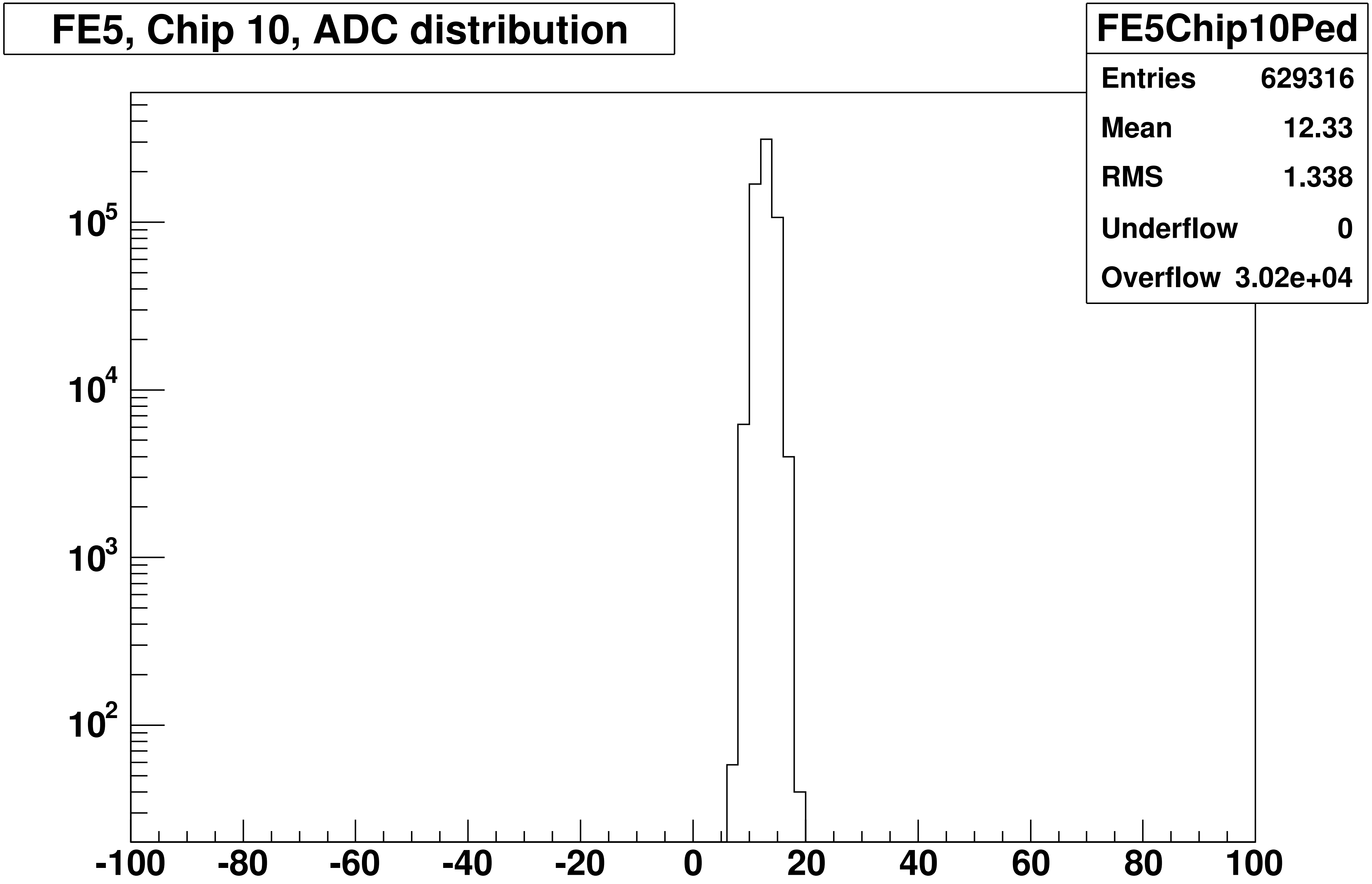
FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 629316
Mean -15.12
RMS 1.362
Underflow 0
Overflow 3.02e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

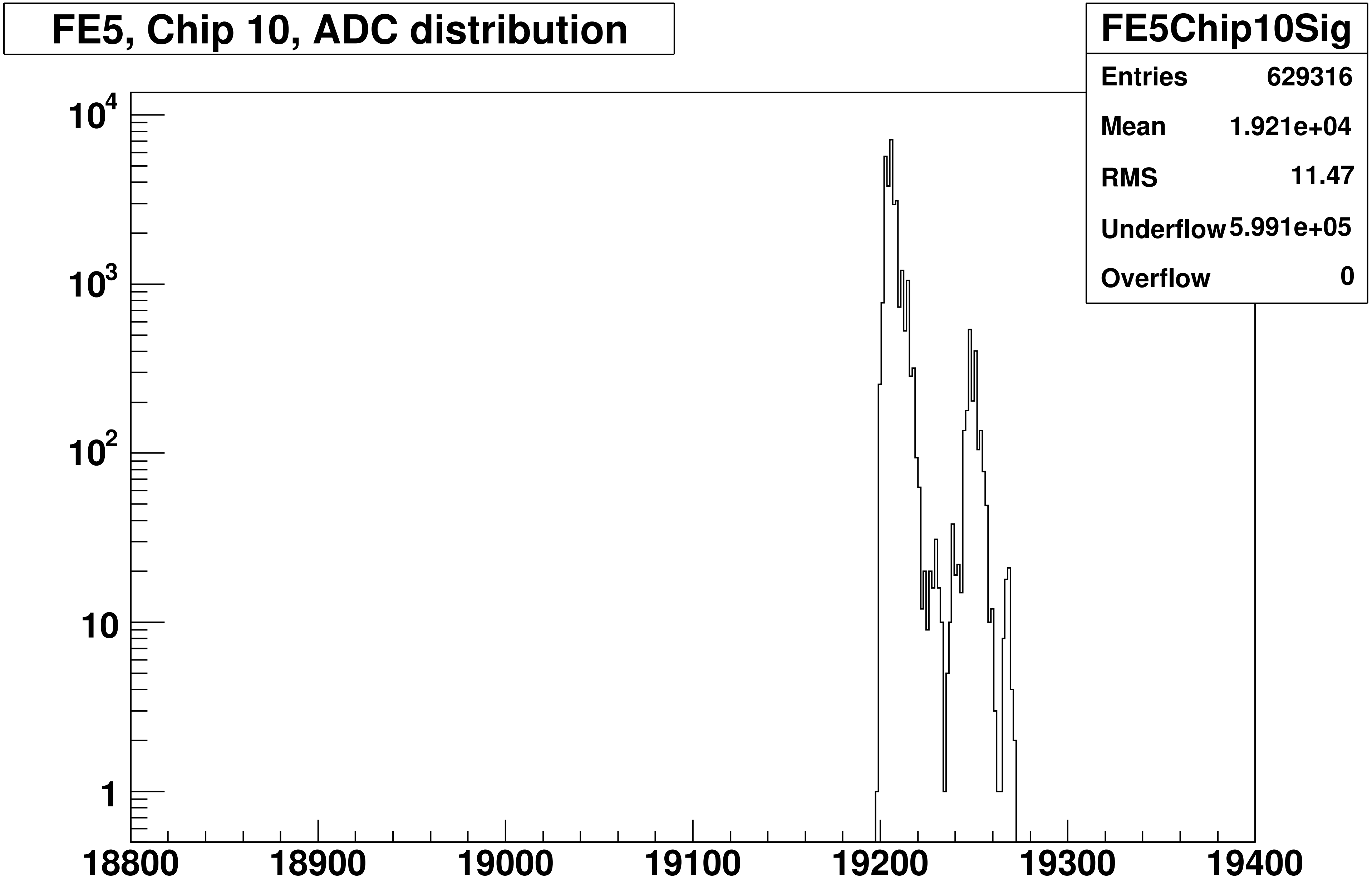


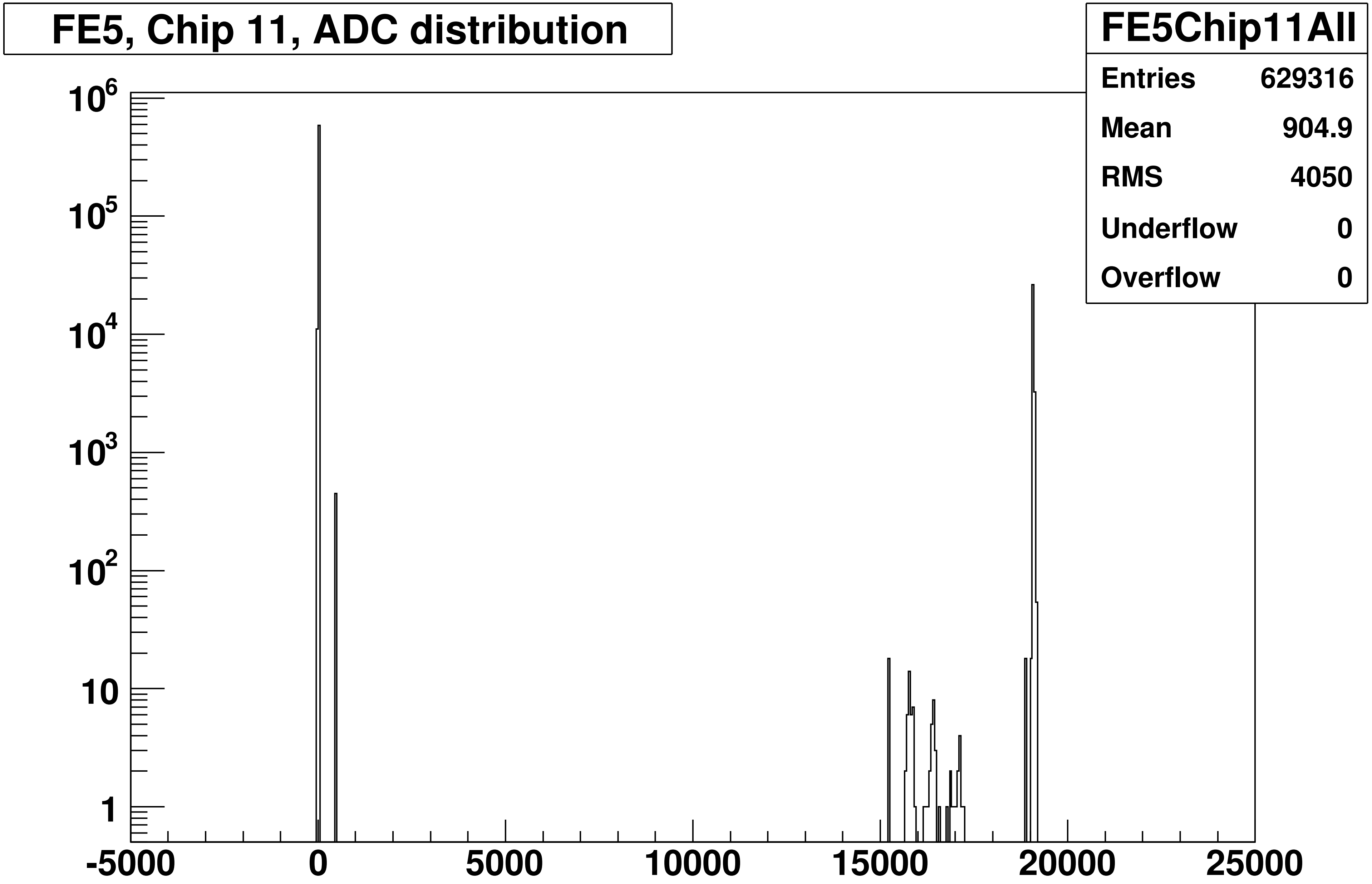
FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 629316
Mean 1.896e+04
RMS 12.52
Underflow 5.992e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

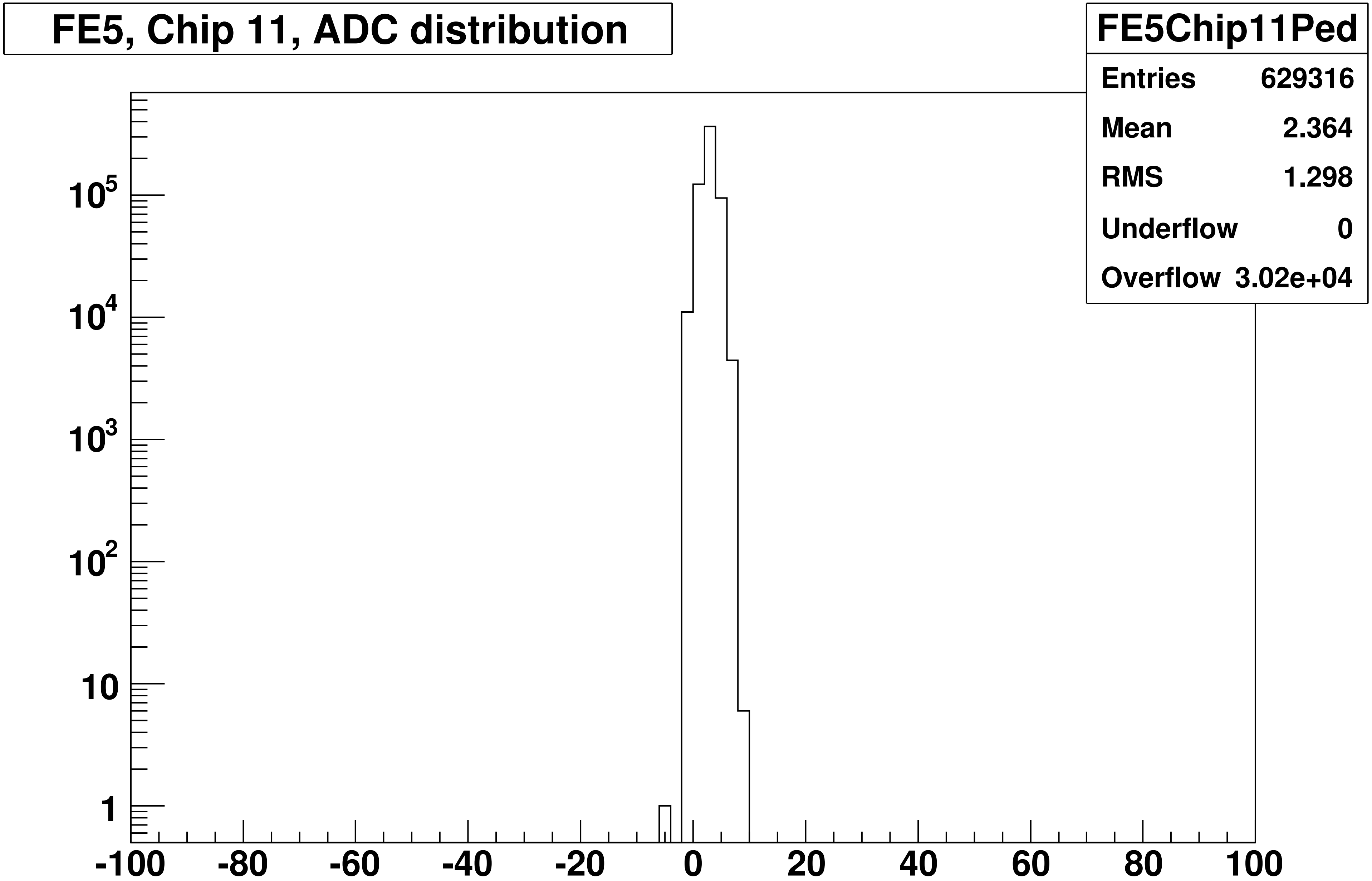


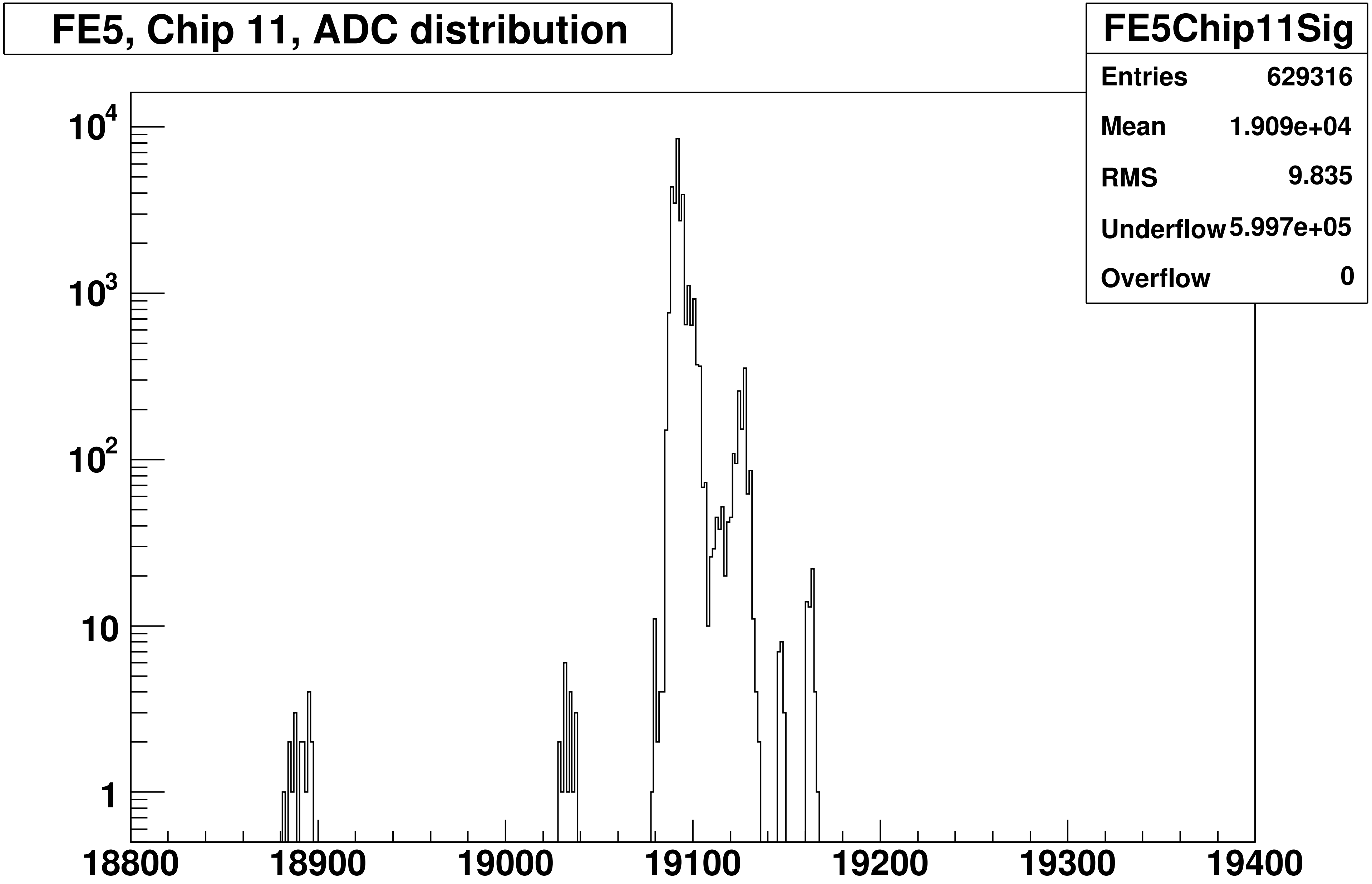
FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 629316
Mean 933.7
RMS 4103
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 629316
Mean 12.33
RMS 1.338
Underflow 0
Overflow 3.02e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

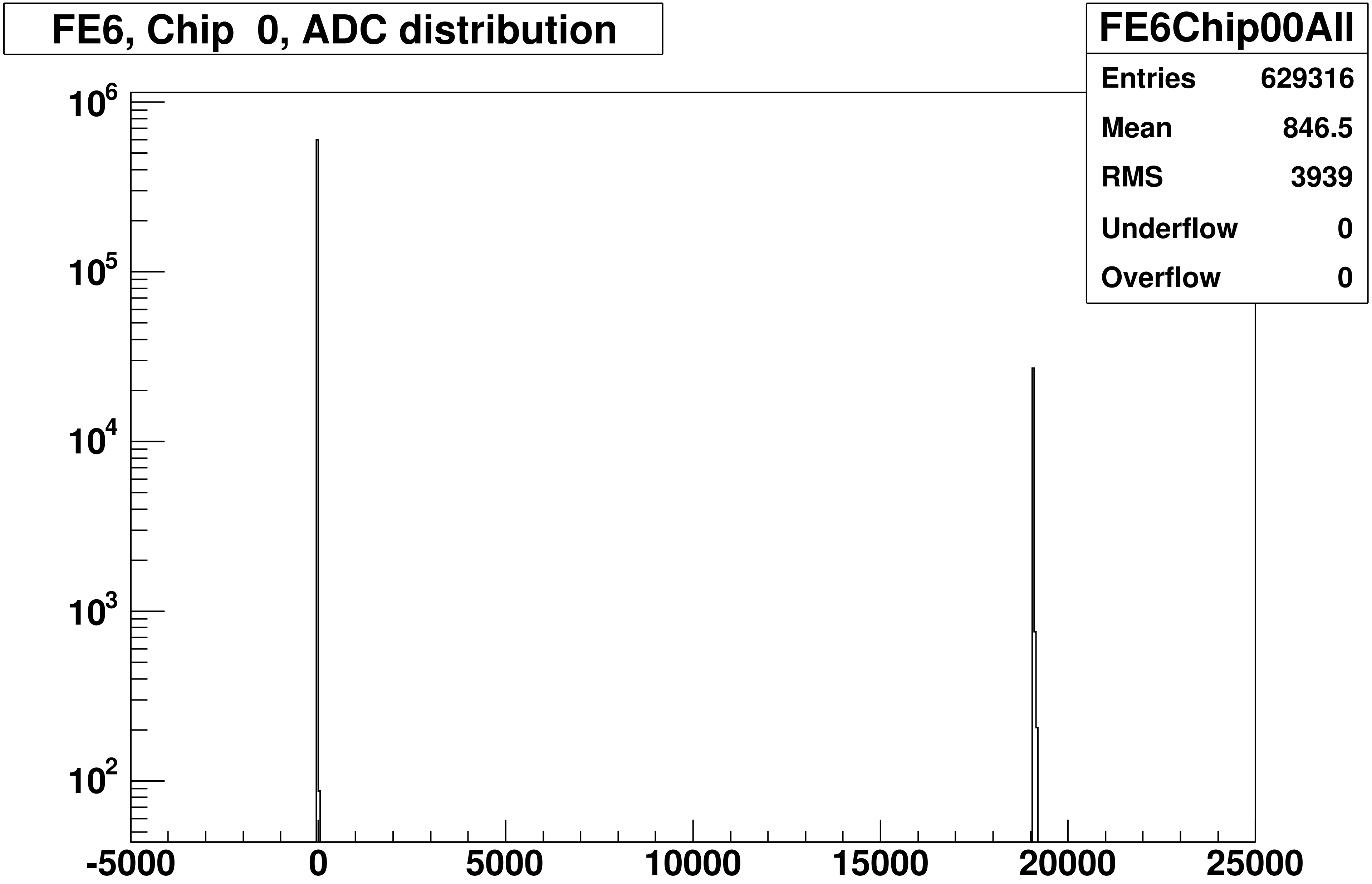

FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 629316
Mean 1.921e+04
RMS 11.47
Underflow 5.991e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

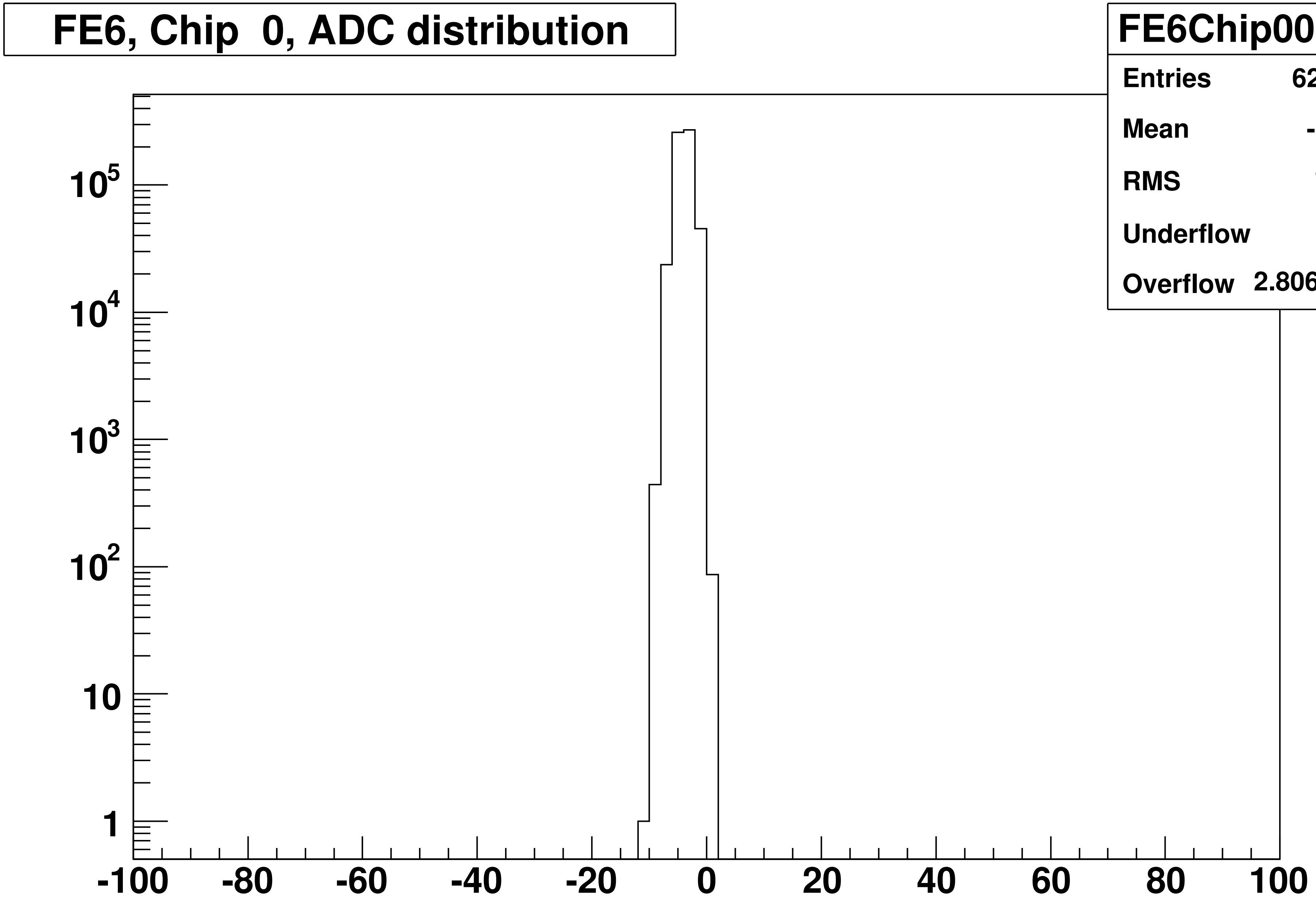

FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 629316
Mean 904.9
RMS 4050
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 629316
Mean 2.364
RMS 1.298
Underflow 0
Overflow 3.02e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

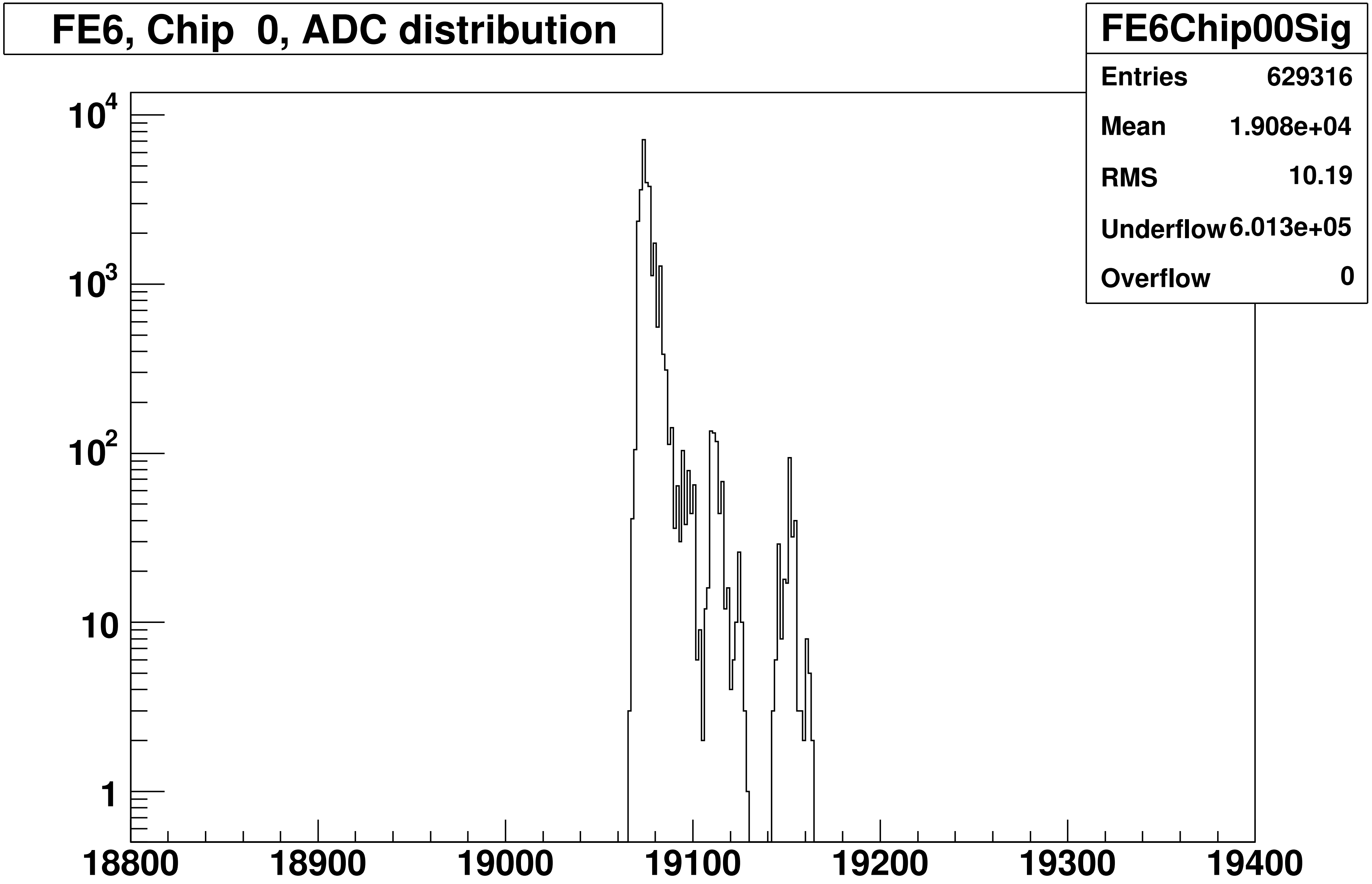


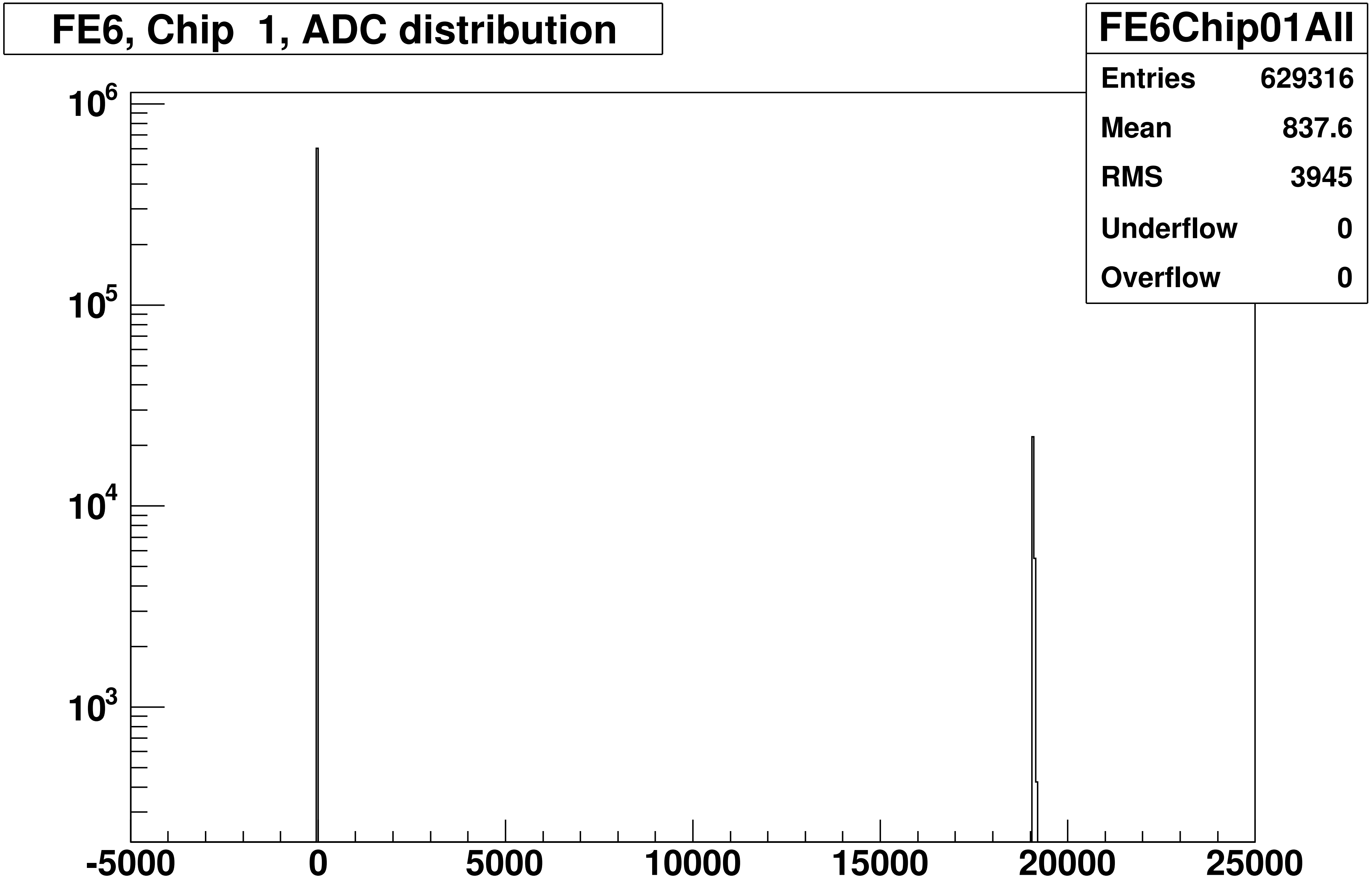
FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 629316
Mean 1.909e+04
RMS 9.835
Underflow 5.997e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

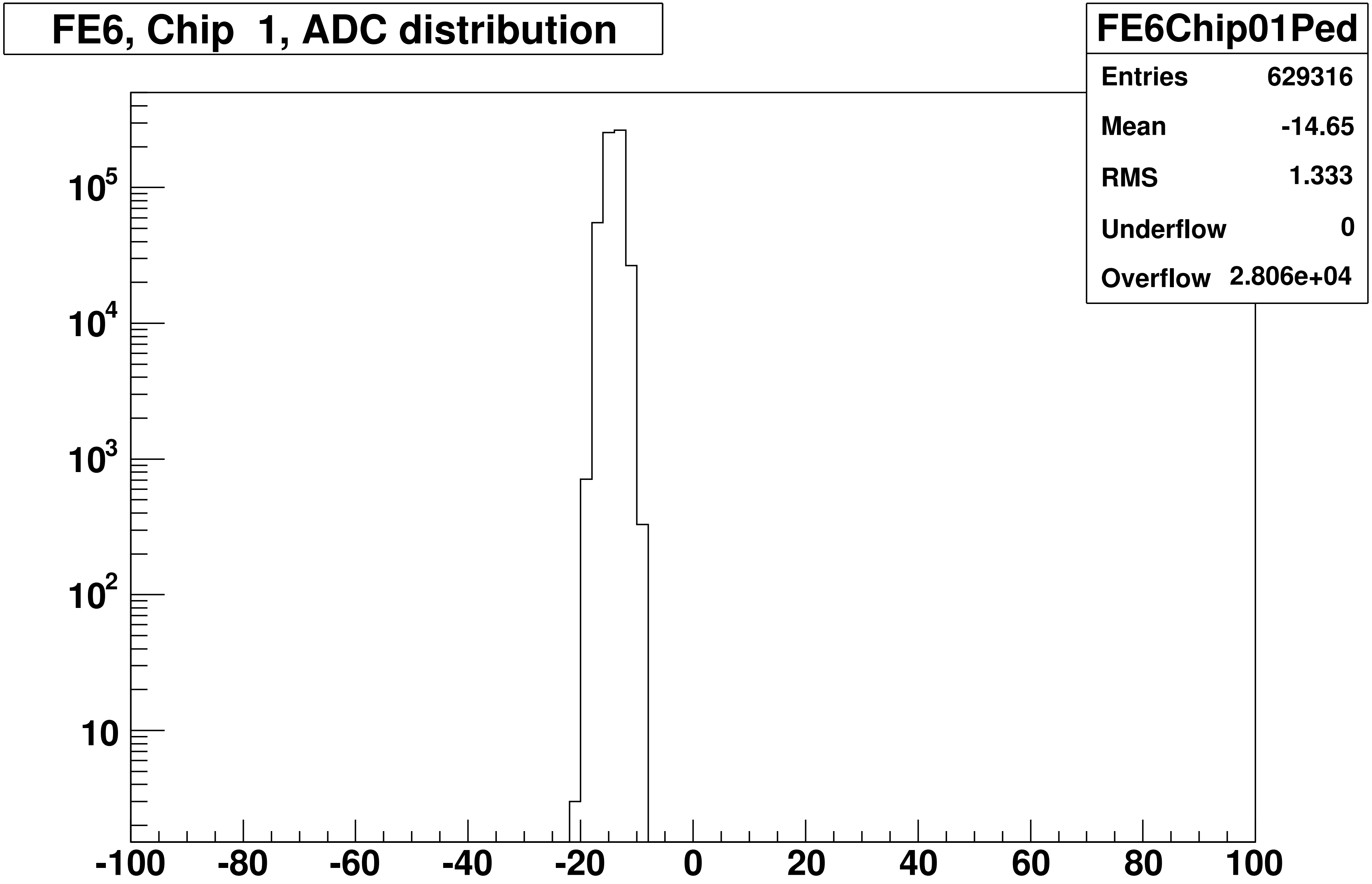

FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 629316
Mean 846.5
RMS 3939
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



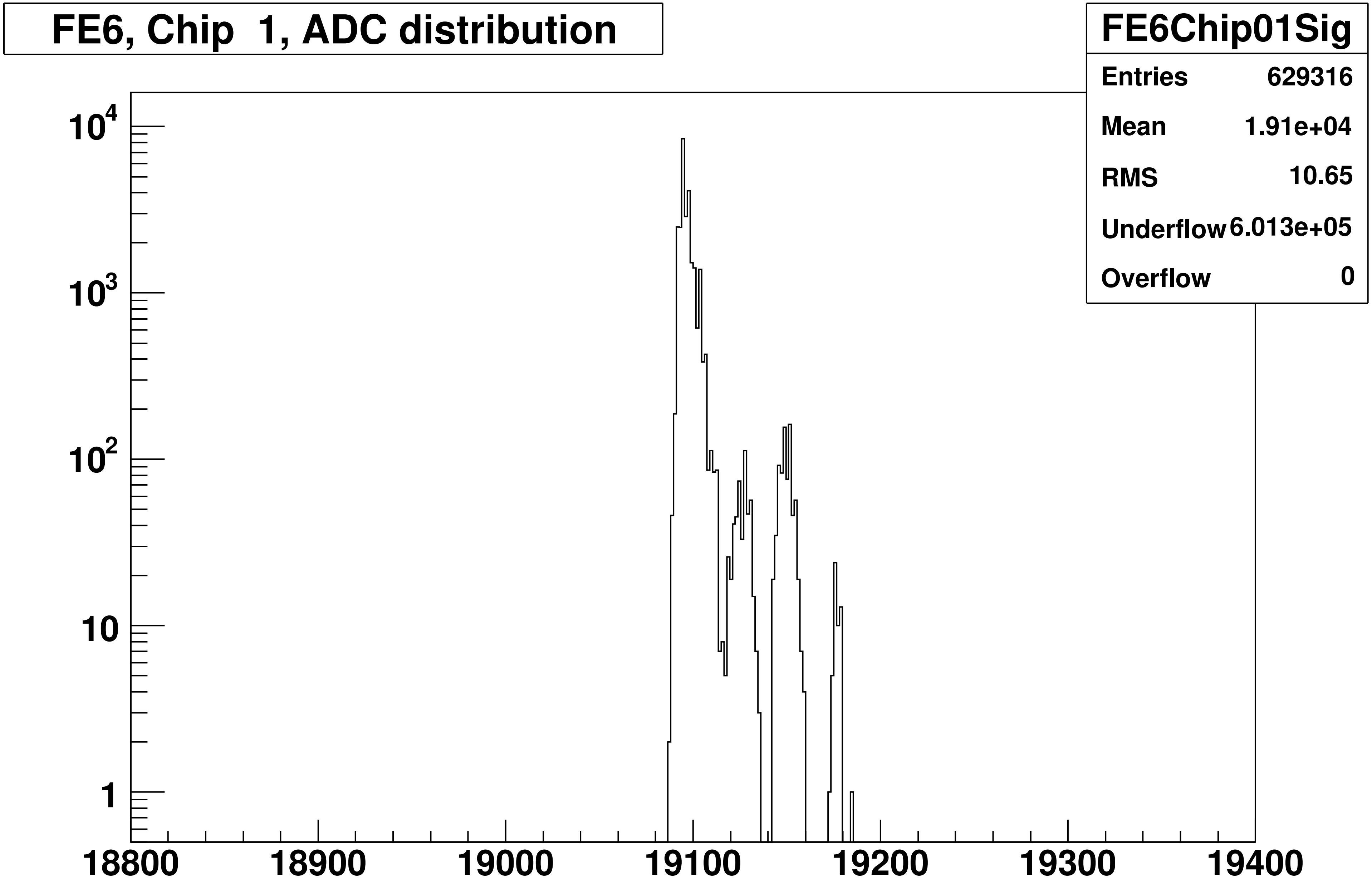
FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 629316
Mean -4.373
RMS 1.313
Underflow 0
Overflow 2.806e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

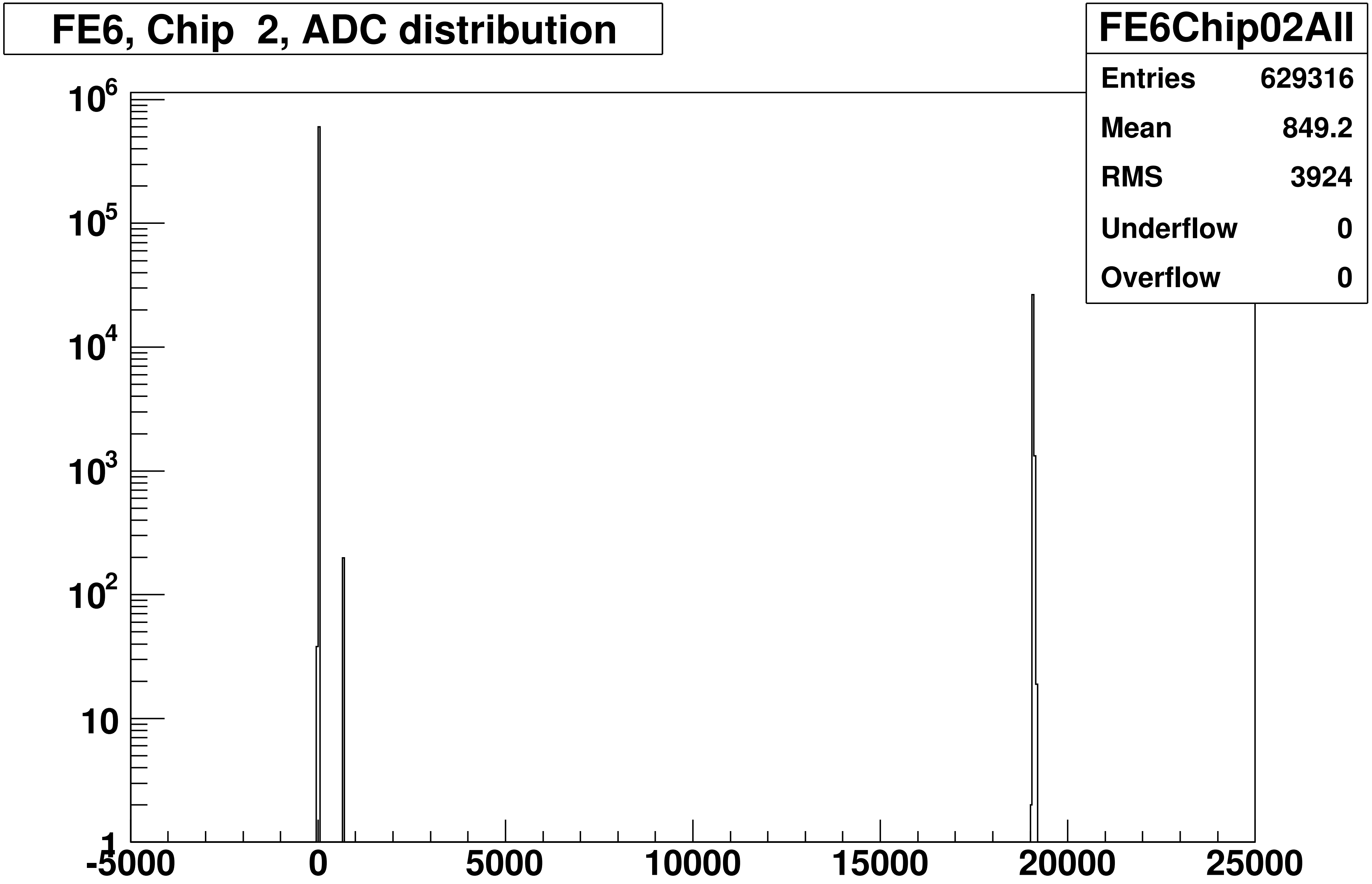

FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 629316
Mean 1.908e+04
RMS 10.19
Underflow 6.013e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 629316
Mean 837.6
RMS 3945
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

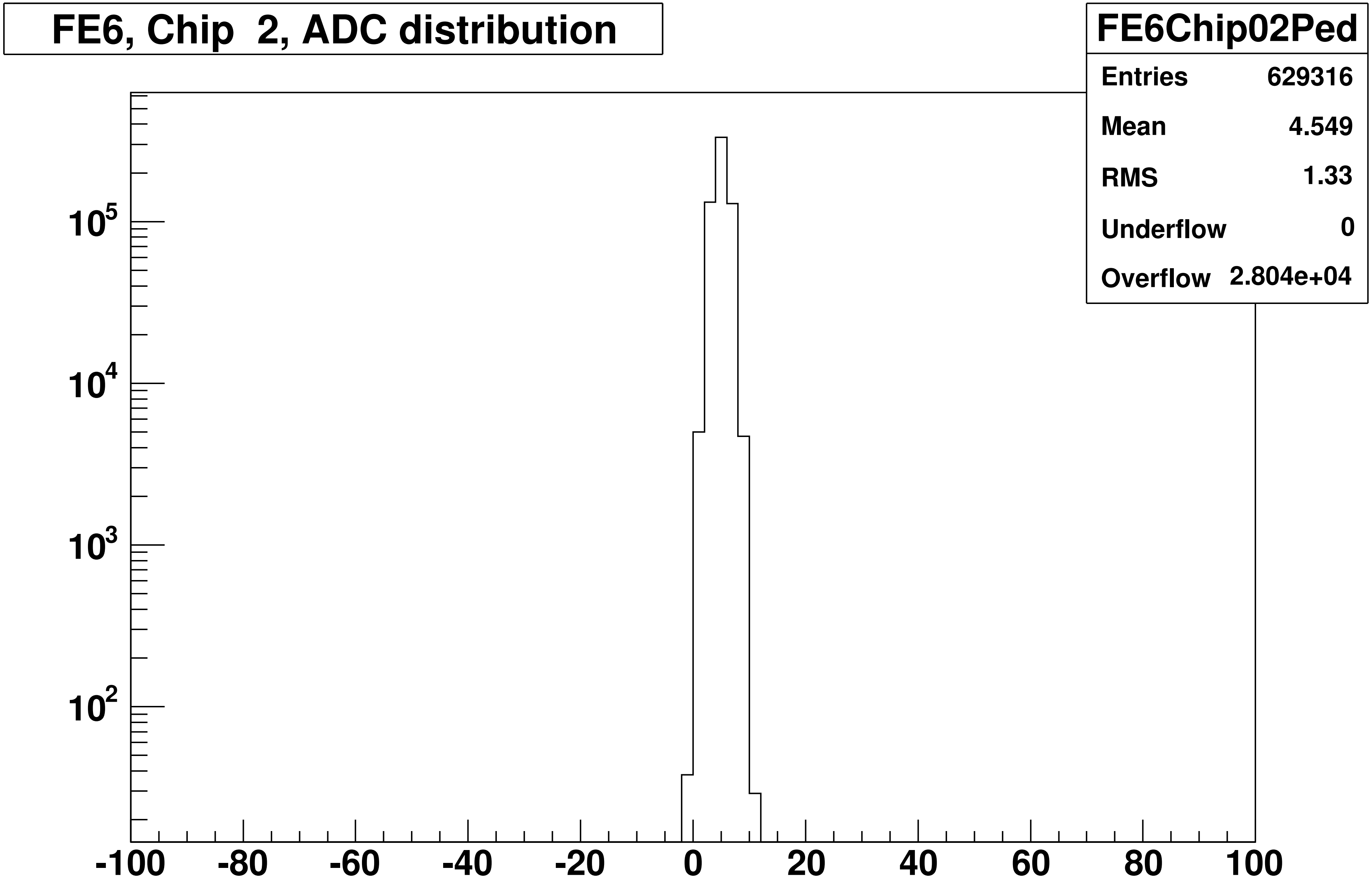


FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 629316
Mean -14.65
RMS 1.333
Underflow 0
Overflow 2.806e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

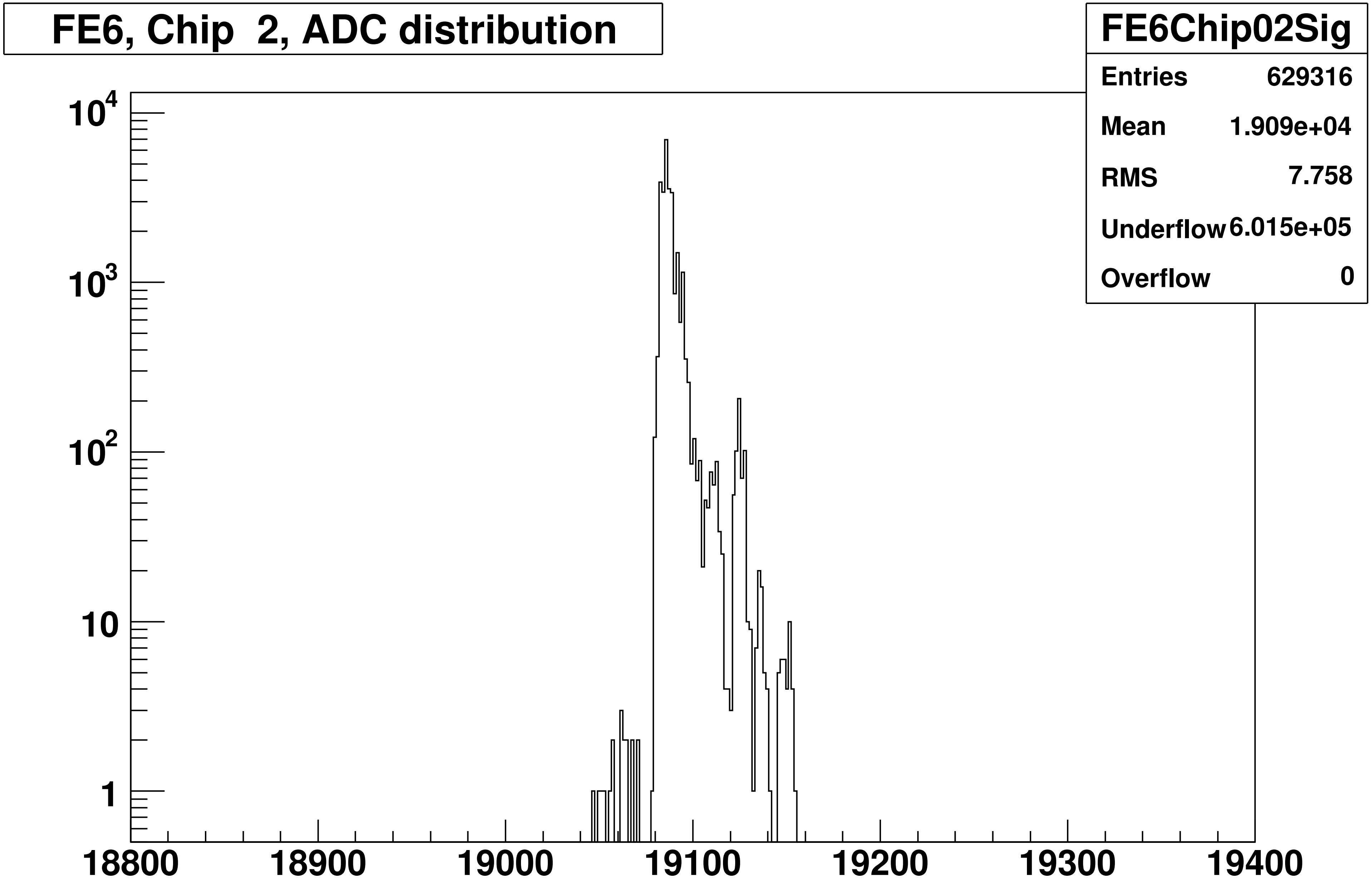

FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 629316
Mean 1.91e+04
RMS 10.65
Underflow 6.013e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

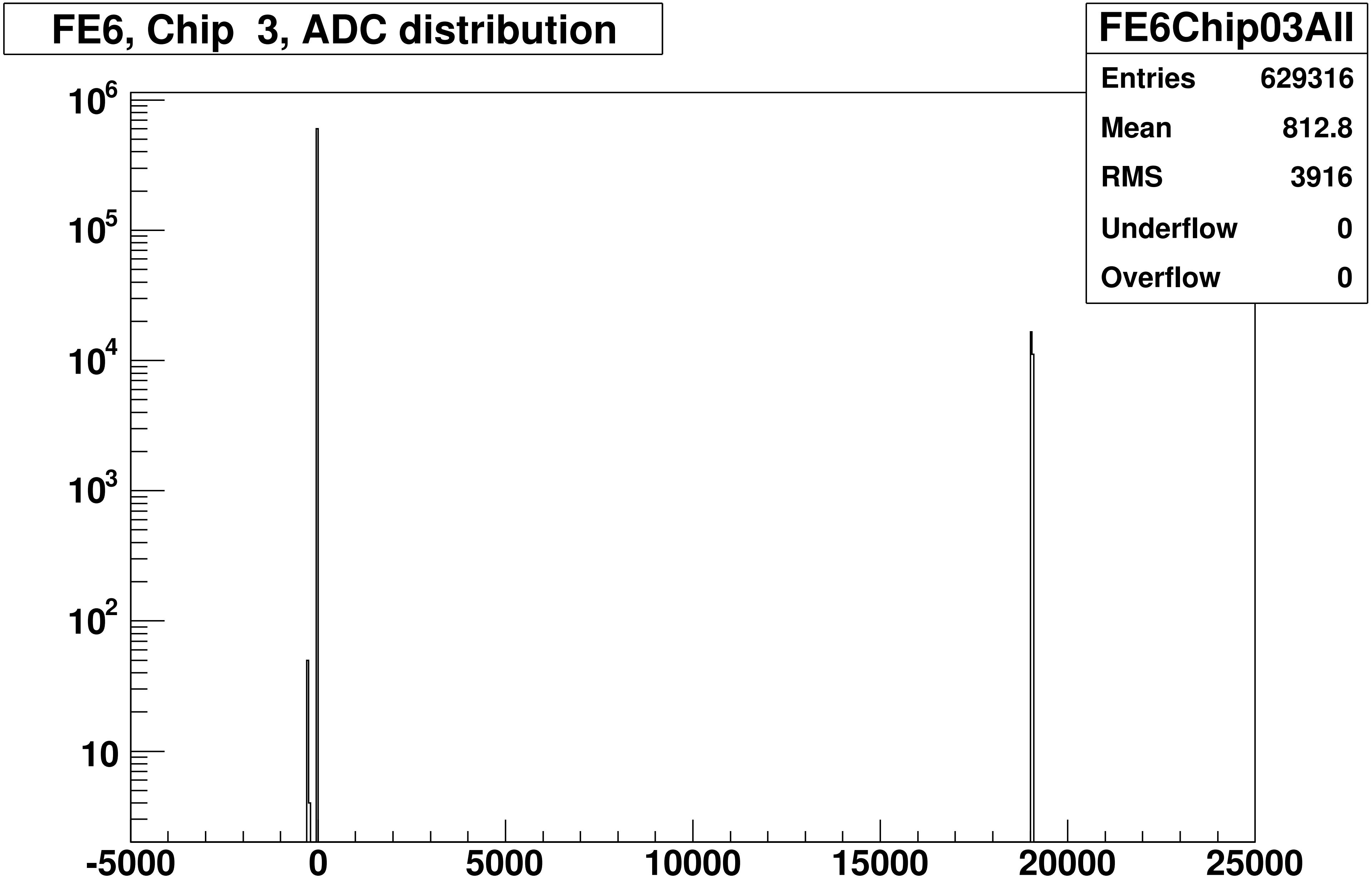


FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 629316
Mean 849.2
RMS 3924
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000

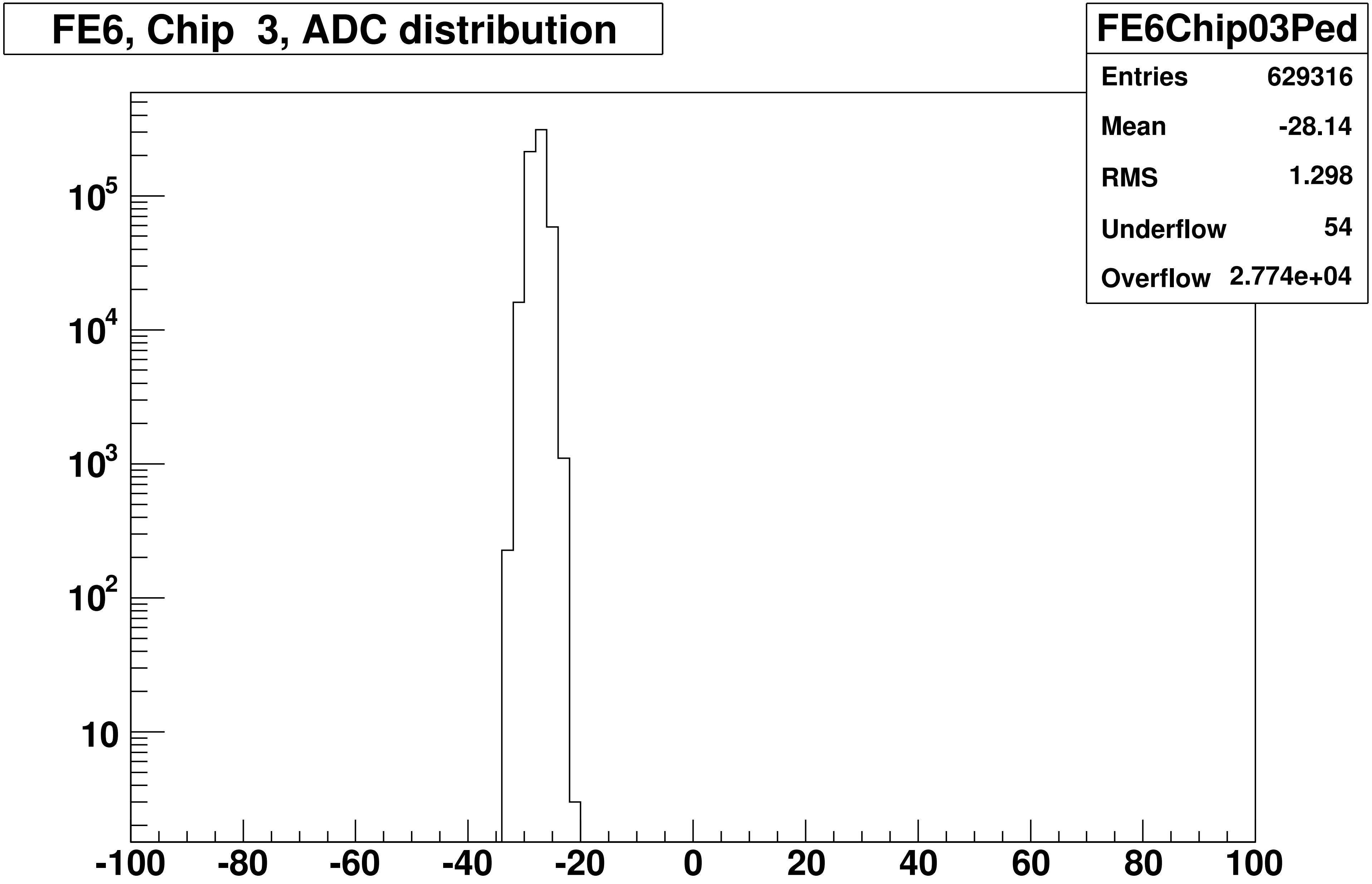


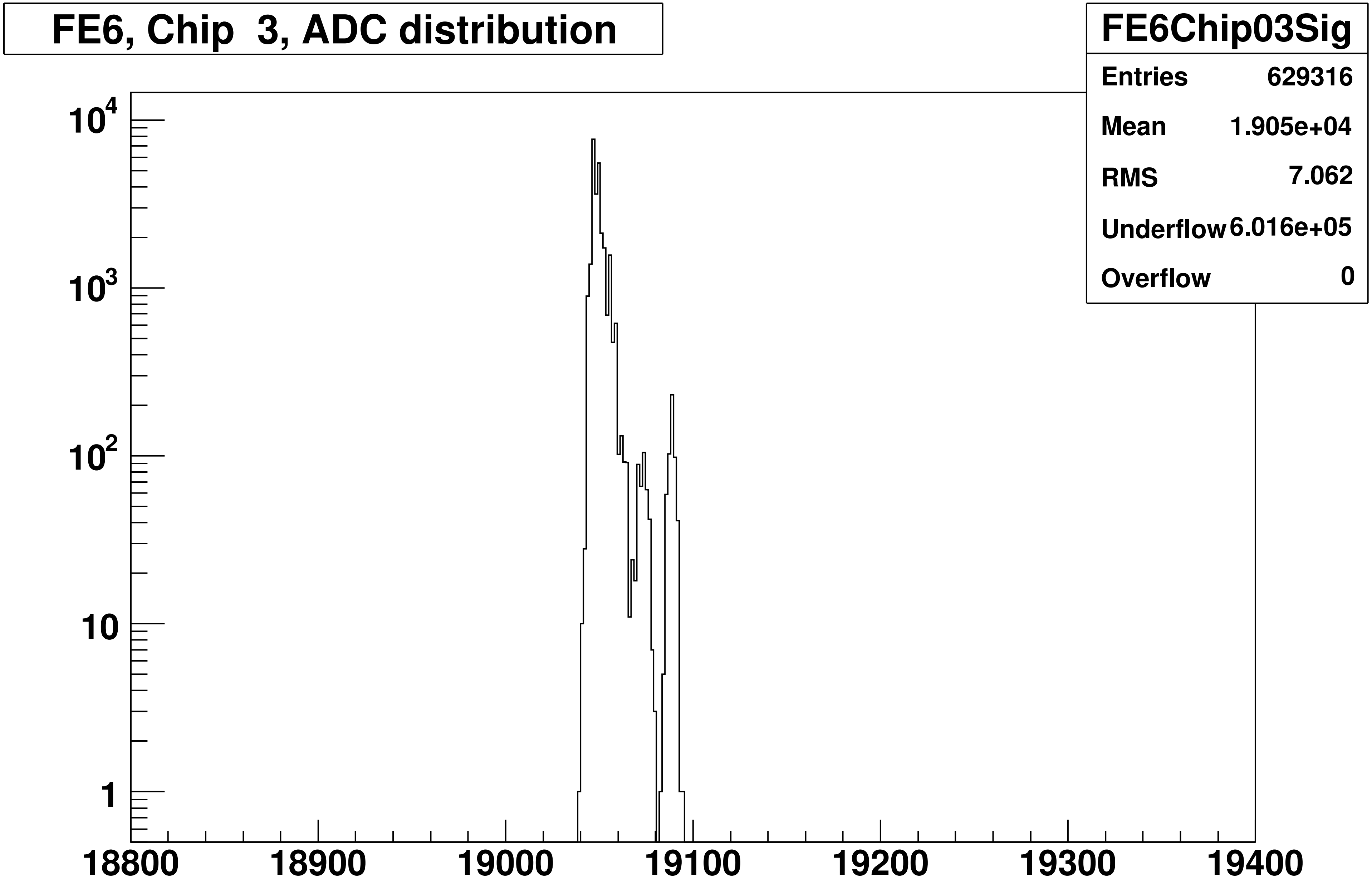
FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 629316
Mean 4.549
RMS 1.33
Underflow 0
Overflow 2.804e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 629316
Mean 1.909e+04
RMS 7.758
Underflow 6.015e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

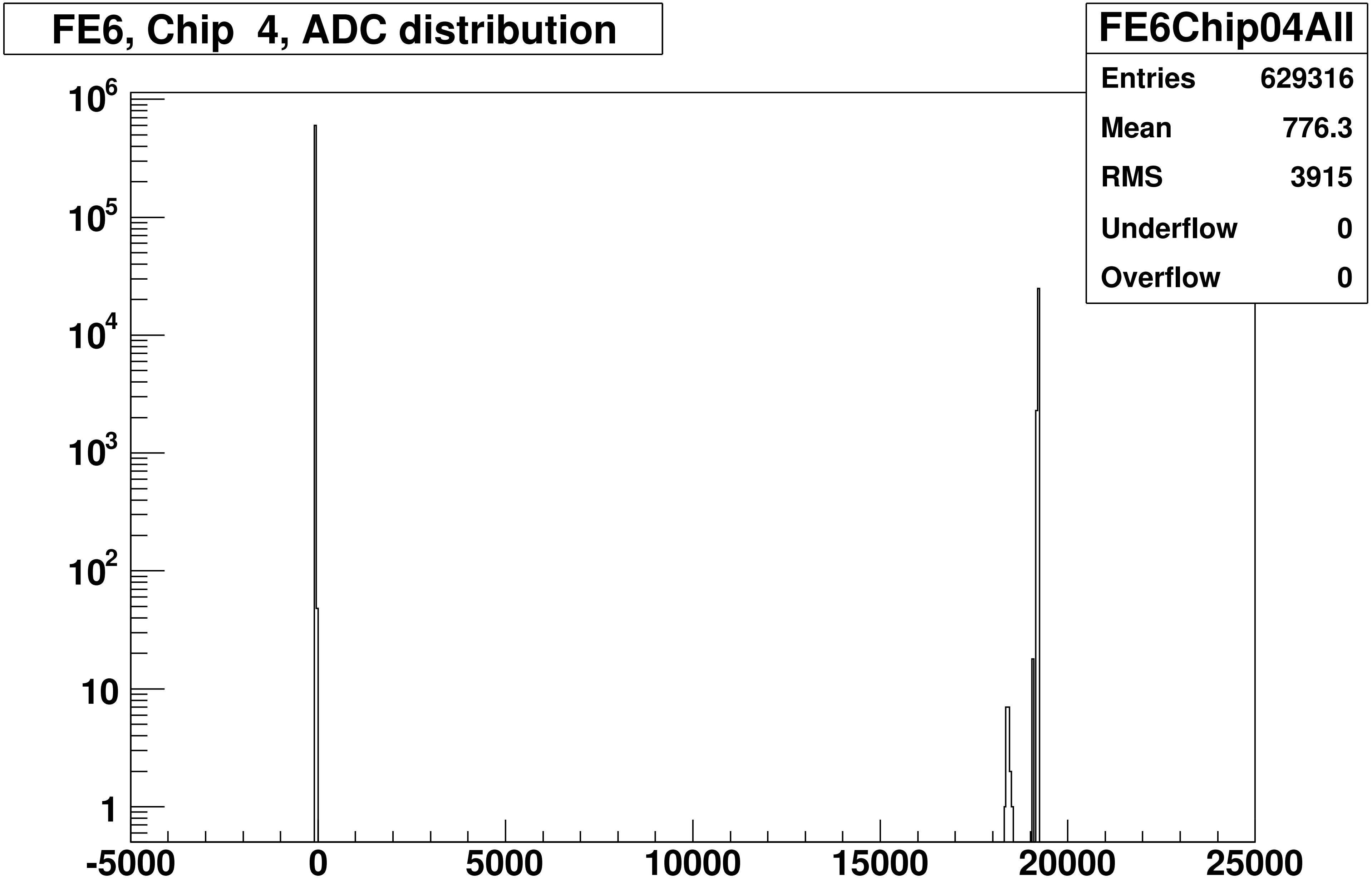


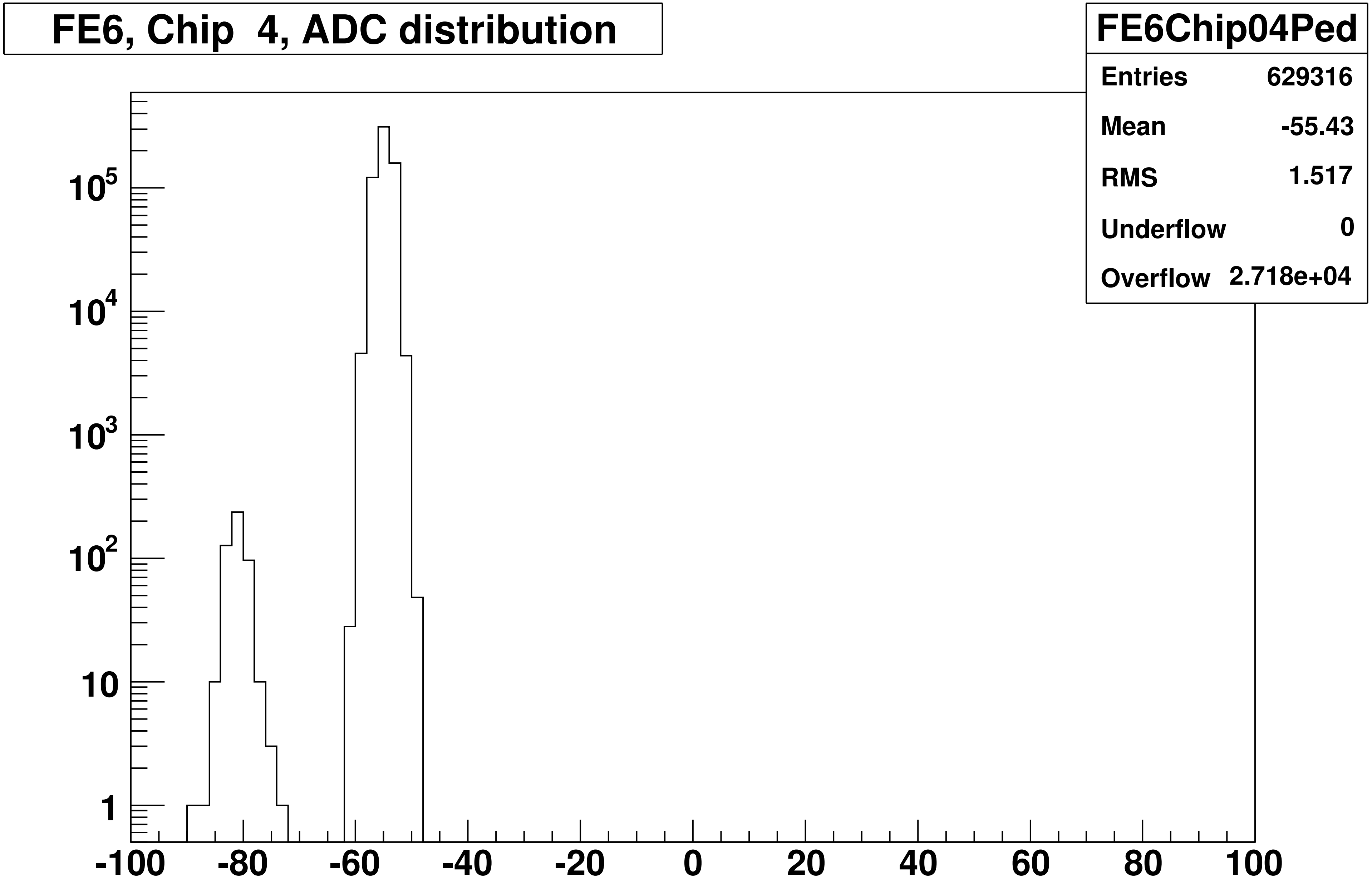
FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 629316
Mean 812.8
RMS 3916
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 629316
Mean -28.14
RMS 1.298
Underflow 54
Overflow 2.774e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

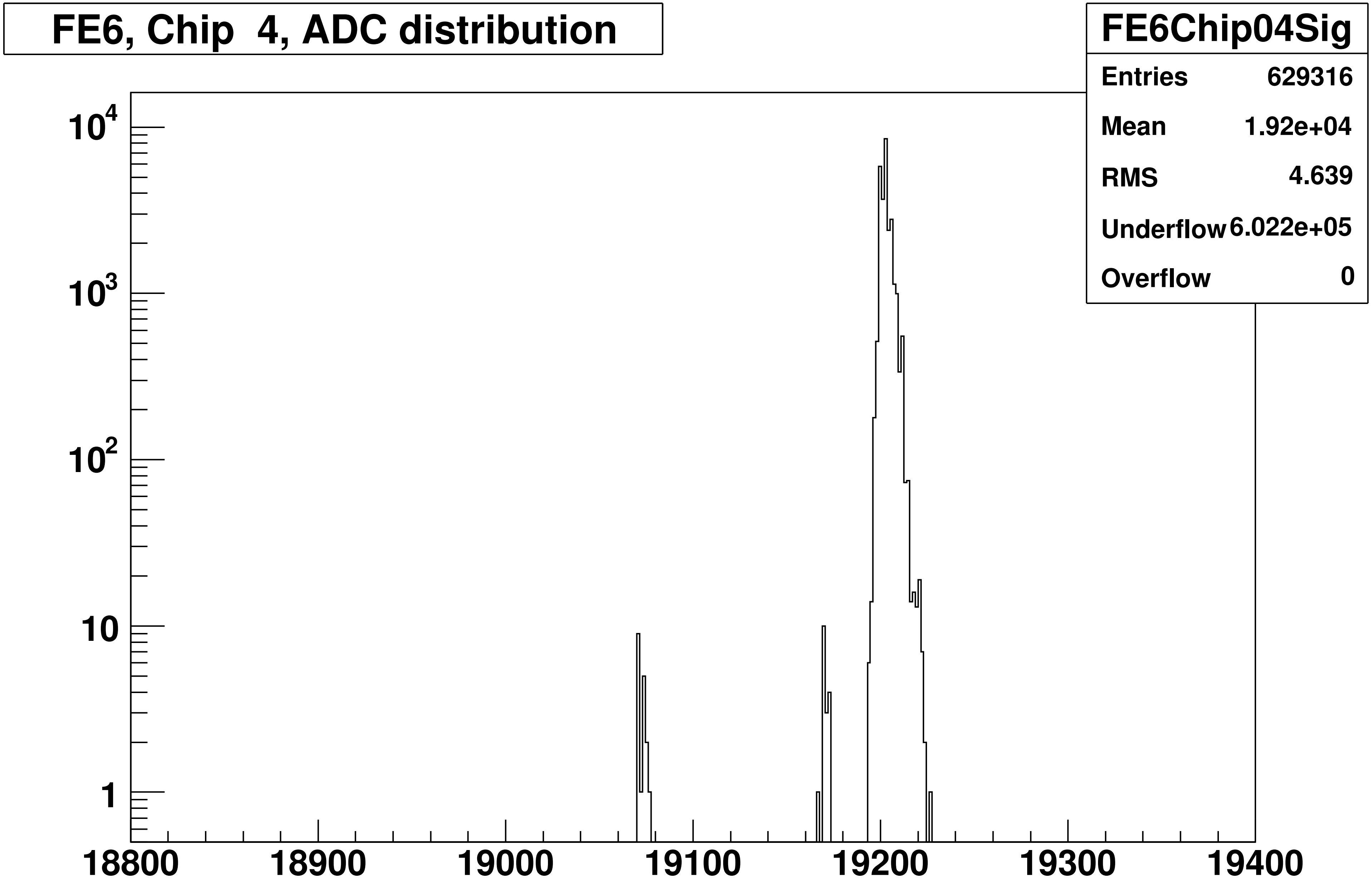


FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 629316
Mean 1.905e+04
RMS 7.062
Underflow 6.016e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

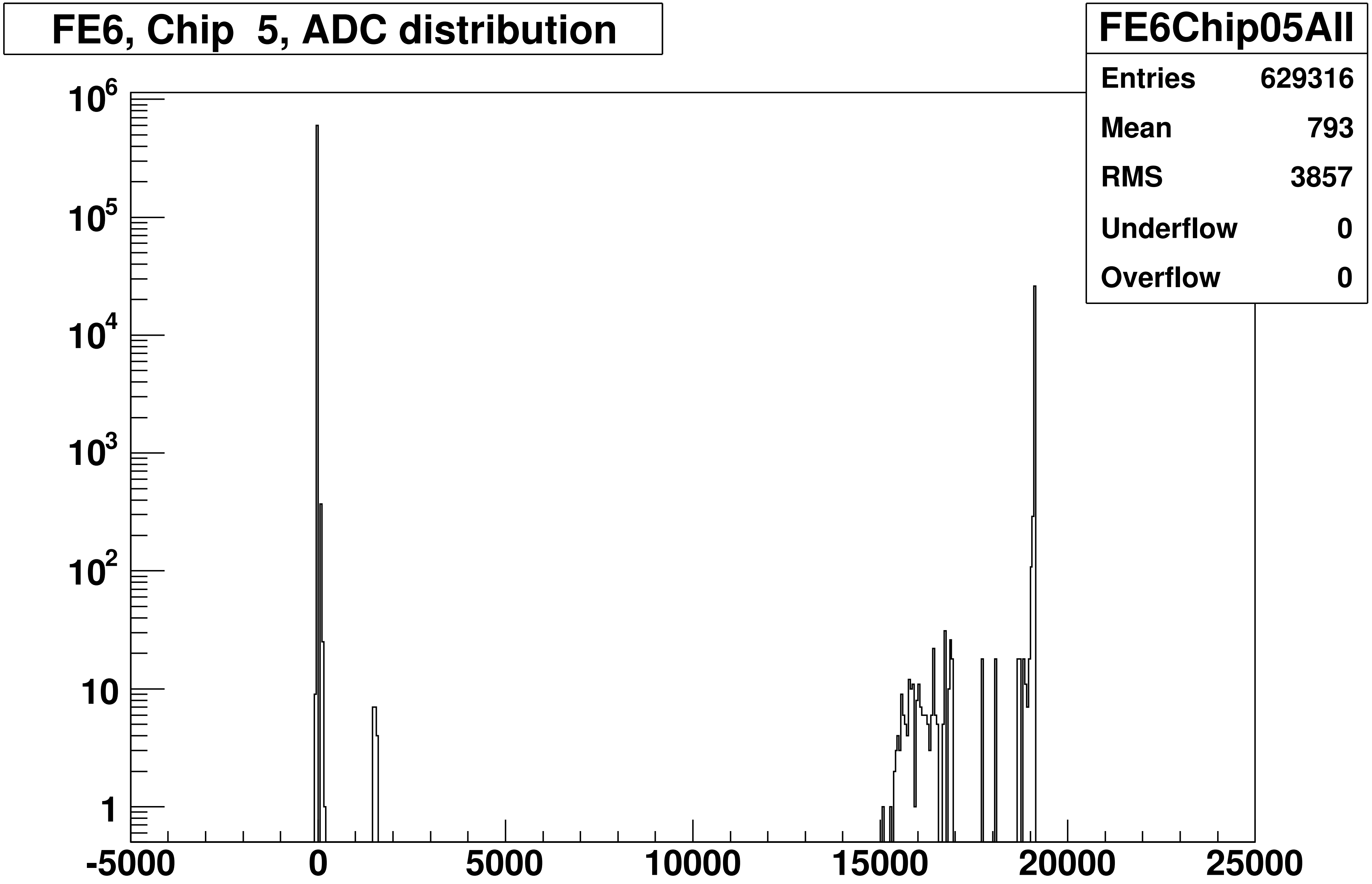

FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 629316
Mean 776.3
RMS 3915
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

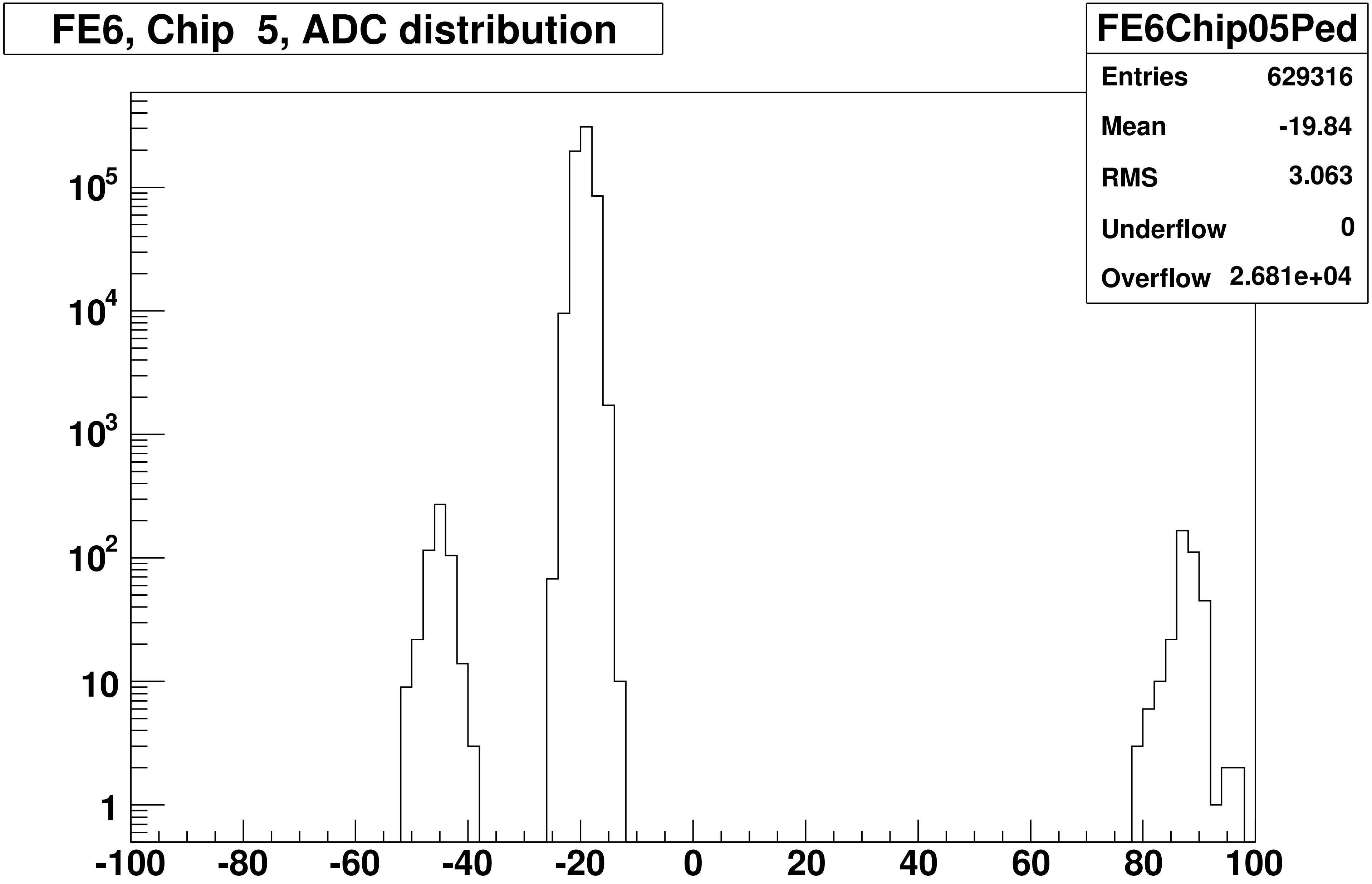


FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 629316
Mean -55.43
RMS 1.517
Underflow 0
Overflow 2.718e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

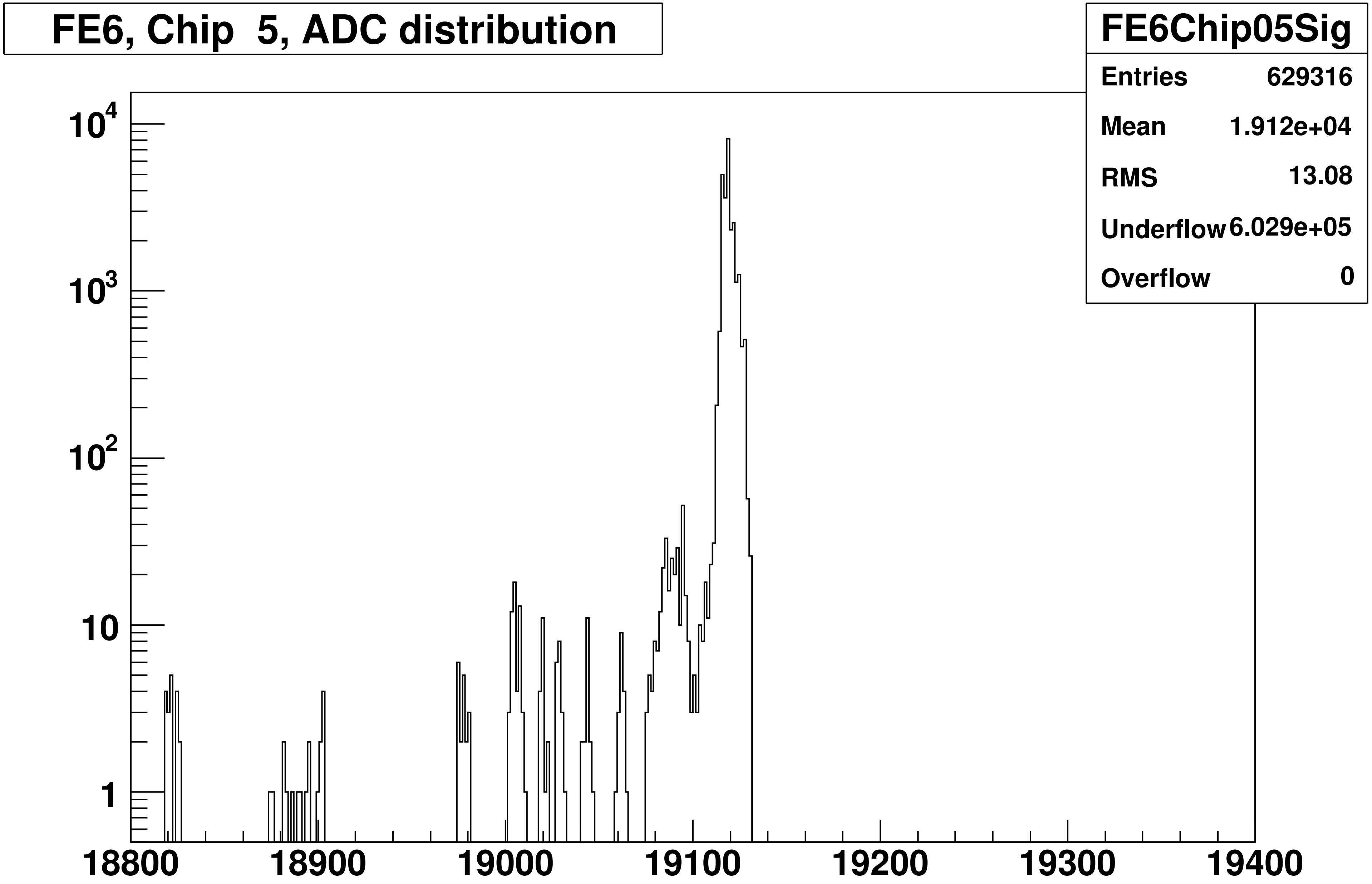


FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 629316
Mean 1.92e+04
RMS 4.639
Underflow 6.022e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

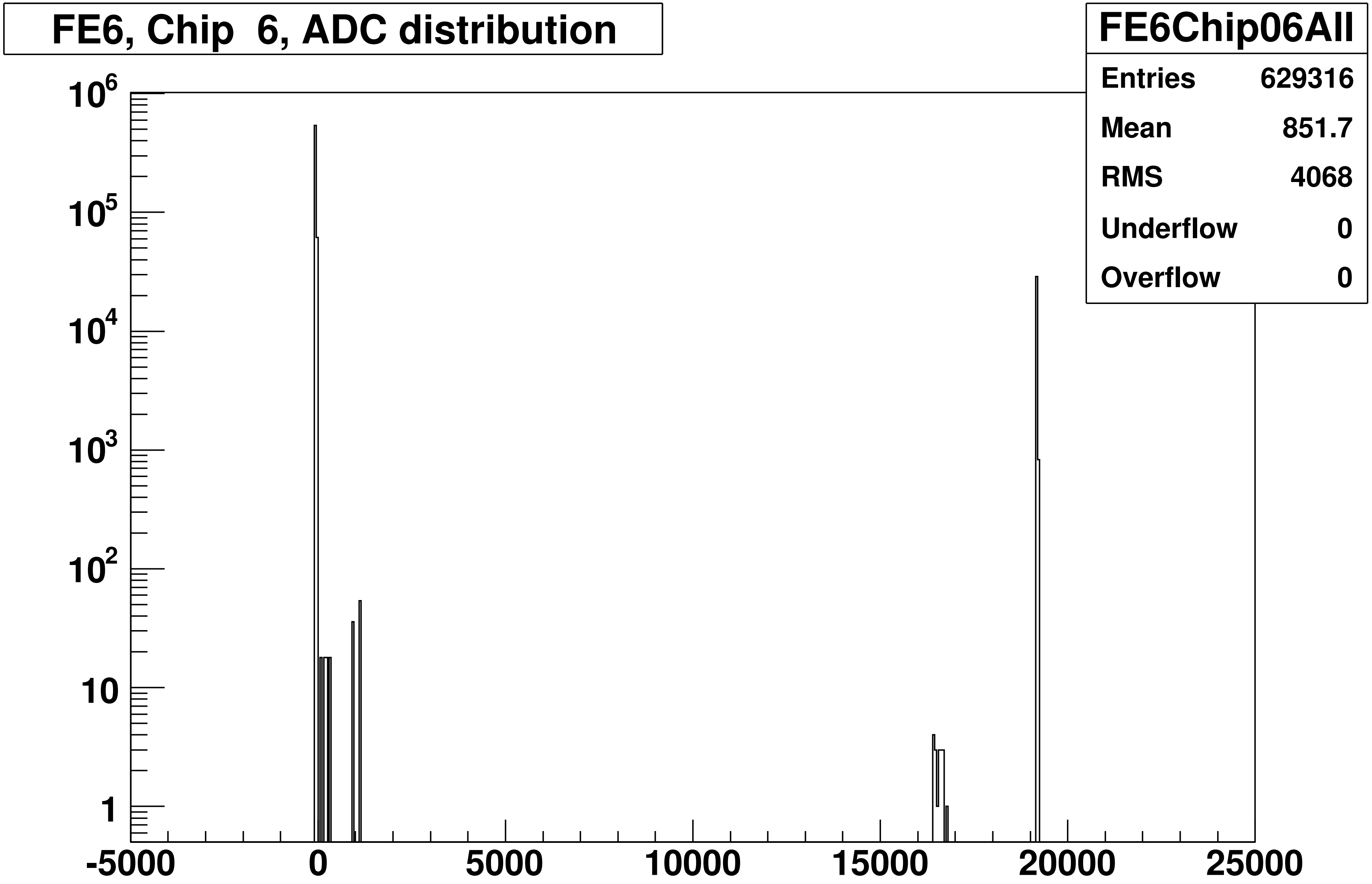

FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 629316
Mean 793
RMS 3857
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

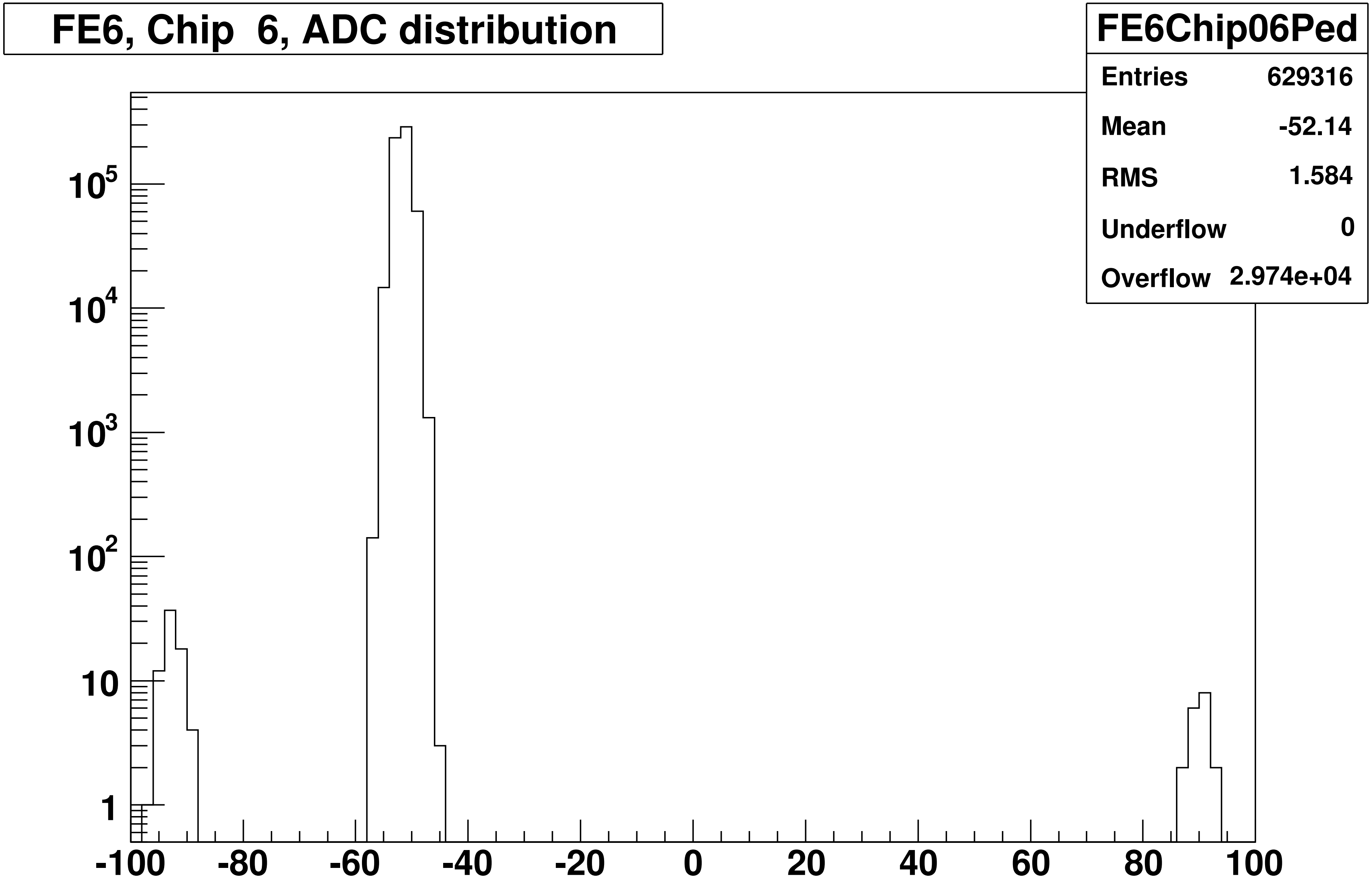


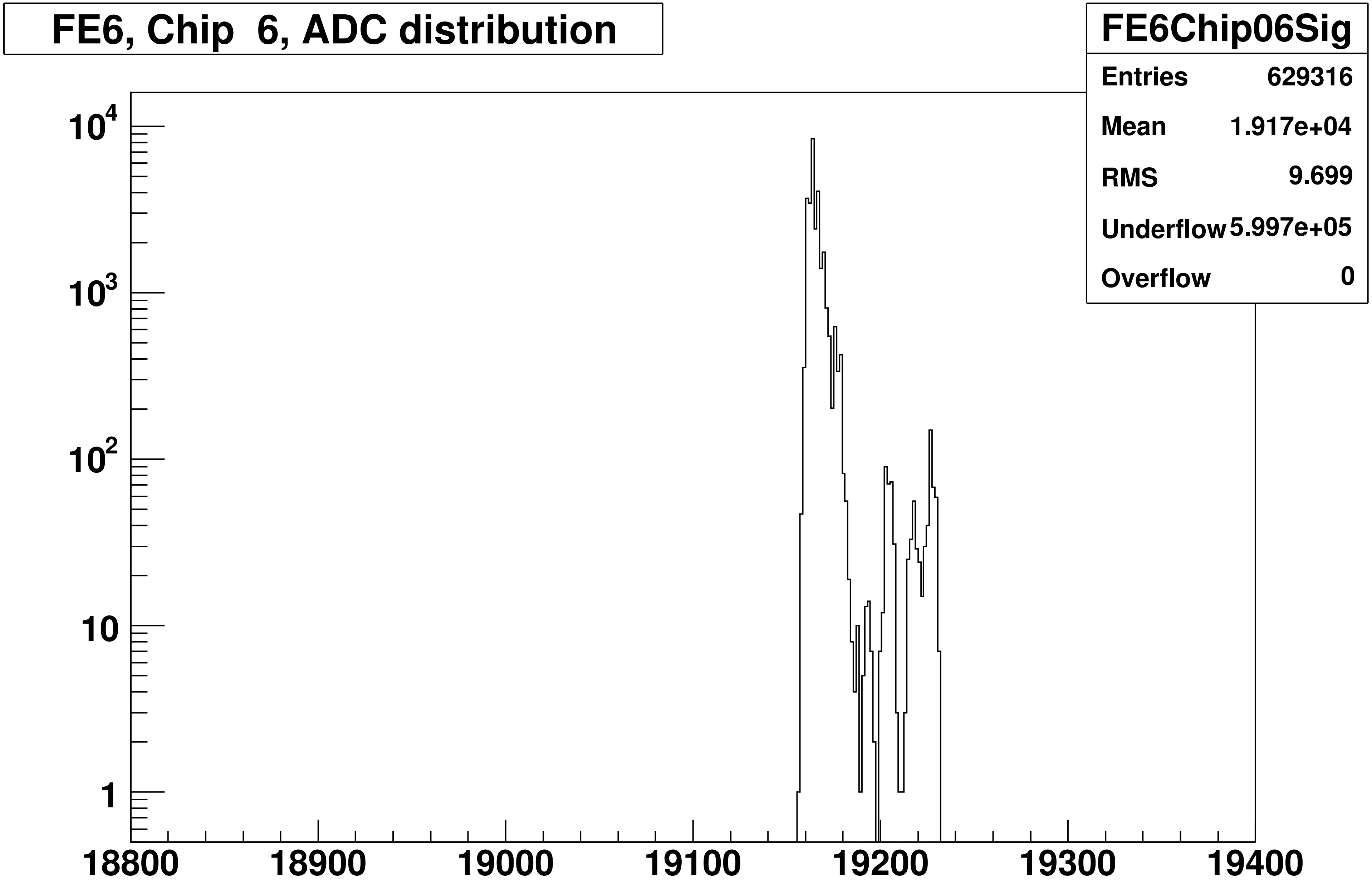
FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 629316
Mean -19.84
RMS 3.063
Underflow 0
Overflow 2.681e+04
1
10
10^2
10^3
10^4
10^5
-100 -80 -60 -40 -20 0 20 40 60 80 100

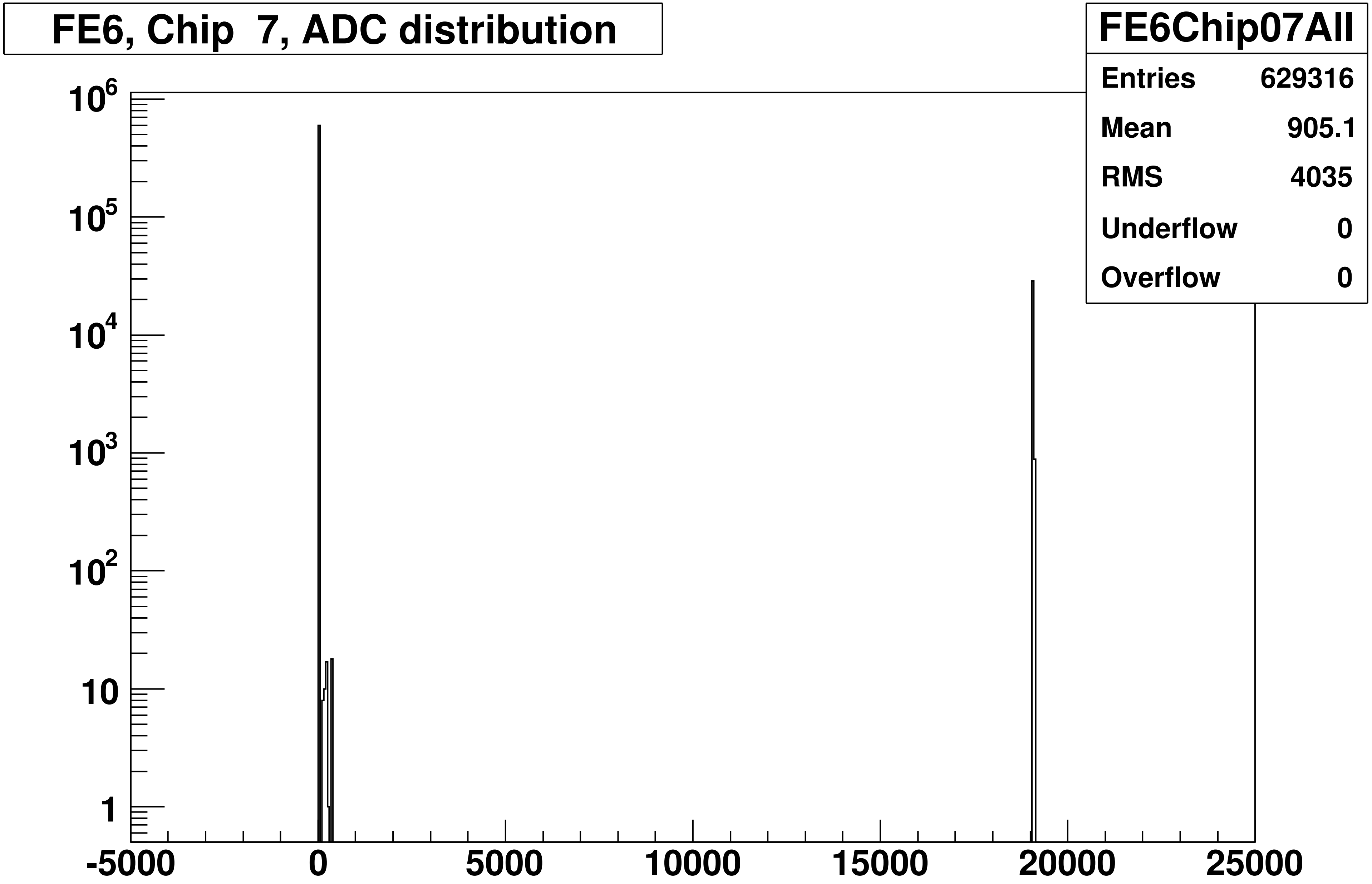


FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 629316
Mean 1.912e+04
RMS 13.08
Underflow 6.029e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

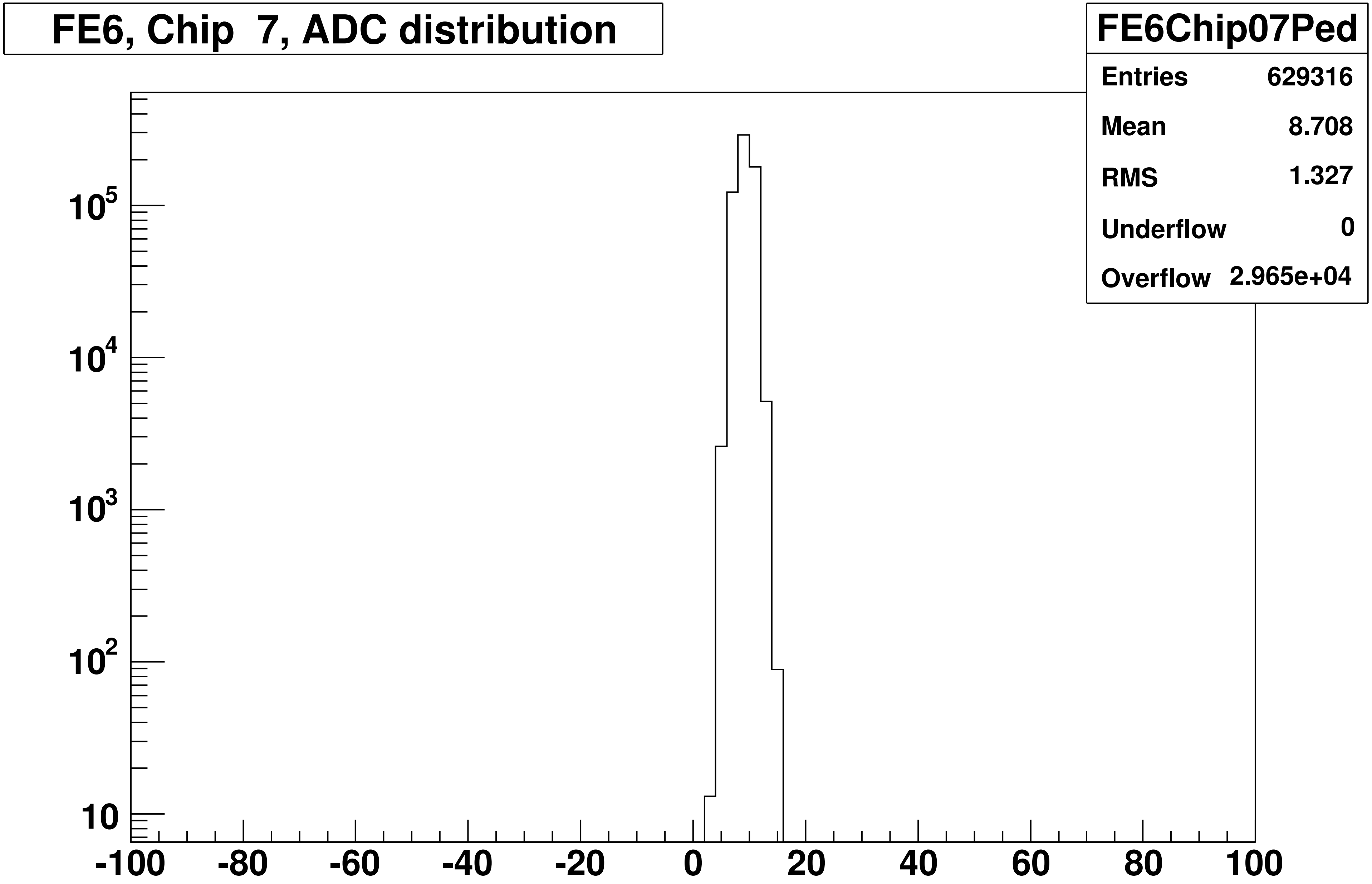

FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 629316
Mean 851.7
RMS 4068
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

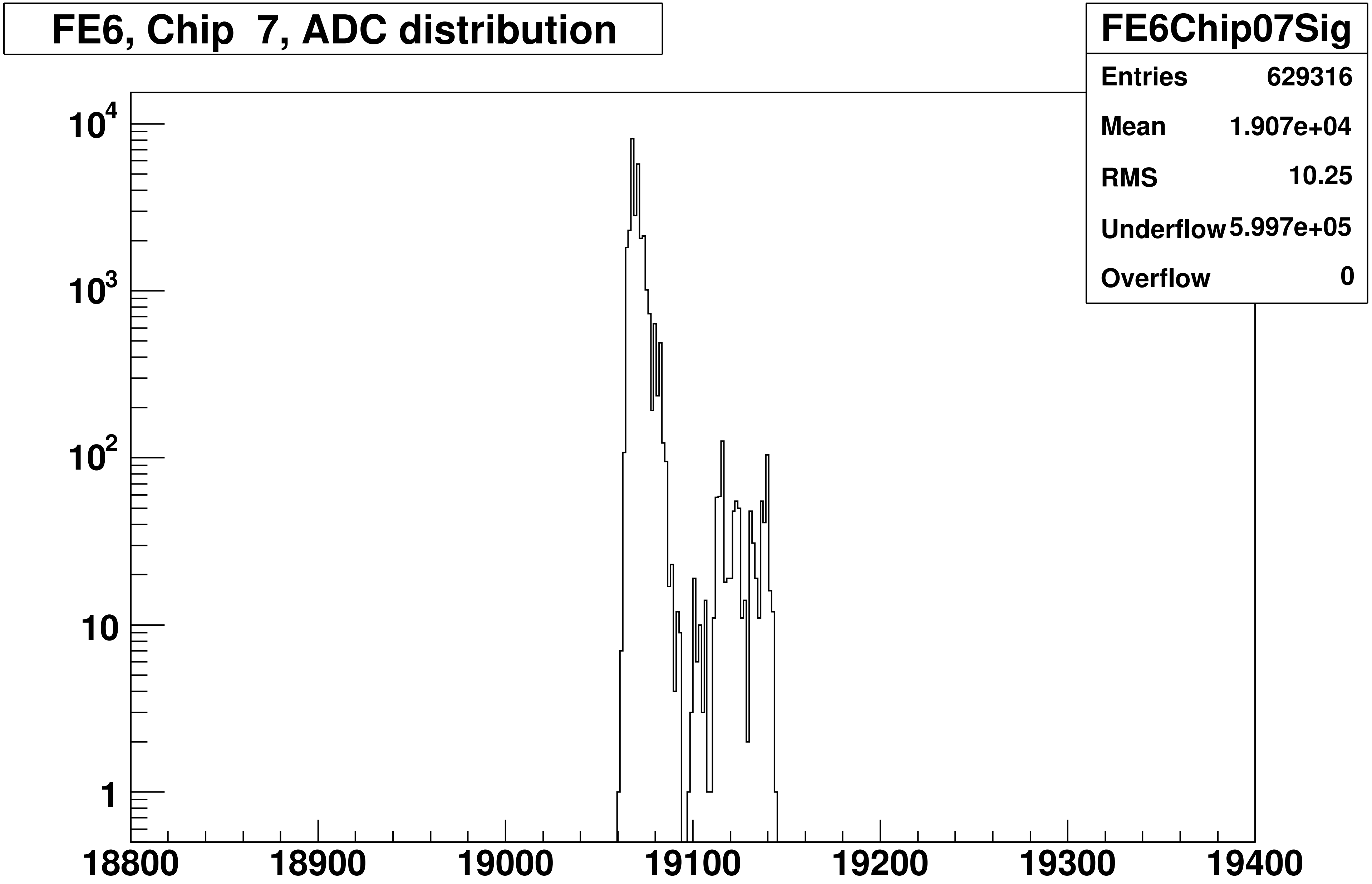

FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 629316
Mean -52.14
RMS 1.584
Underflow 0
Overflow 2.974e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 629316
Mean 1.917e+04
RMS 9.699
Underflow 5.997e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

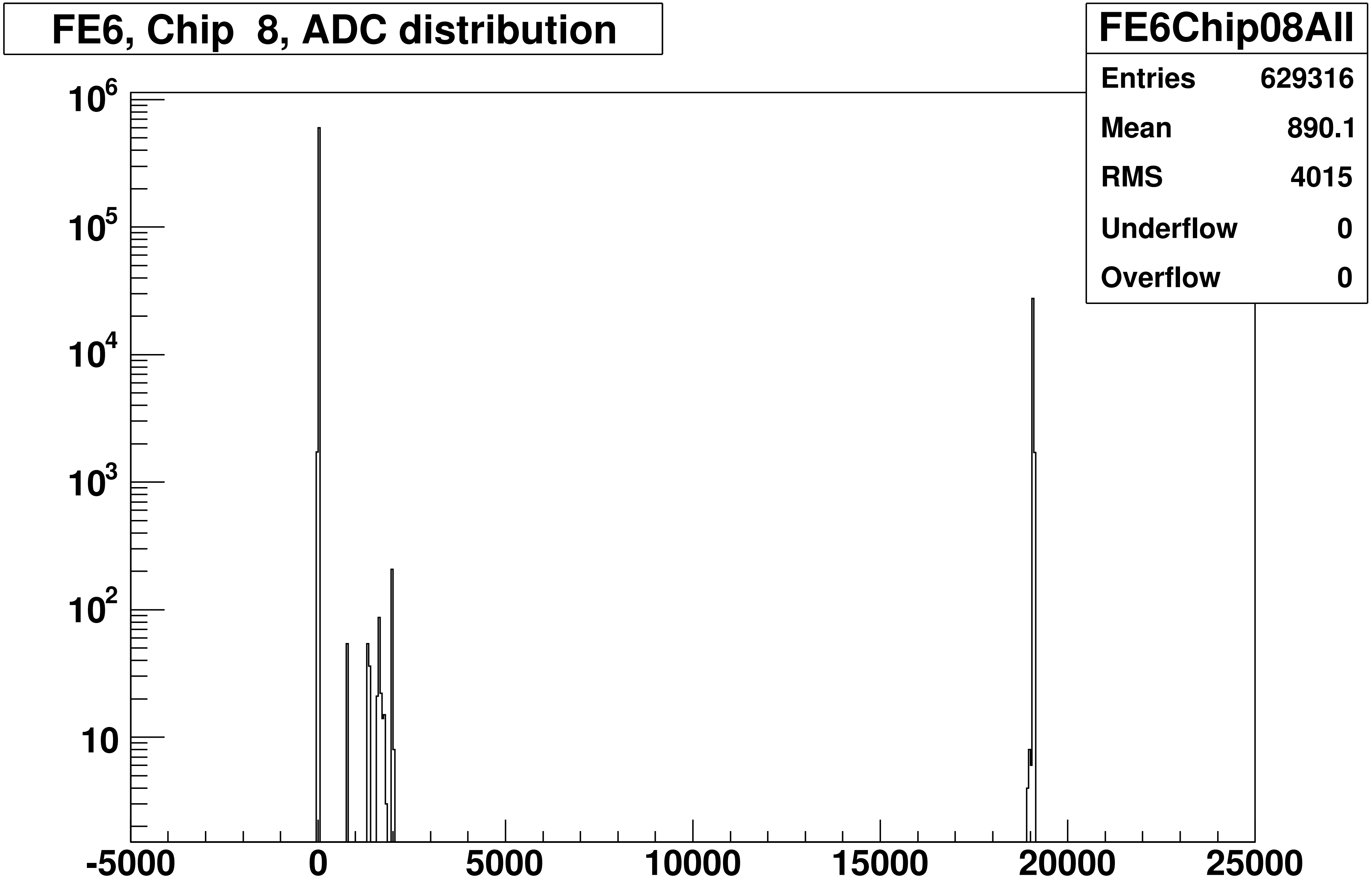


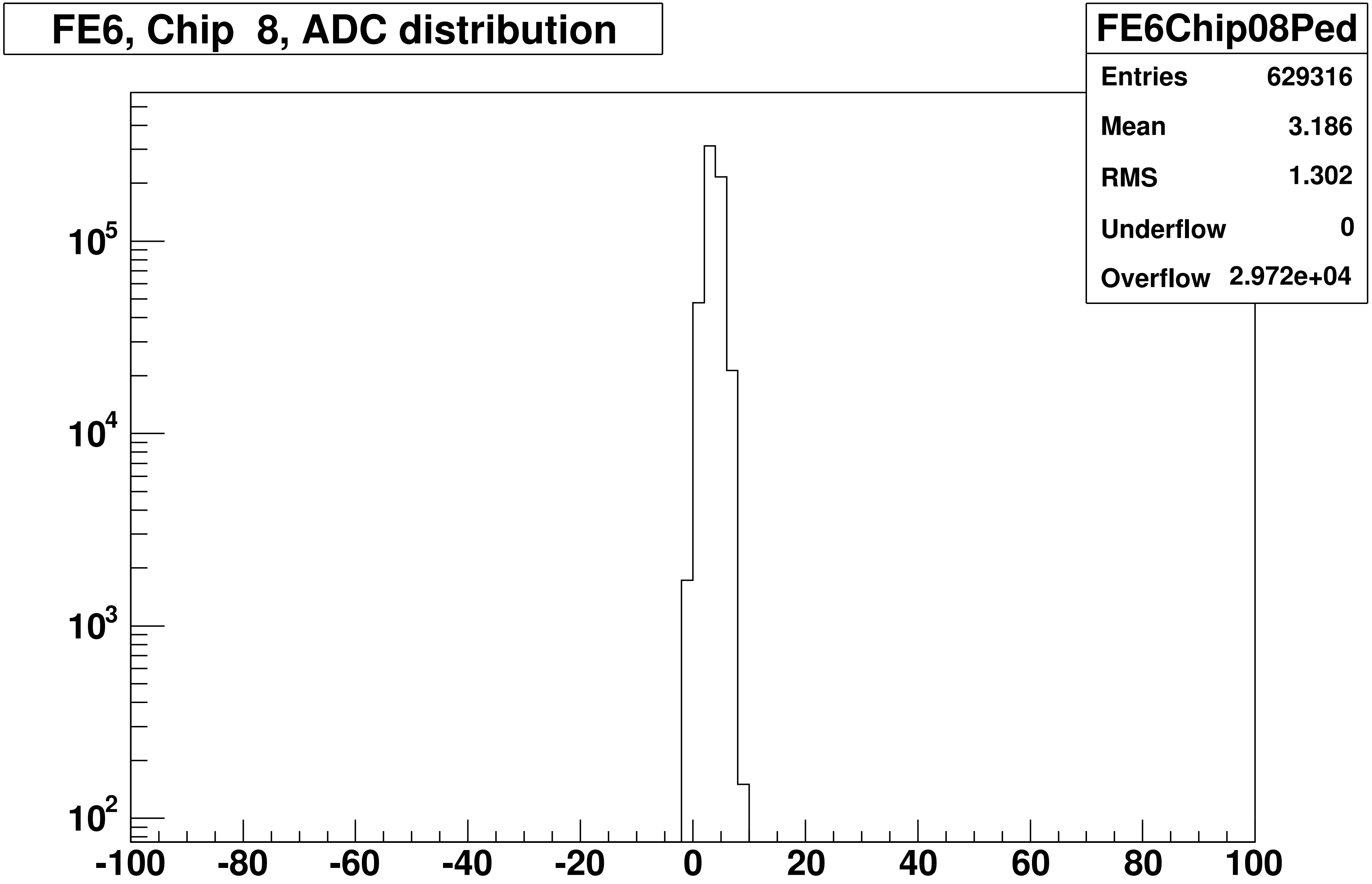
FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 629316
Mean 905.1
RMS 4035
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 629316
Mean 8.708
RMS 1.327
Underflow 0
Overflow 2.965e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

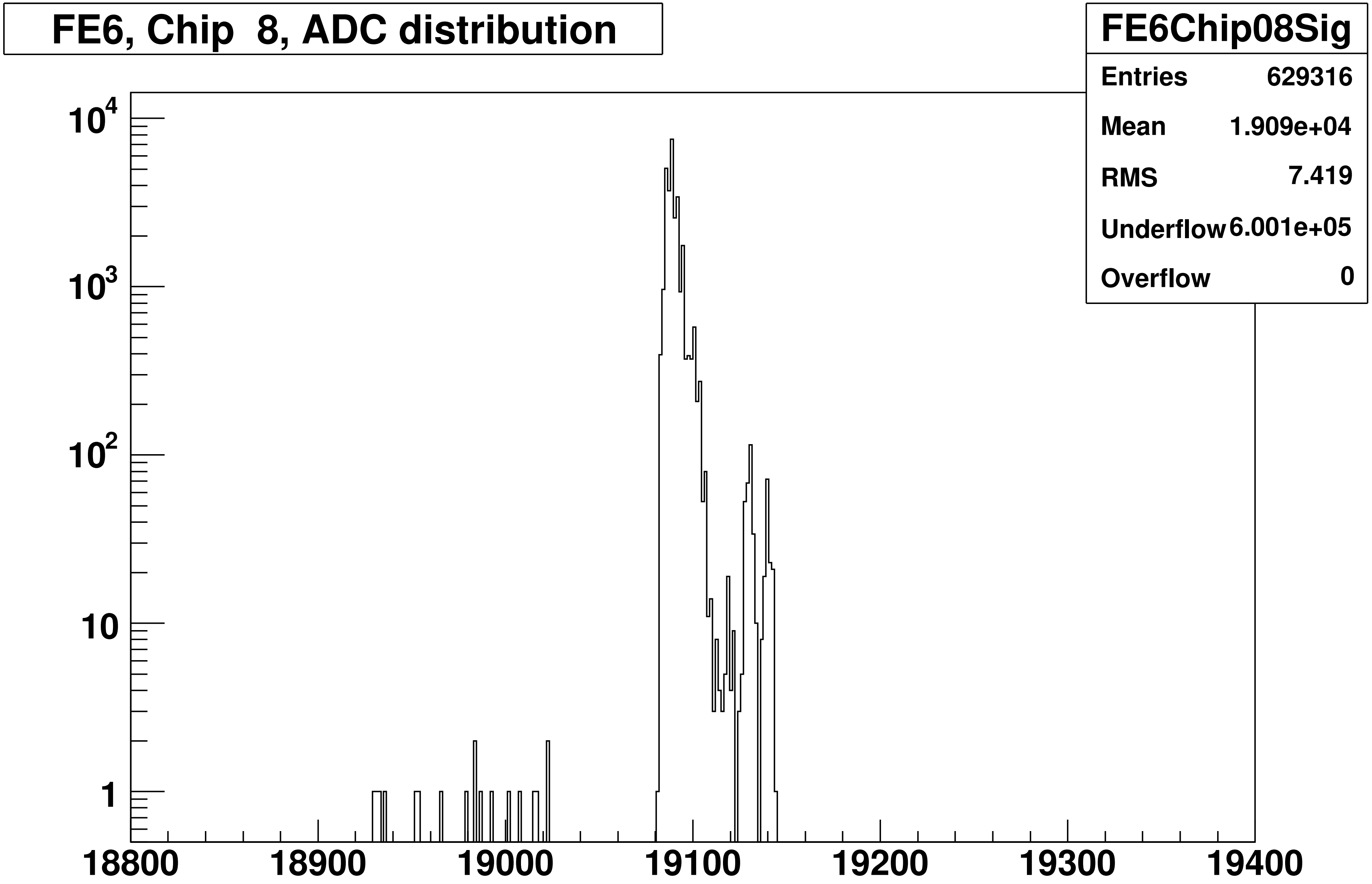


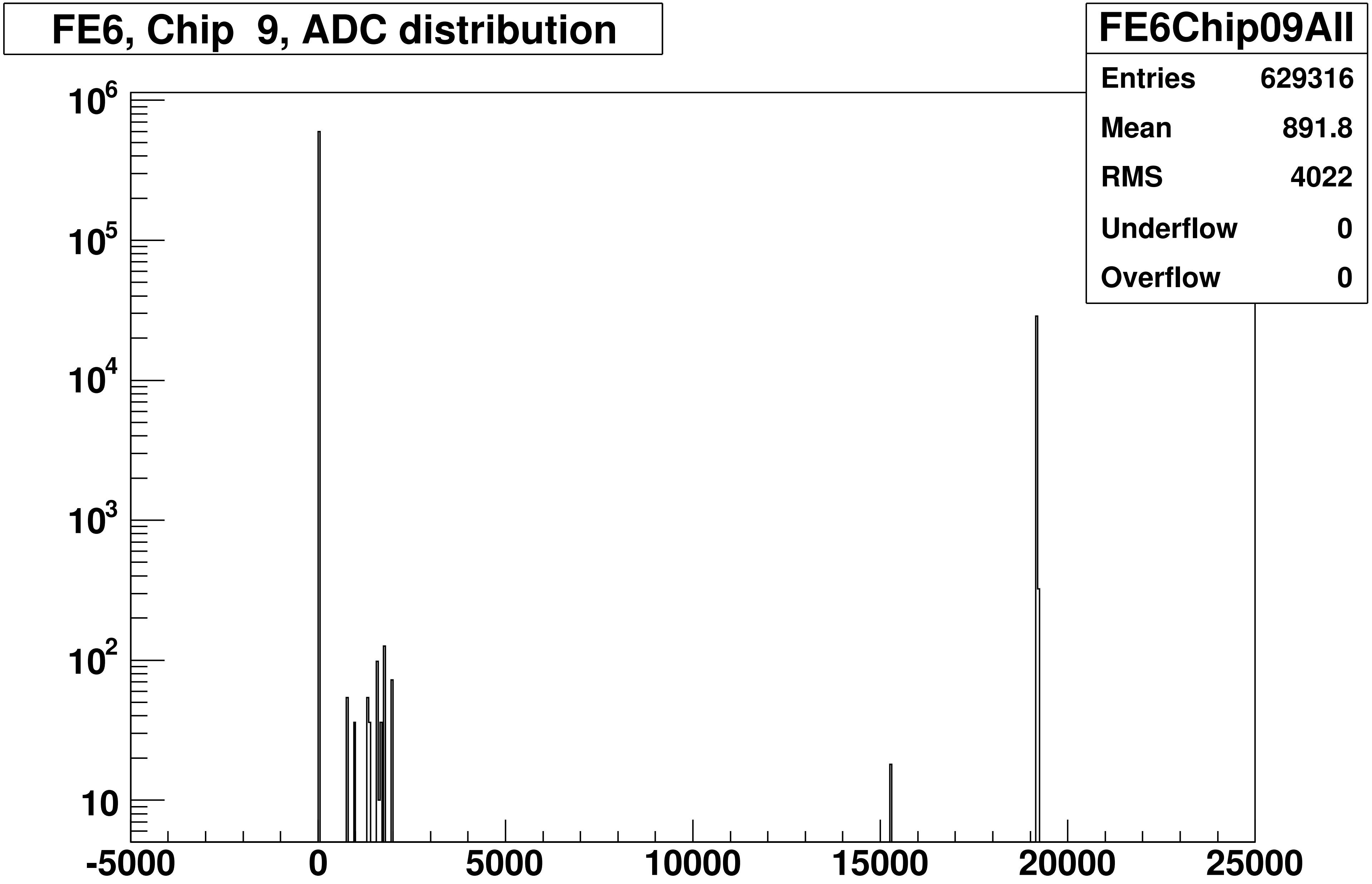
FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 629316
Mean 1.907e+04
RMS 10.25
Underflow 5.997e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

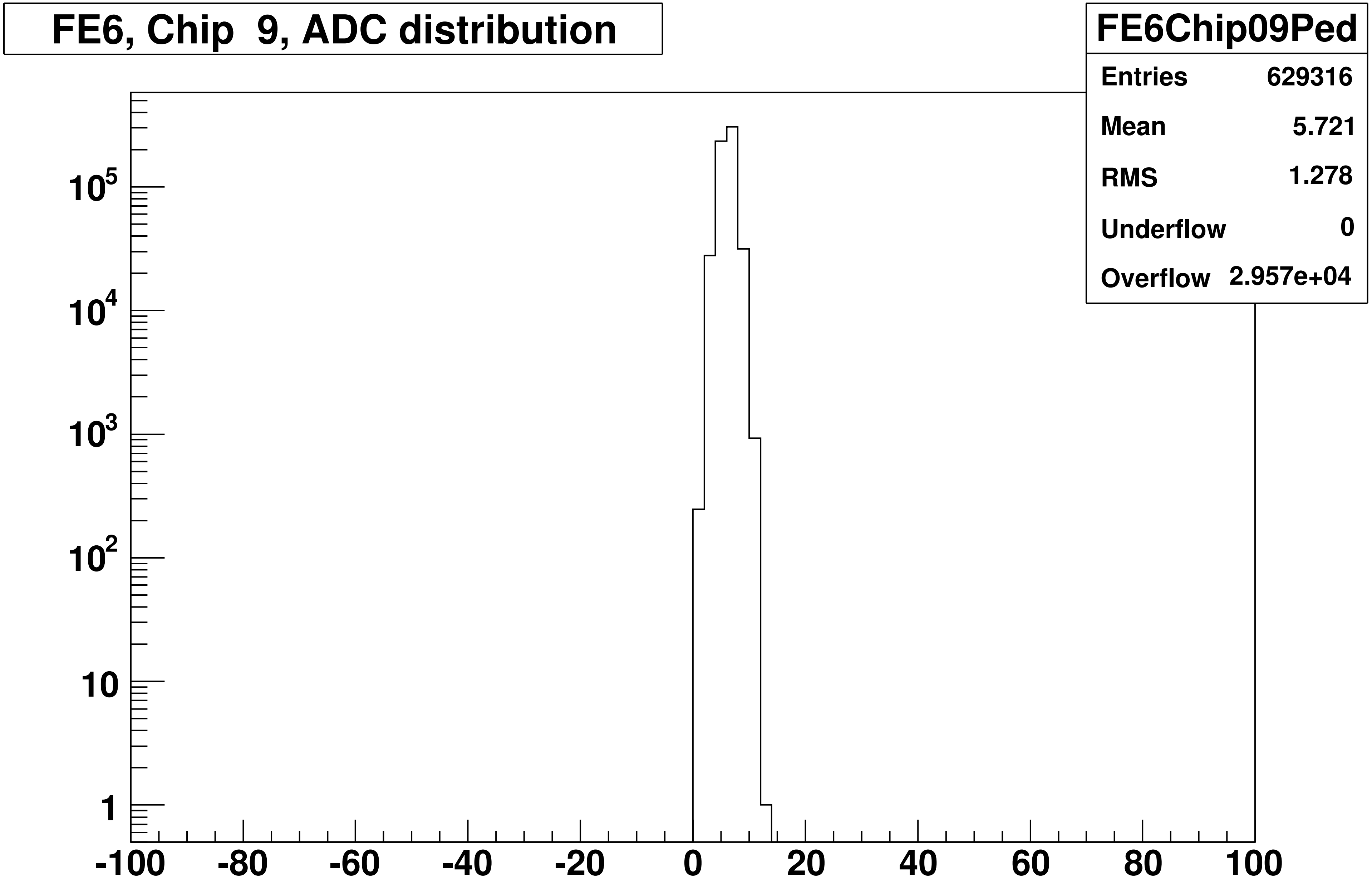

FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 629316
Mean 890.1
RMS 4015
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

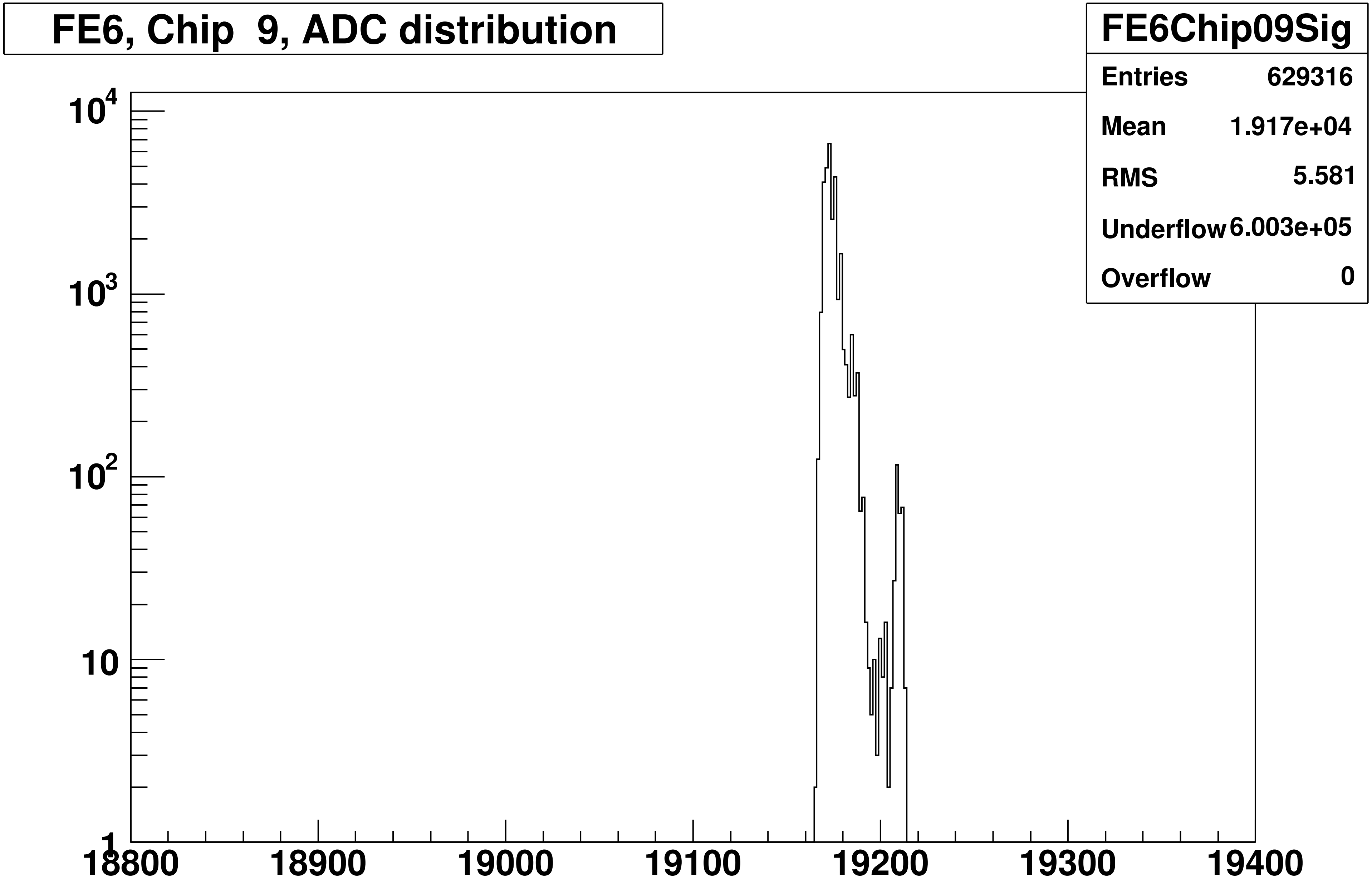


FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 629316
Mean 3.186
RMS 1.302
Underflow 0
Overflow 2.972e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

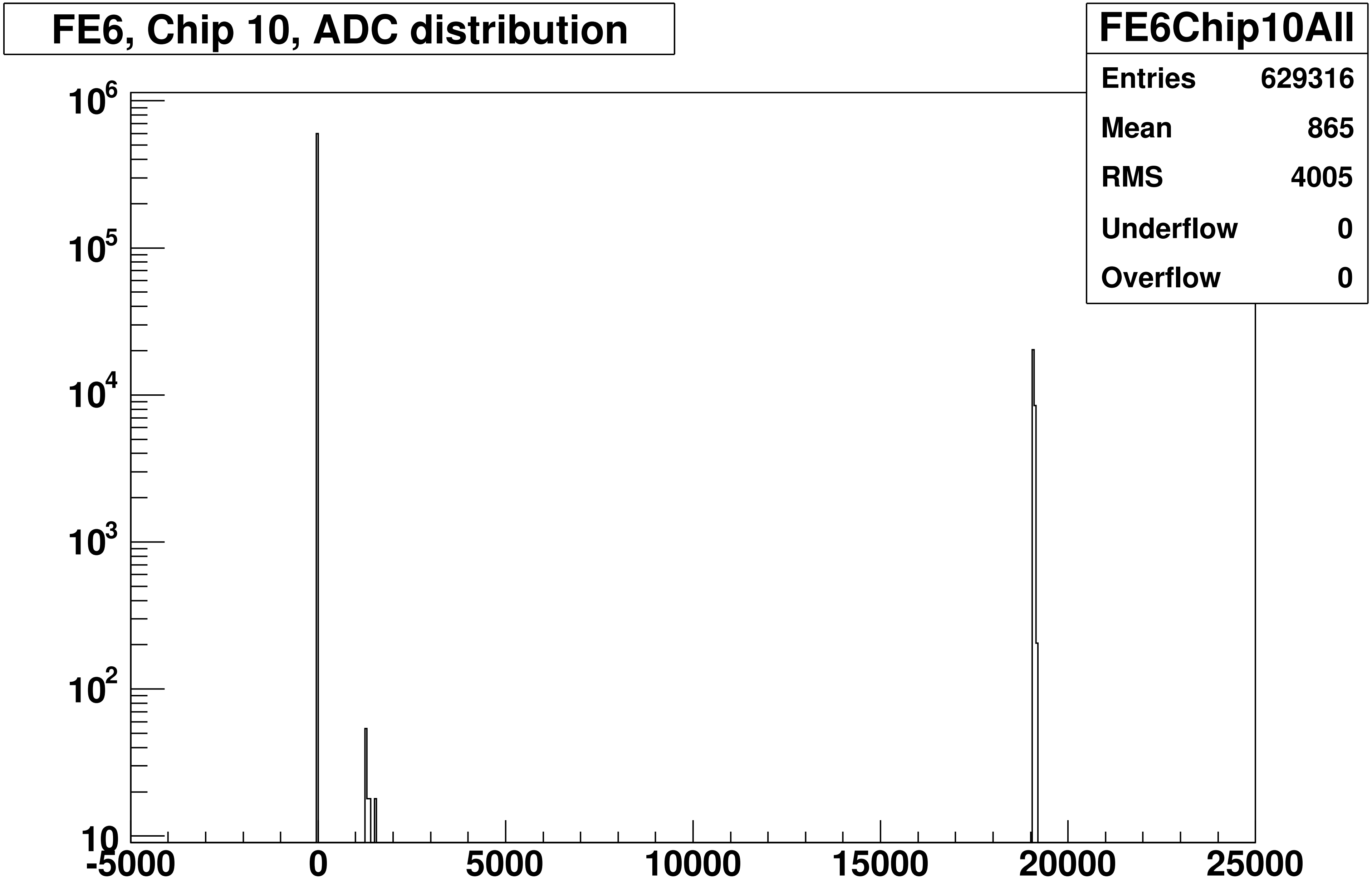

FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 629316
Mean 1.909e+04
RMS 7.419
Underflow 6.001e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

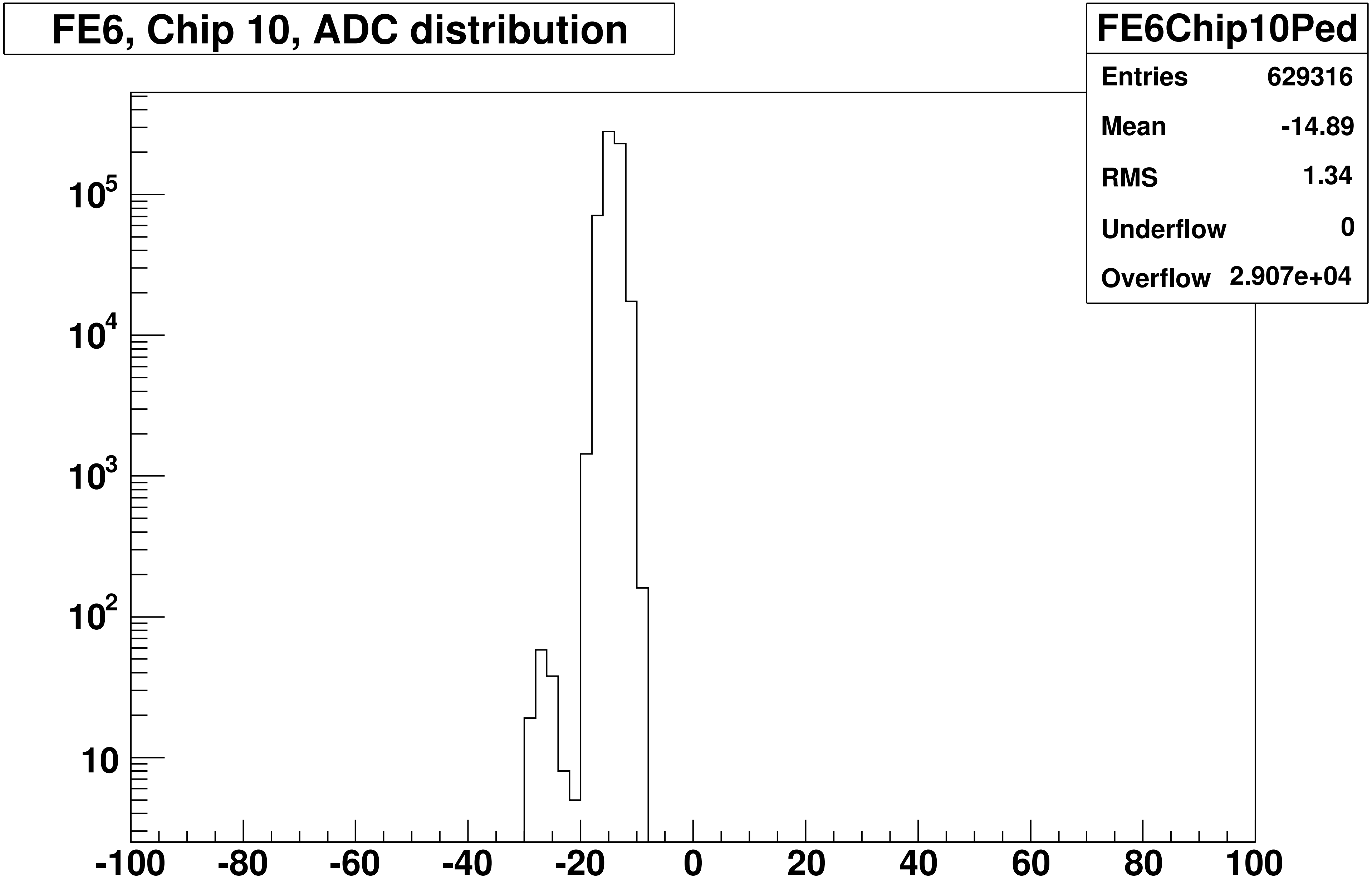

FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 629316
Mean 891.8
RMS 4022
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 629316
Mean 5.721
RMS 1.278
Underflow 0
Overflow 2.957e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

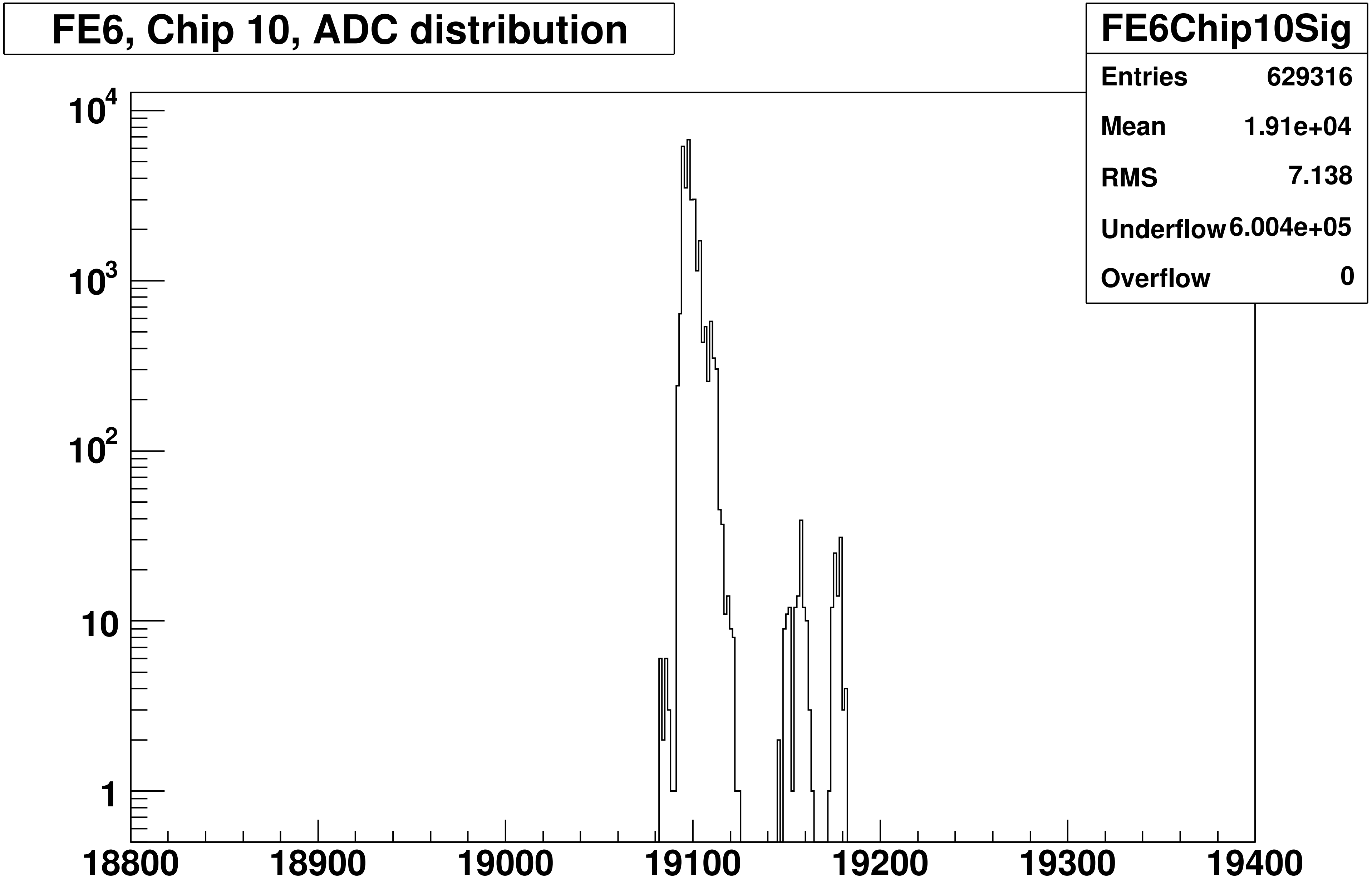


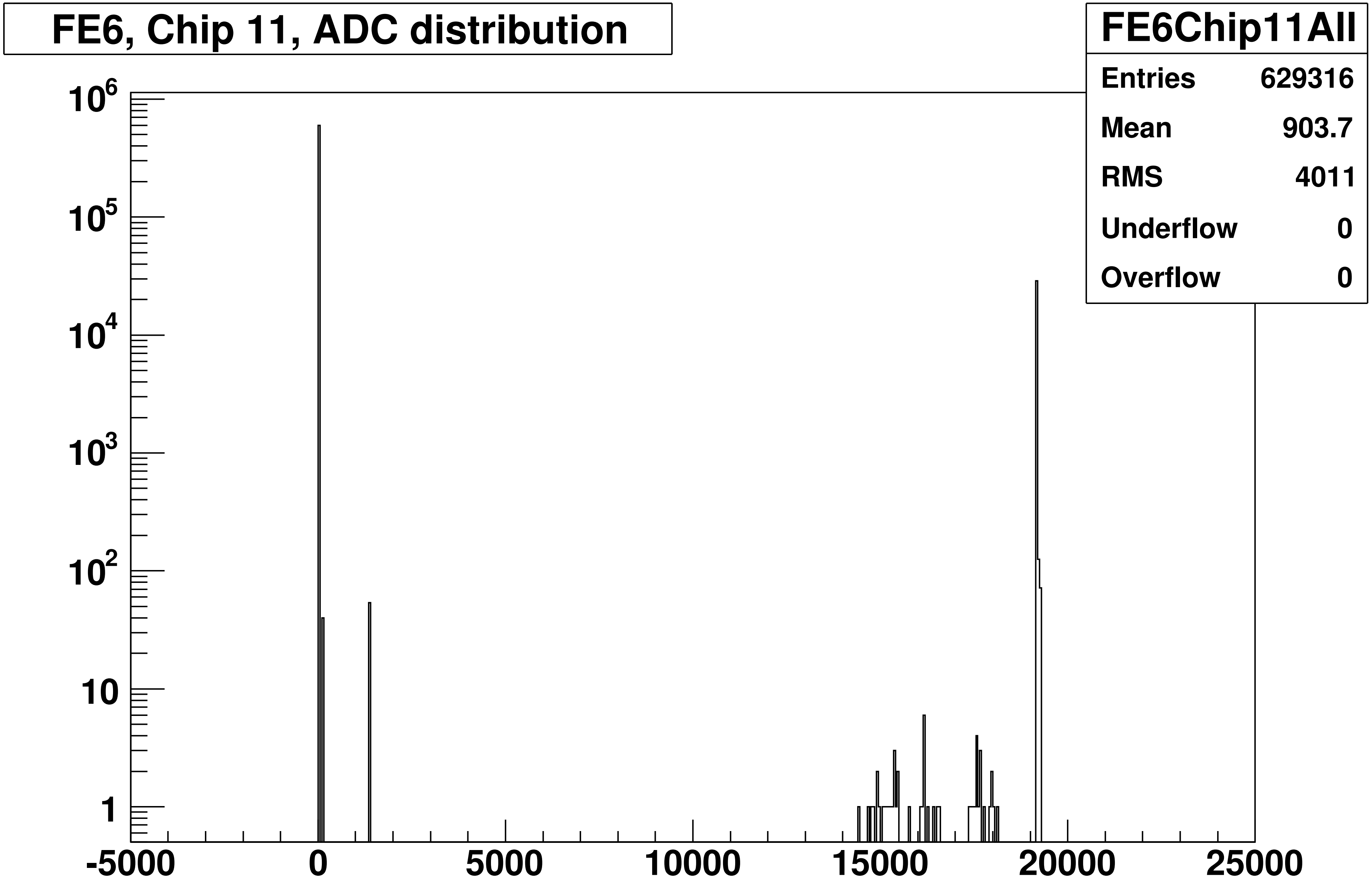
FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 629316
Mean 1.917e+04
RMS 5.581
Underflow 6.003e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 629316
Mean 865
RMS 4005
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

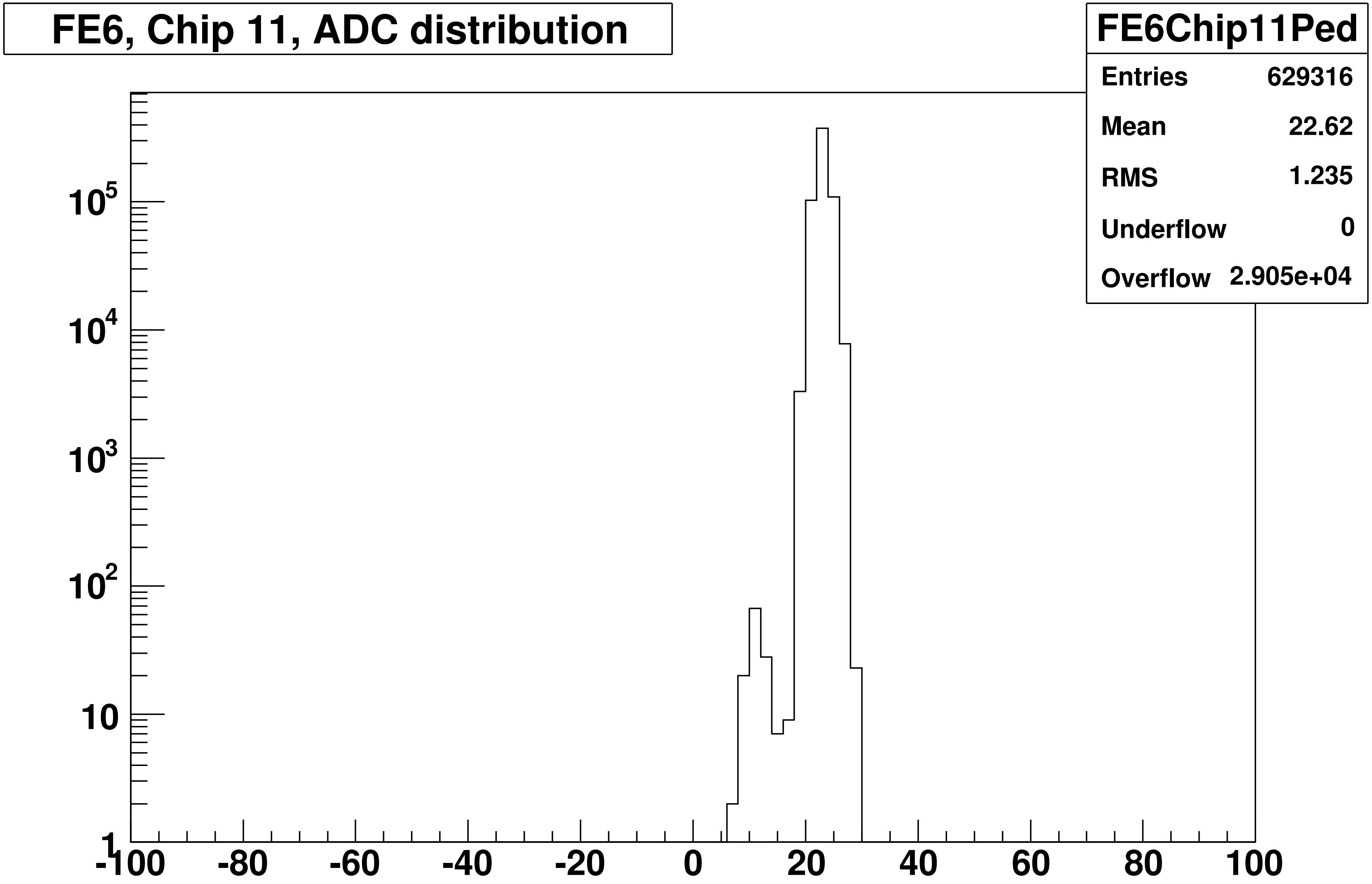


FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 629316
Mean -14.89
RMS 1.34
Underflow 0
Overflow 2.907e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

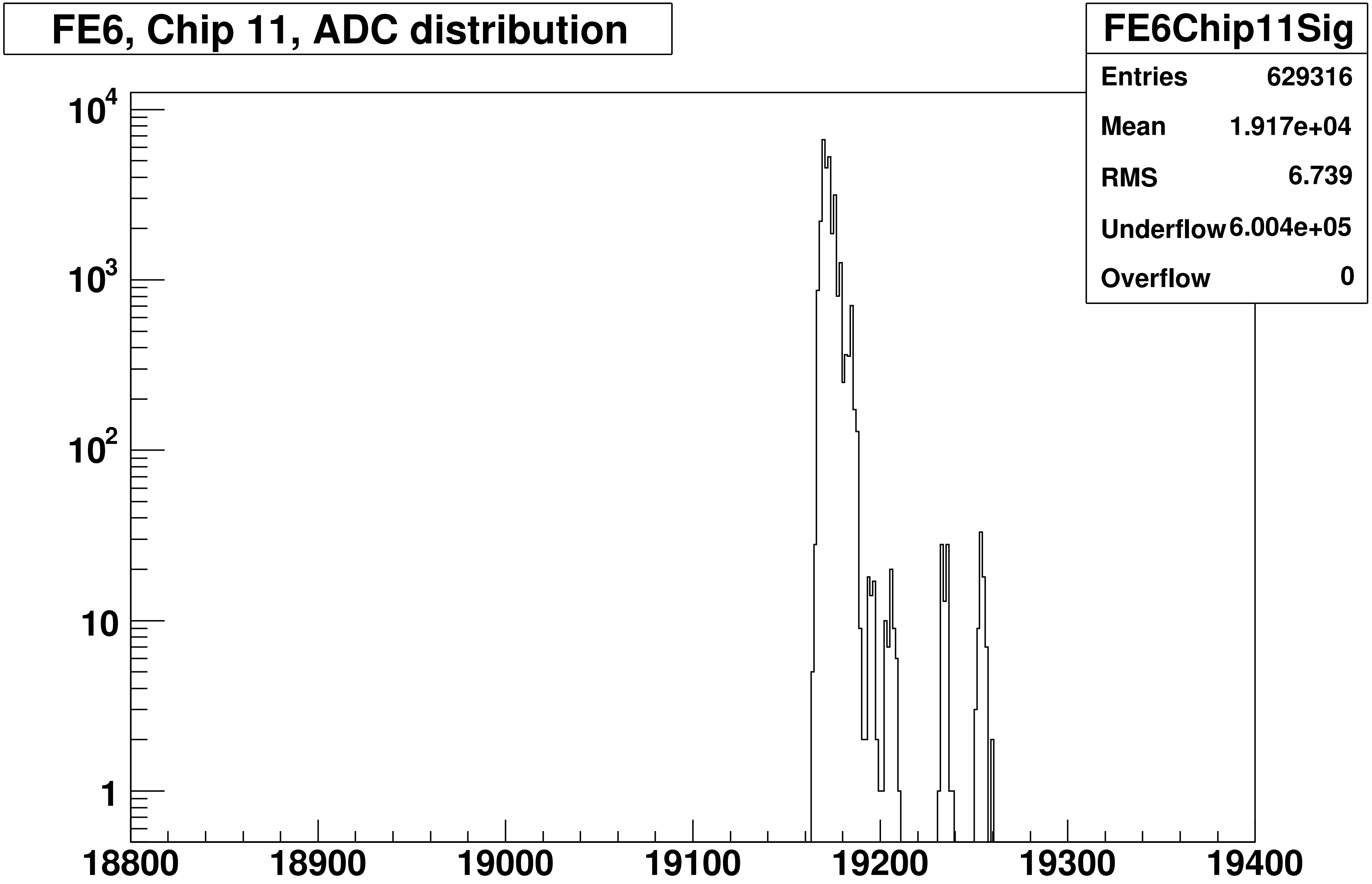

FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 629316
Mean 1.91e+04
RMS 7.138
Underflow 6.004e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



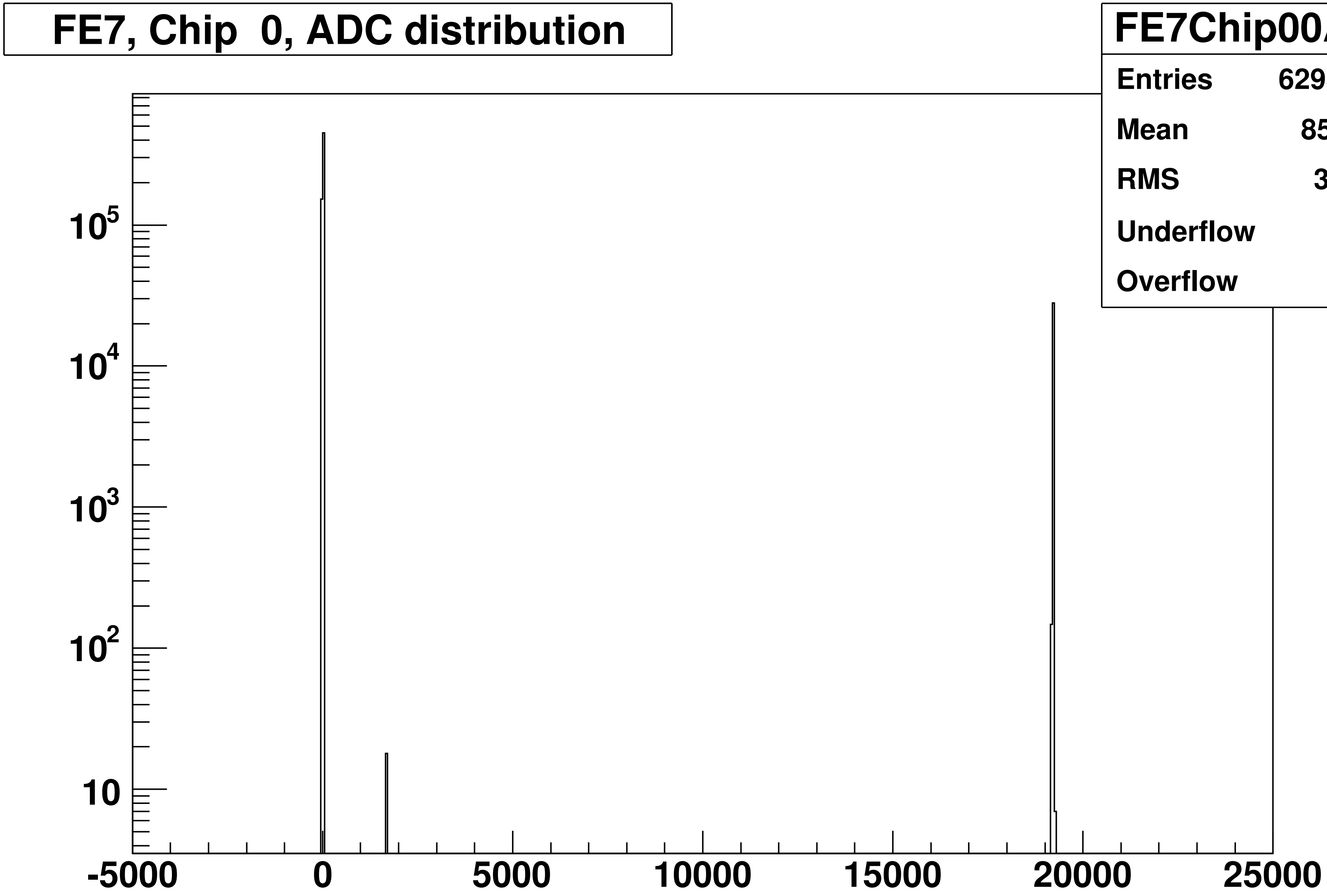
FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 629316
Mean 903.7
RMS 4011
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

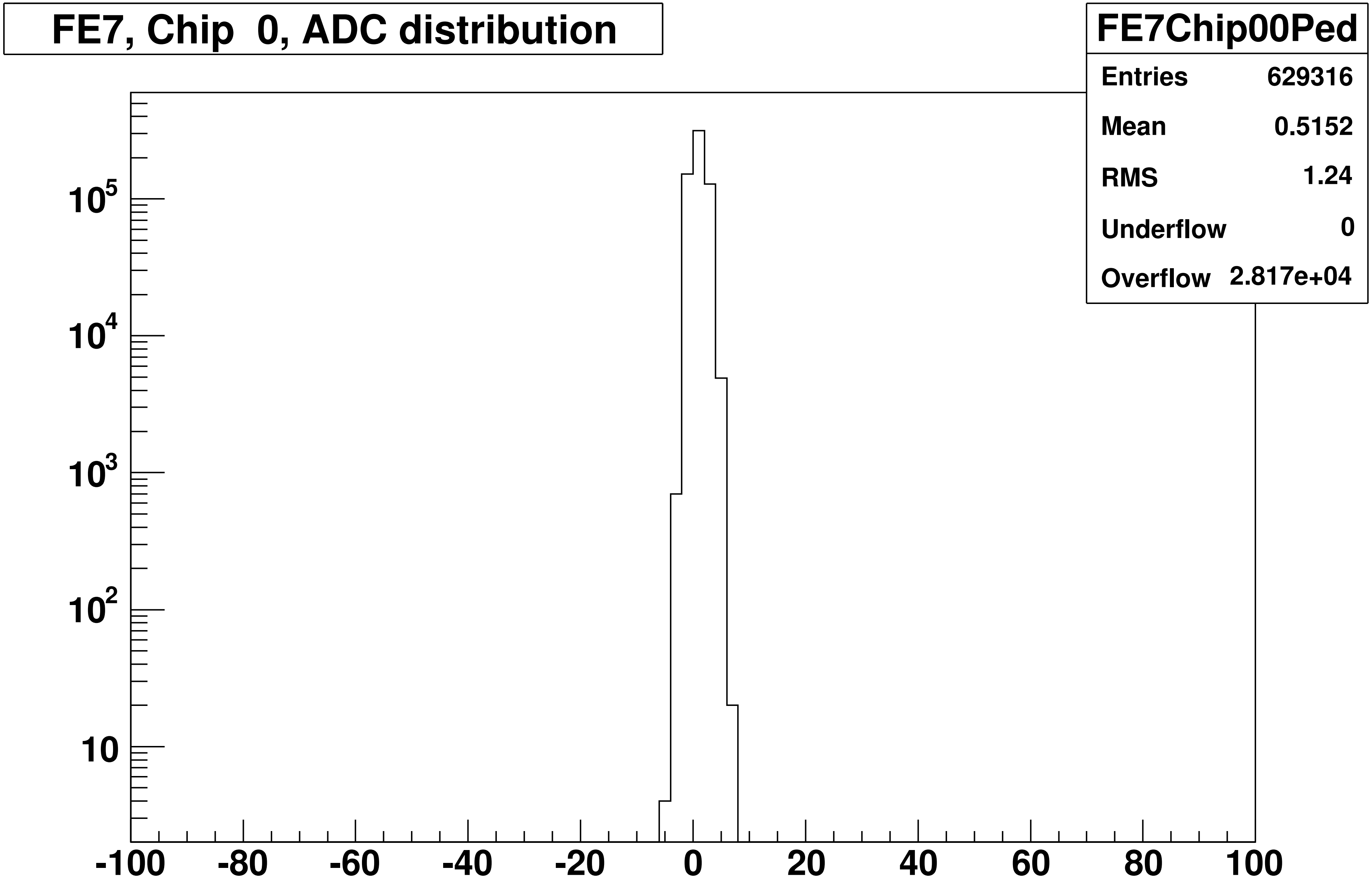


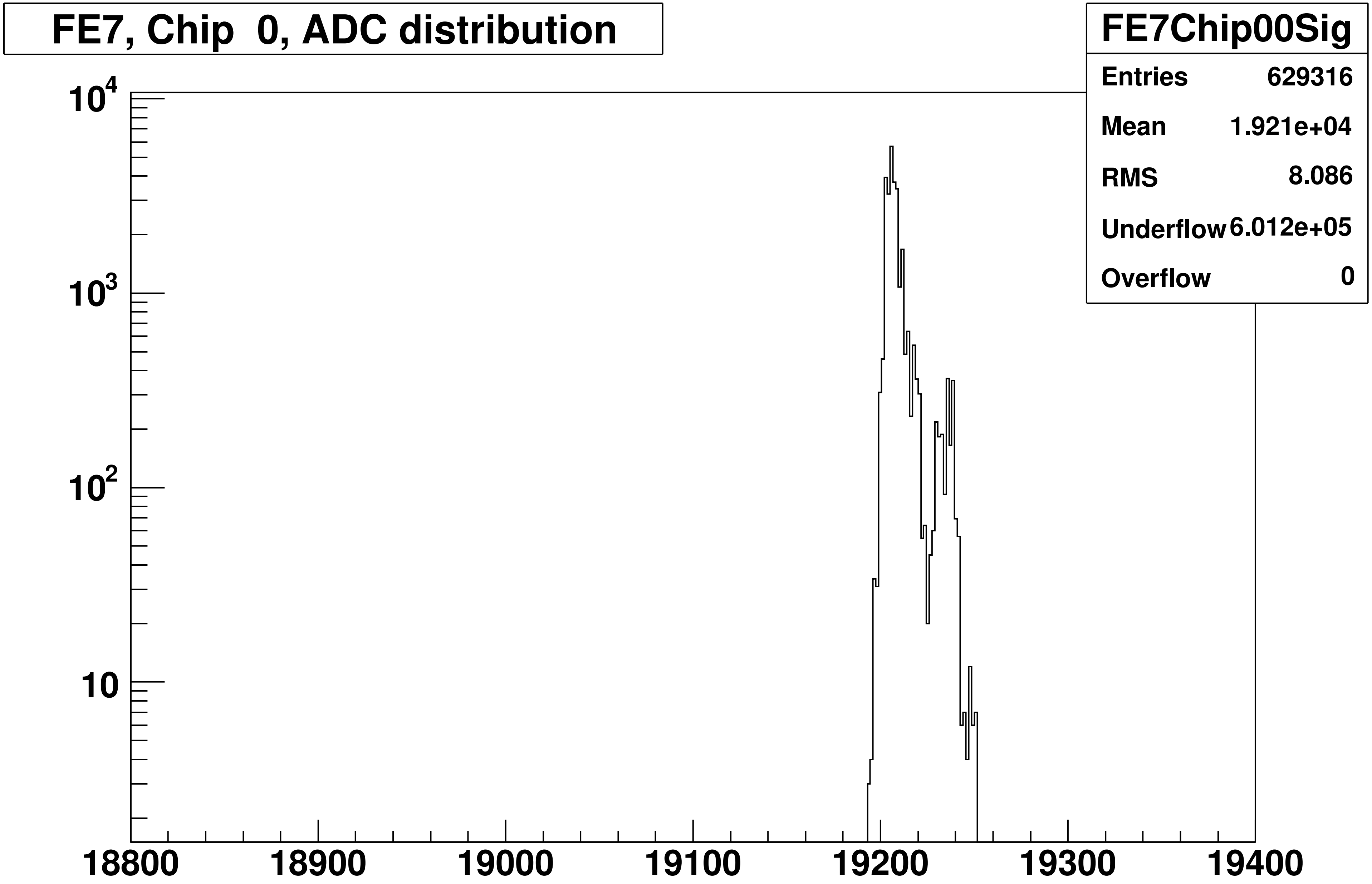
FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 629316
Mean 22.62
RMS 1.235
Underflow 0
Overflow 2.905e+04
1
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



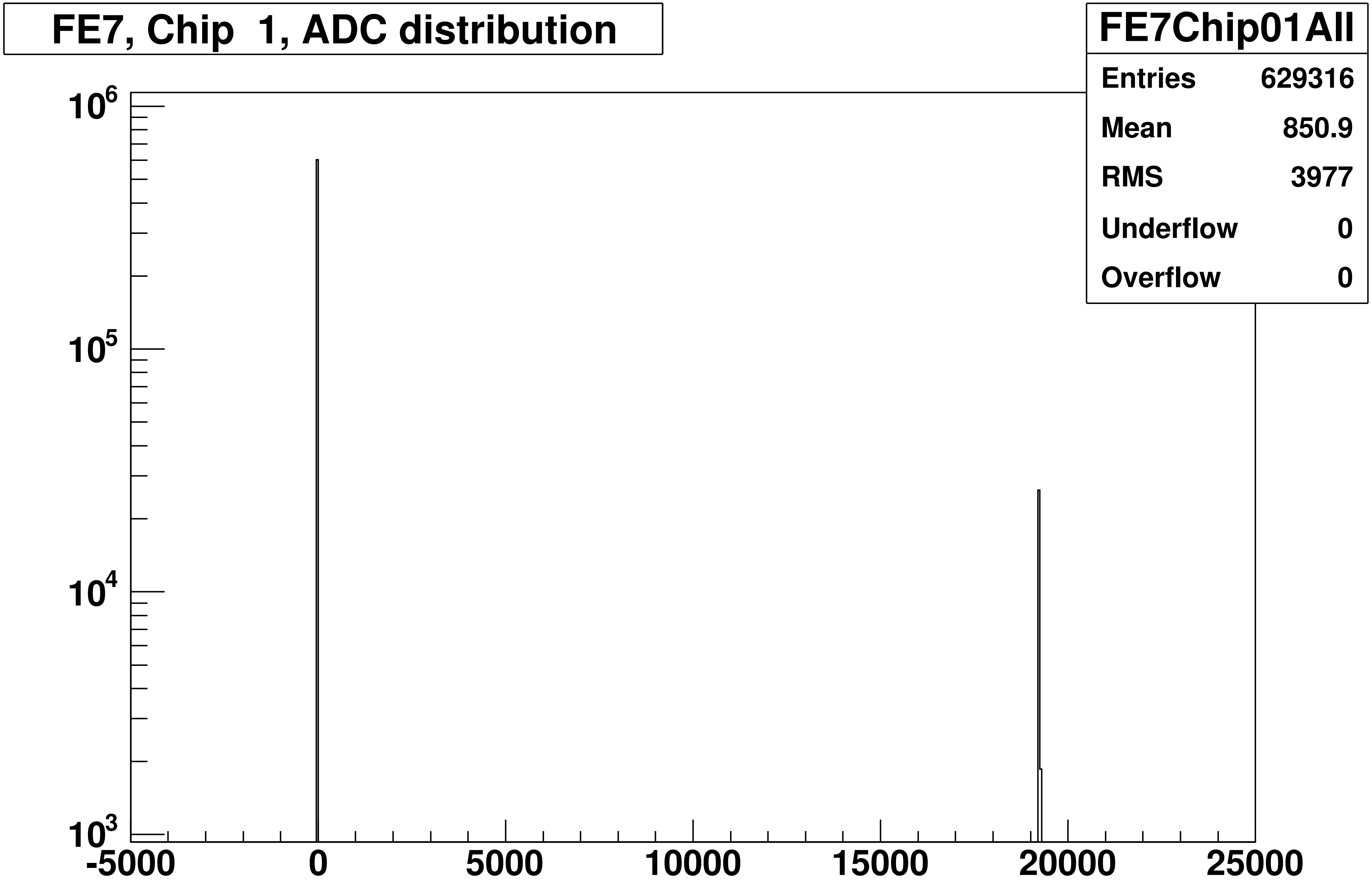
FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 629316
Mean 1.917e+04
RMS 6.739
Underflow 6.004e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 629316
Mean 859.8
RMS 3971
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

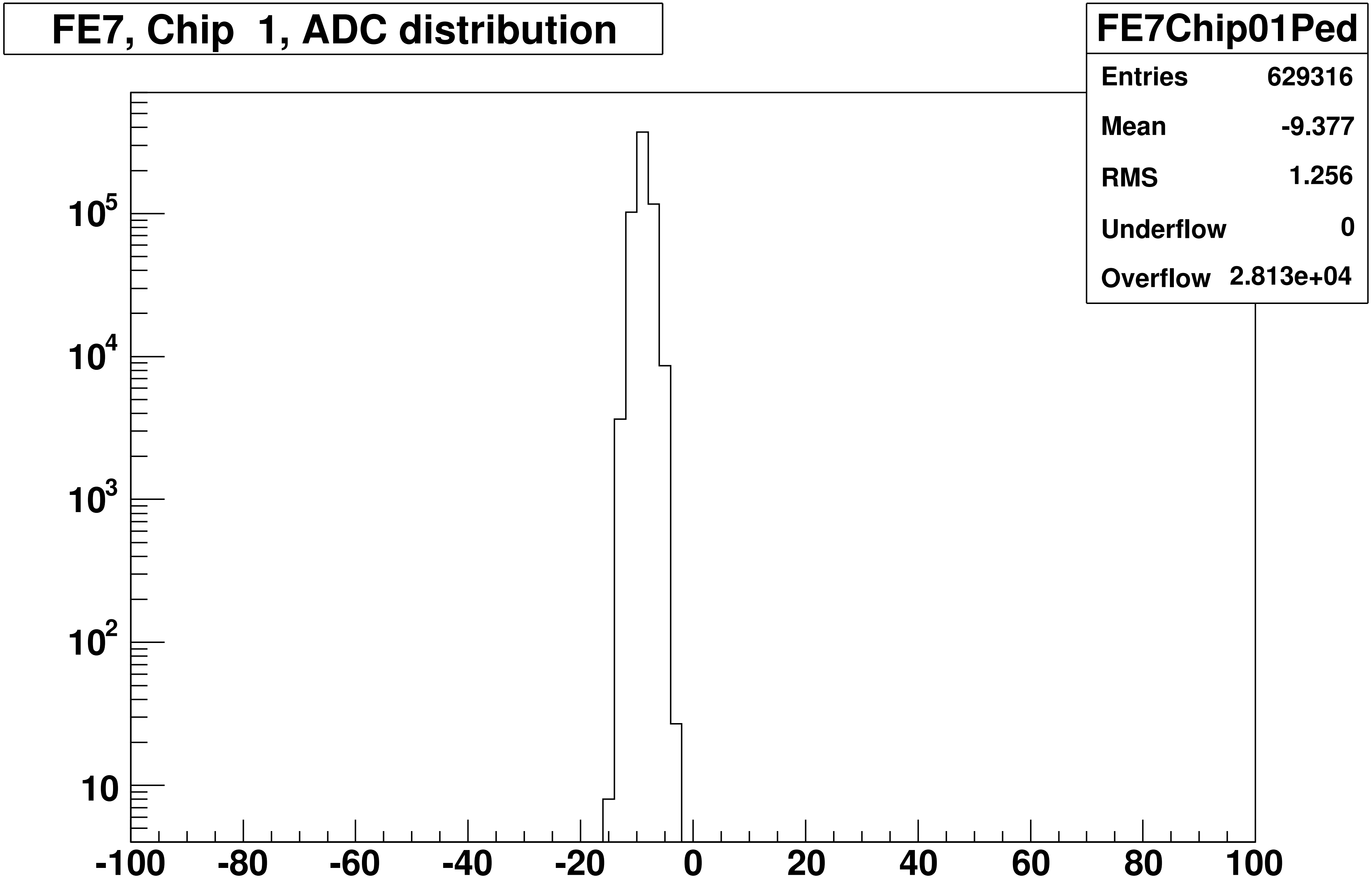

FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 629316
Mean 0.5152
RMS 1.24
Underflow 0
Overflow 2.817e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



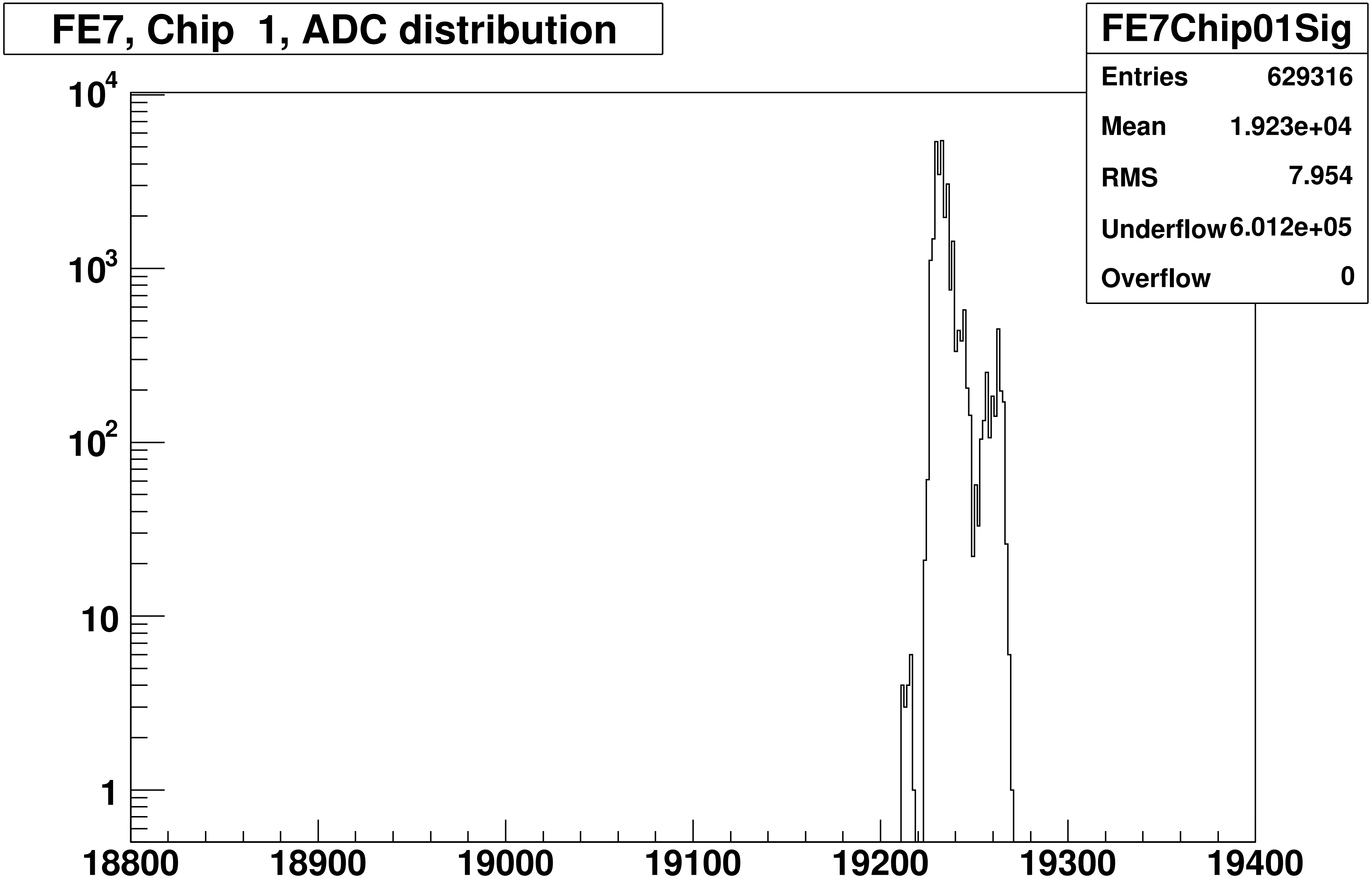
FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 629316
Mean 1.921e+04
RMS 8.086
Underflow 6.012e+05
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400

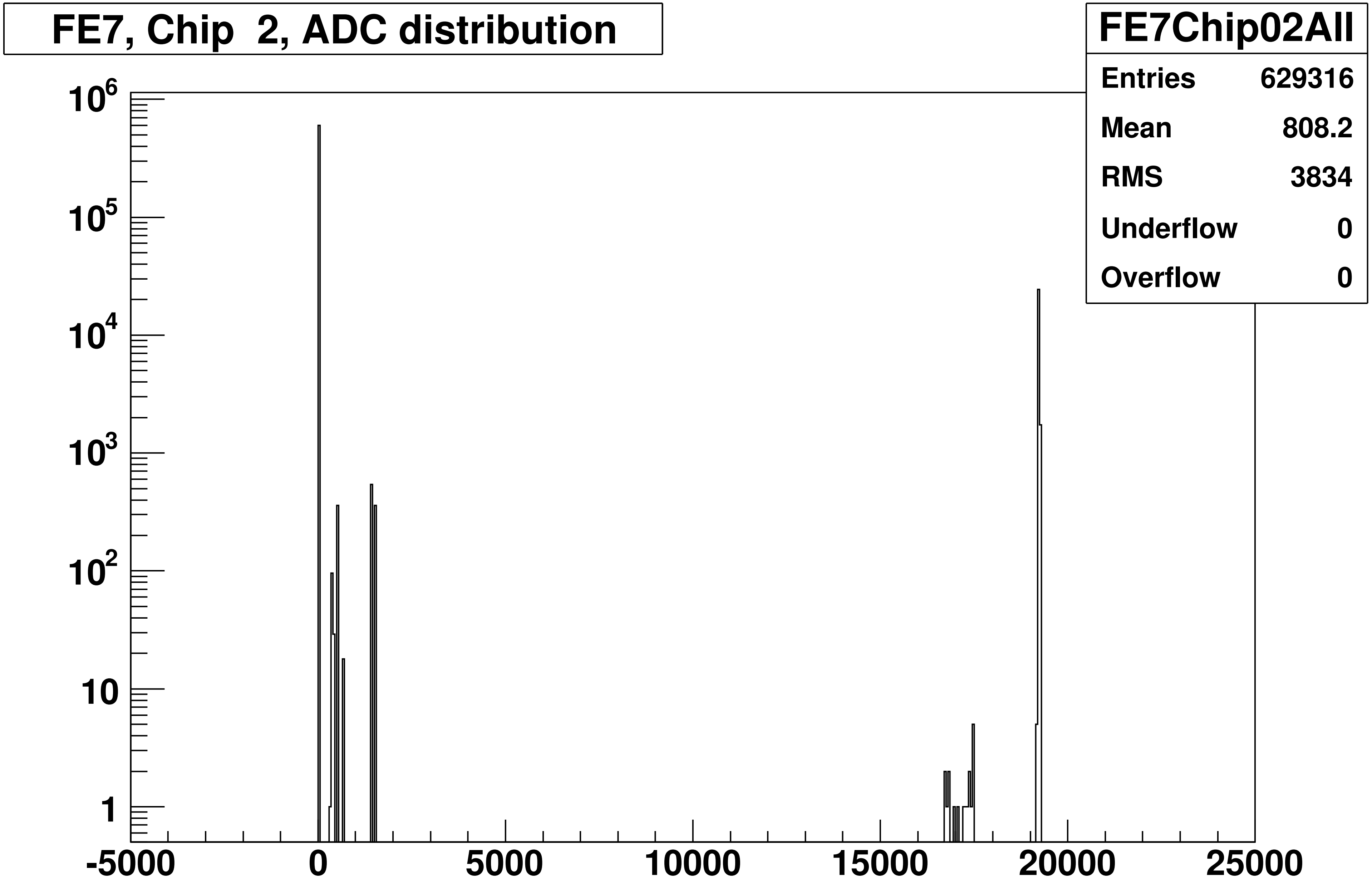


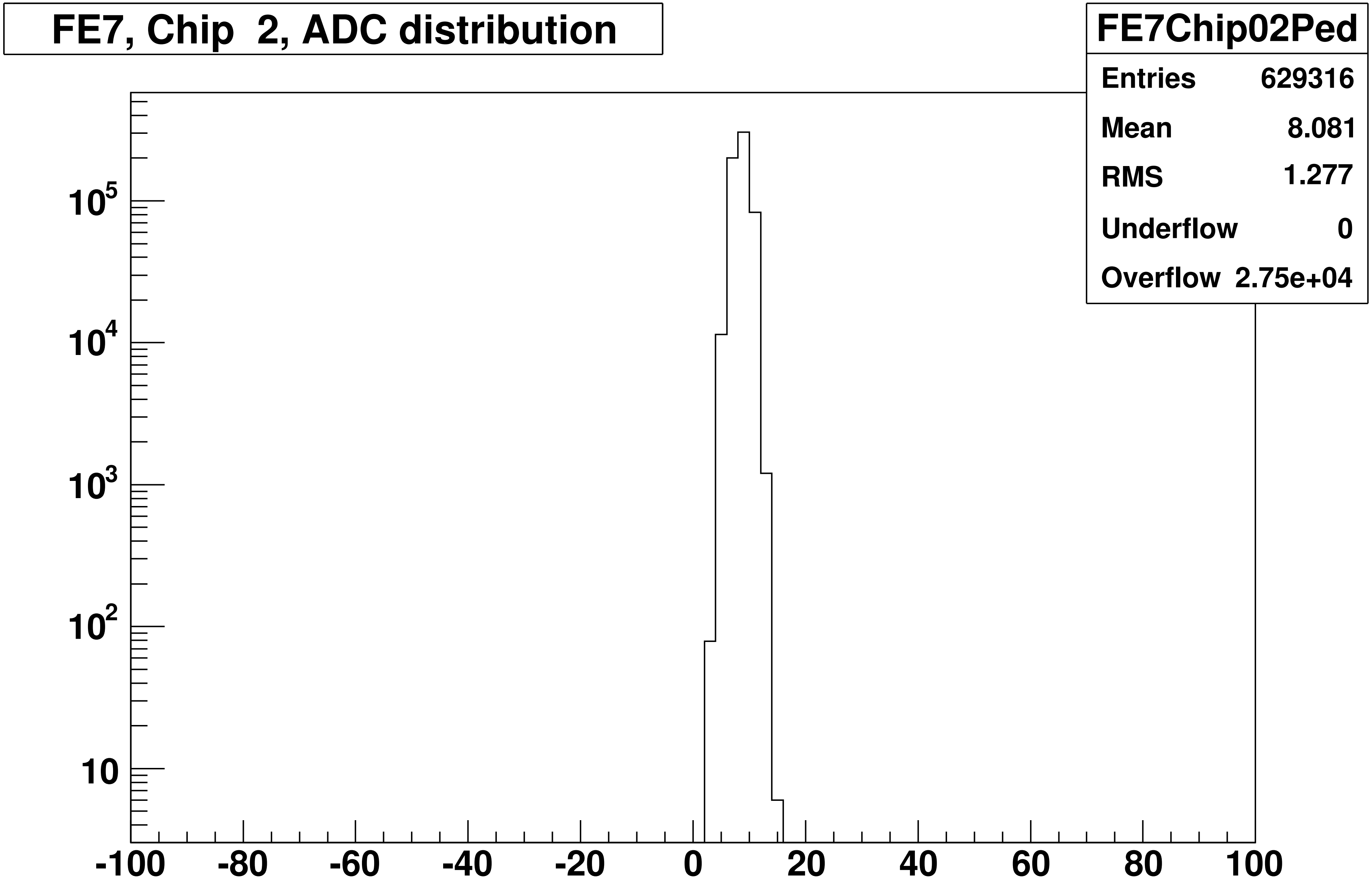
FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 629316
Mean 850.9
RMS 3977
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

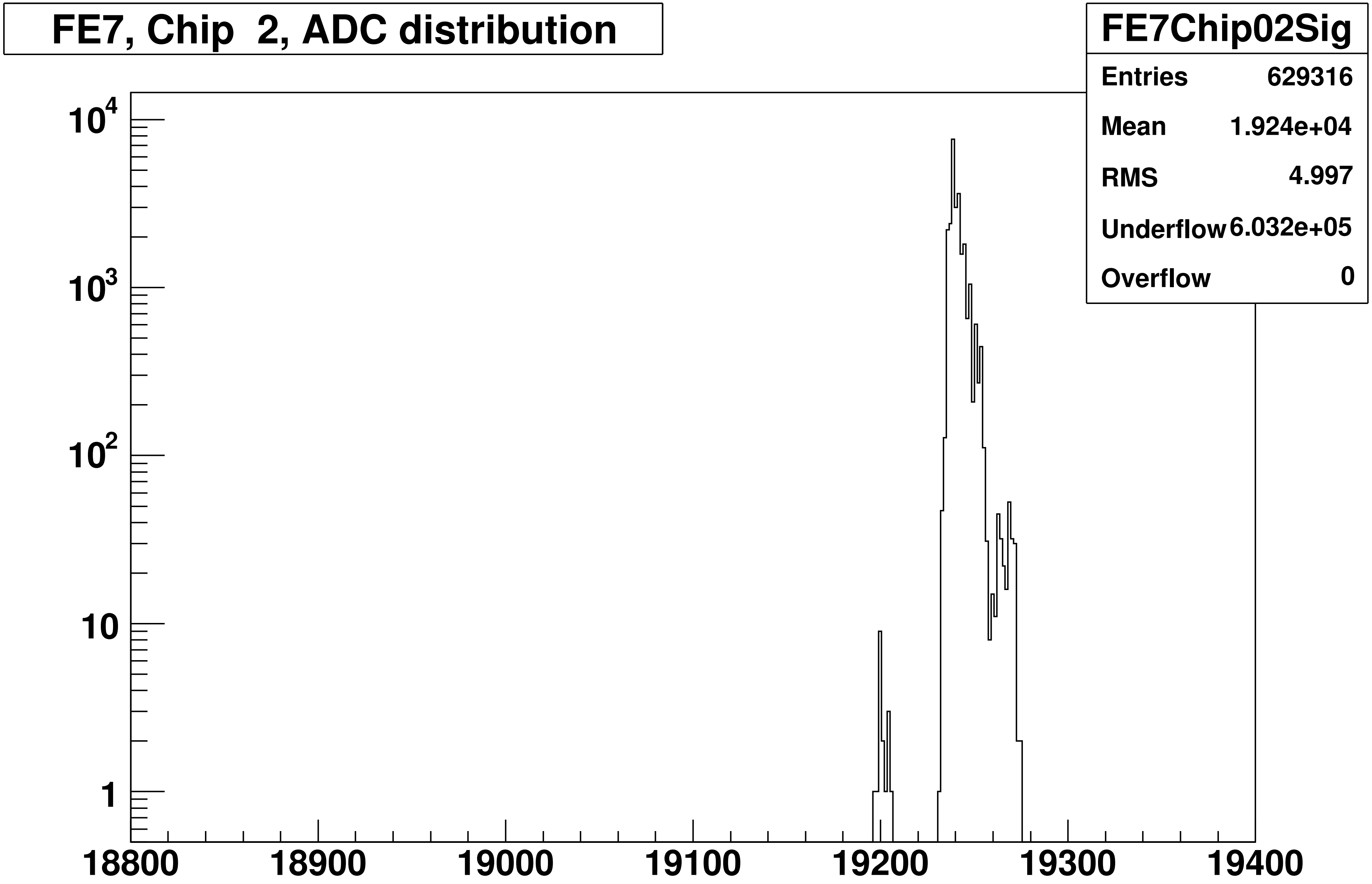


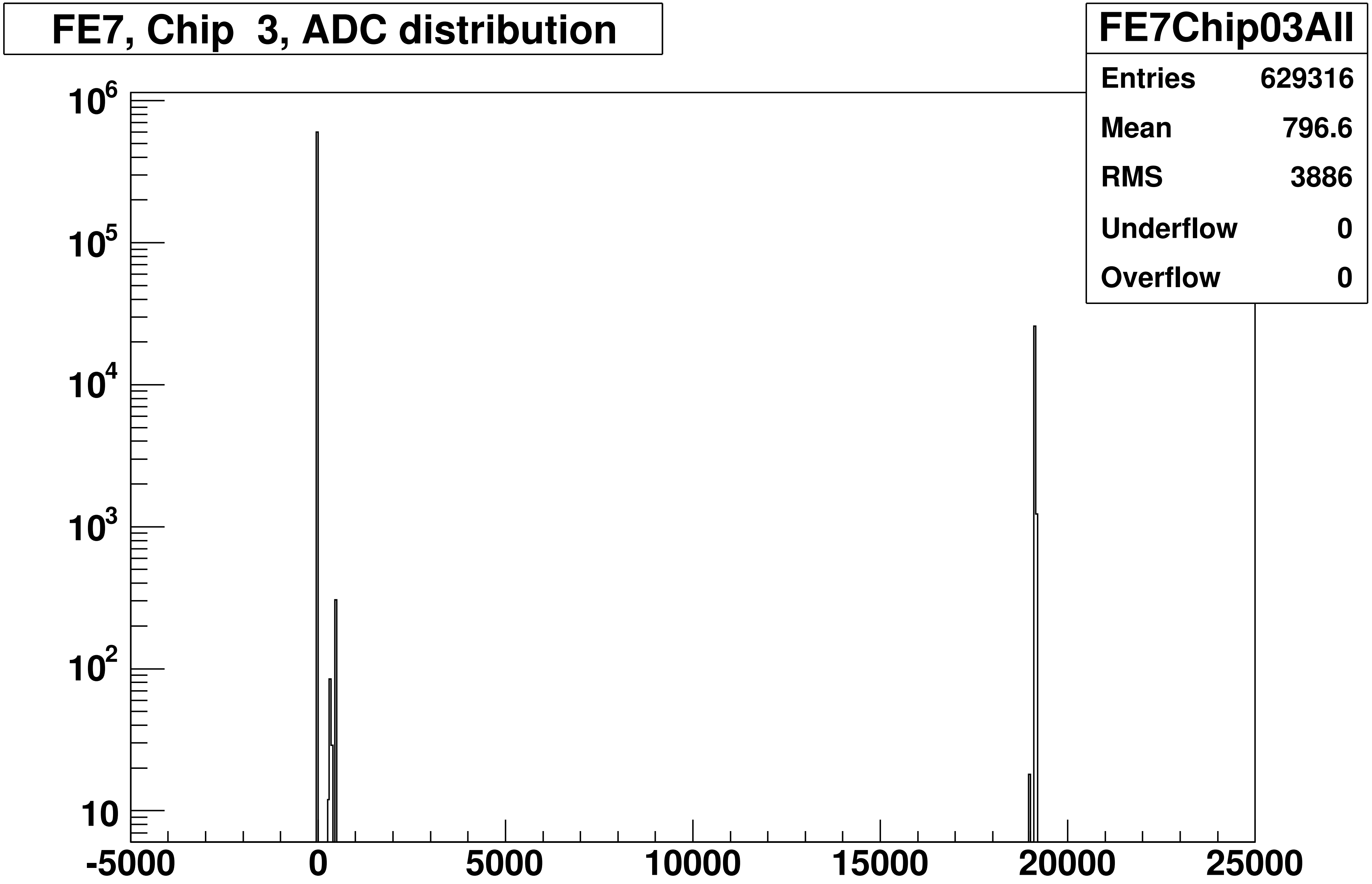
FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 629316
Mean -9.377
RMS 1.256
Underflow 0
Overflow 2.813e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

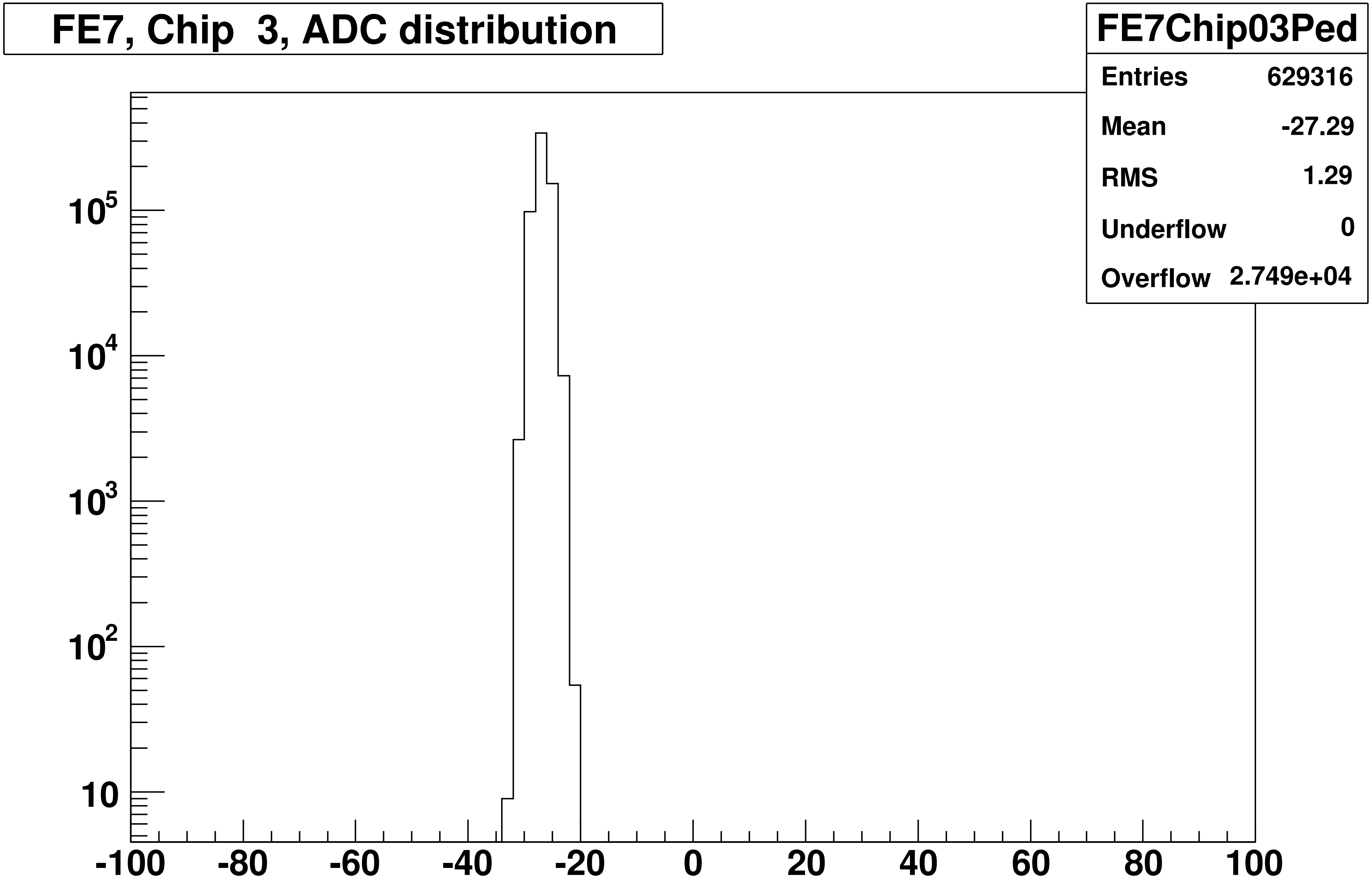

FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 629316
Mean 1.923e+04
RMS 7.954
Underflow 6.012e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

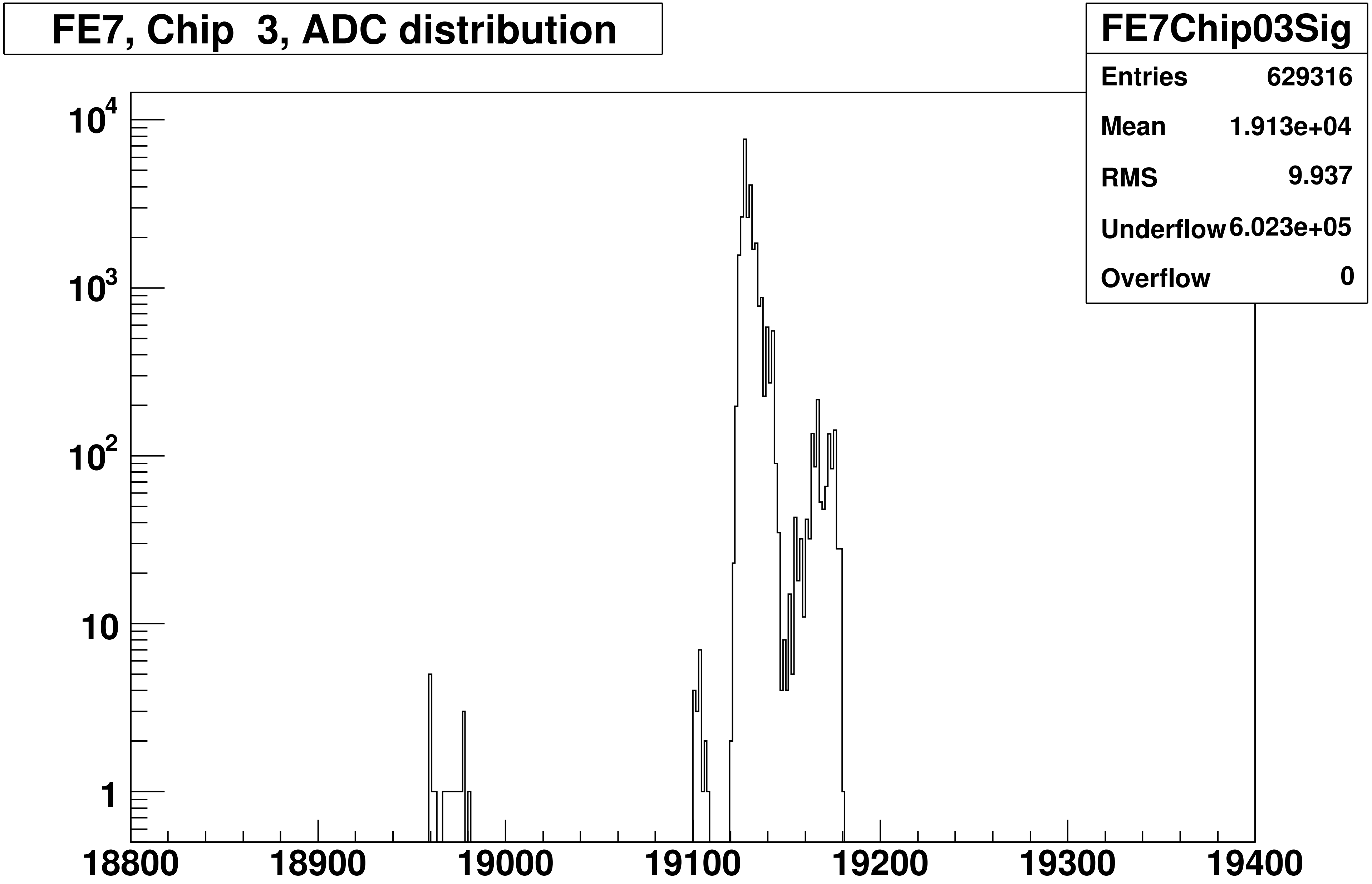

FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 629316
Mean 808.2
RMS 3834
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

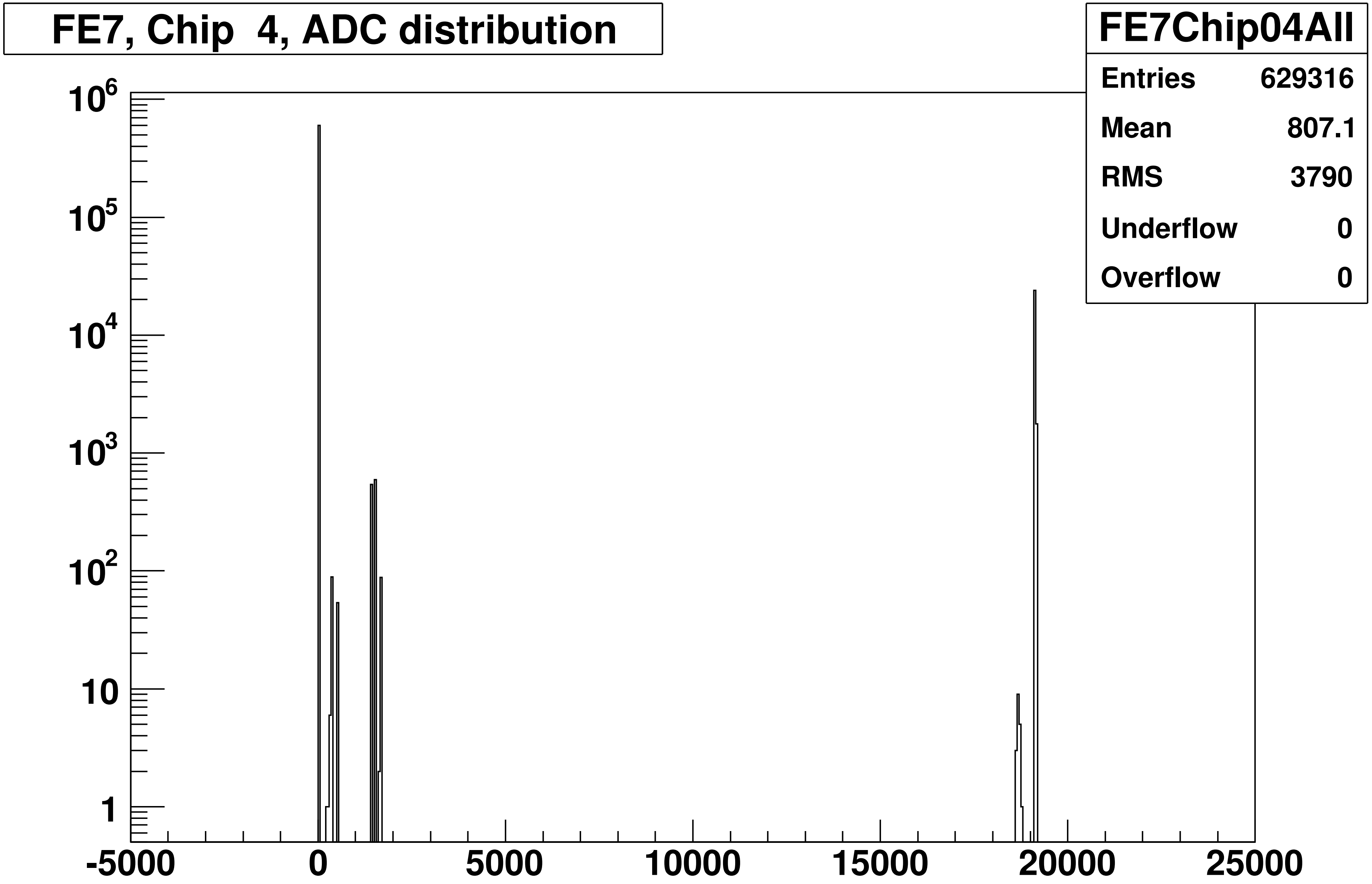

FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 629316
Mean 8.081
RMS 1.277
Underflow 0
Overflow 2.75e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

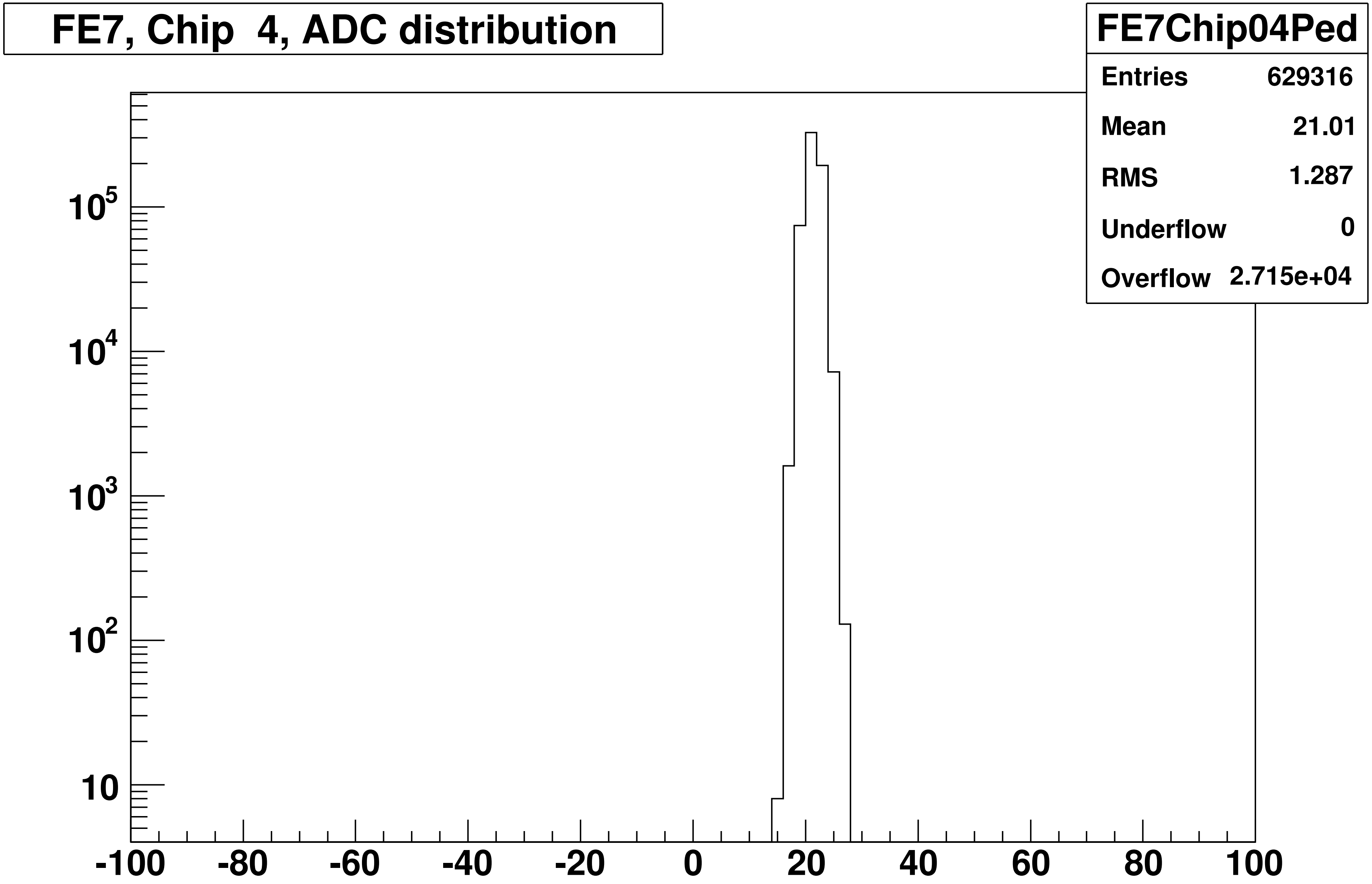

FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 629316
Mean 1.924e+04
RMS 4.997
Underflow 6.032e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 629316
Mean 796.6
RMS 3886
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

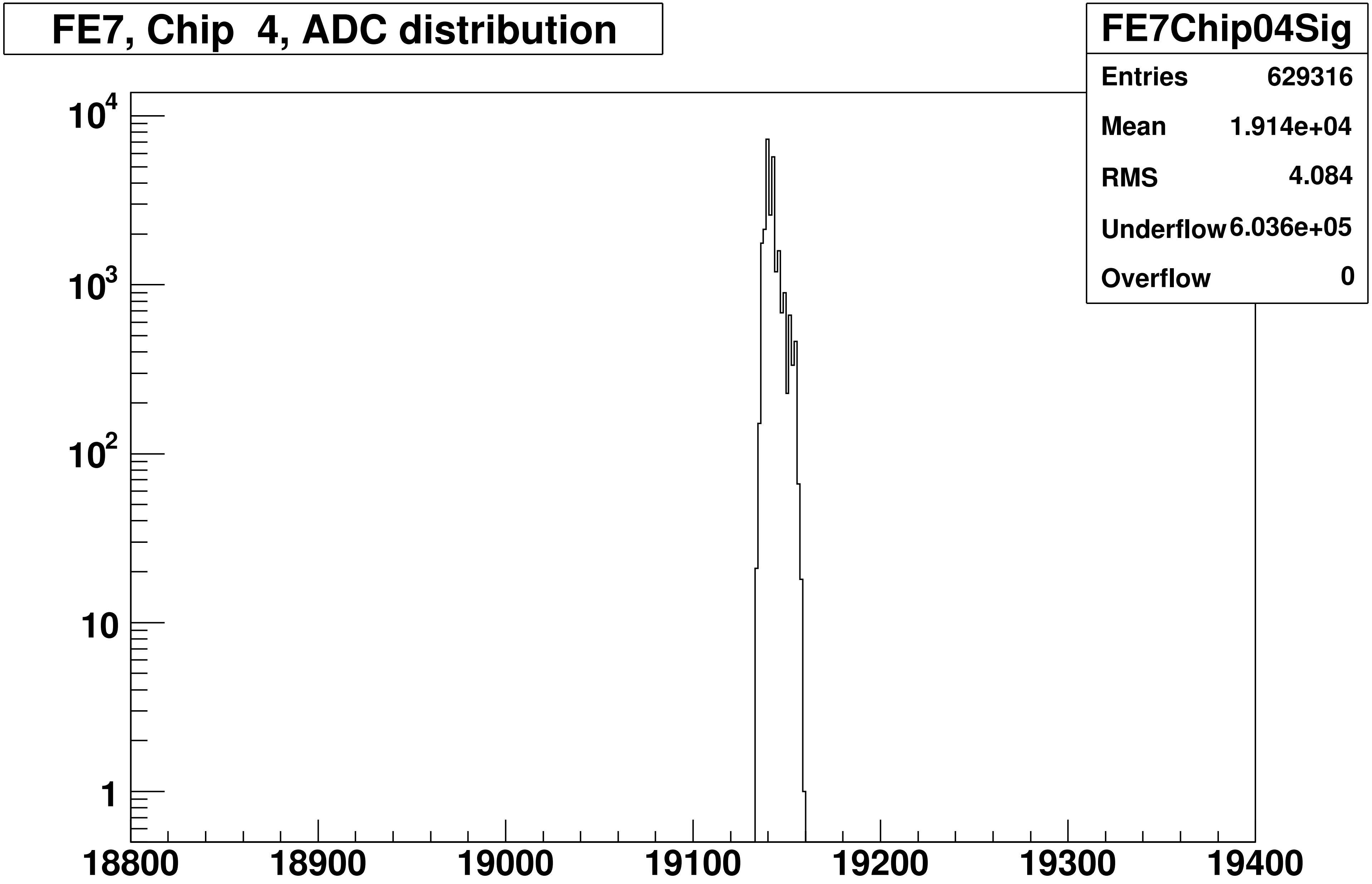

FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 629316
Mean -27.29
RMS 1.29
Underflow 0
Overflow 2.749e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

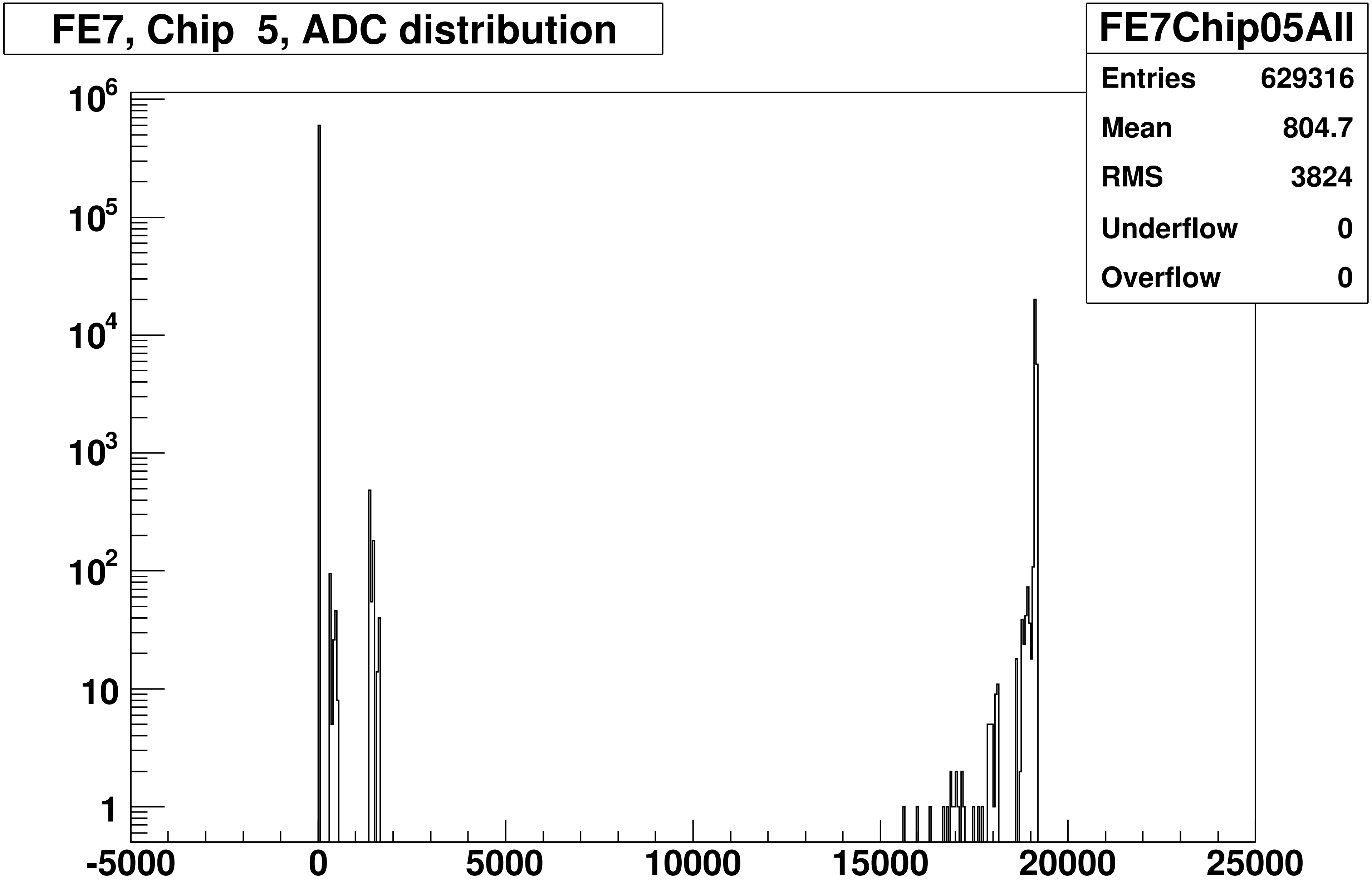

FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 629316
Mean 1.913e+04
RMS 9.937
Underflow 6.023e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 629316
Mean 807.1
RMS 3790
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

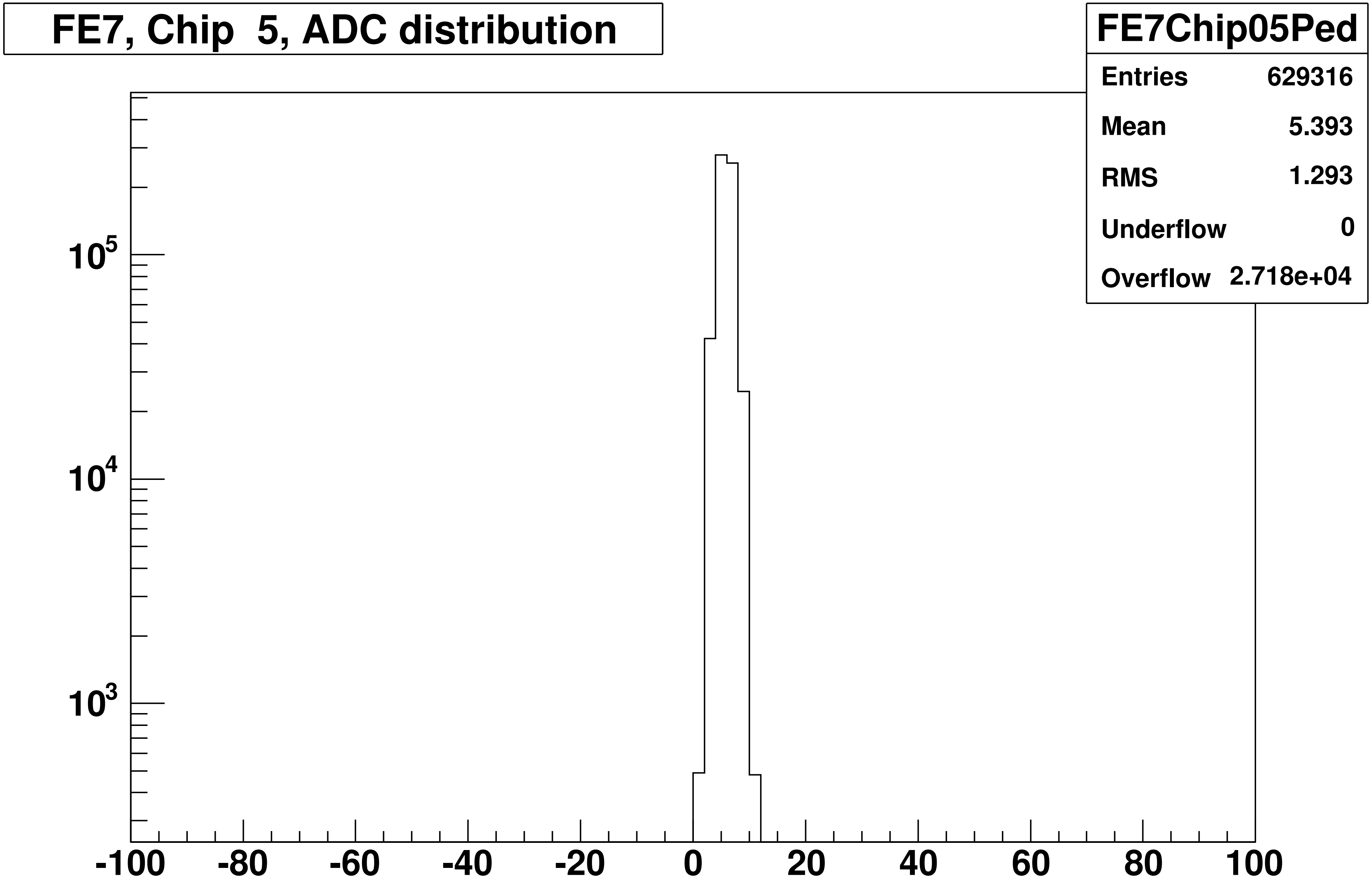


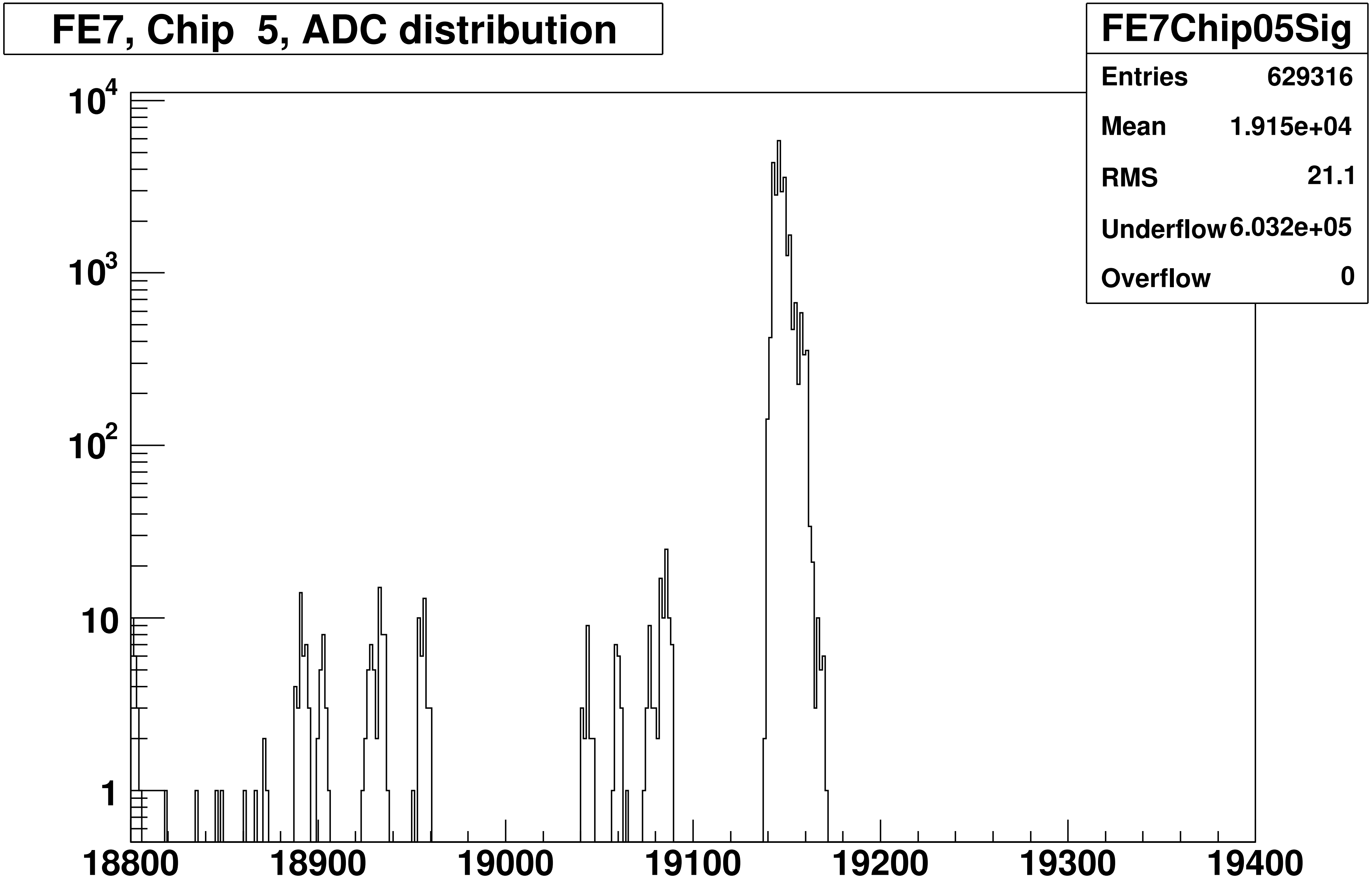
FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 629316
Mean 21.01
RMS 1.287
Underflow 0
Overflow 2.715e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

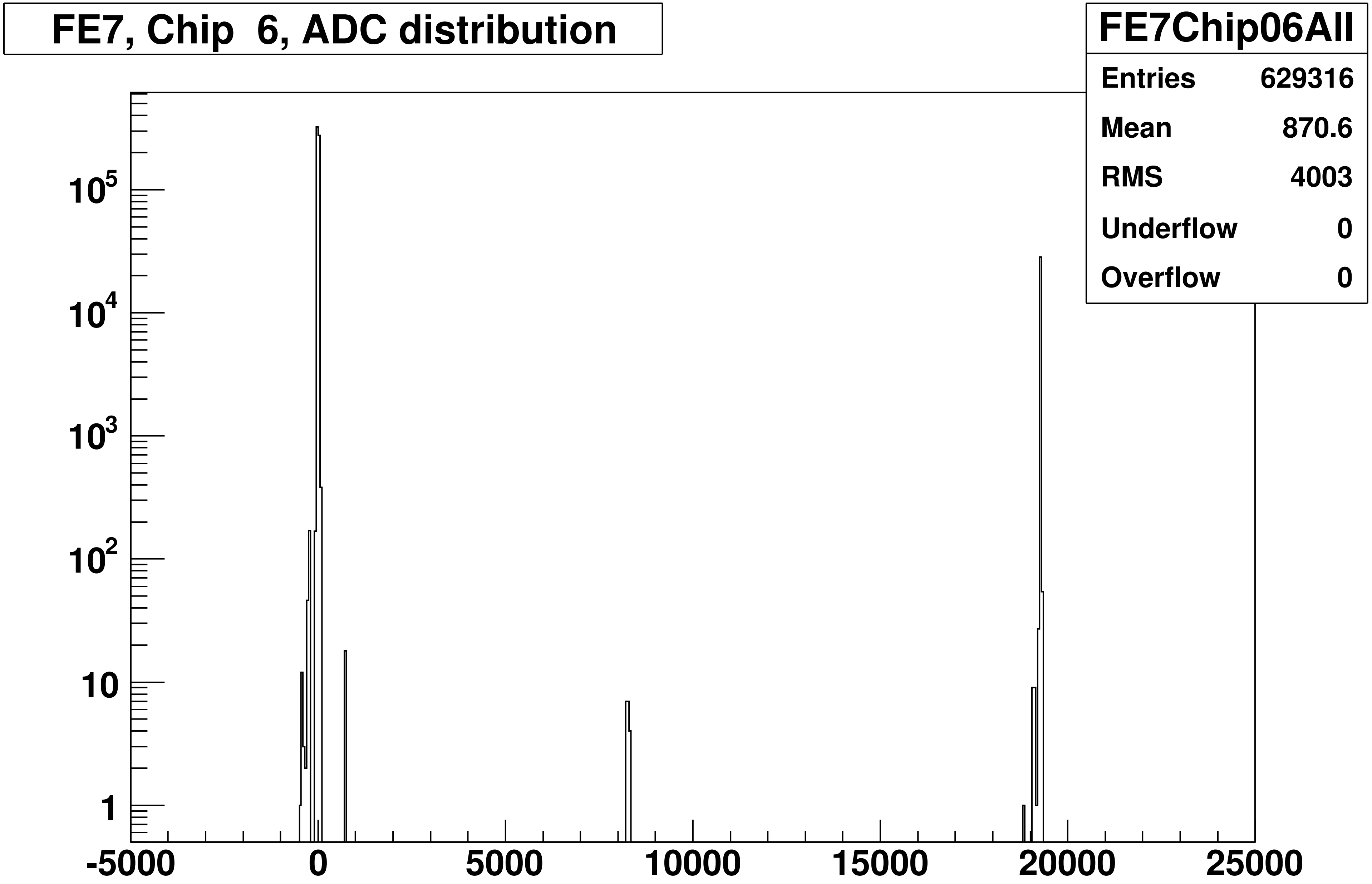

FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 629316
Mean 1.914e+04
RMS 4.084
Underflow 6.036e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



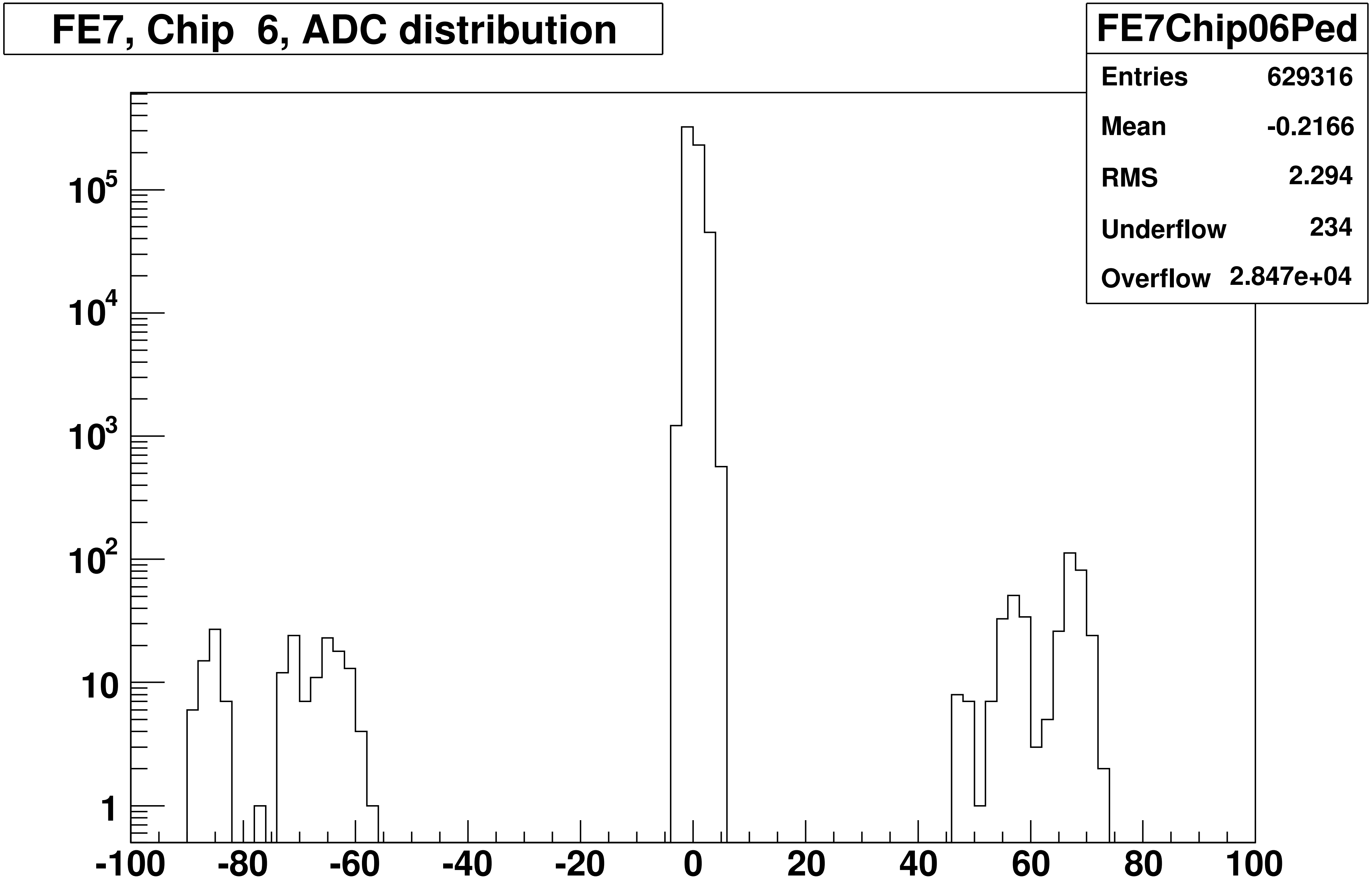
FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 629316
Mean 804.7
RMS 3824
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

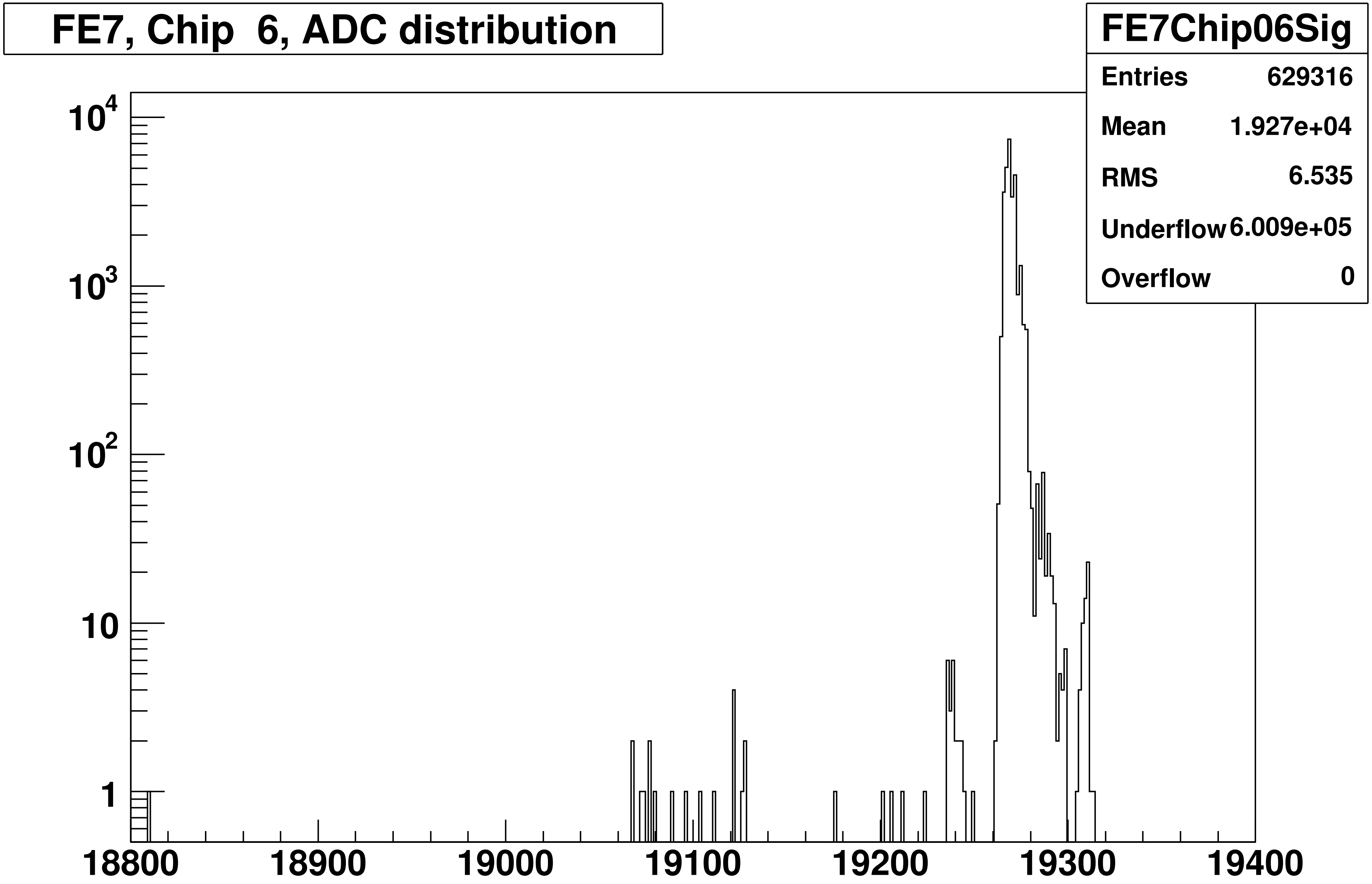

FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 629316
Mean 5.393
RMS 1.293
Underflow 0
Overflow 2.718e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100

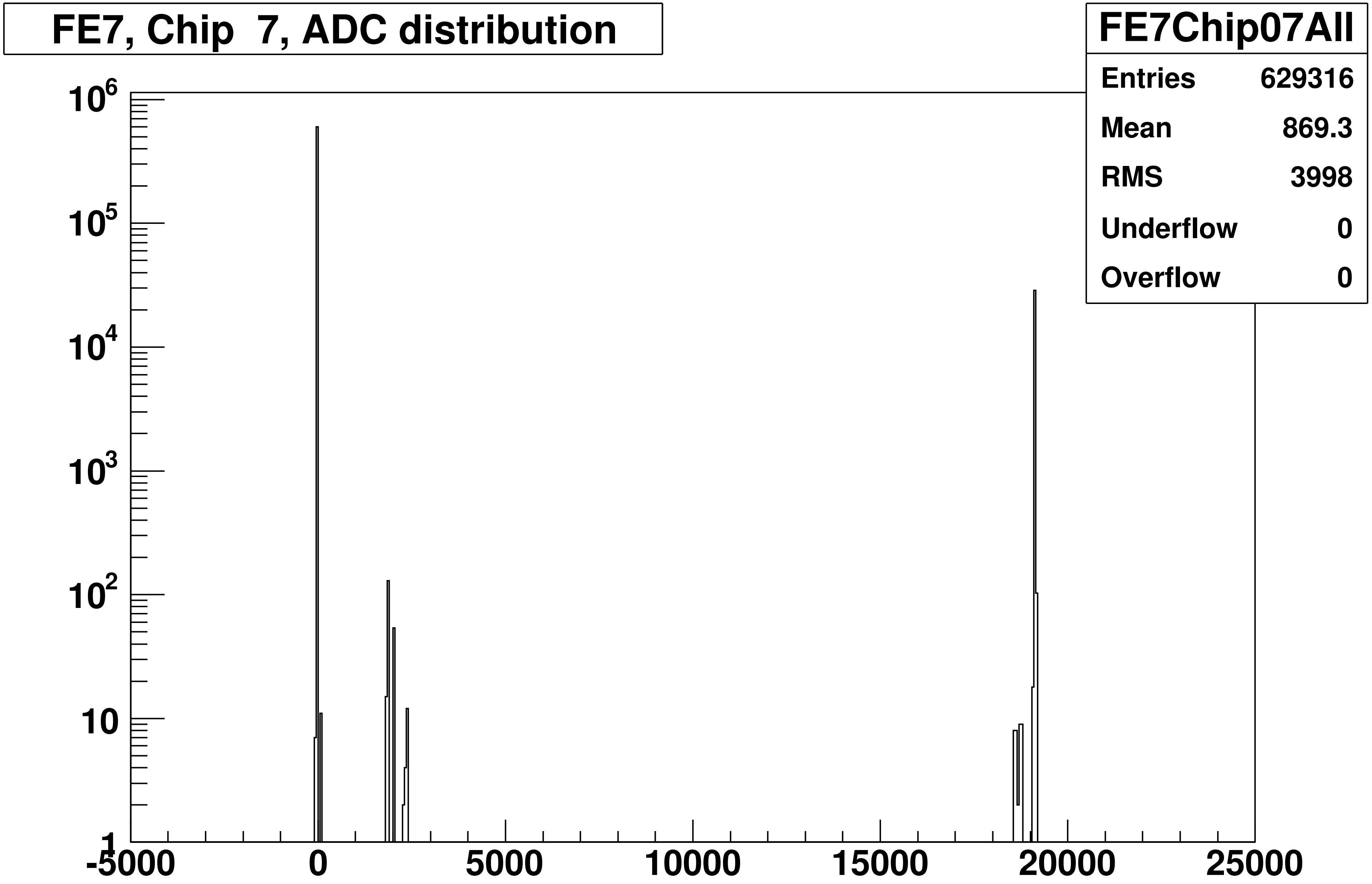

FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 629316
Mean 1.915e+04
RMS 21.1
Underflow 6.032e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



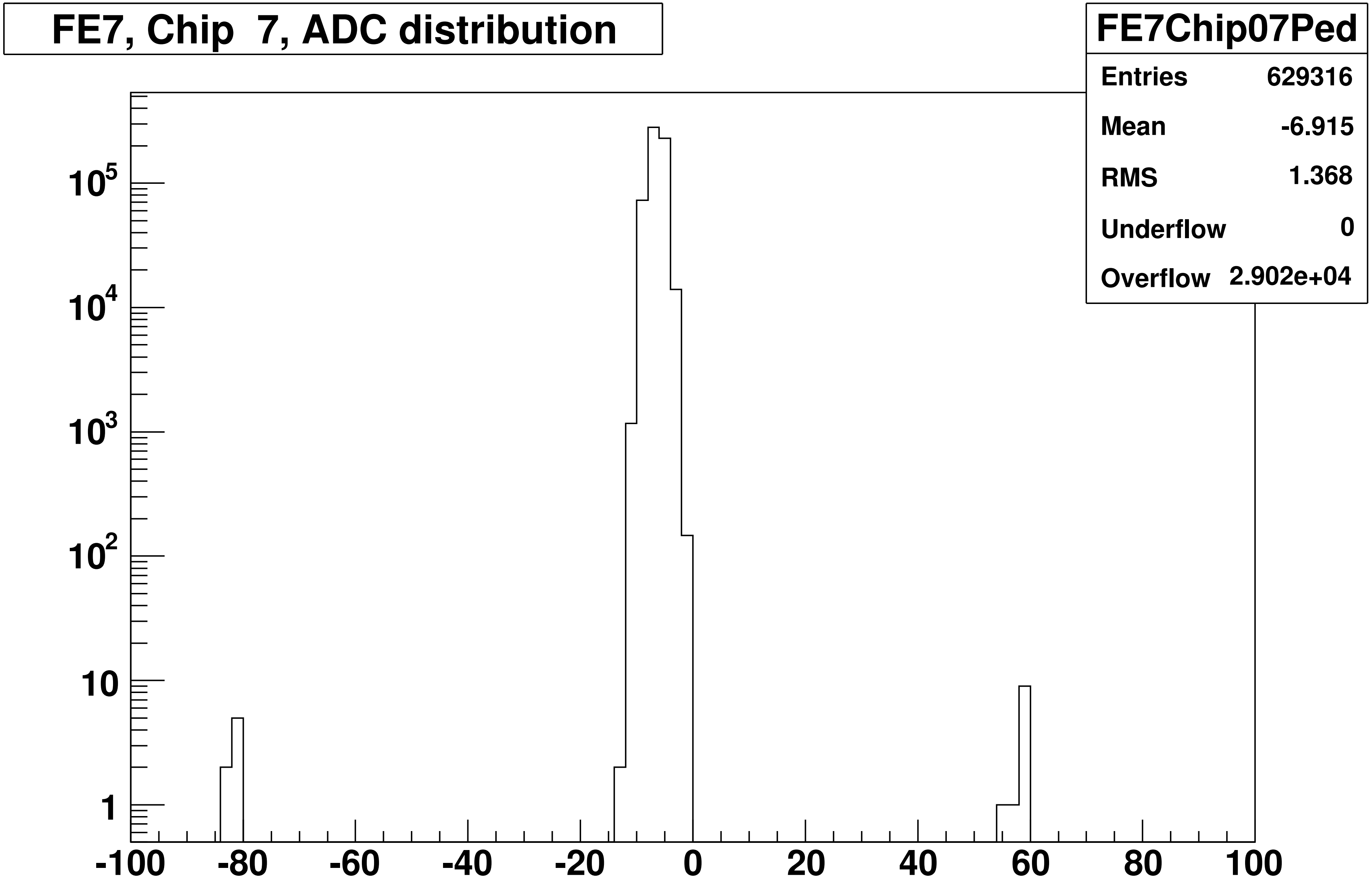
FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 629316
Mean 870.6
RMS 4003
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 629316
Mean -0.2166
RMS 2.294
Underflow 234
Overflow 2.847e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

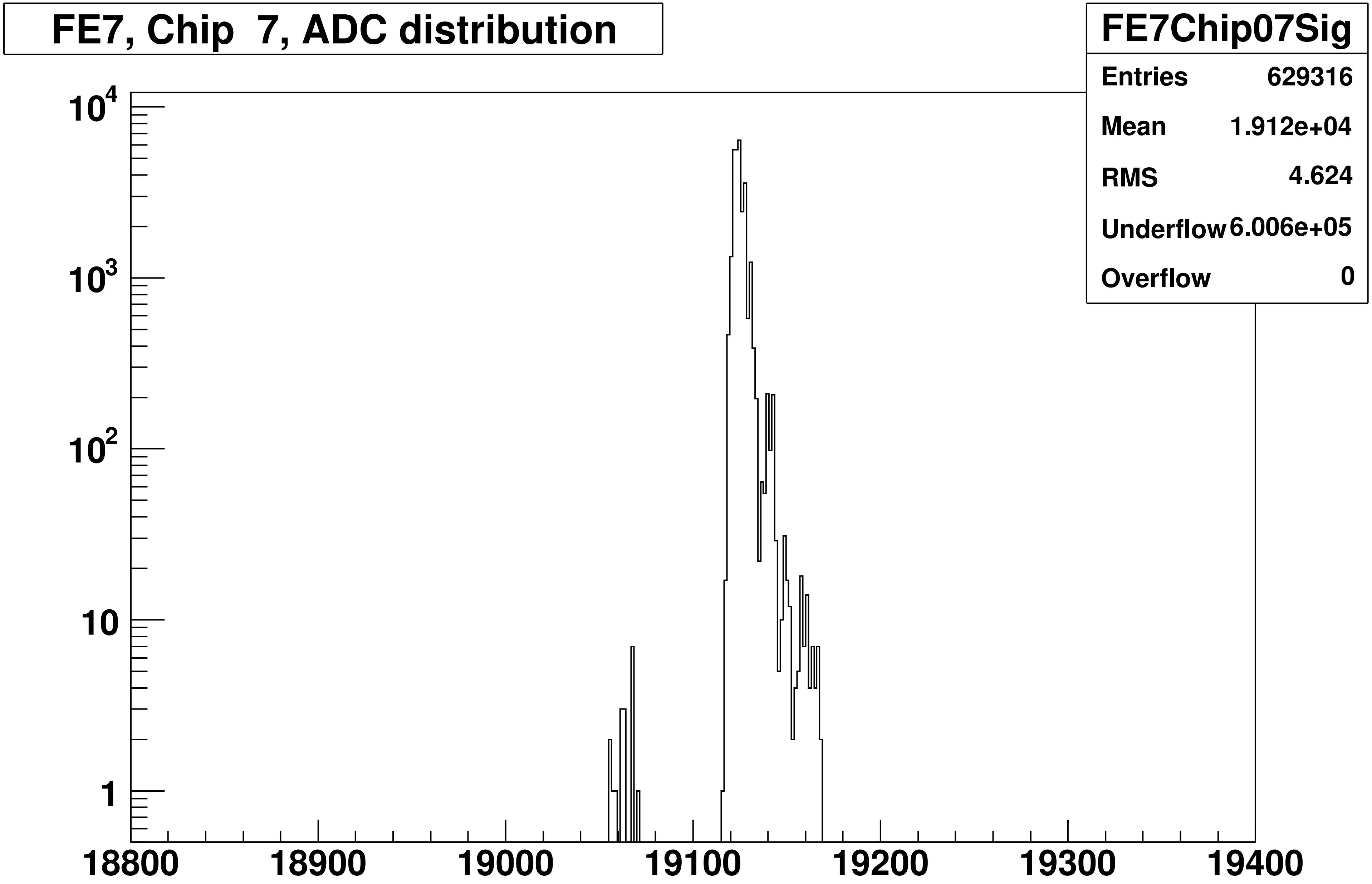

FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 629316
Mean 1.927e+04
RMS 6.535
Underflow 6.009e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



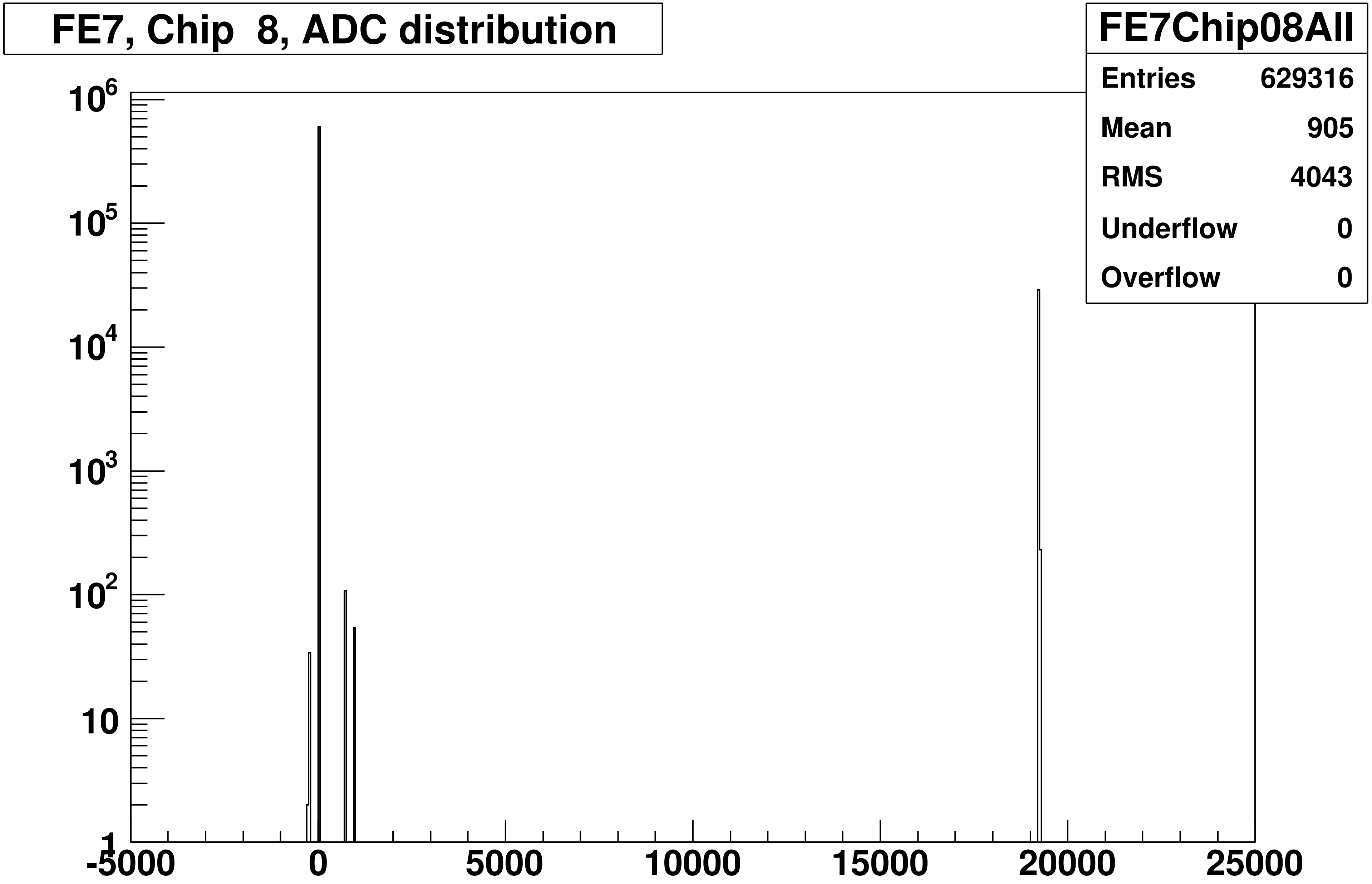
FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 629316
Mean 869.3
RMS 3998
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

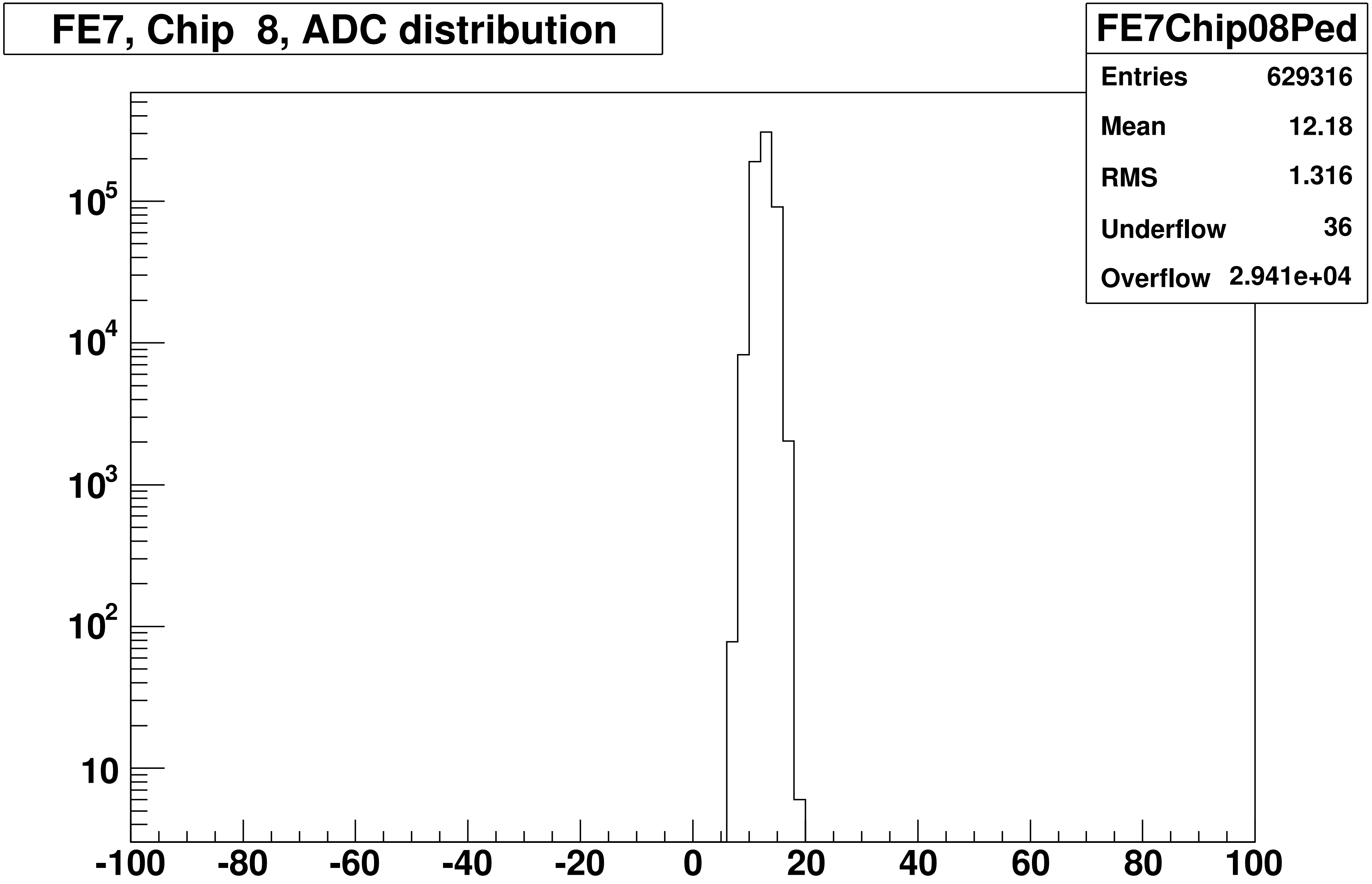


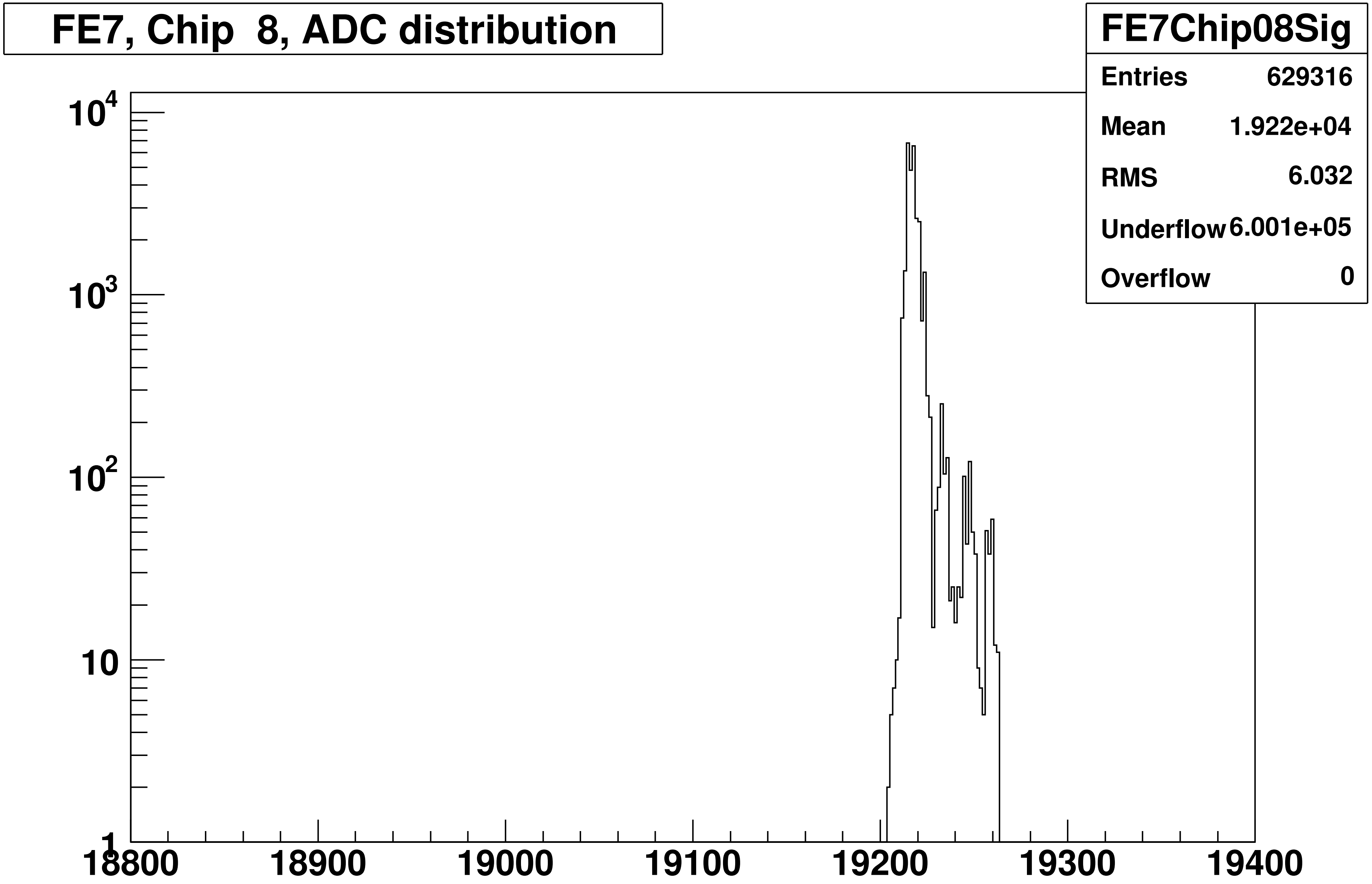
FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 629316
Mean -6.915
RMS 1.368
Underflow 0
Overflow 2.902e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

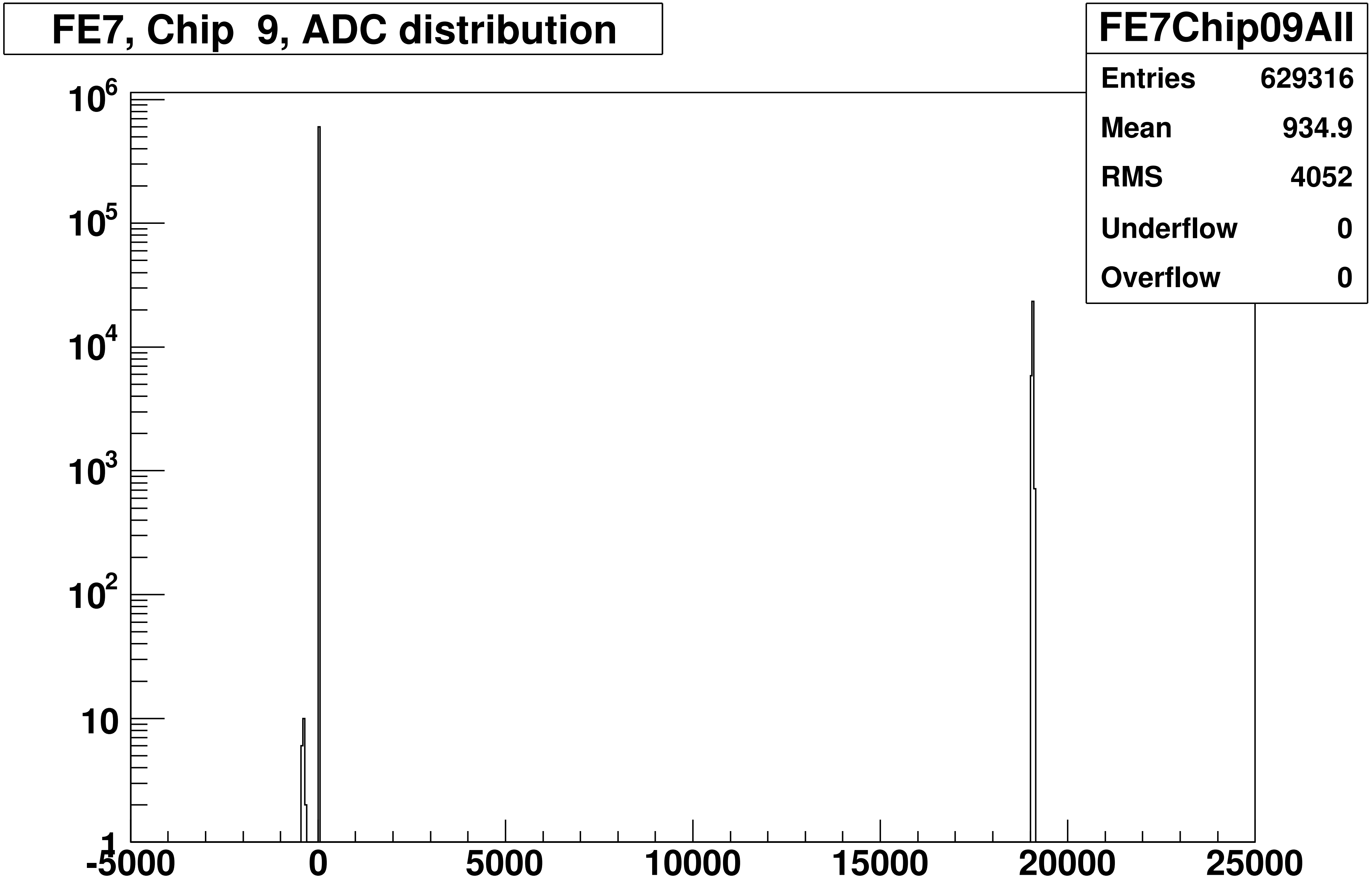


FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 629316
Mean 1.912e+04
RMS 4.624
Underflow 6.006e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

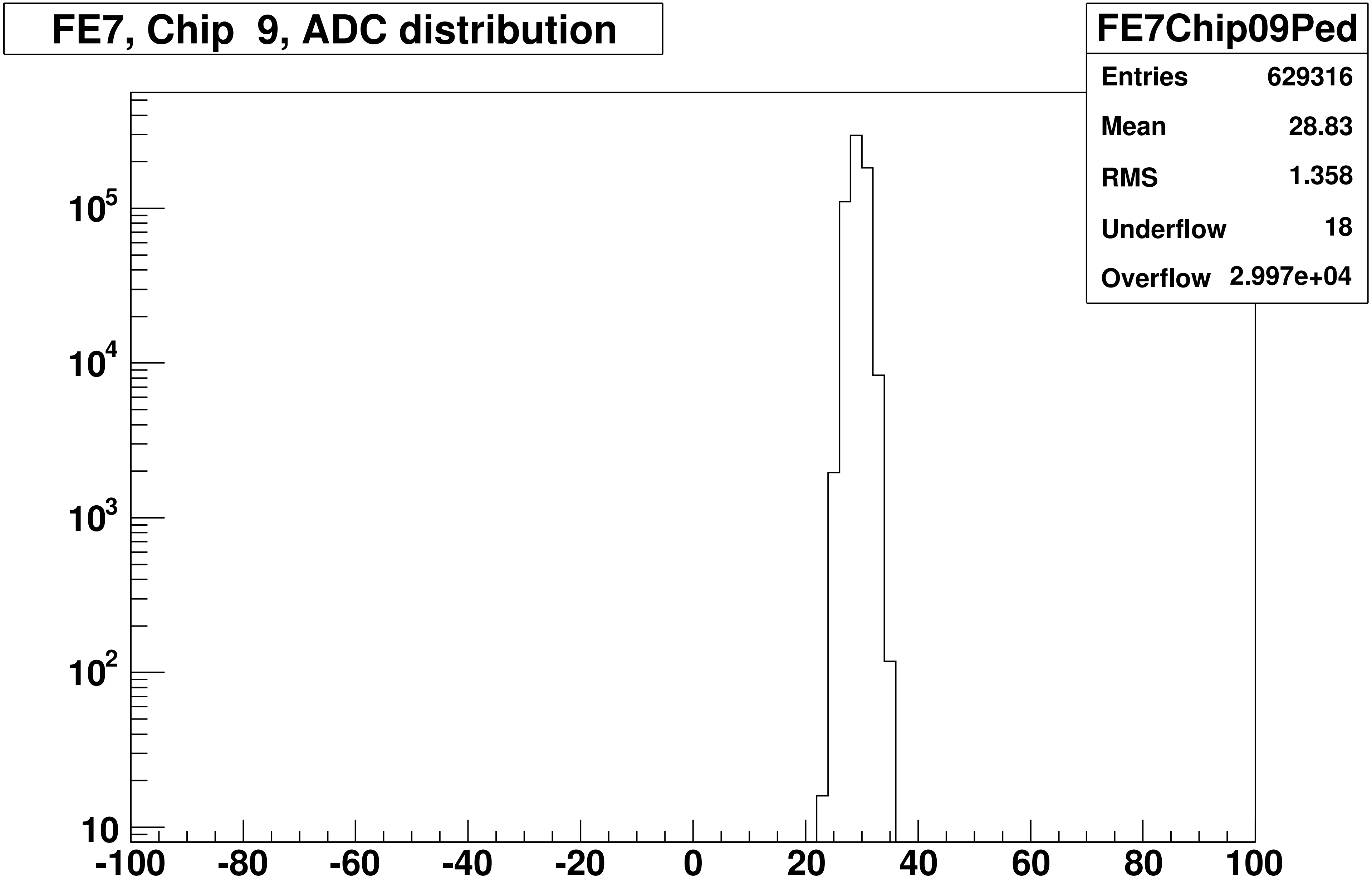

FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 629316
Mean 905
RMS 4043
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 629316
Mean 12.18
RMS 1.316
Underflow 36
Overflow 2.941e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

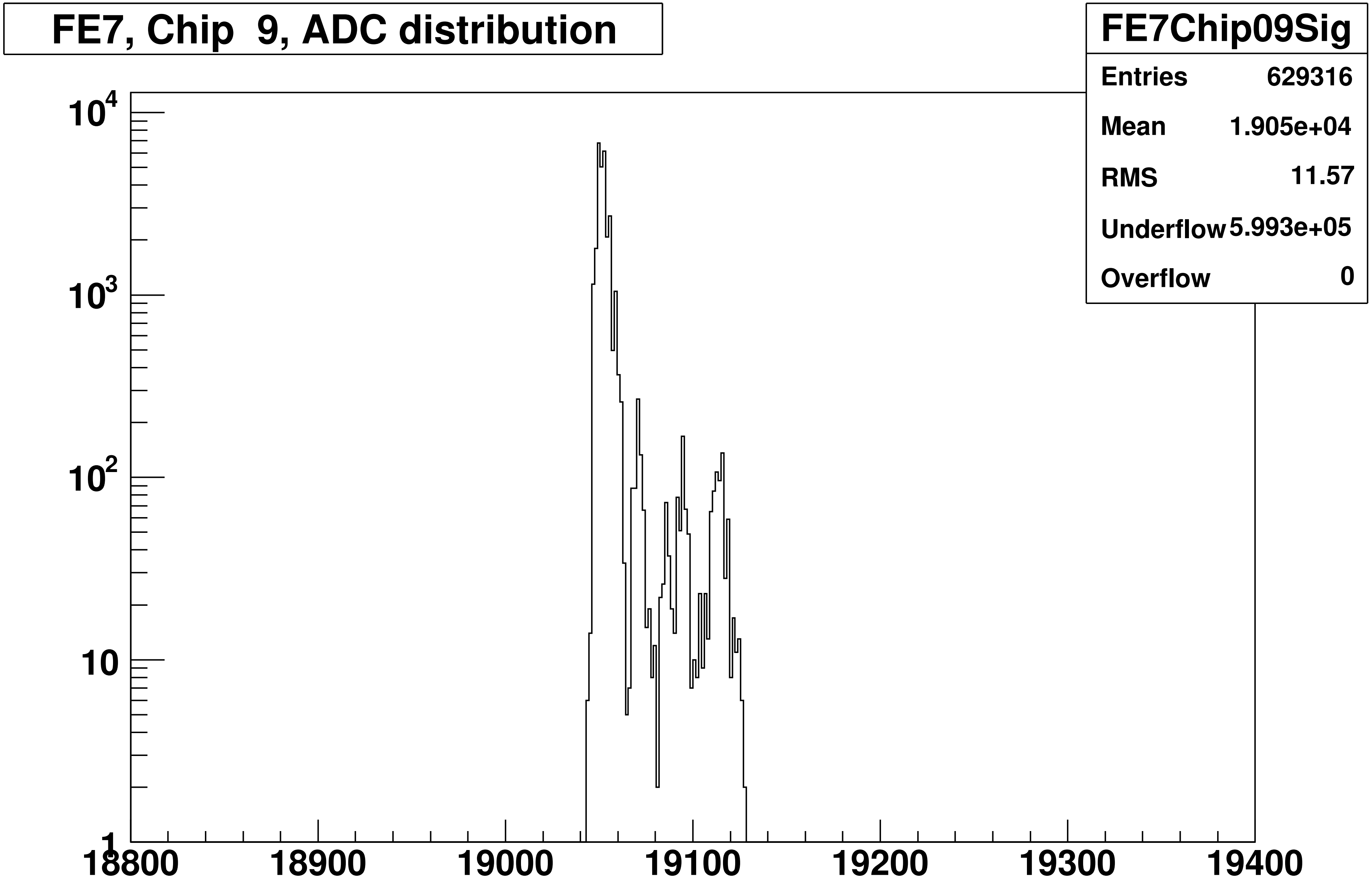

FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 629316
Mean 1.922e+04
RMS 6.032
Underflow 6.001e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



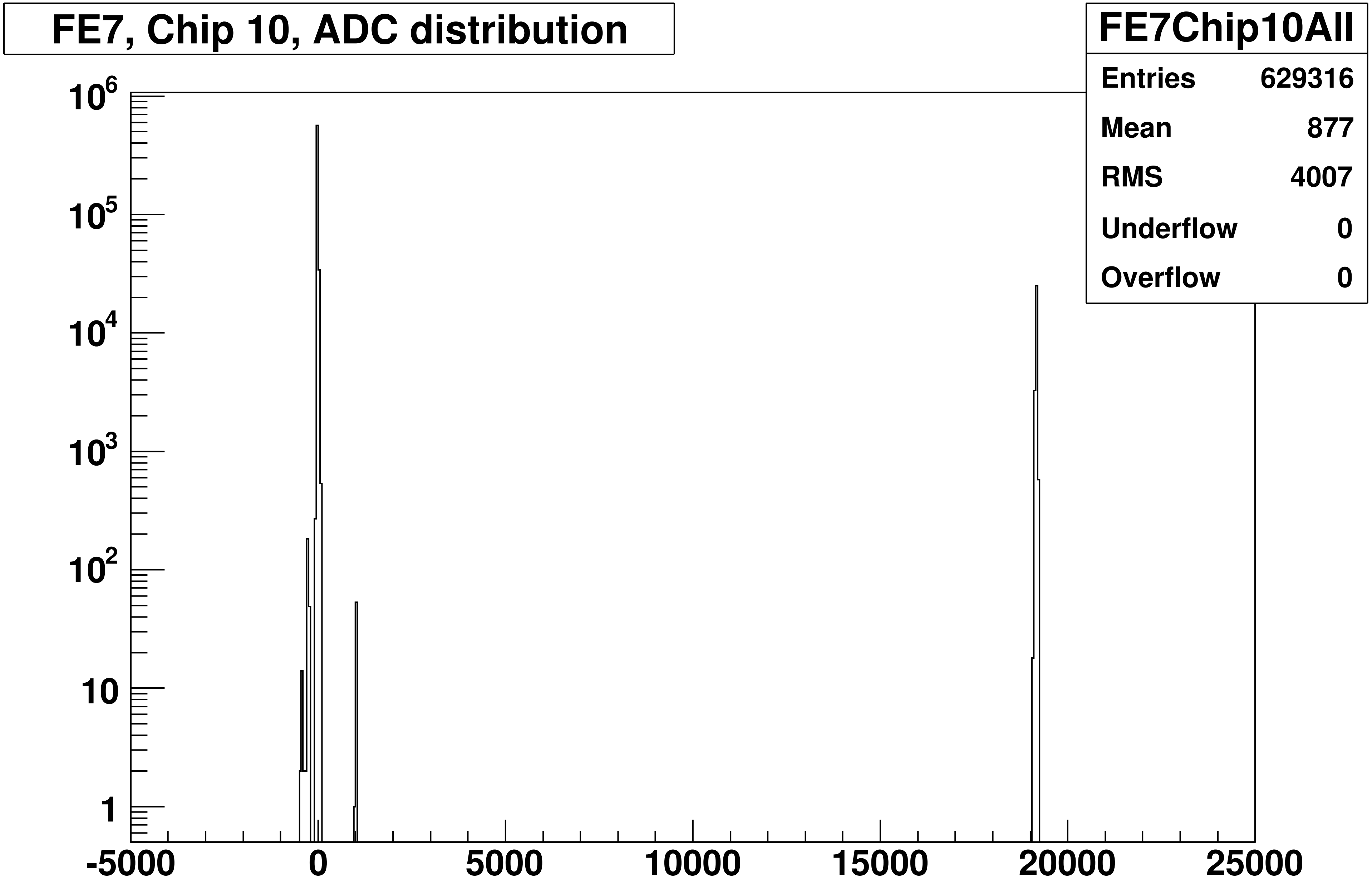
FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 629316
Mean 934.9
RMS 4052
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



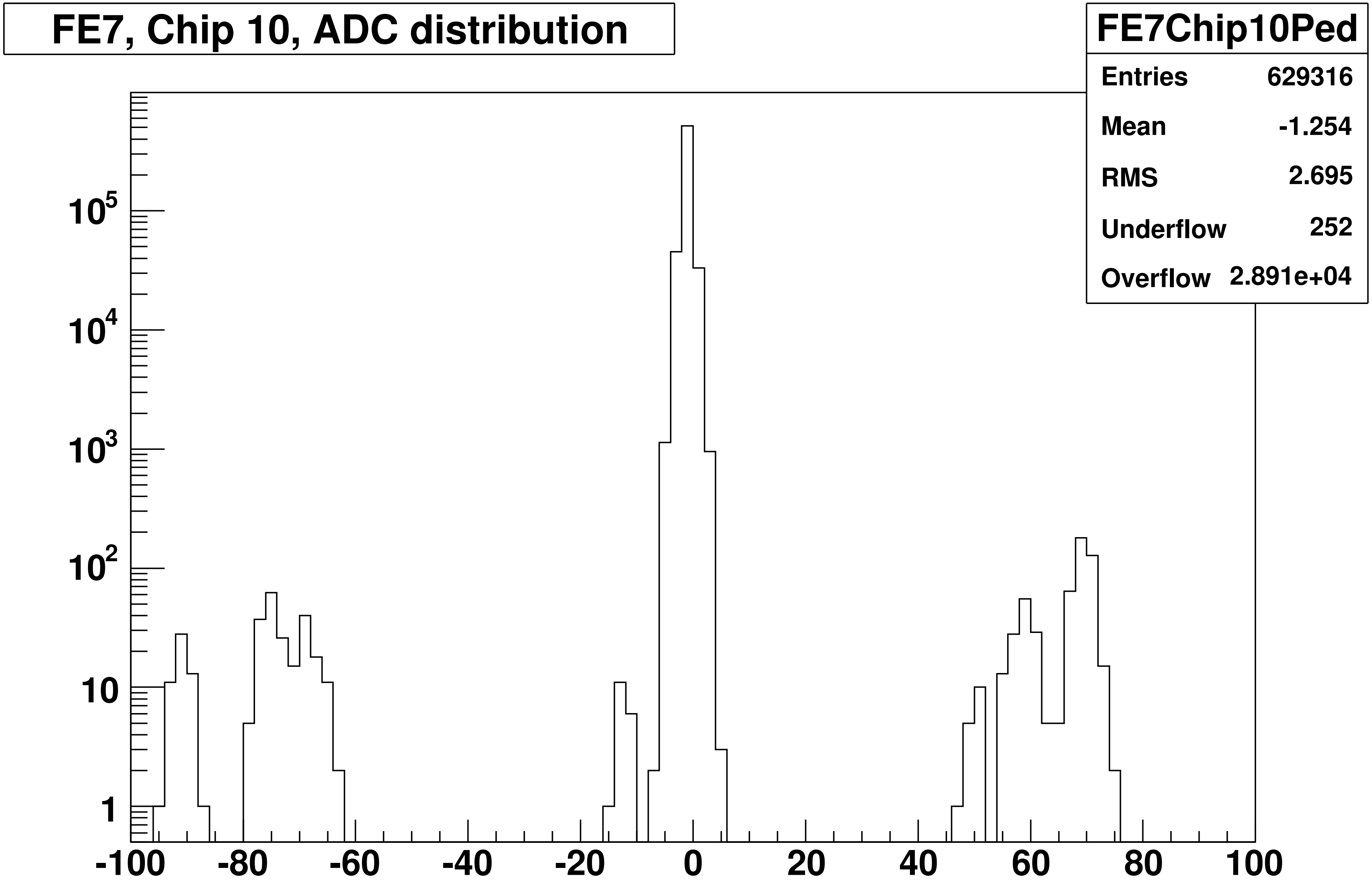
FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 629316
Mean 28.83
RMS 1.358
Underflow 18
Overflow 2.997e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

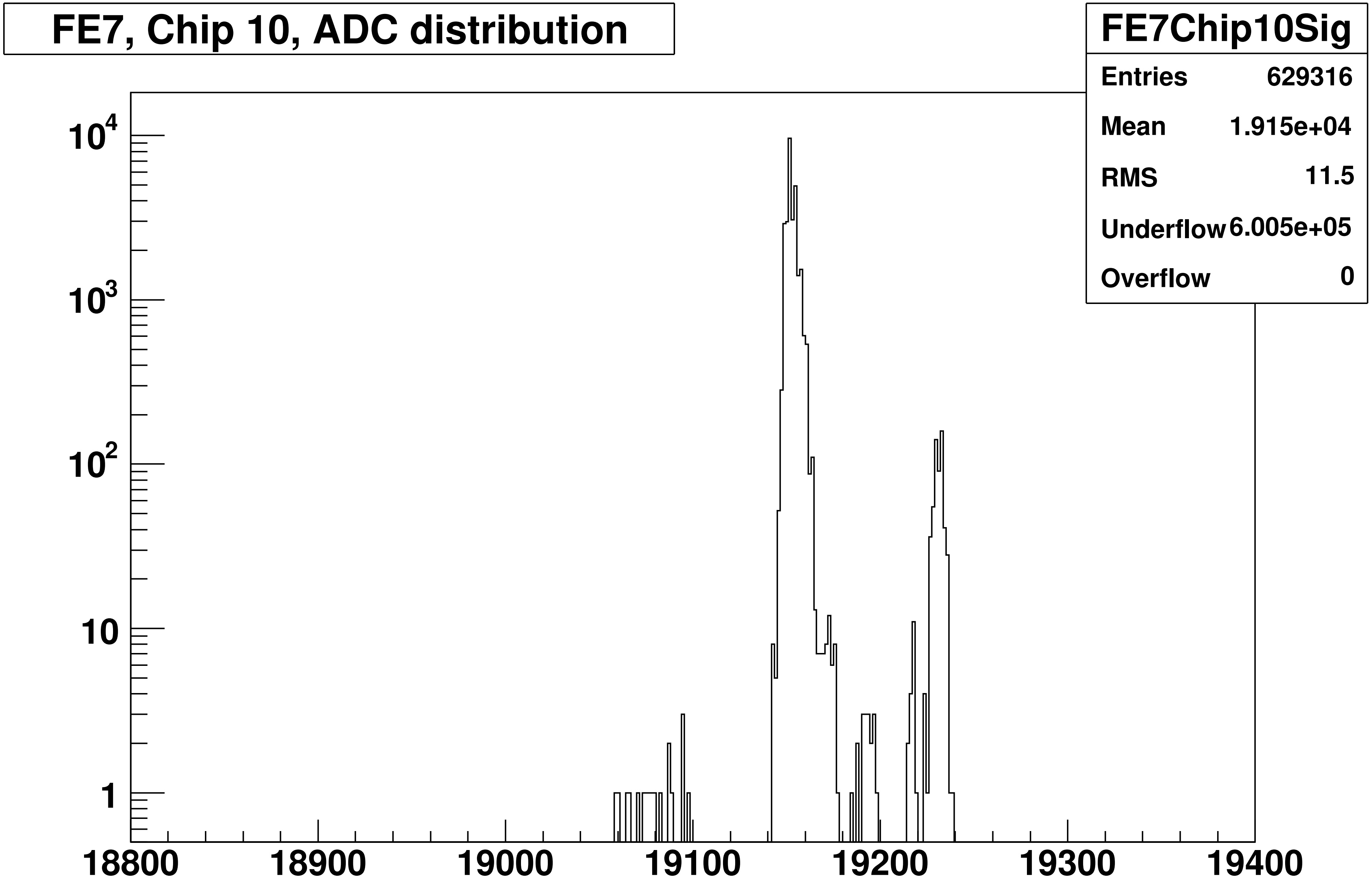


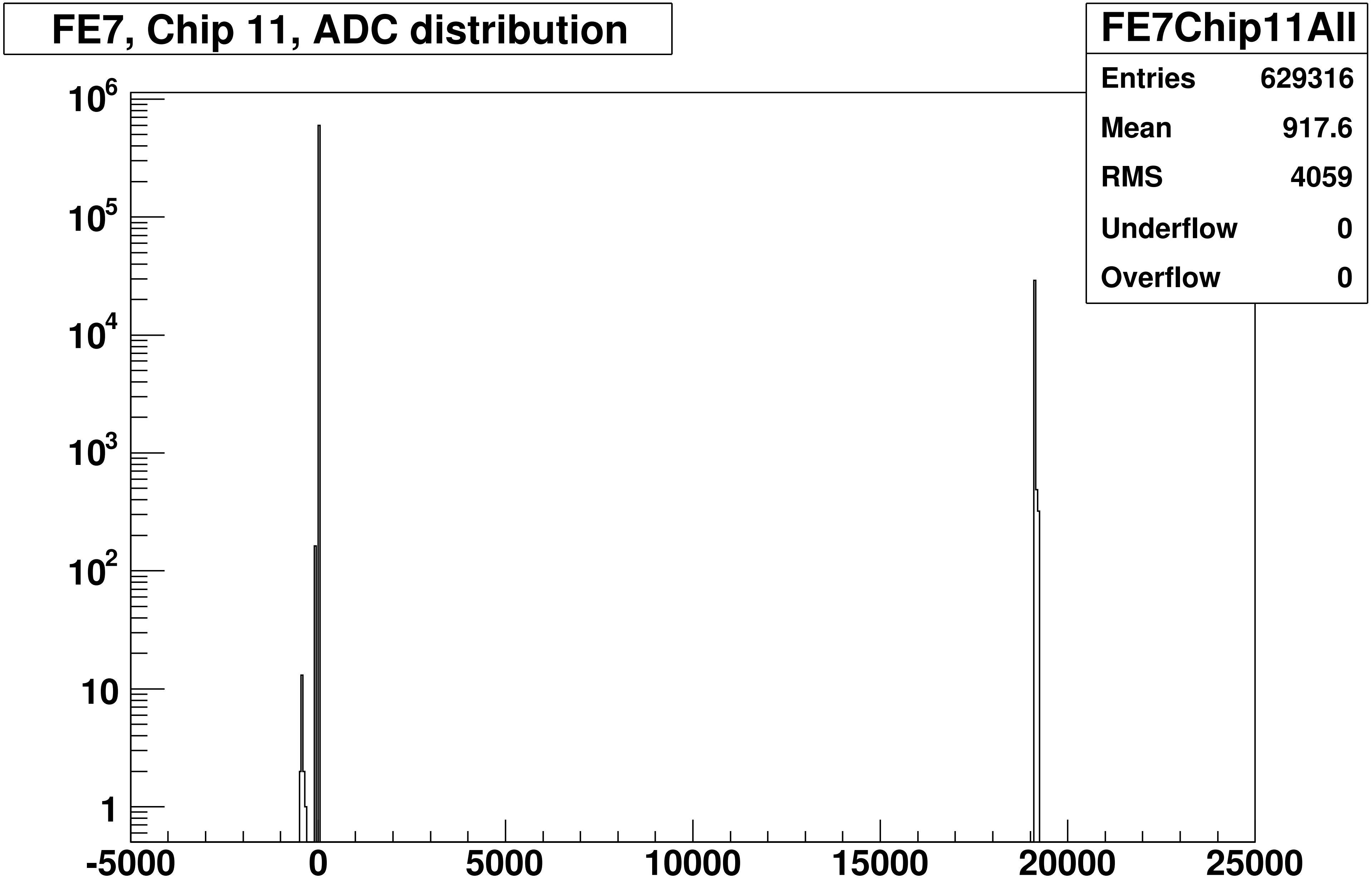
FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 629316
Mean 1.905e+04
RMS 11.57
Underflow 5.993e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

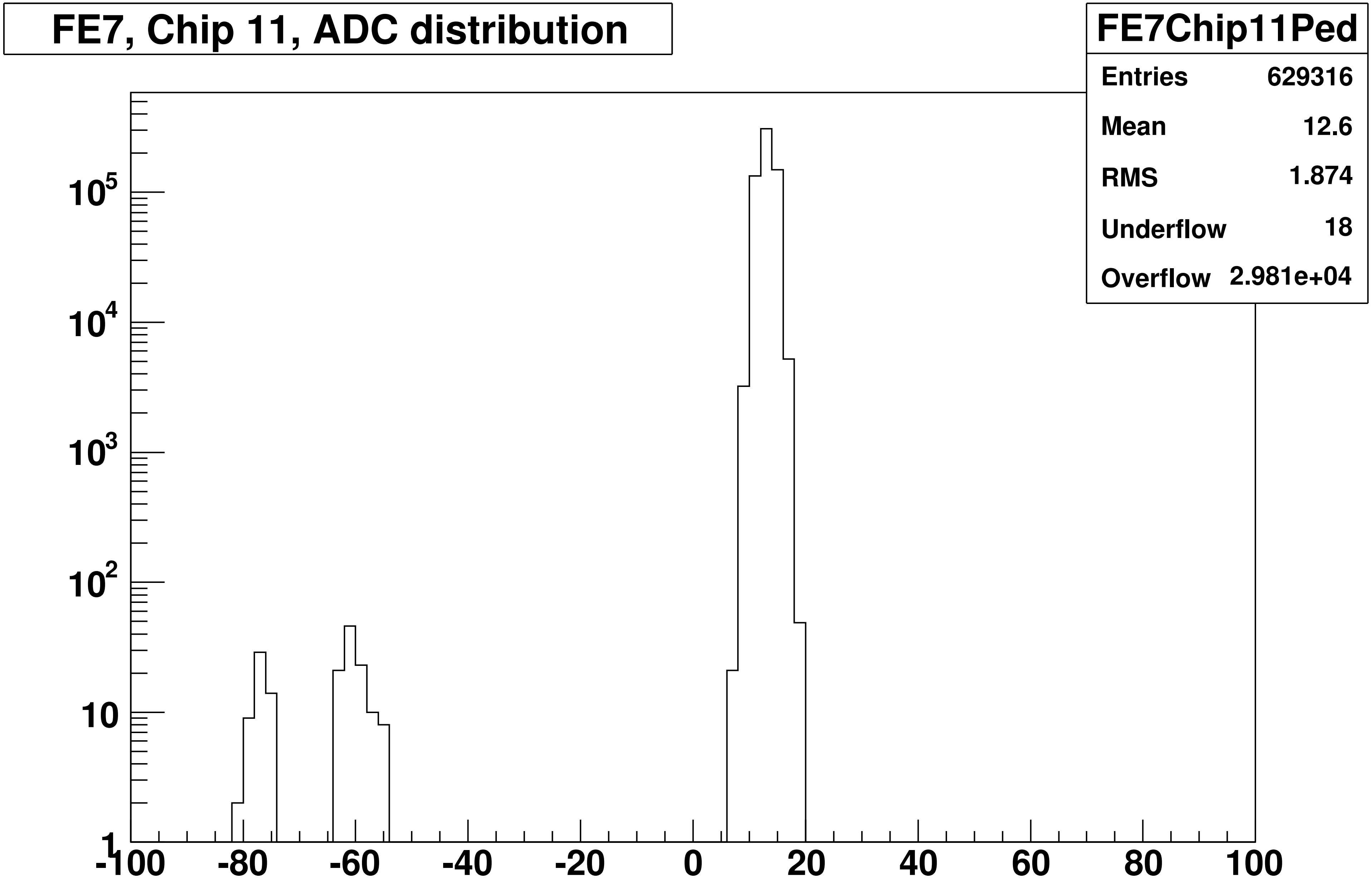


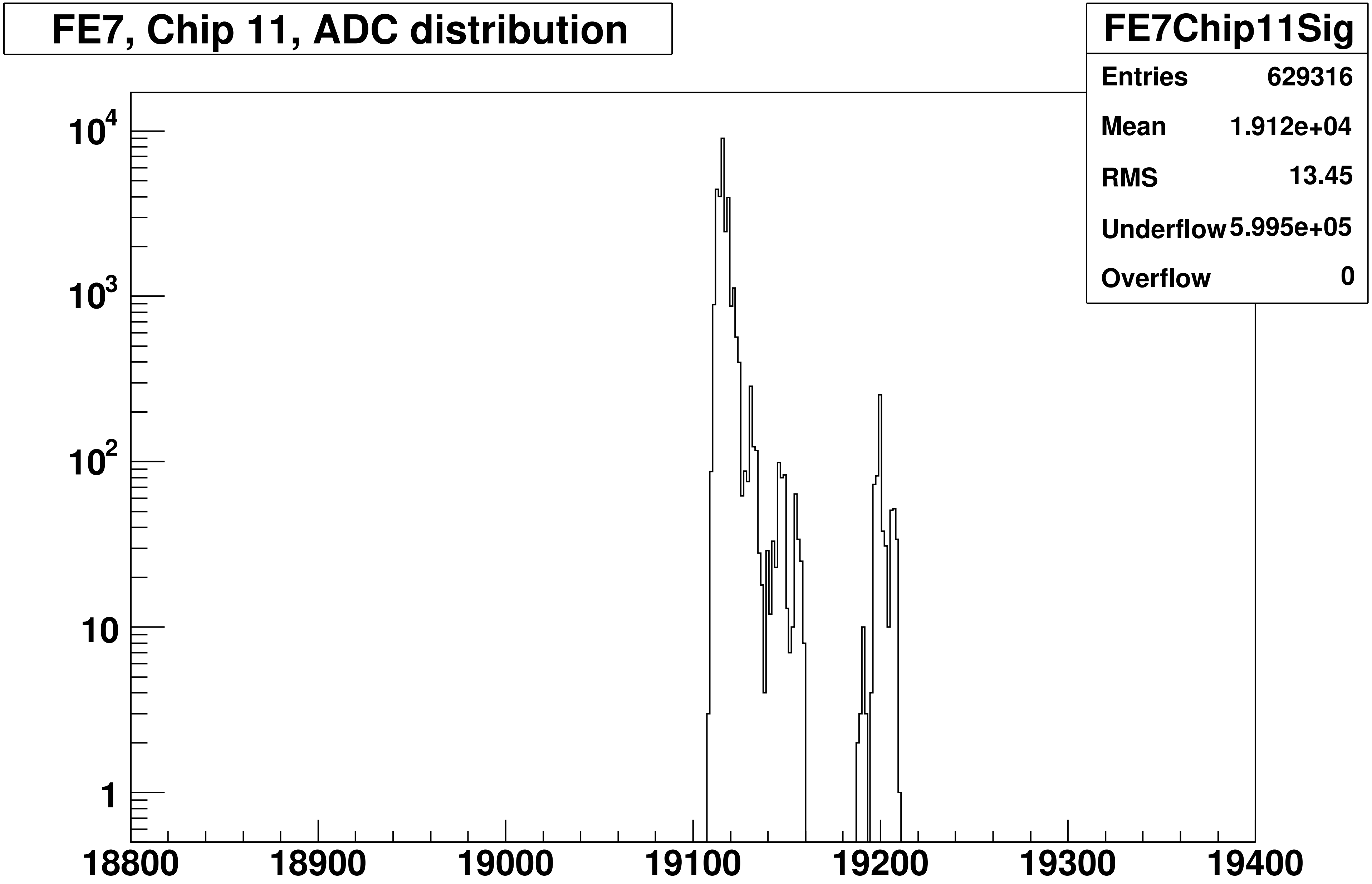
FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 629316
Mean 877
RMS 4007
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

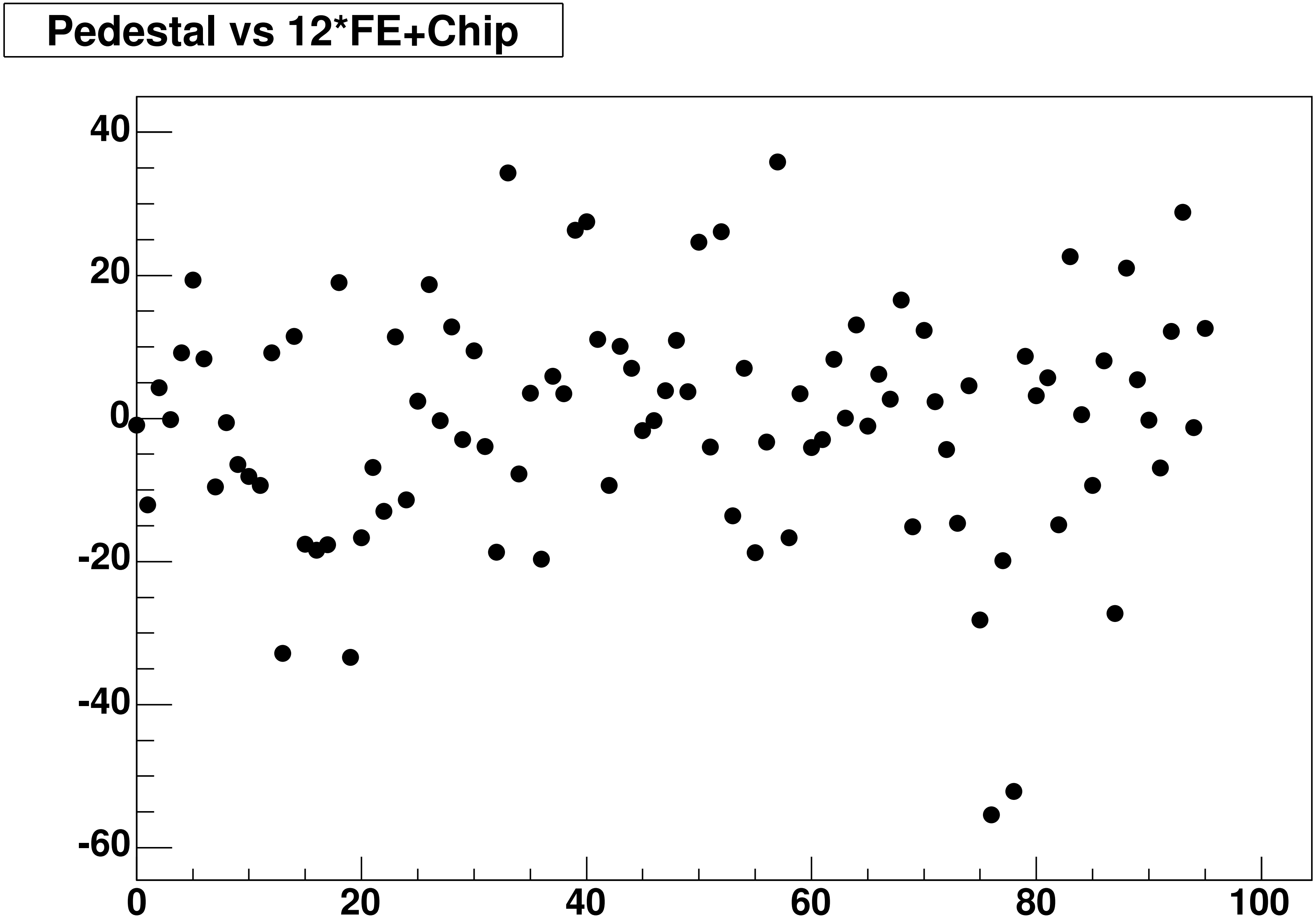

FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 629316
Mean -1.254
RMS 2.695
Underflow 252
Overflow 2.891e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

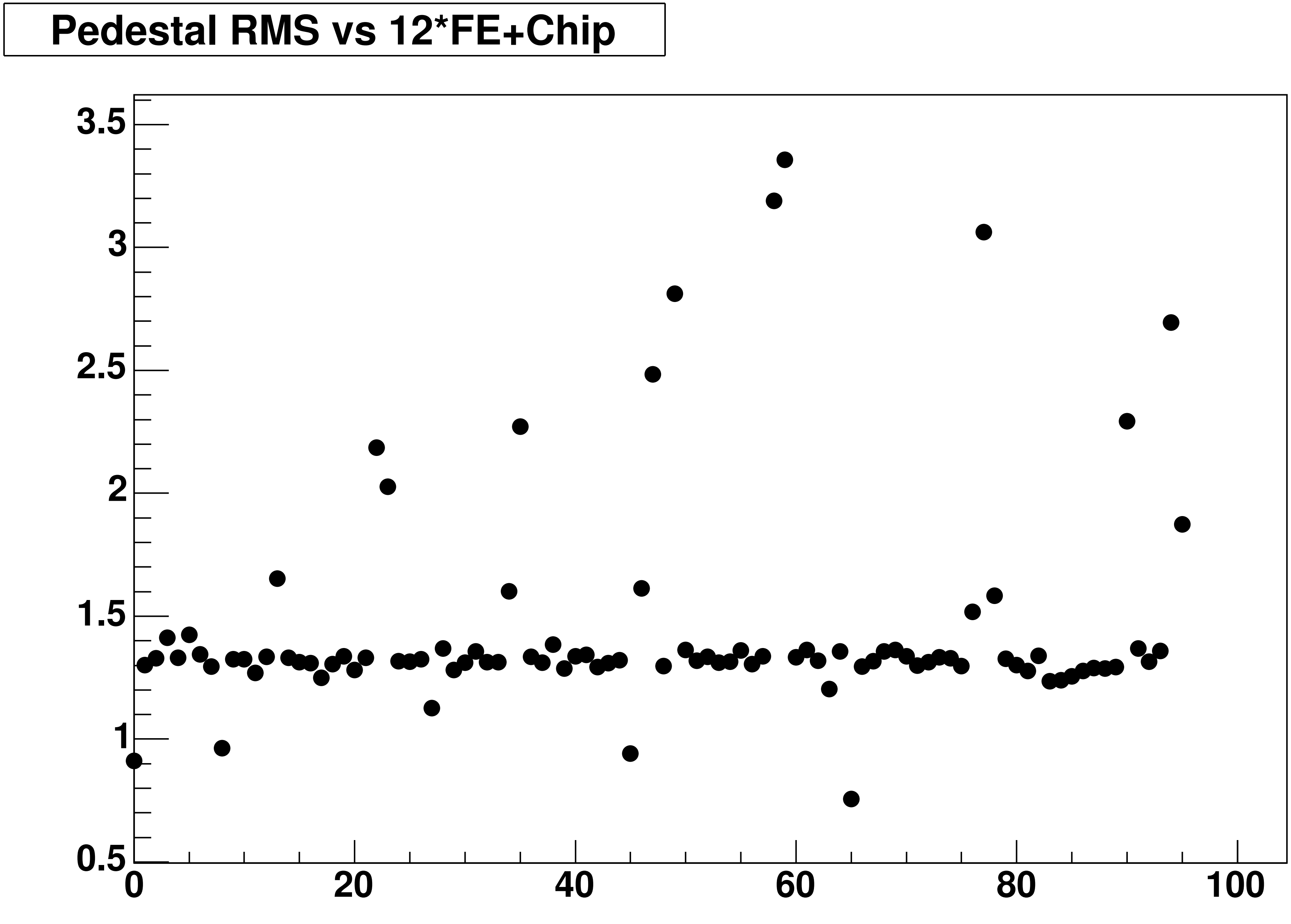

FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 629316
Mean 1.915e+04
RMS 11.5
Underflow 6.005e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 629316
Mean 917.6
RMS 4059
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

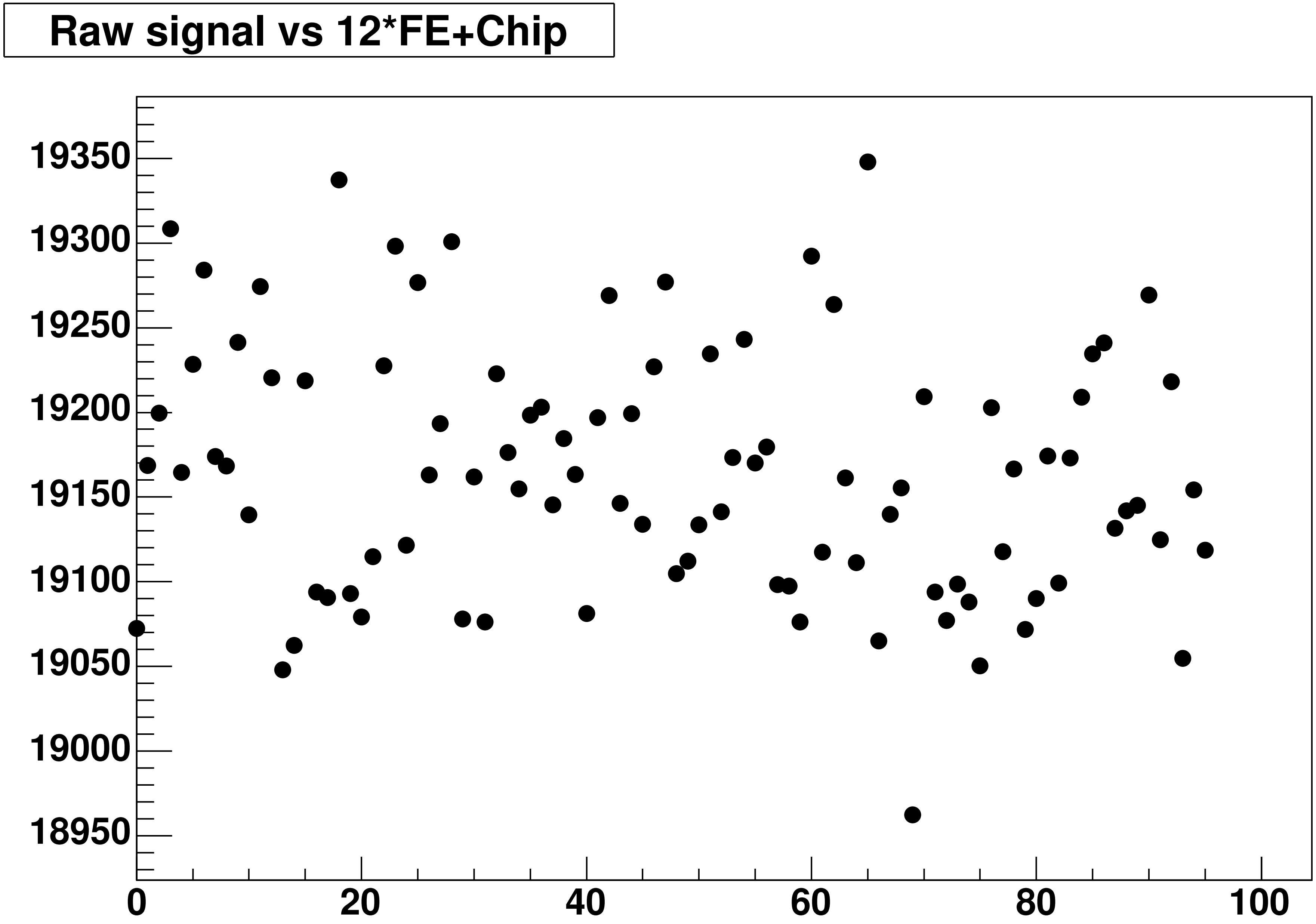

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 629316
Mean 12.6
RMS 1.874
Underflow 18
Overflow 2.981e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

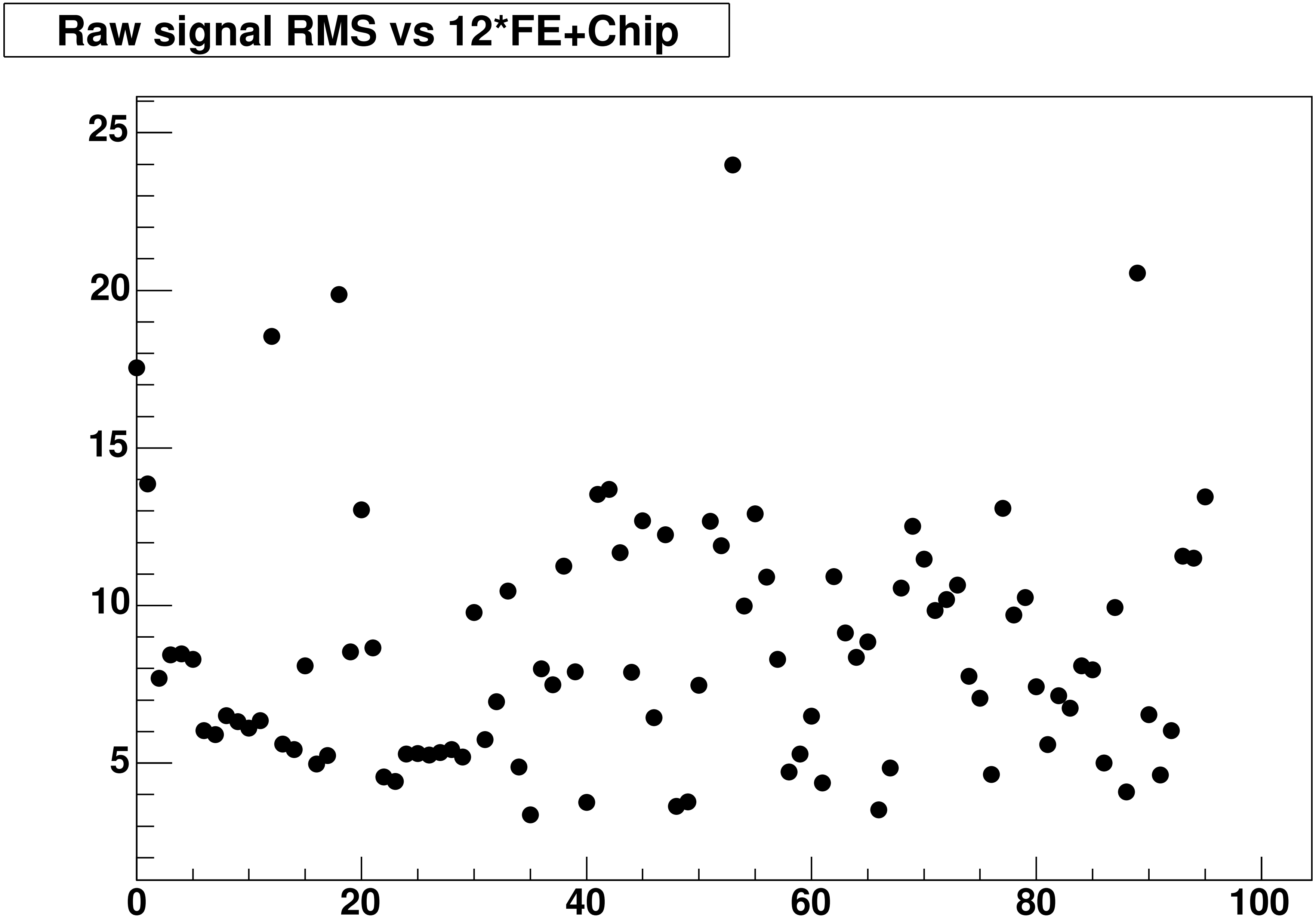

FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 629316
Mean 1.912e+04
RMS 13.45
Underflow 5.995e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

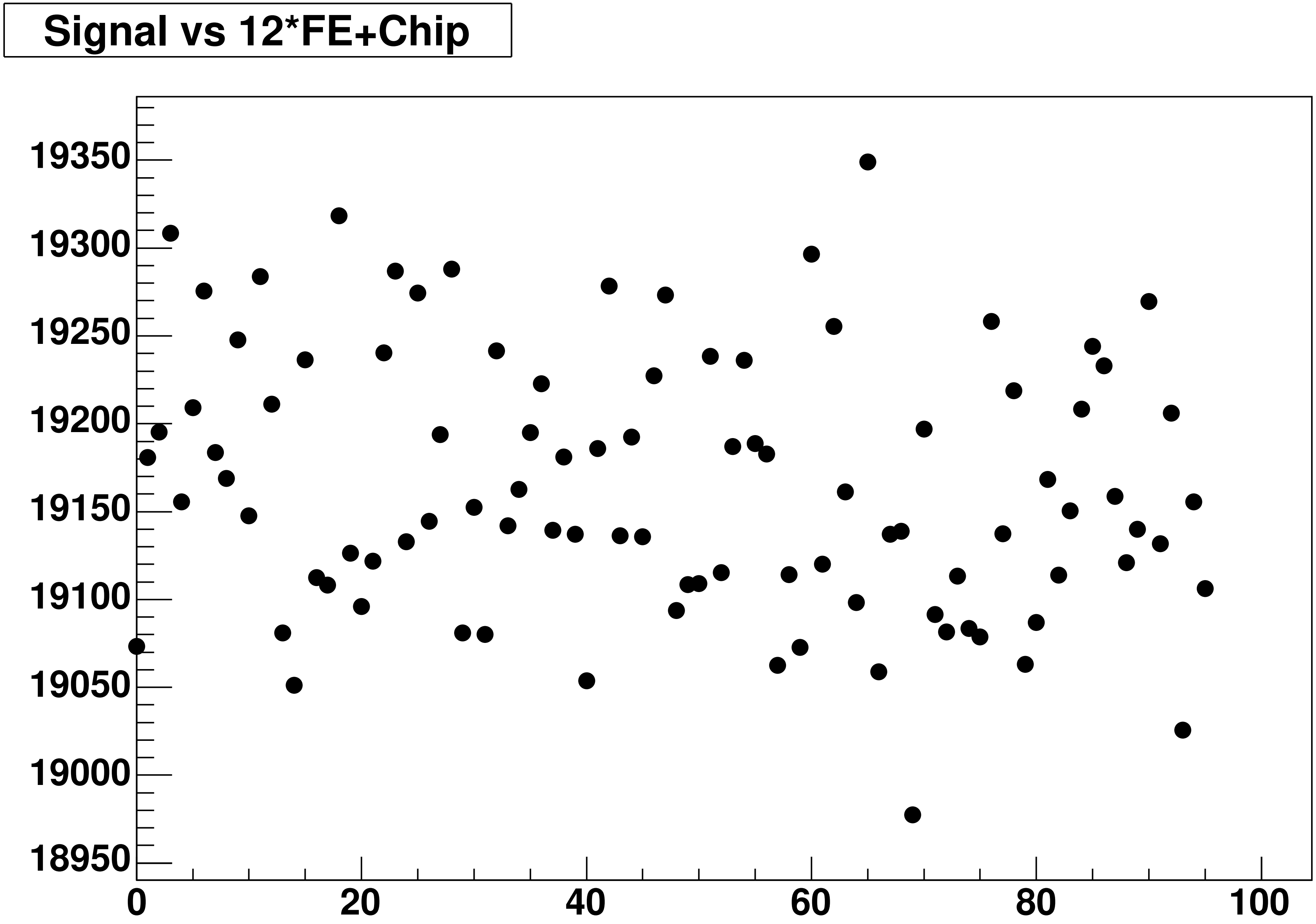

Pedestal vs 12*FE+Chip
40
20
0
-20
-40
-60
0
20
40
60
80
100

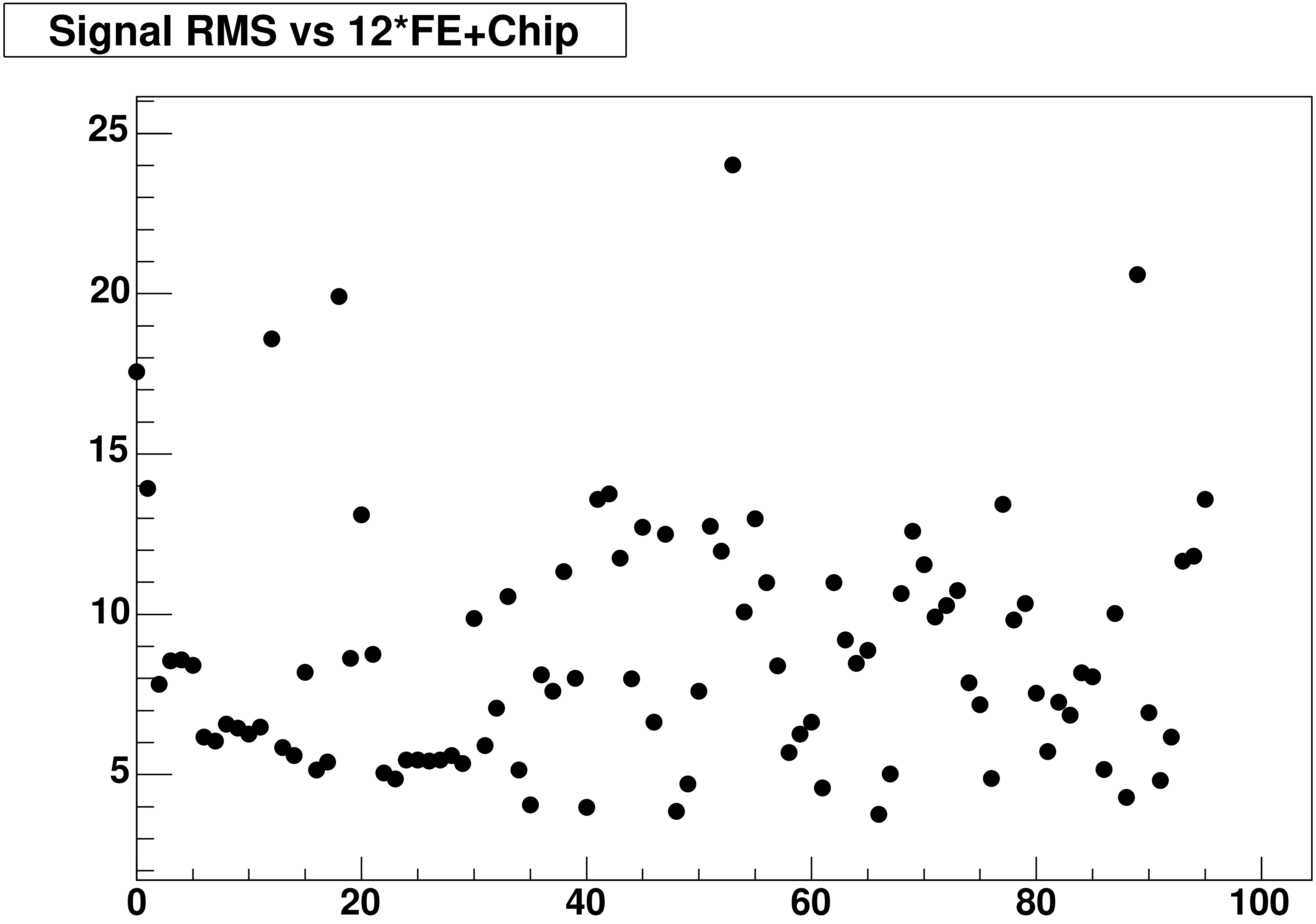


Pedestal RMS vs 12*FE+Chip
3.5
3
2.5
2
1.5
1
0.5
0
20
40
60
80
100


Raw signal vs 12*FE+Chip
19350
19300
19250
19200
19150
19100
19050
19000
18950
0
20
40
60
80
100


Raw signal RMS vs 12*FE+Chip
25
20
15
10
5
0
20
40
60
80
100


Signal vs 12*FE+Chip
19350
19300
19250
19200
19150
19100
19050
19000
18950
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
25
20
15
10
5
0
20
40
60
80
100