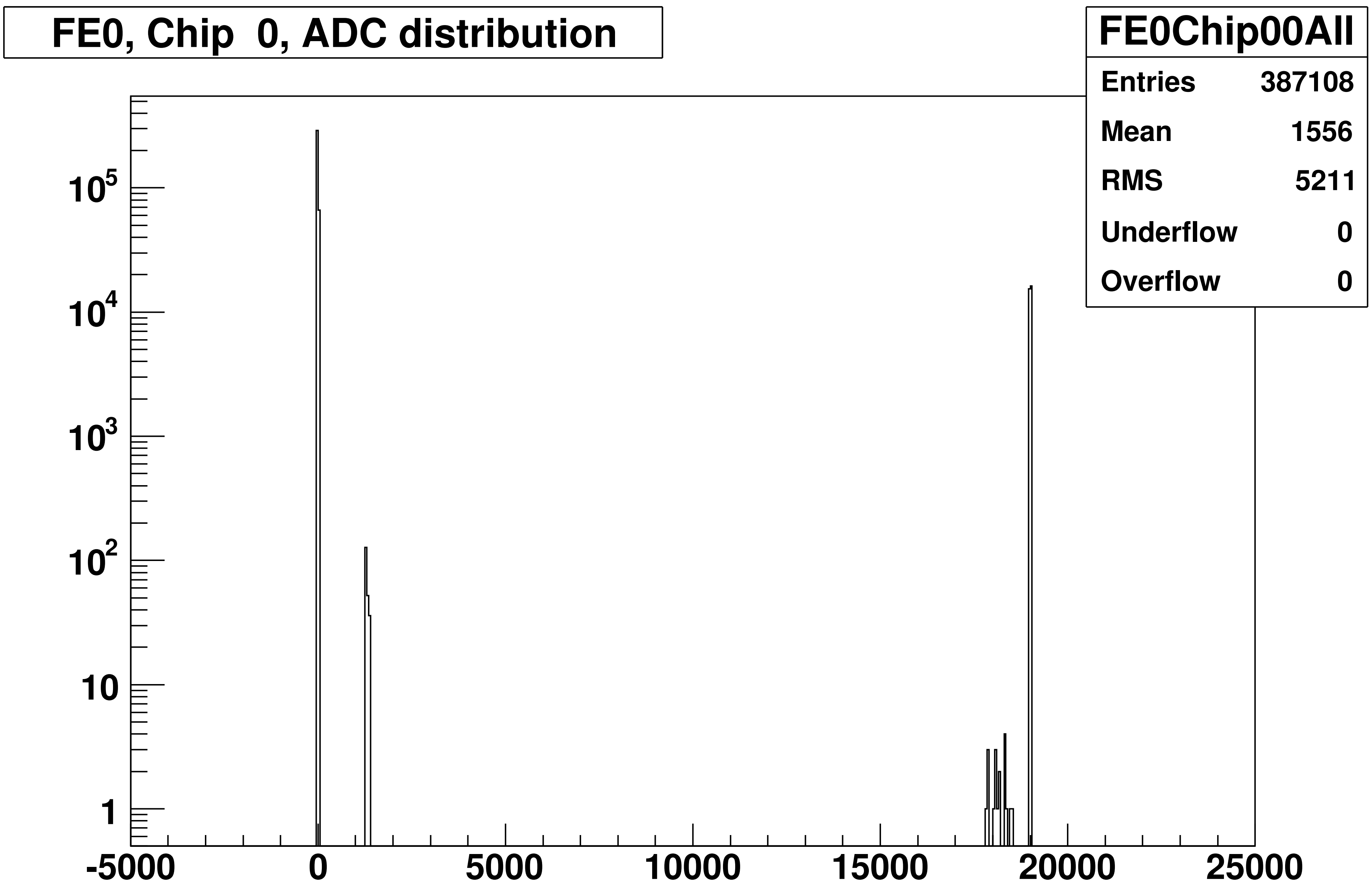
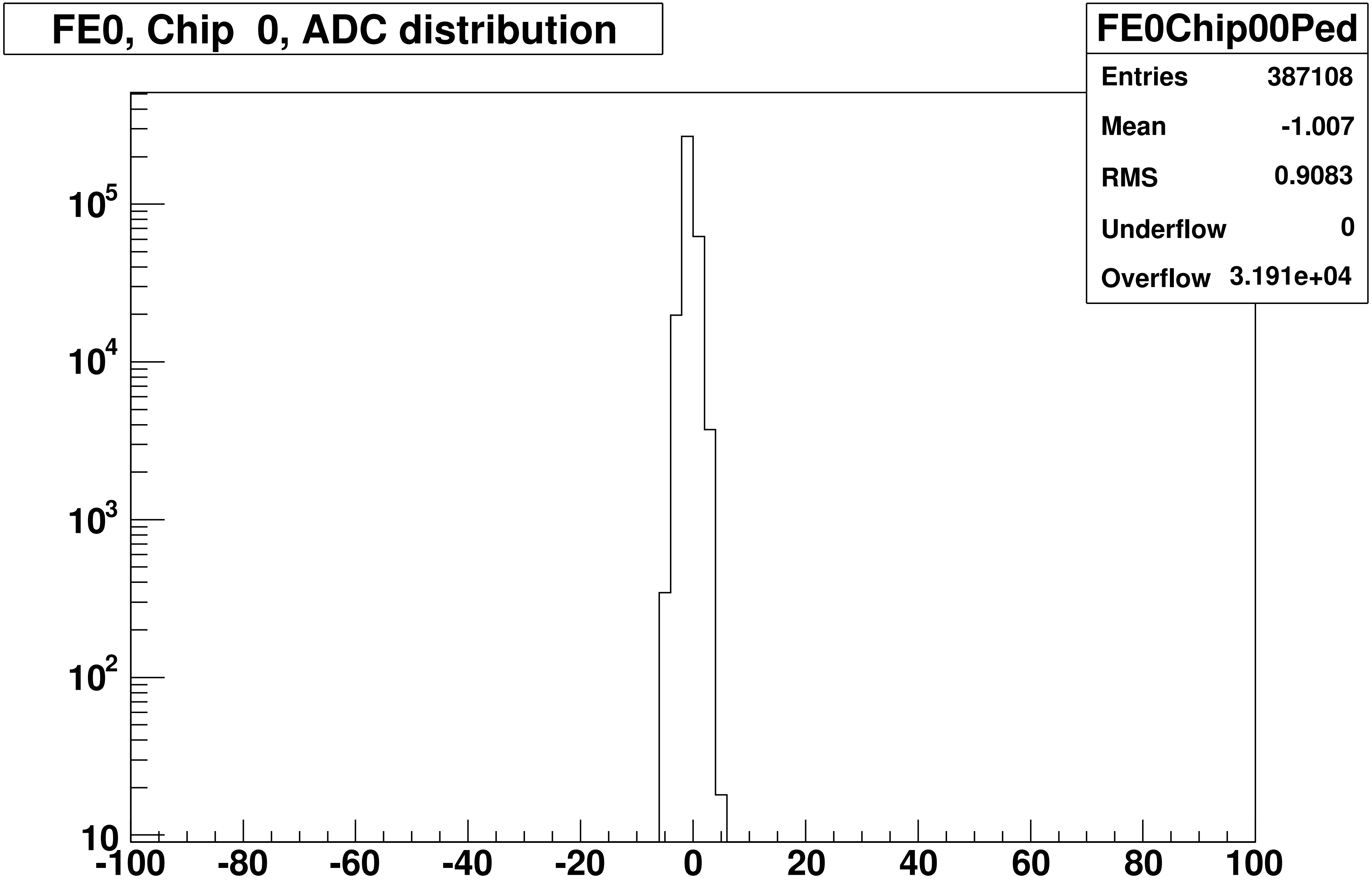
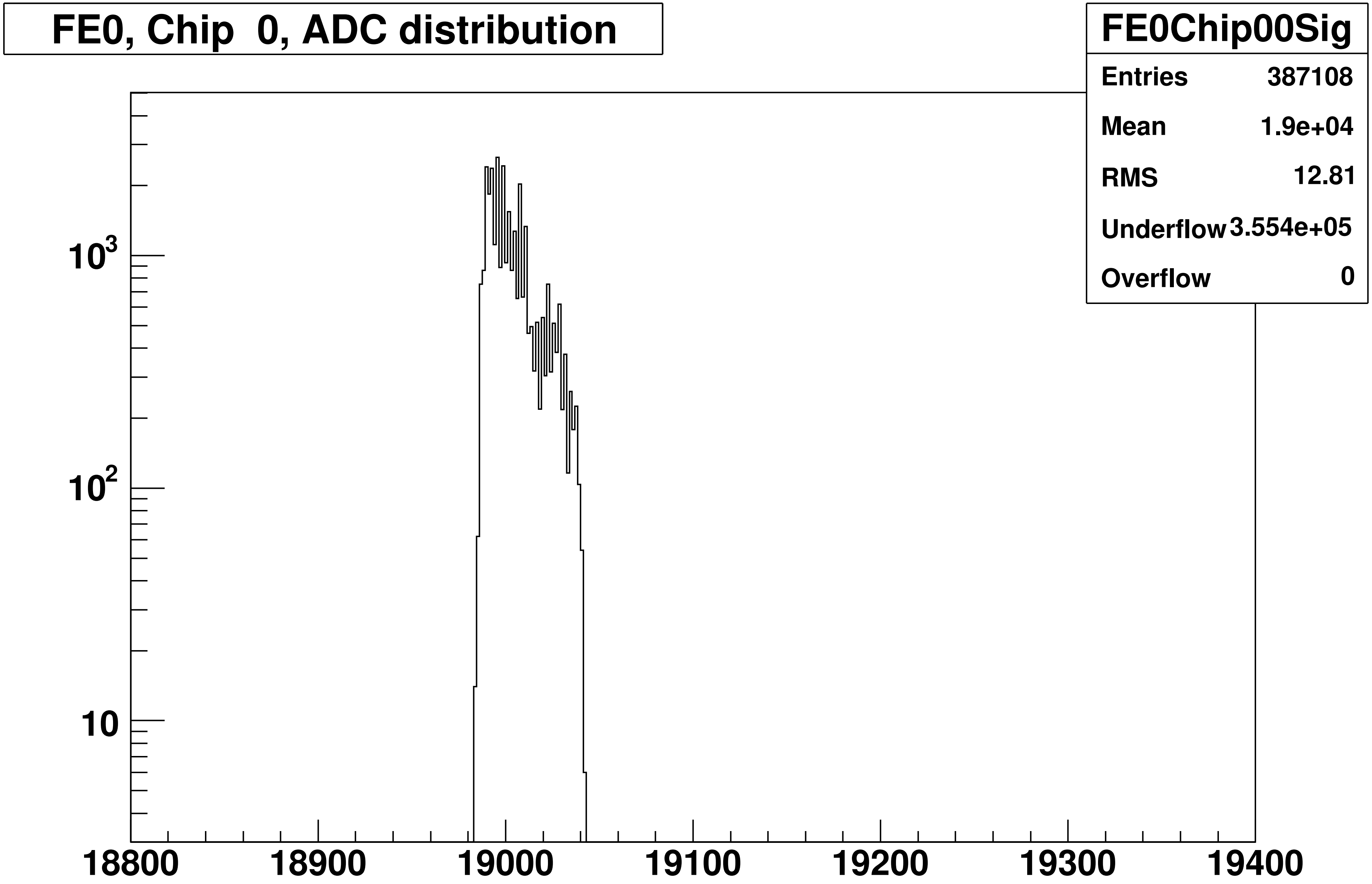


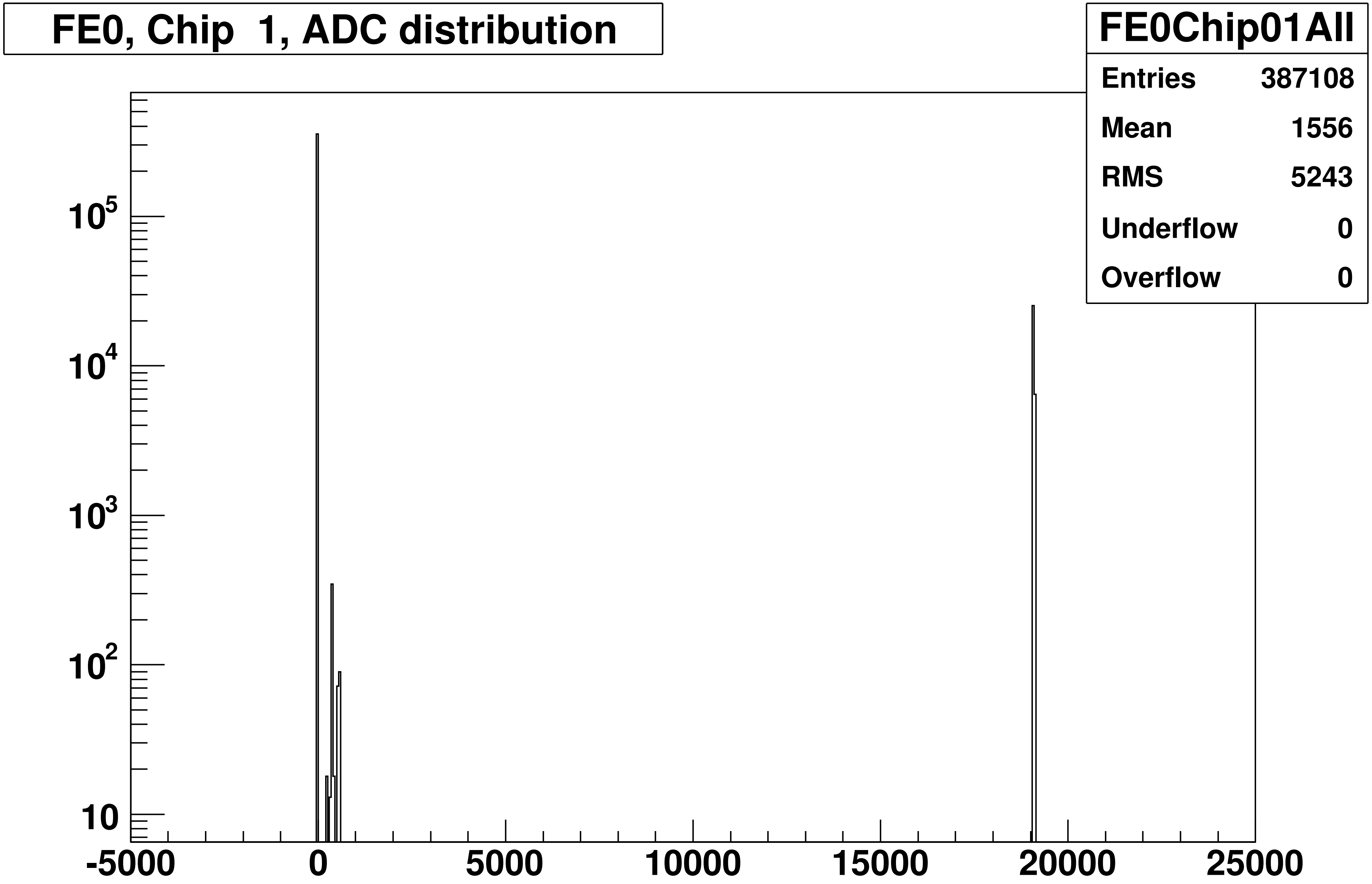
FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 387108
Mean 1556
RMS 5211
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

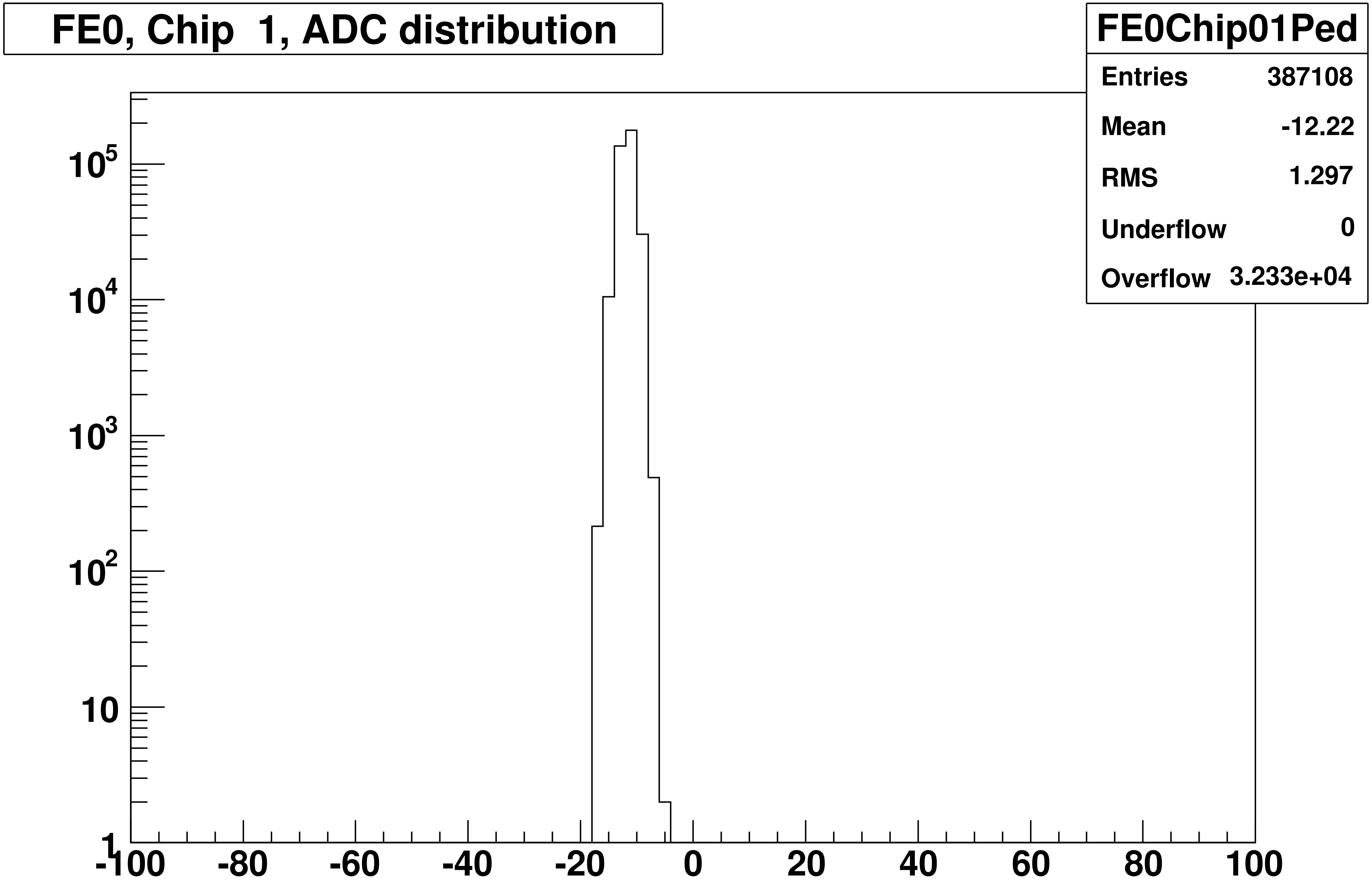


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 387108
Mean -1.007
RMS 0.9083
Underflow 0
Overflow 3.191e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

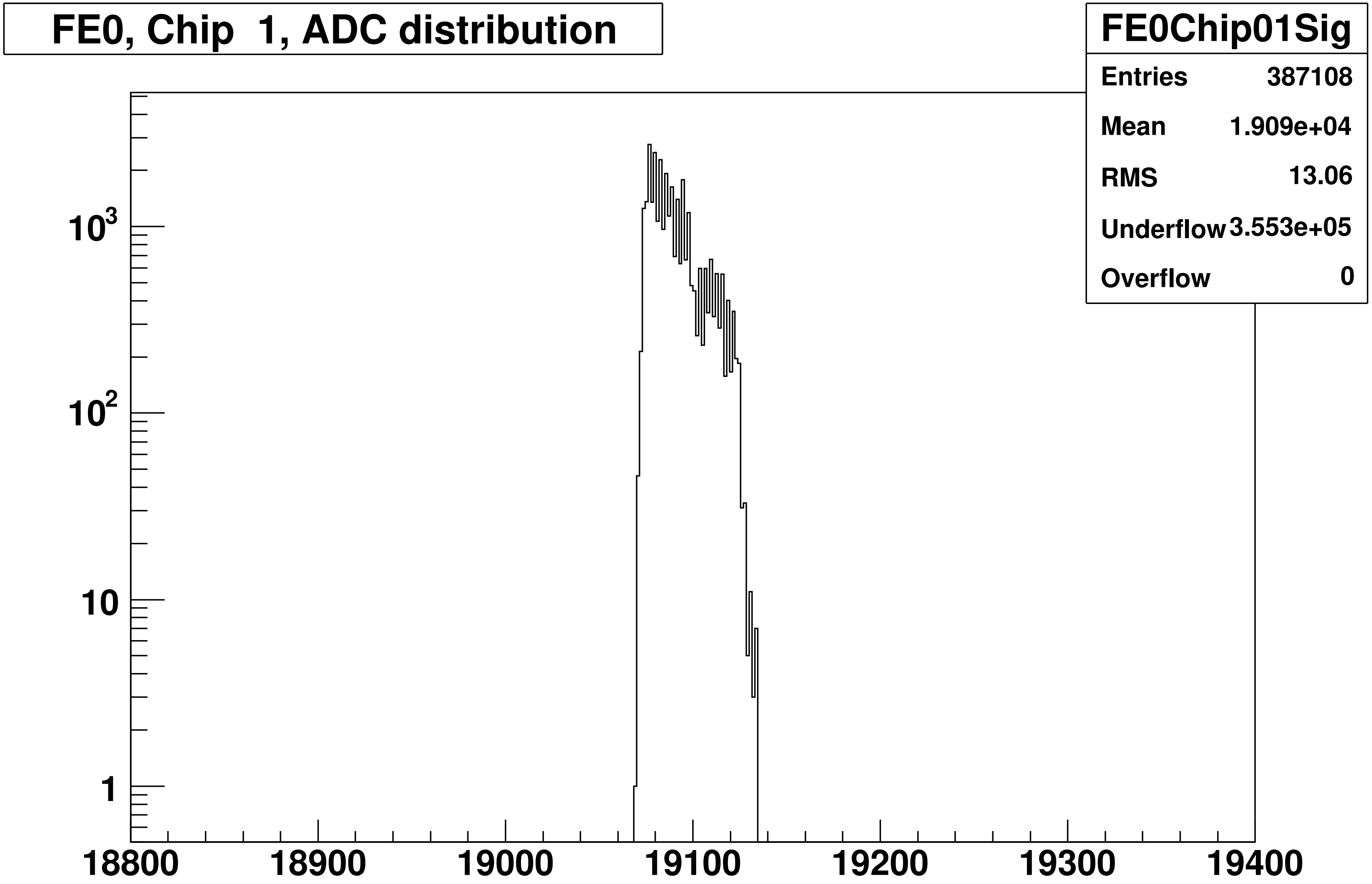


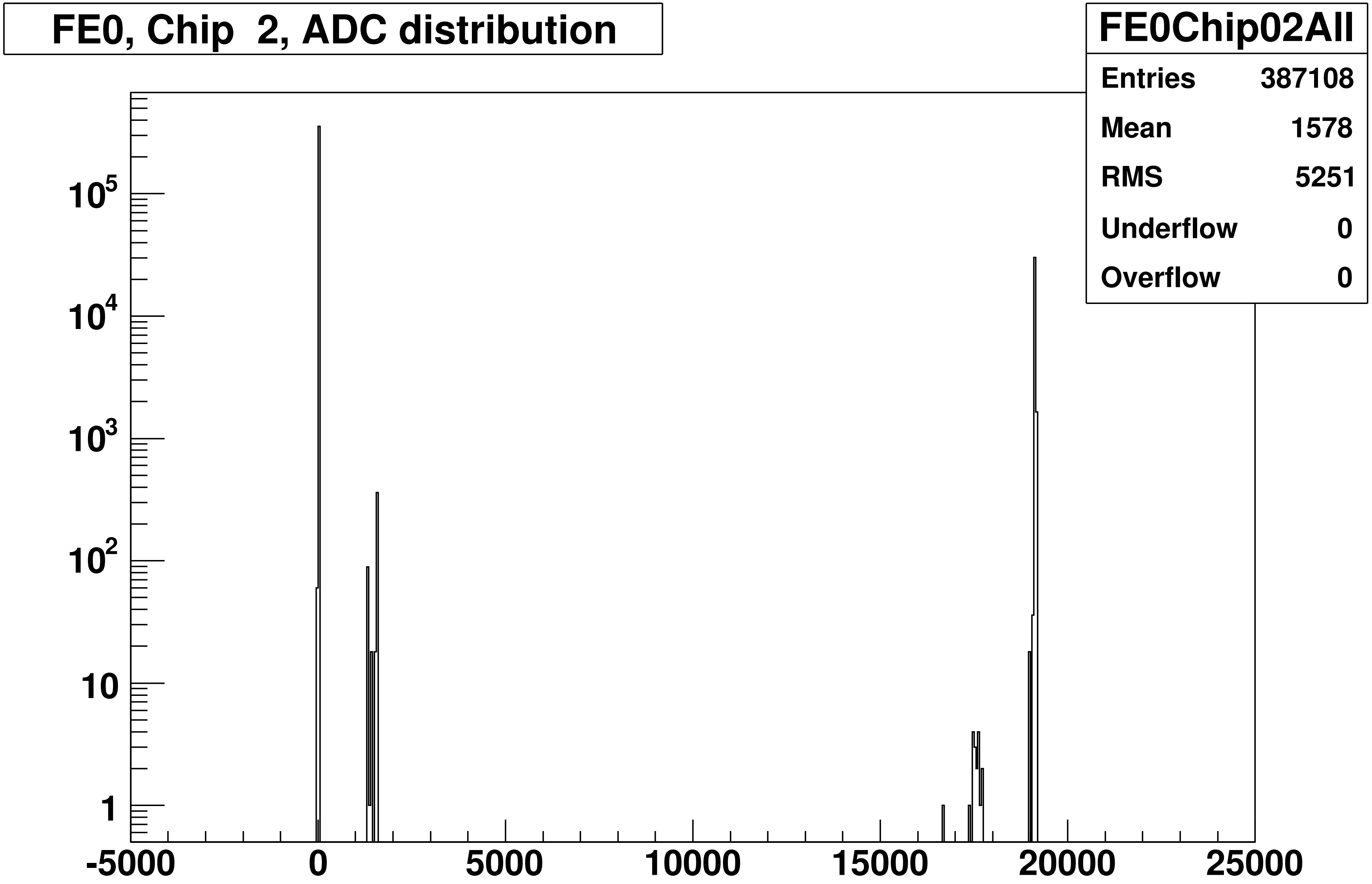
FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 387108
Mean 1.9e+04
RMS 12.81
Underflow 3.554e+05
Overflow 0
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 387108
Mean 1556
RMS 5243
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

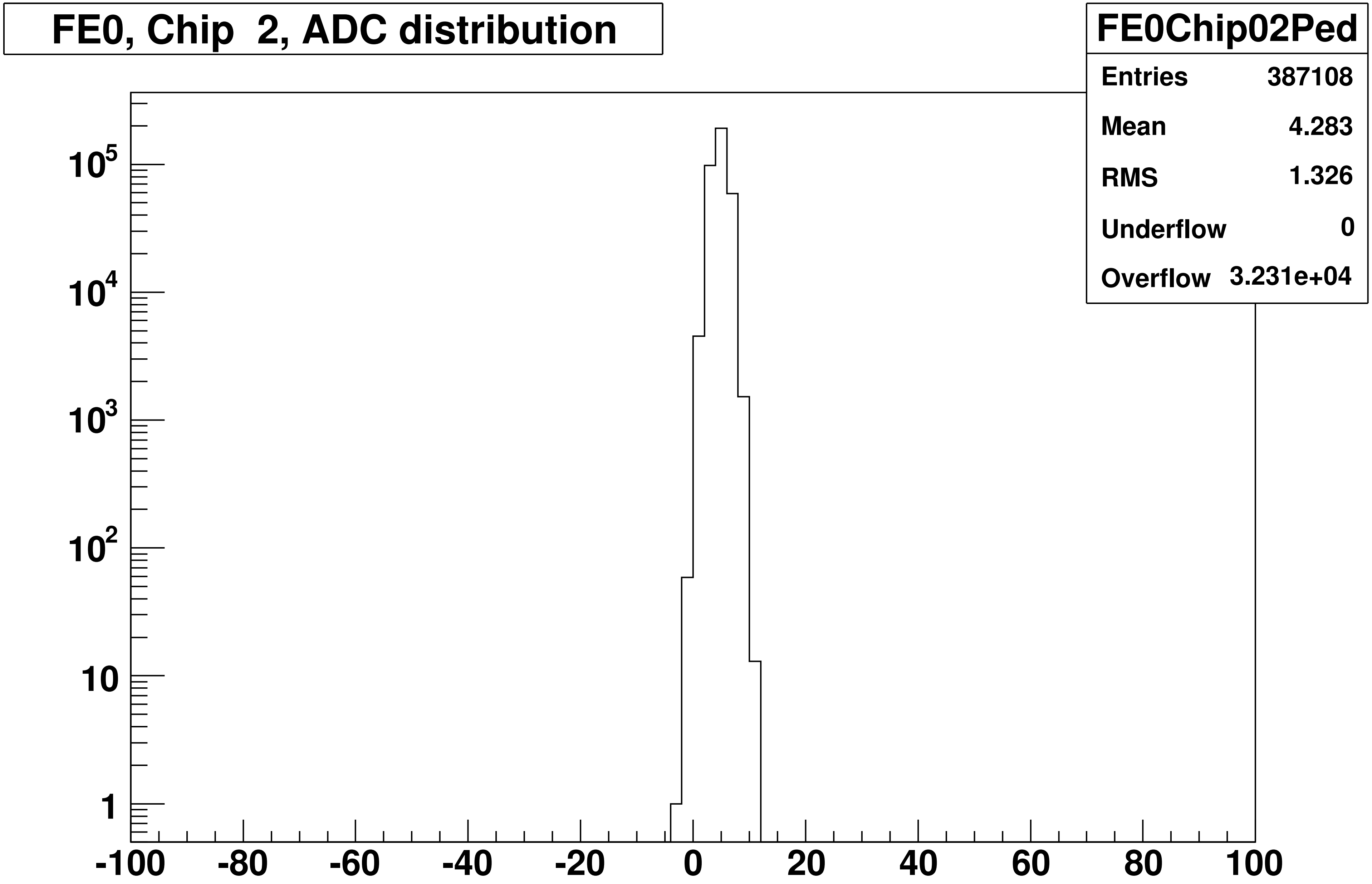


FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 387108
Mean -12.22
RMS 1.297
Underflow 0
Overflow 3.233e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

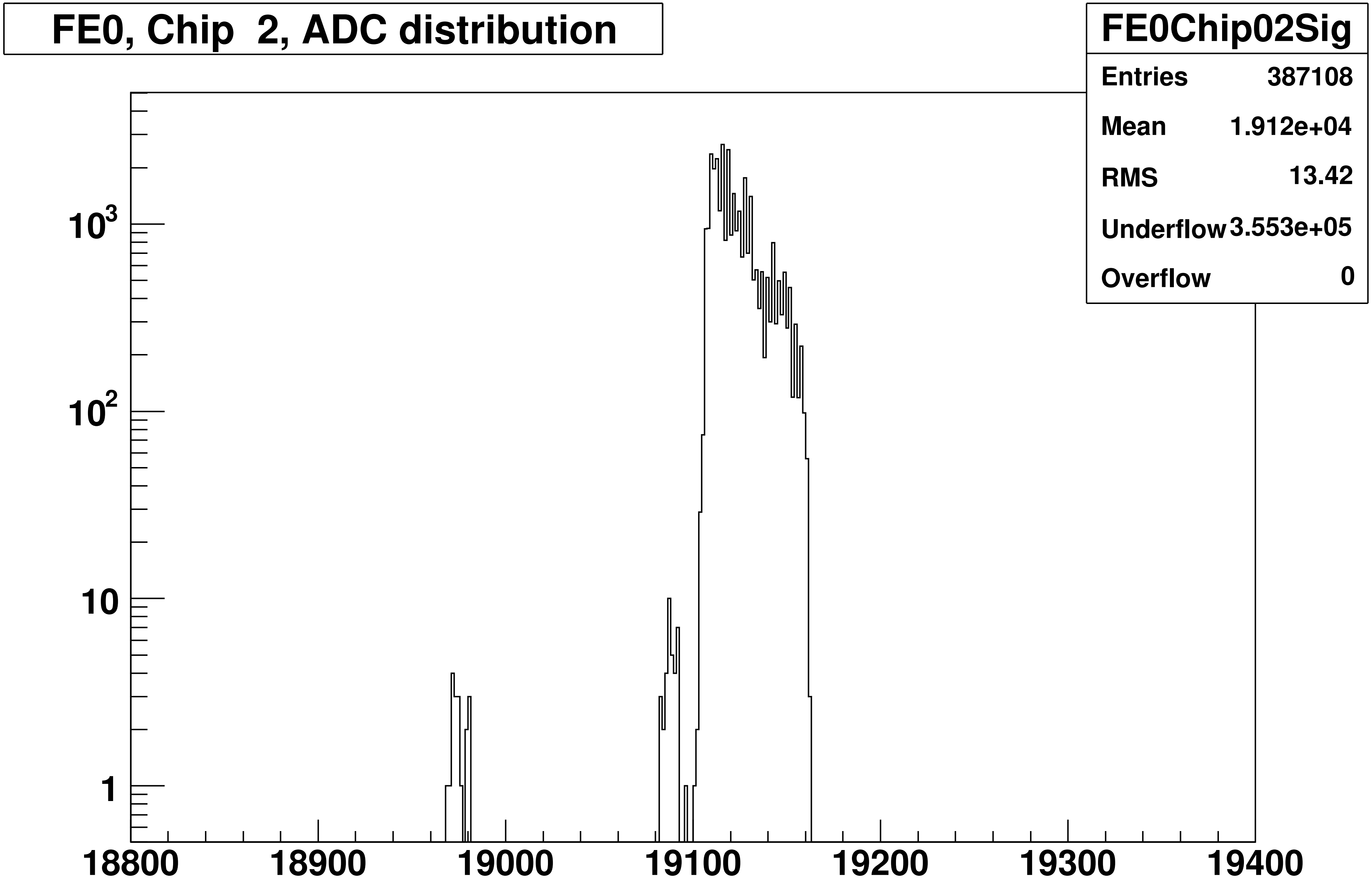

FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 387108
Mean 1.909e+04
RMS 13.06
Underflow 3.553e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

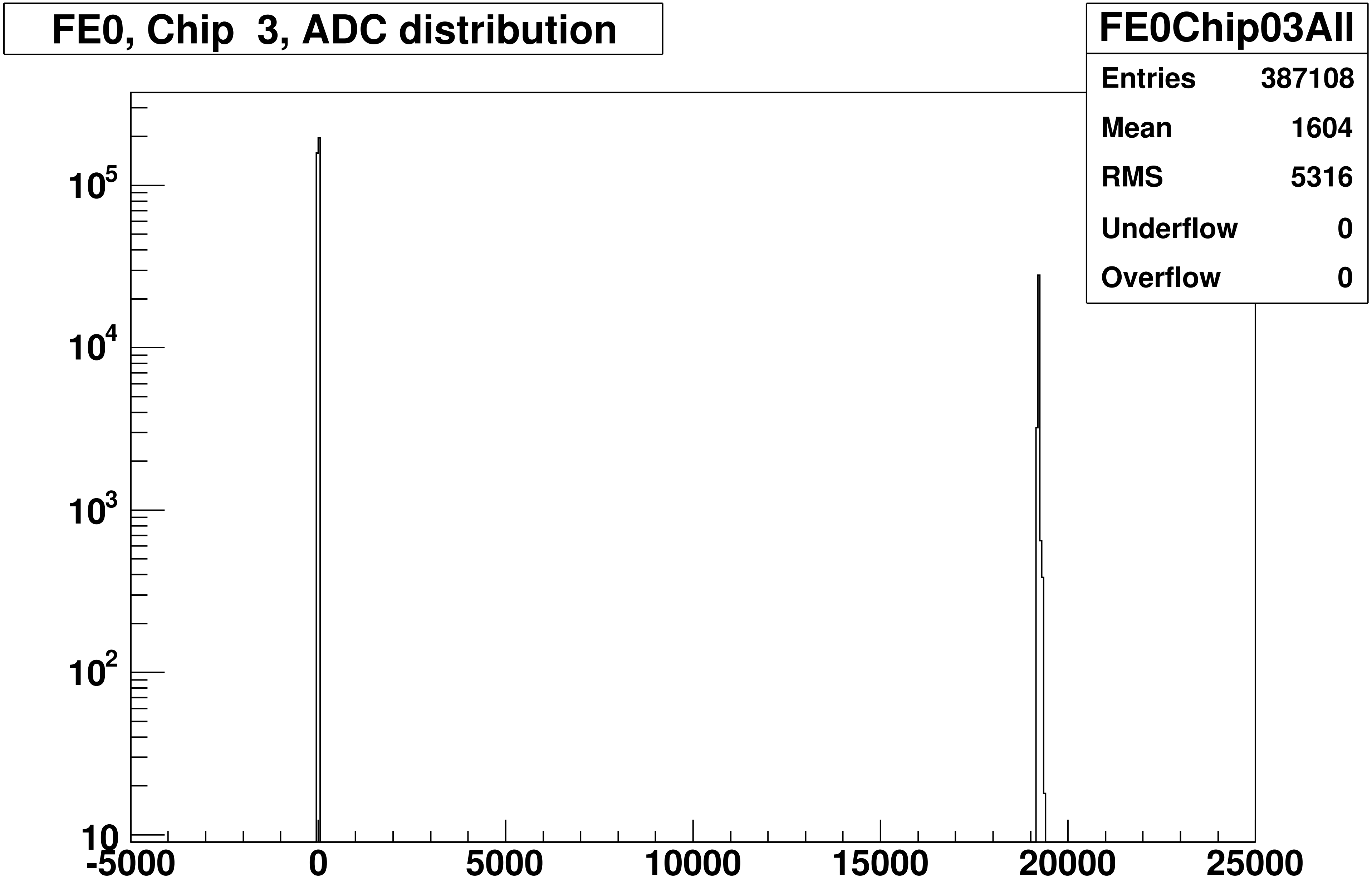


FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 387108
Mean 1578
RMS 5251
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

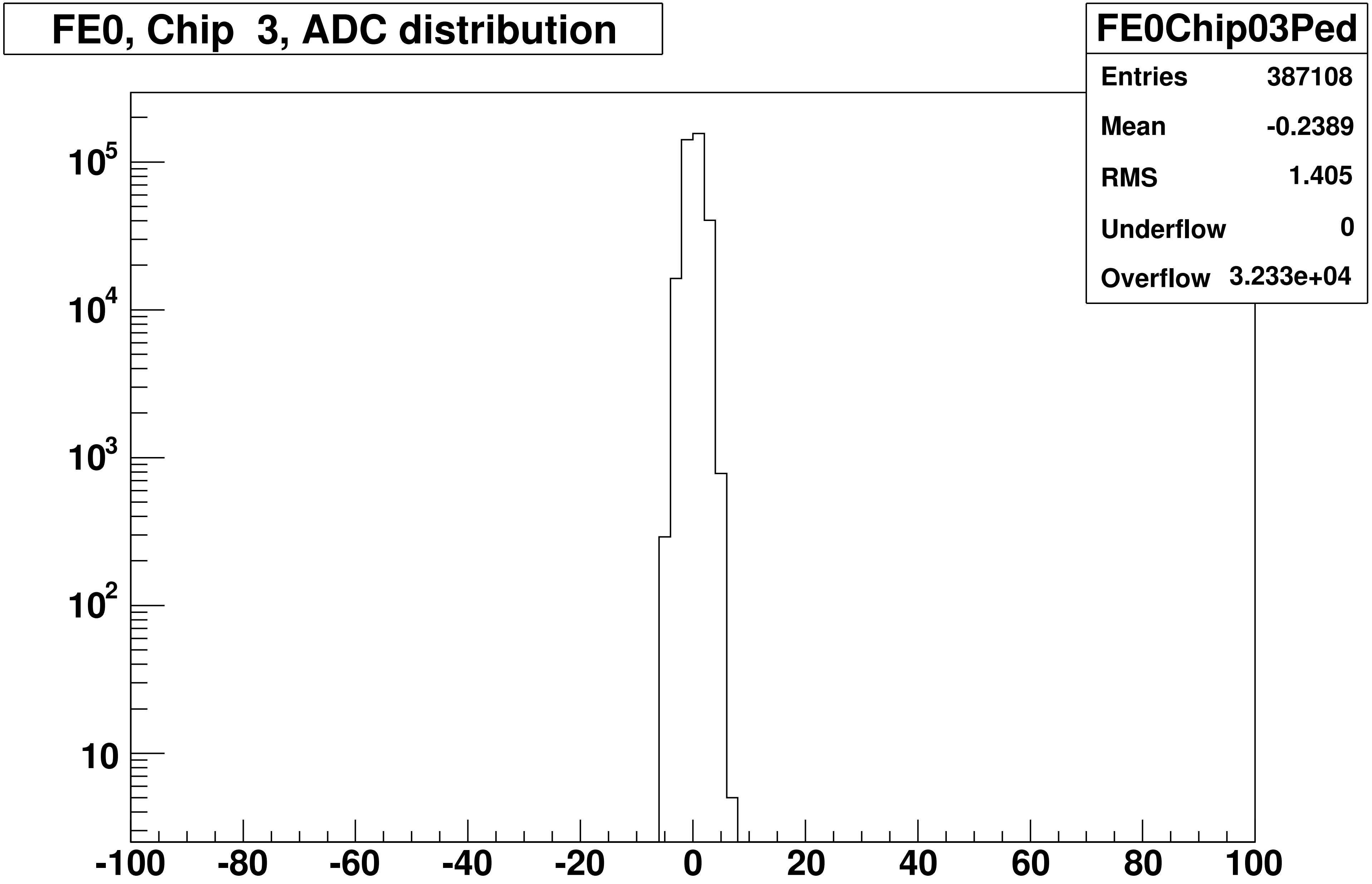


FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 387108
Mean 4.283
RMS 1.326
Underflow 0
Overflow 3.231e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

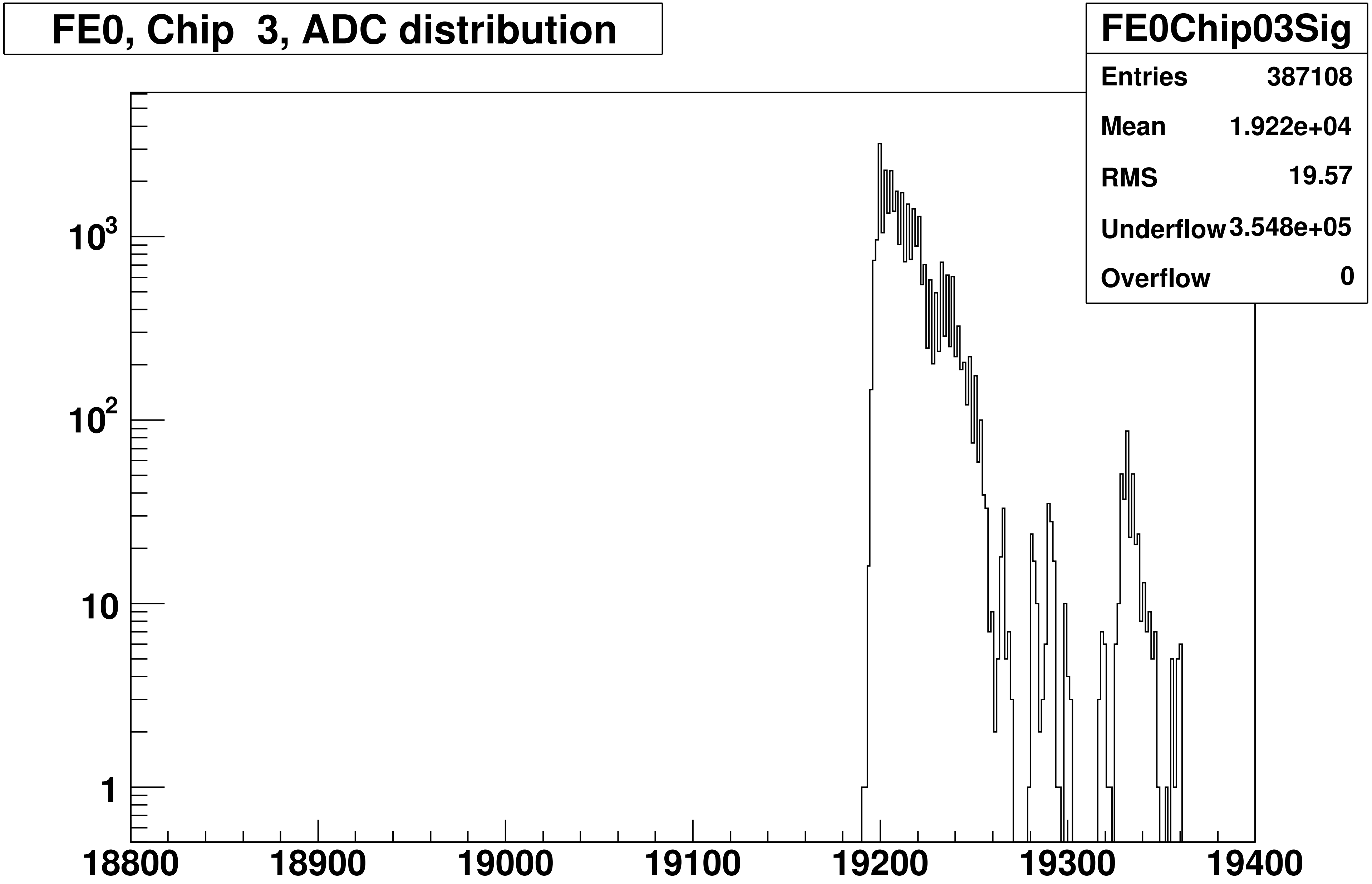

FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 387108
Mean 1.912e+04
RMS 13.42
Underflow 3.553e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

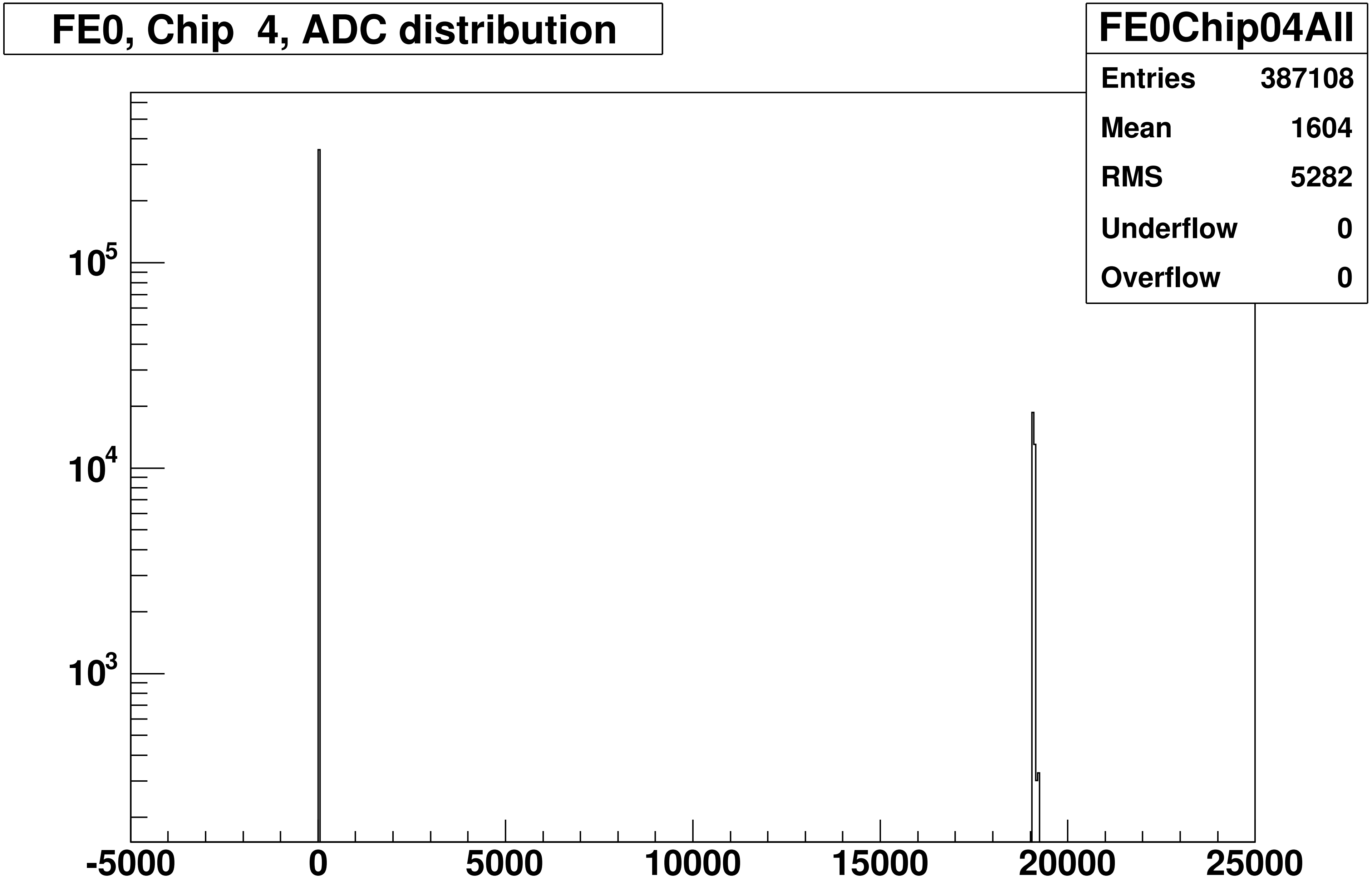


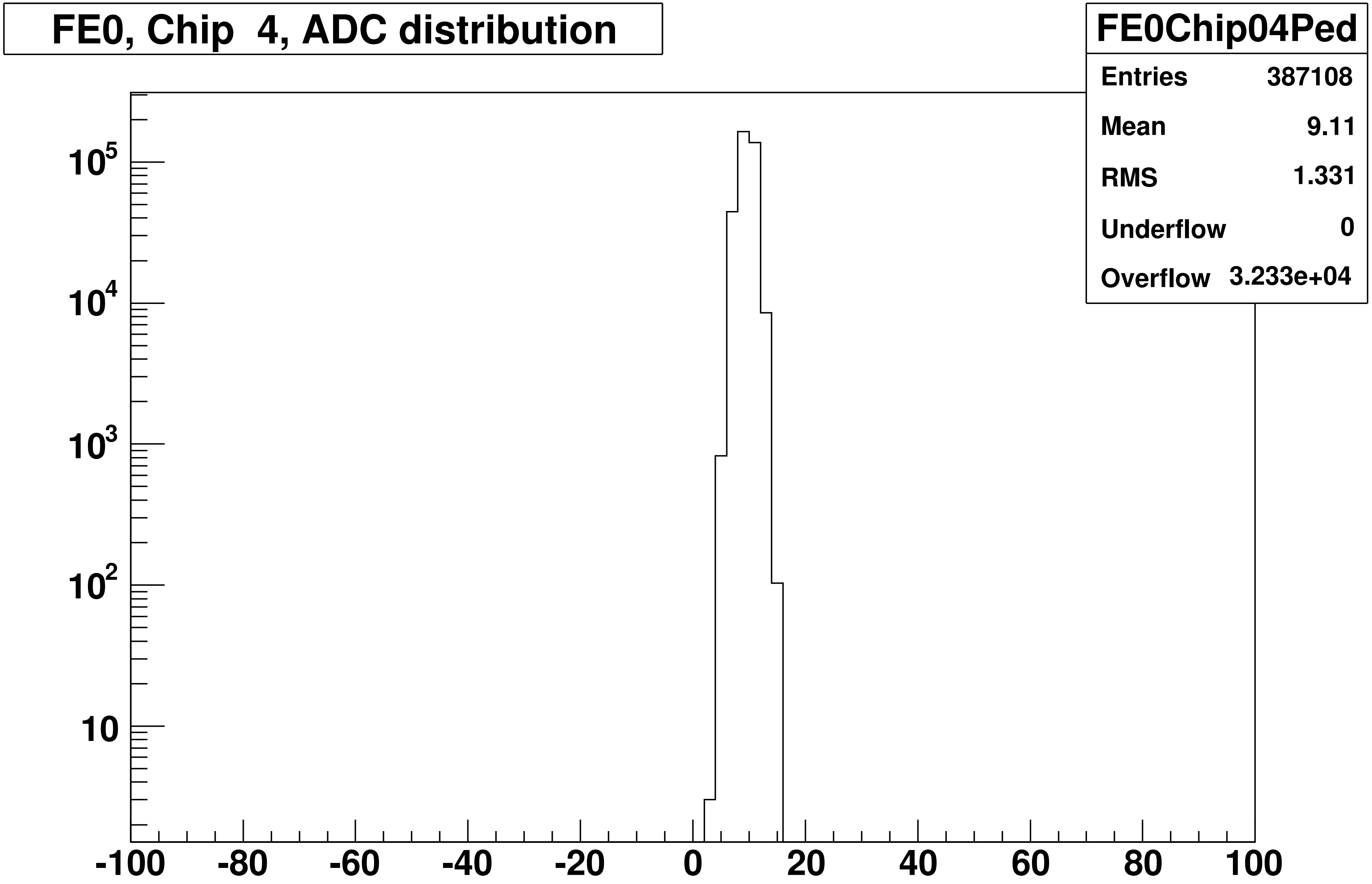
FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 387108
Mean 1604
RMS 5316
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

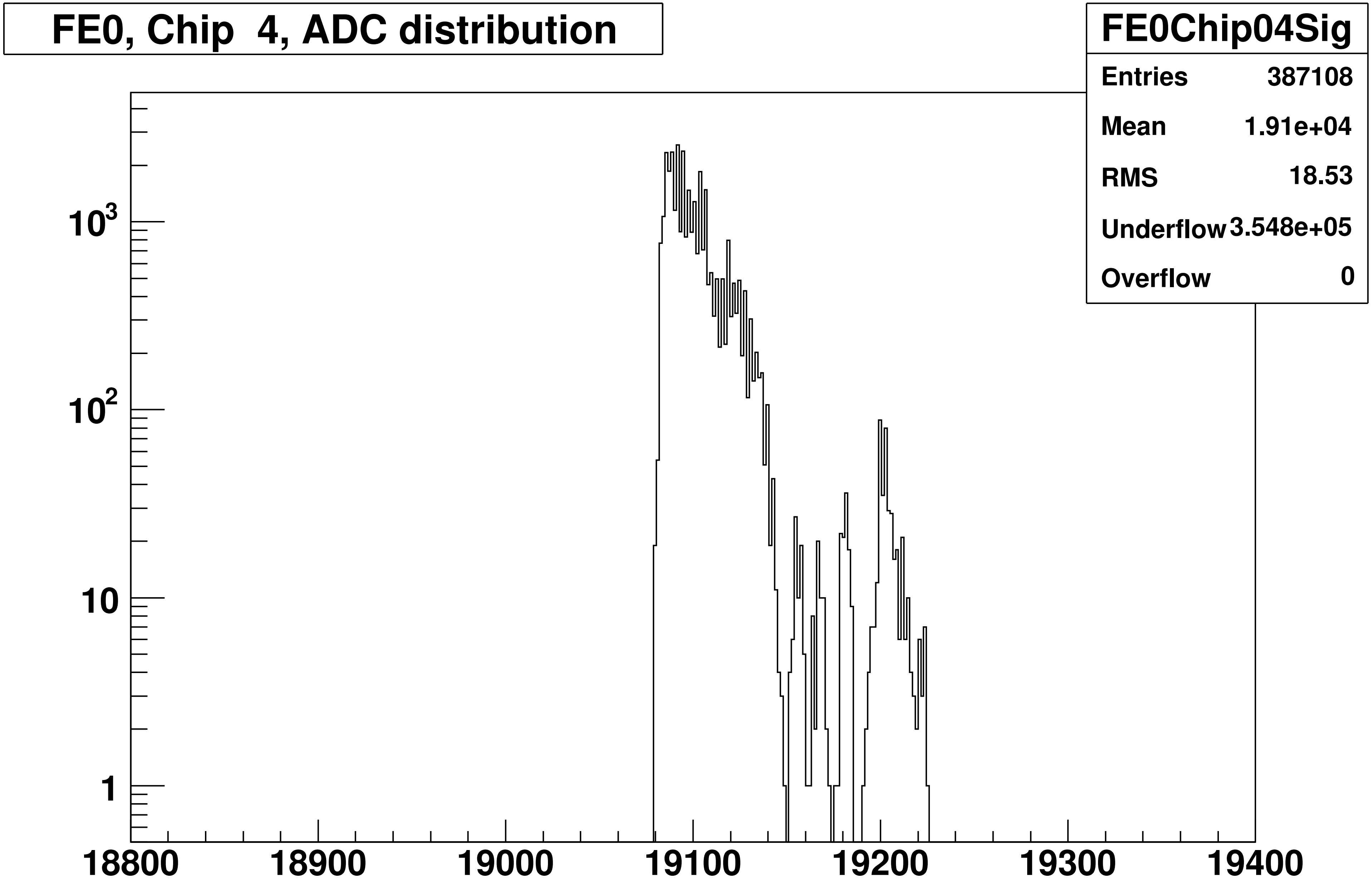


FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 387108
Mean -0.2389
RMS 1.405
Underflow 0
Overflow 3.233e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

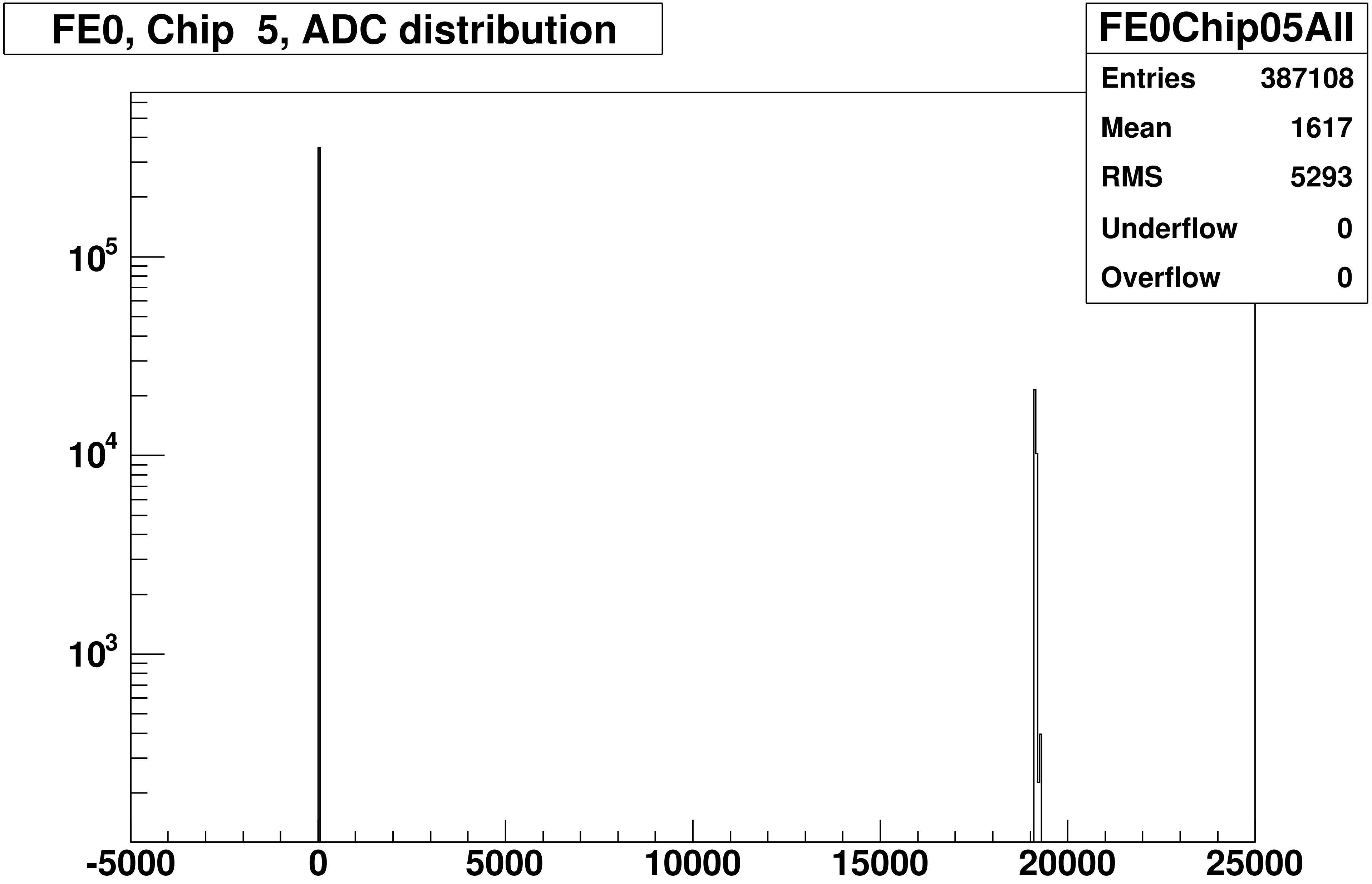

FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 387108
Mean 1.922e+04
RMS 19.57
Underflow 3.548e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

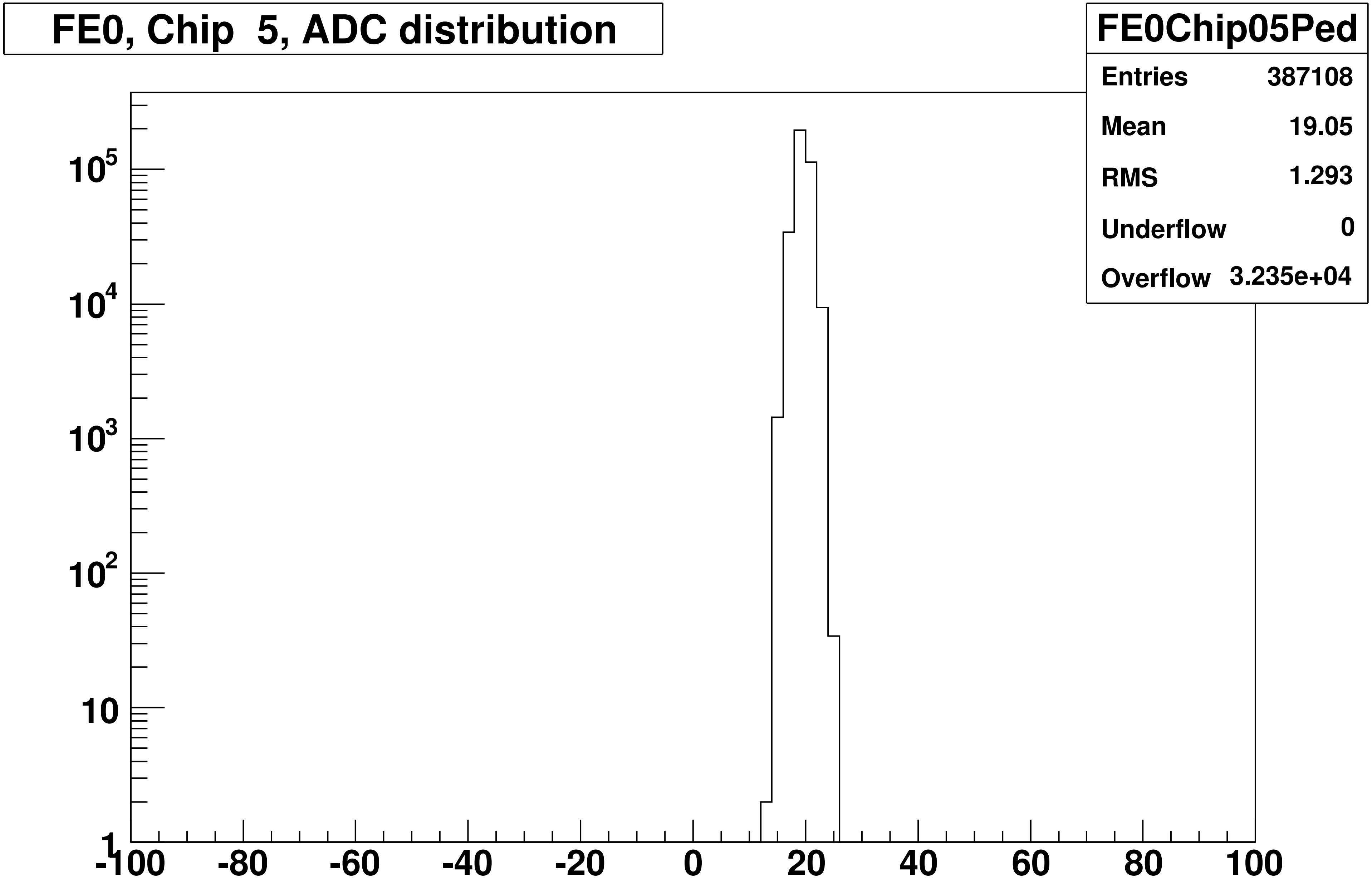

FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 387108
Mean 1604
RMS 5282
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 387108
Mean 9.11
RMS 1.331
Underflow 0
Overflow 3.233e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

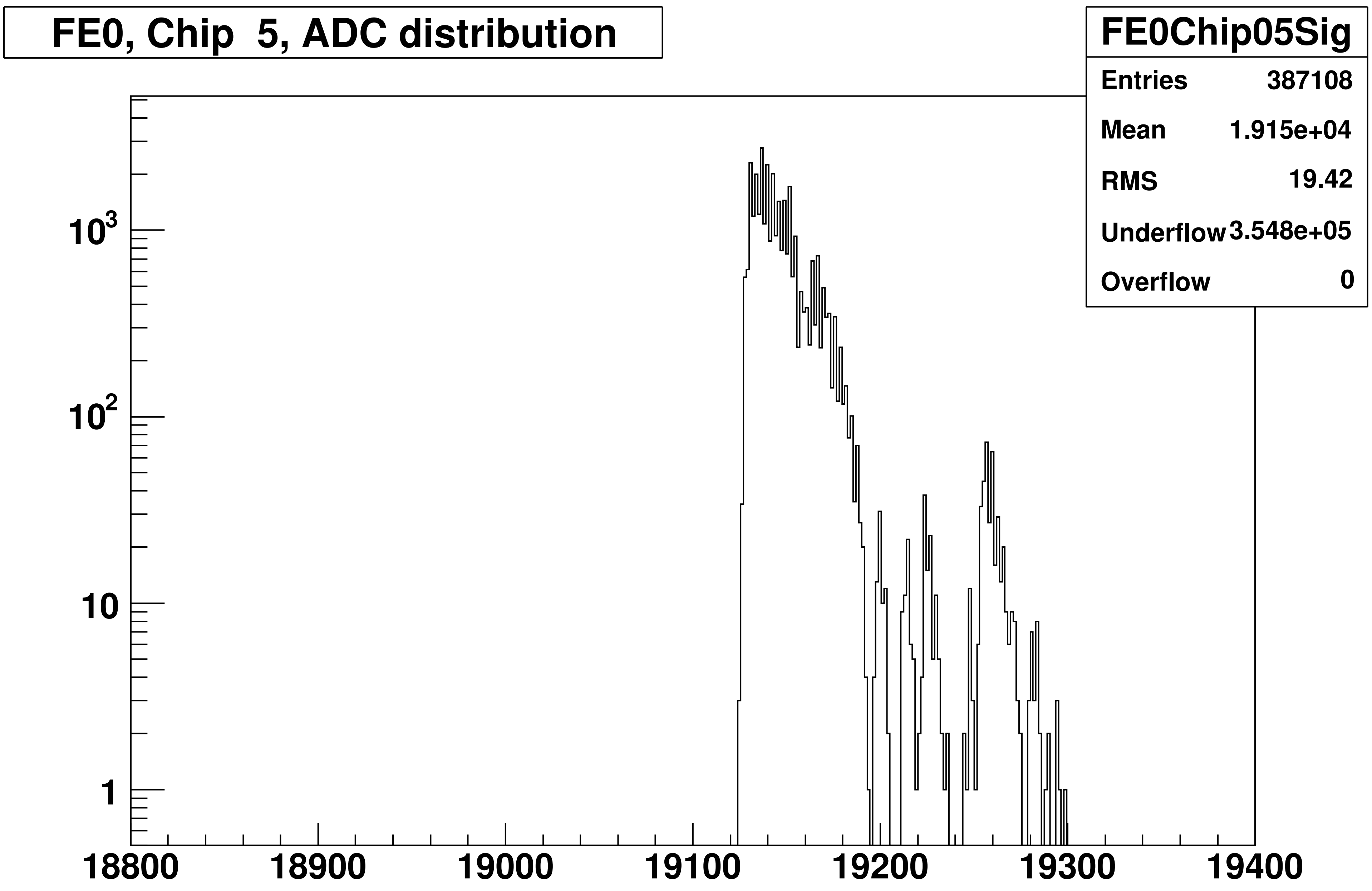


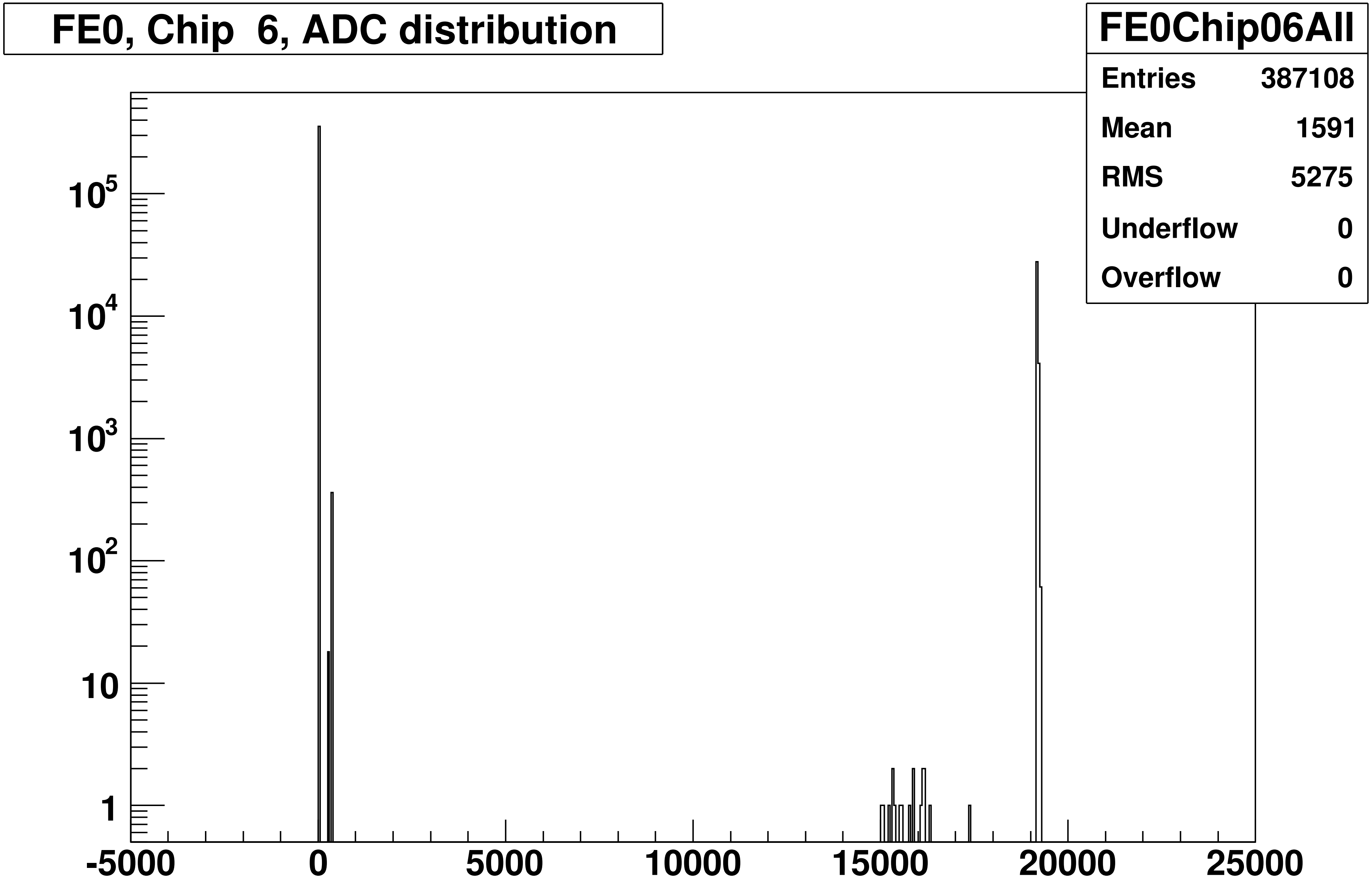
FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 387108
Mean 1.91e+04
RMS 18.53
Underflow 3.548e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

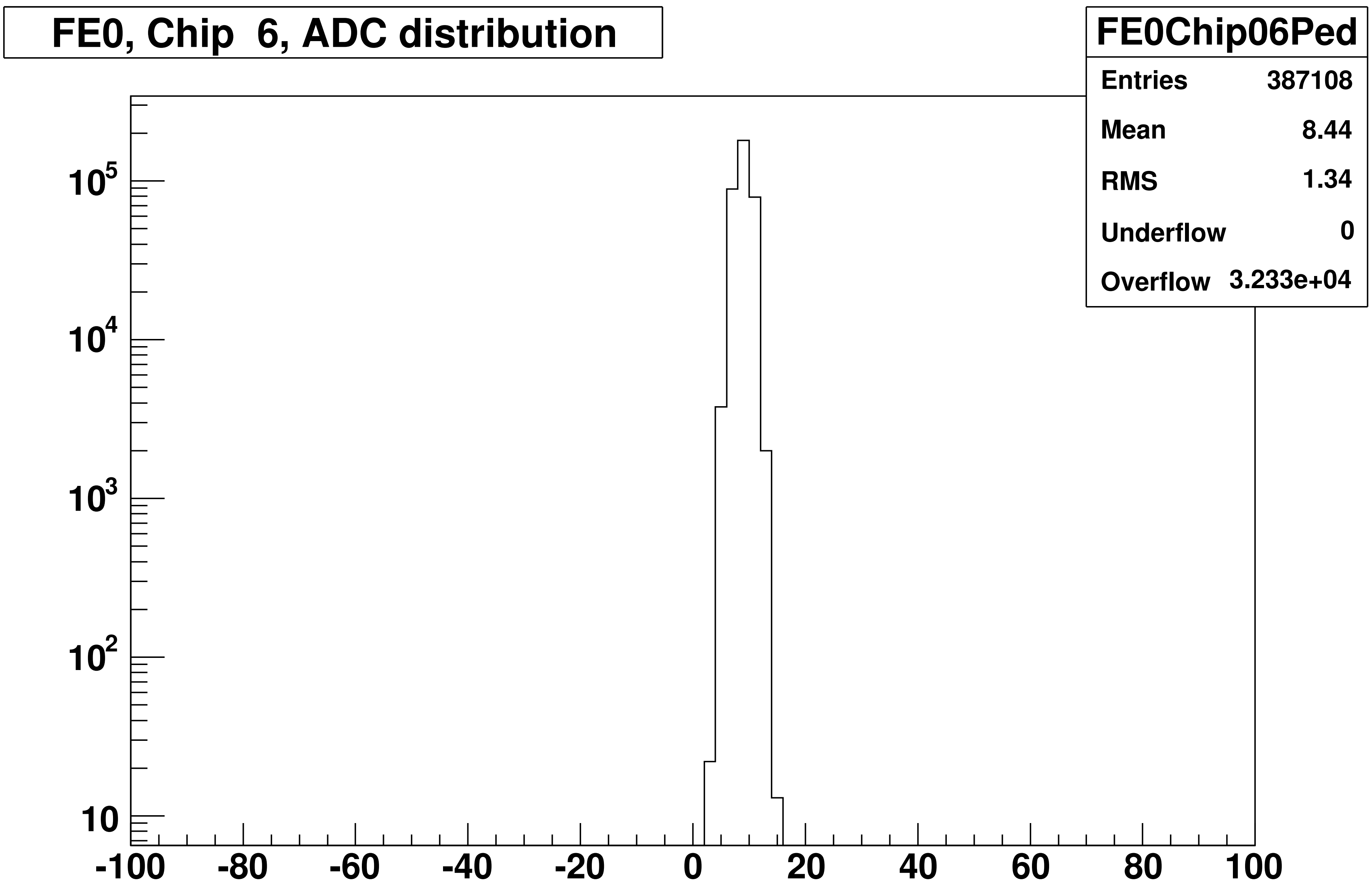

FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 387108
Mean 1617
RMS 5293
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



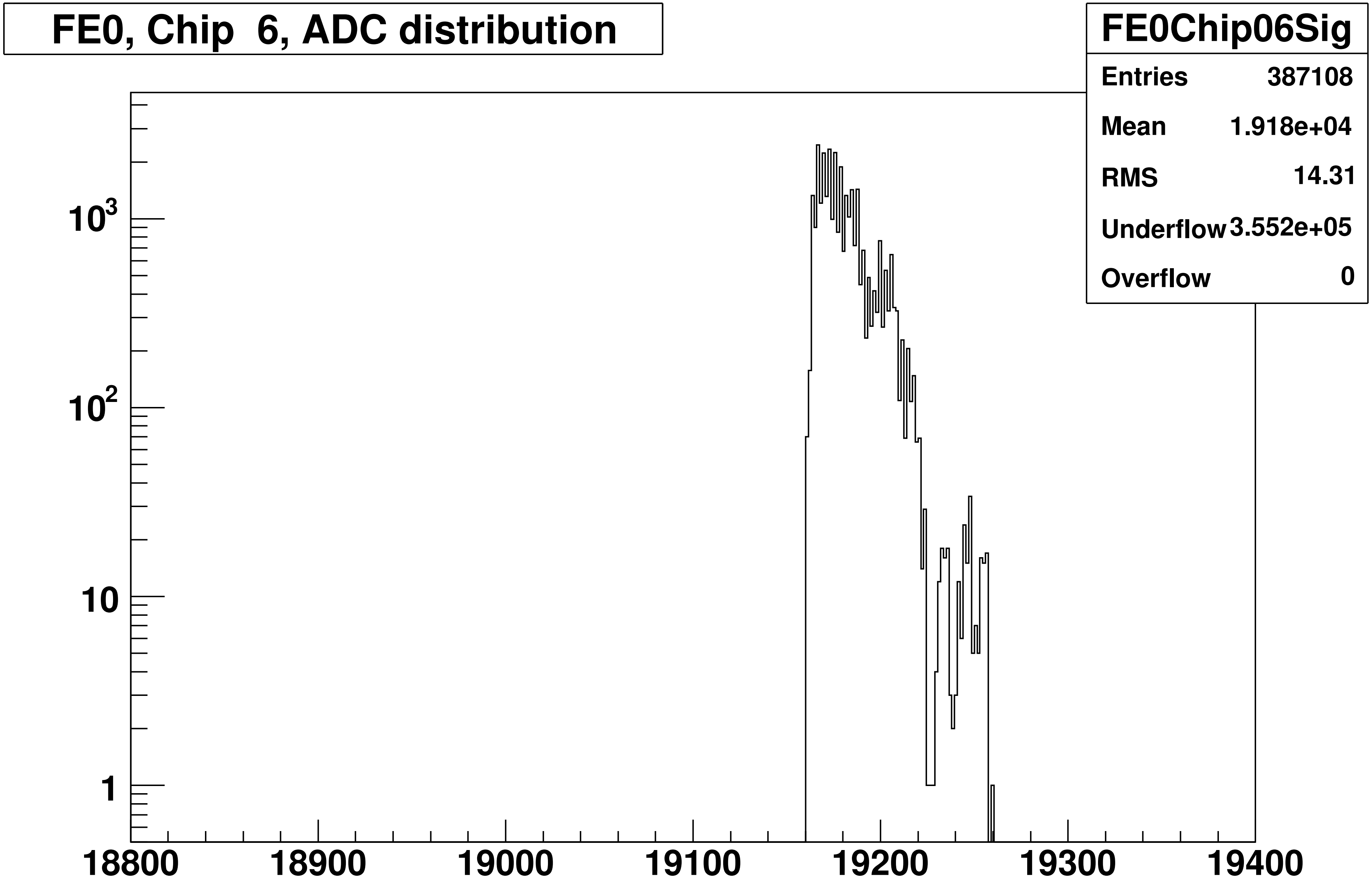
FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 387108
Mean 19.05
RMS 1.293
Underflow 0
Overflow 3.235e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

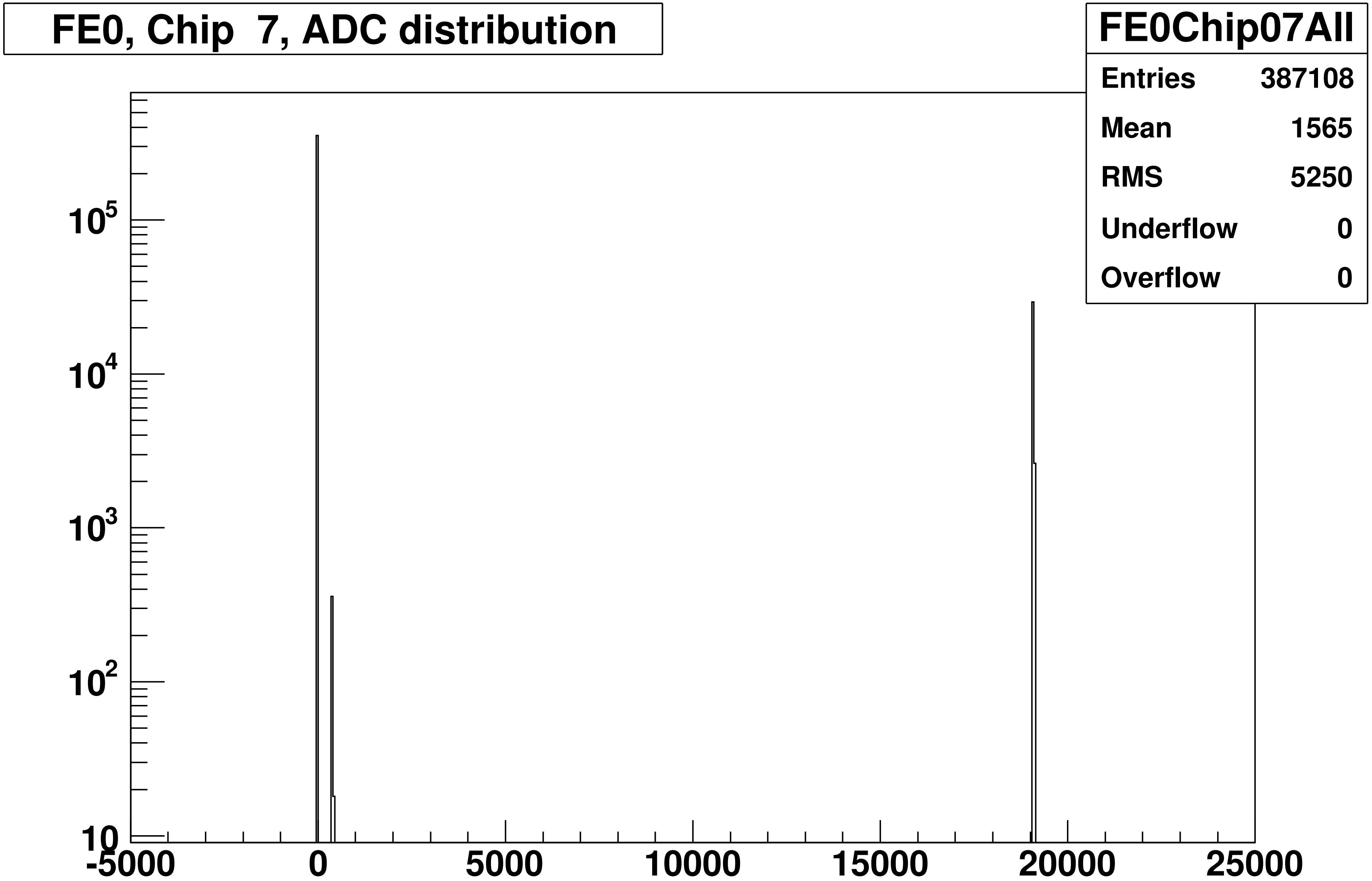

FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 387108
Mean 1.915e+04
RMS 19.42
Underflow 3.548e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

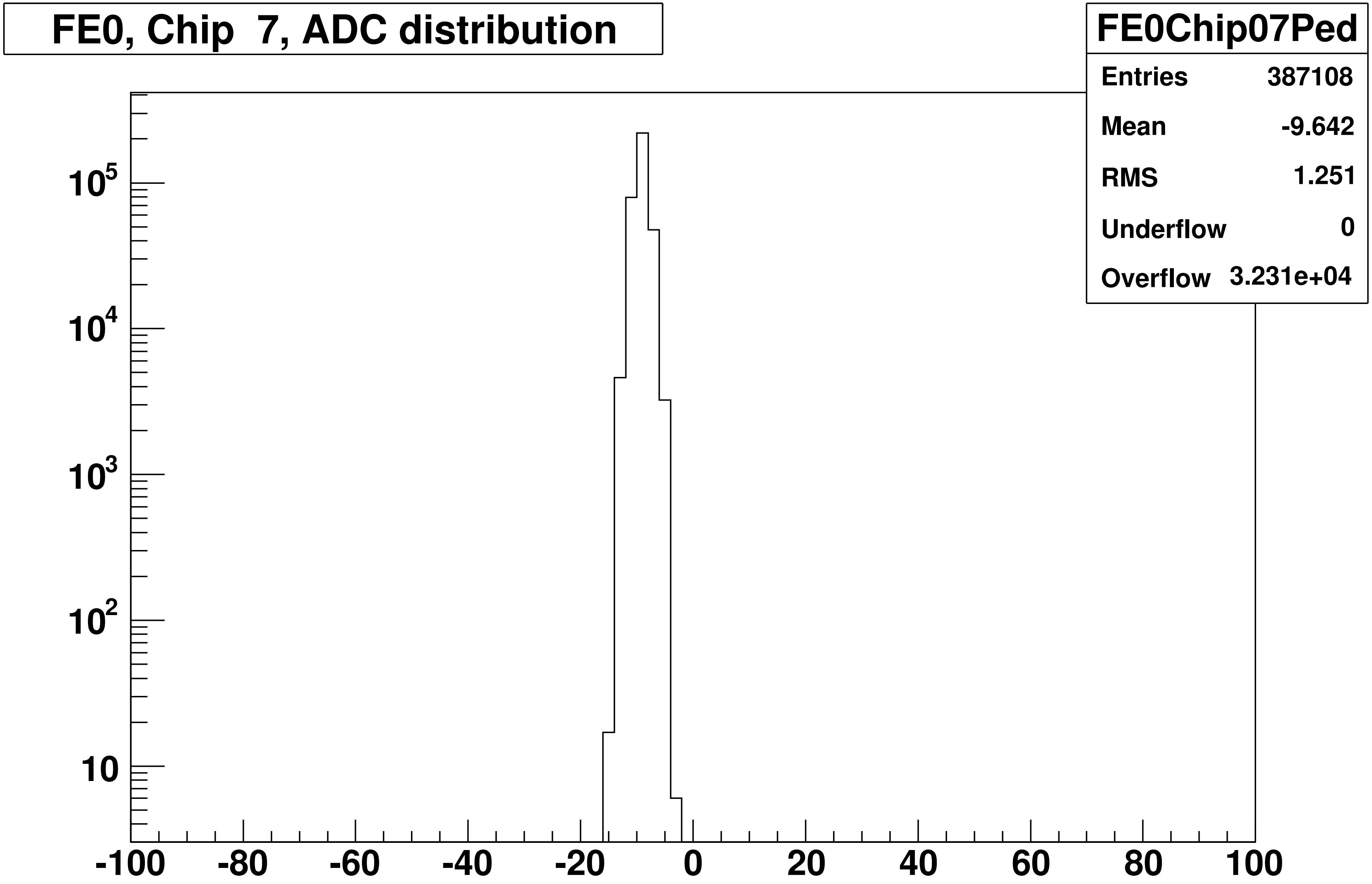

FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 387108
Mean 1591
RMS 5275
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

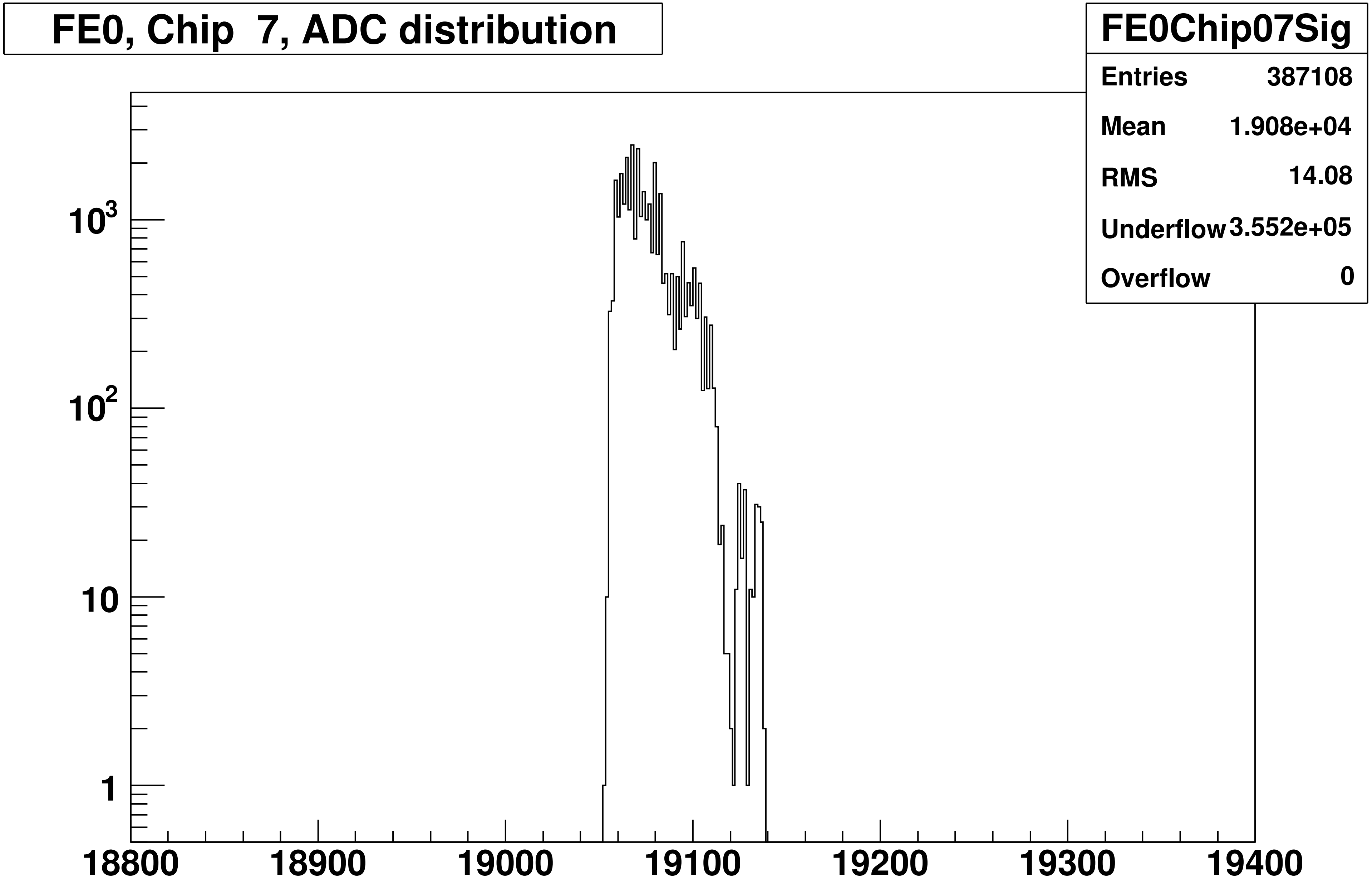


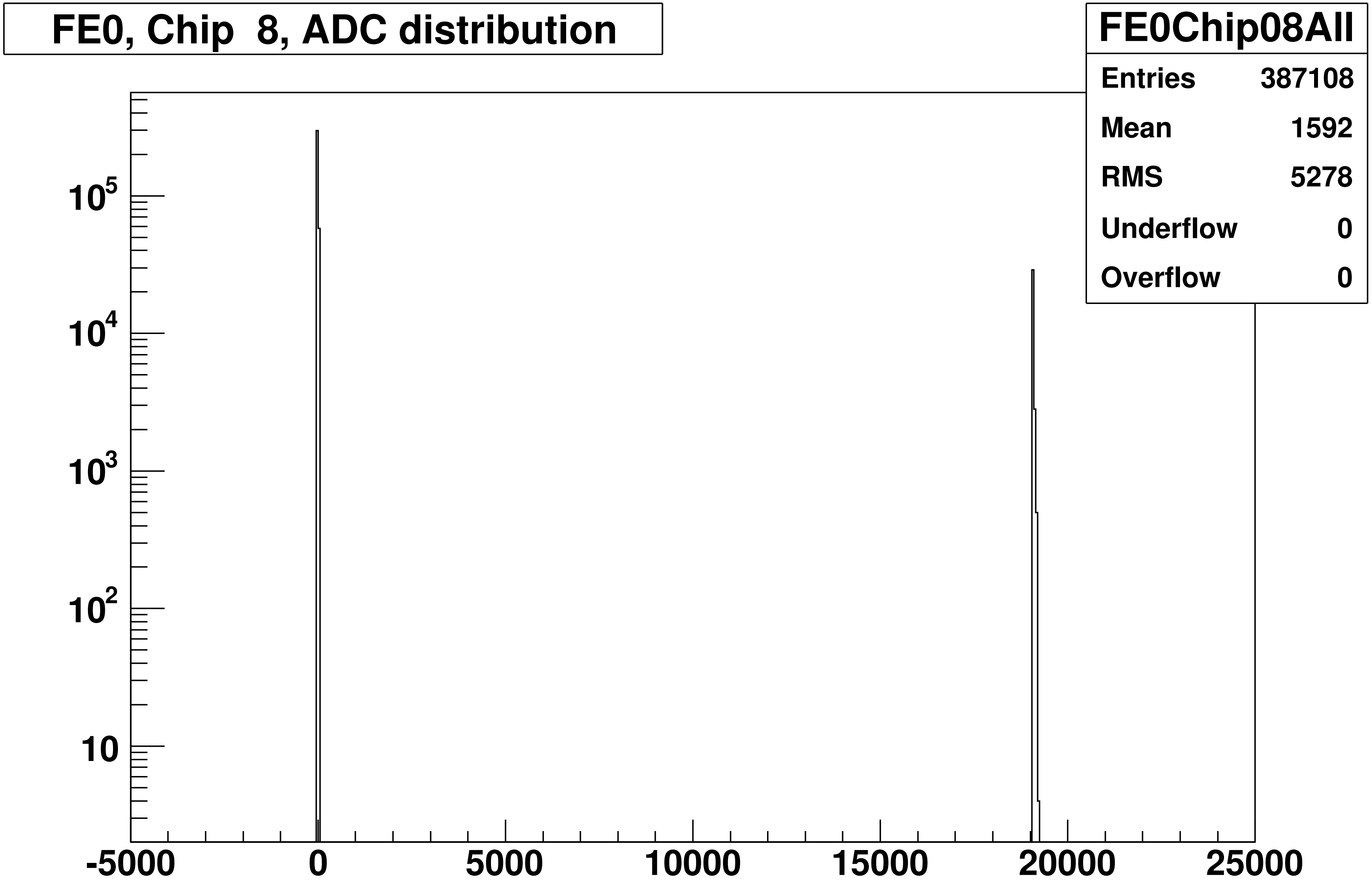
FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 387108
Mean 8.44
RMS 1.34
Underflow 0
Overflow 3.233e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

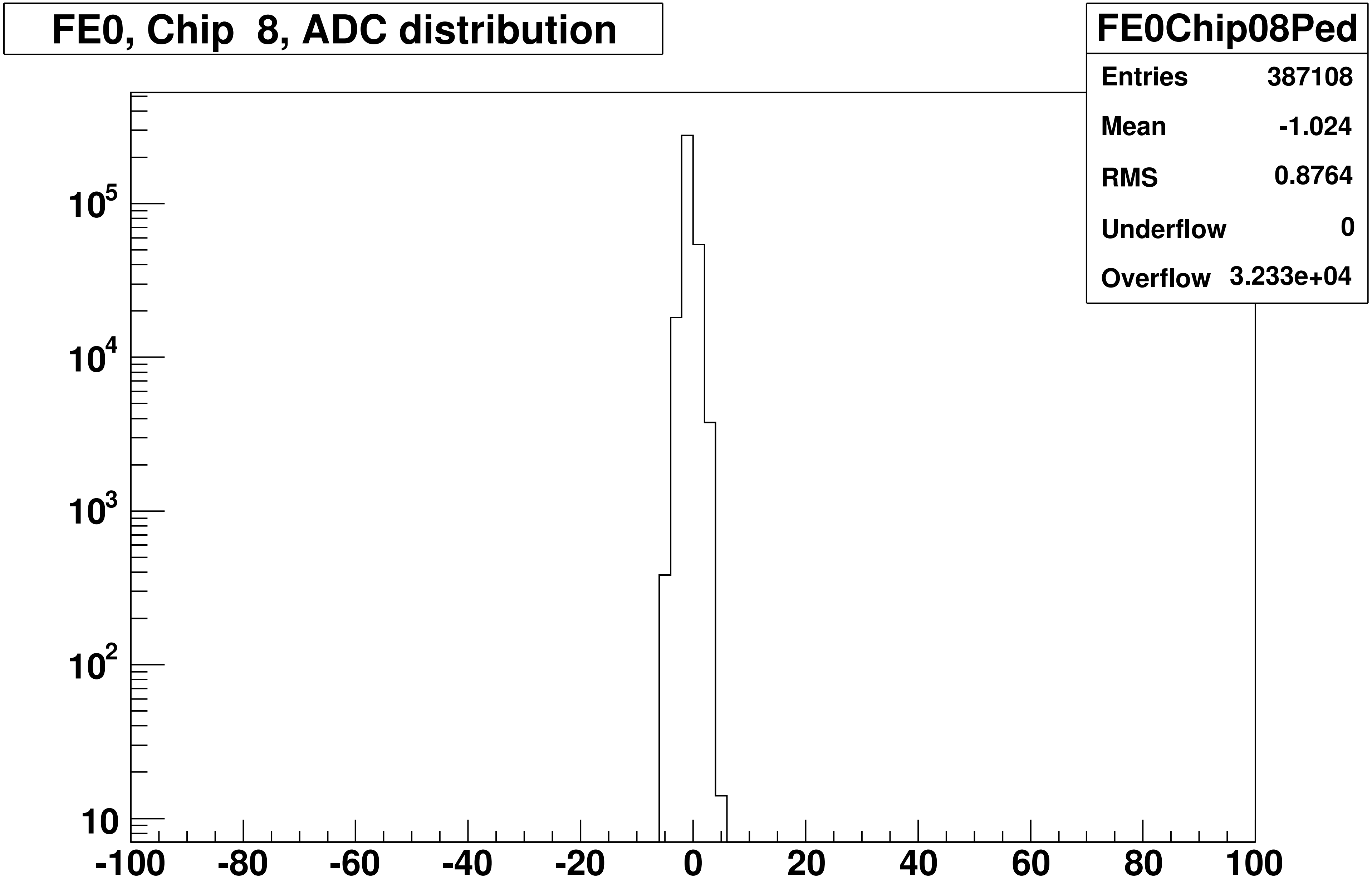

FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 387108
Mean 1.918e+04
RMS 14.31
Underflow 3.552e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 387108
Mean 1565
RMS 5250
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

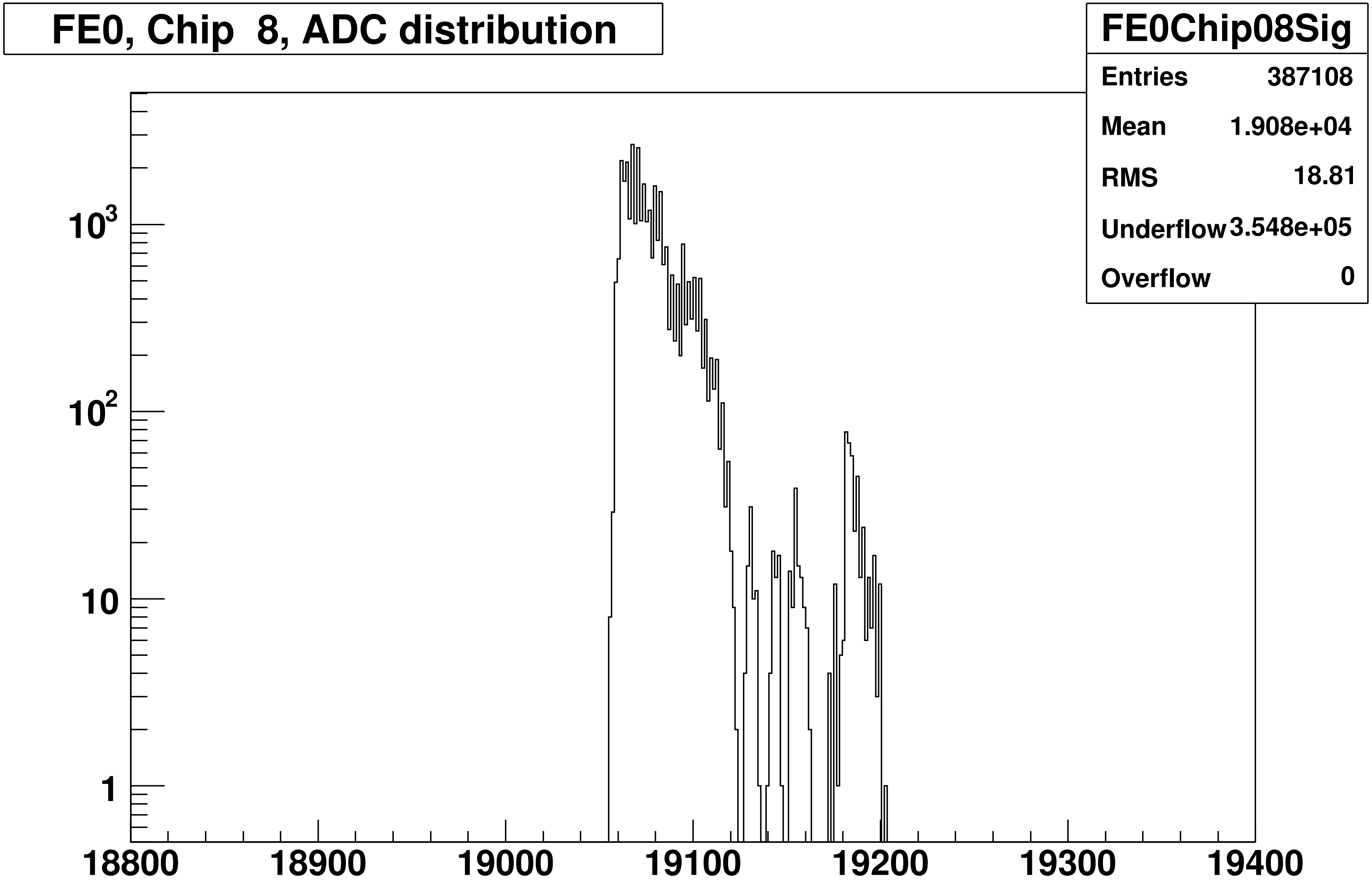

FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 387108
Mean -9.642
RMS 1.251
Underflow 0
Overflow 3.231e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 387108
Mean 1.908e+04
RMS 14.08
Underflow 3.552e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400

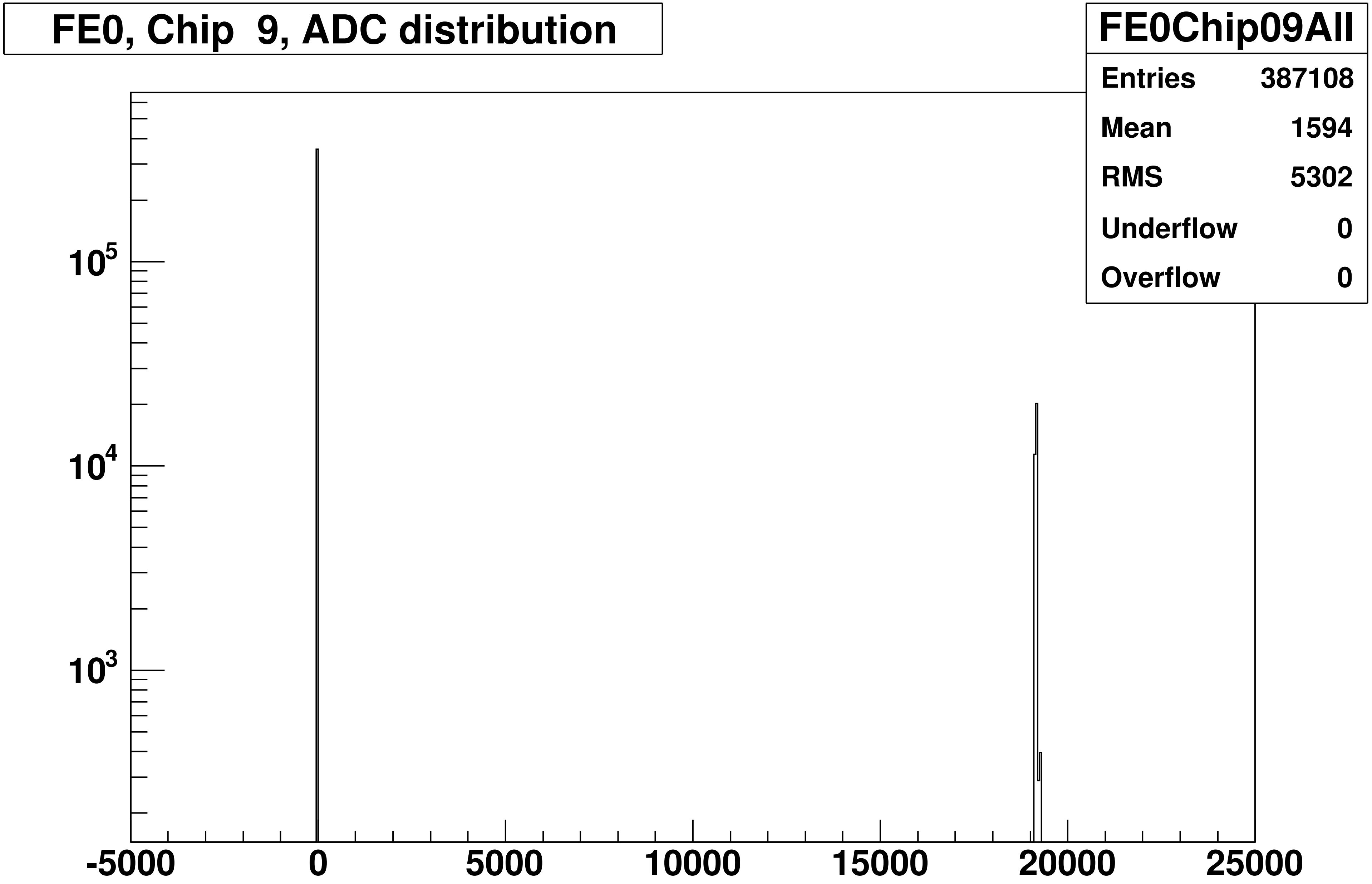

FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 387108
Mean 1592
RMS 5278
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

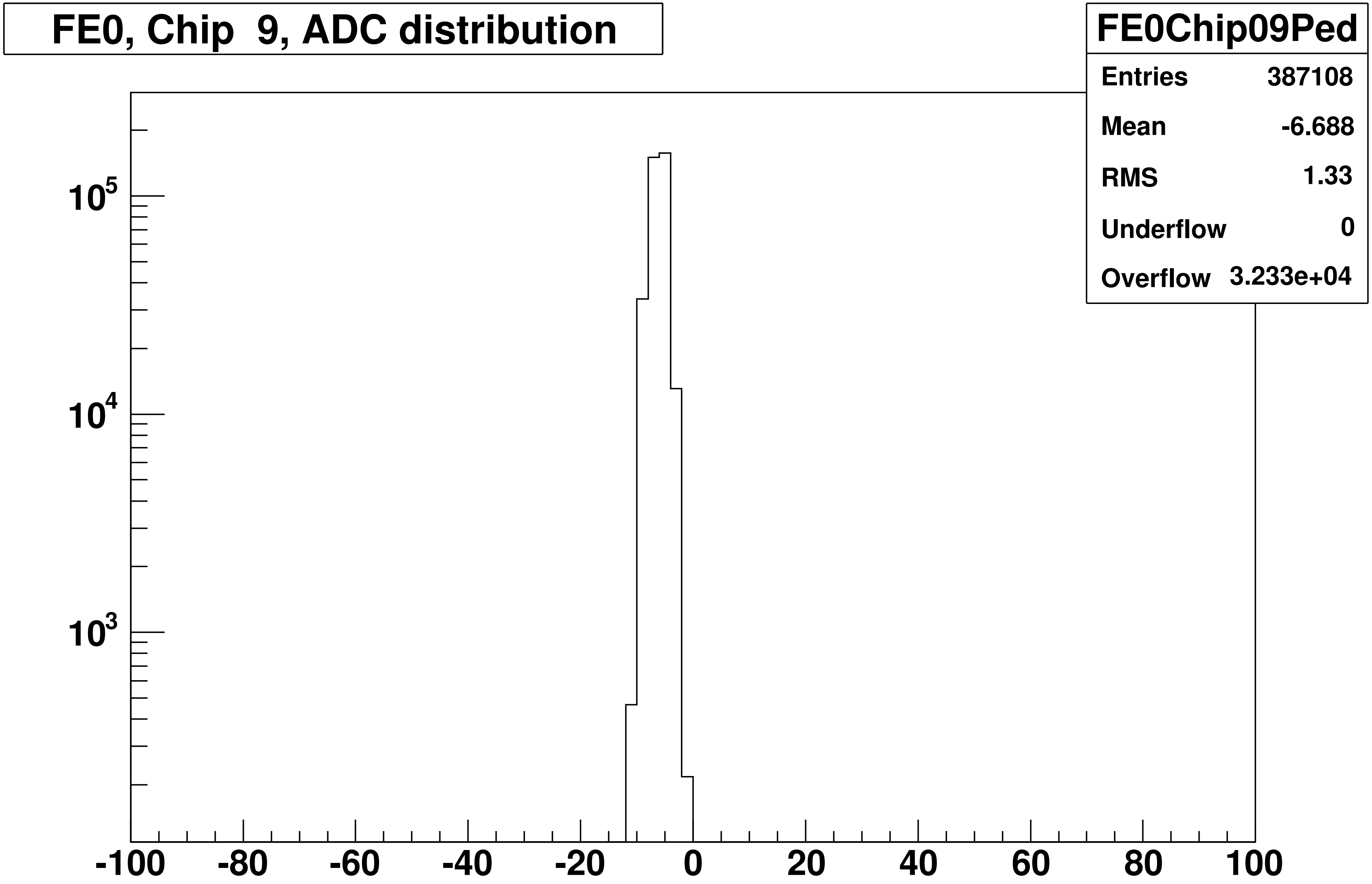


FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 387108
Mean -1.024
RMS 0.8764
Underflow 0
Overflow 3.233e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

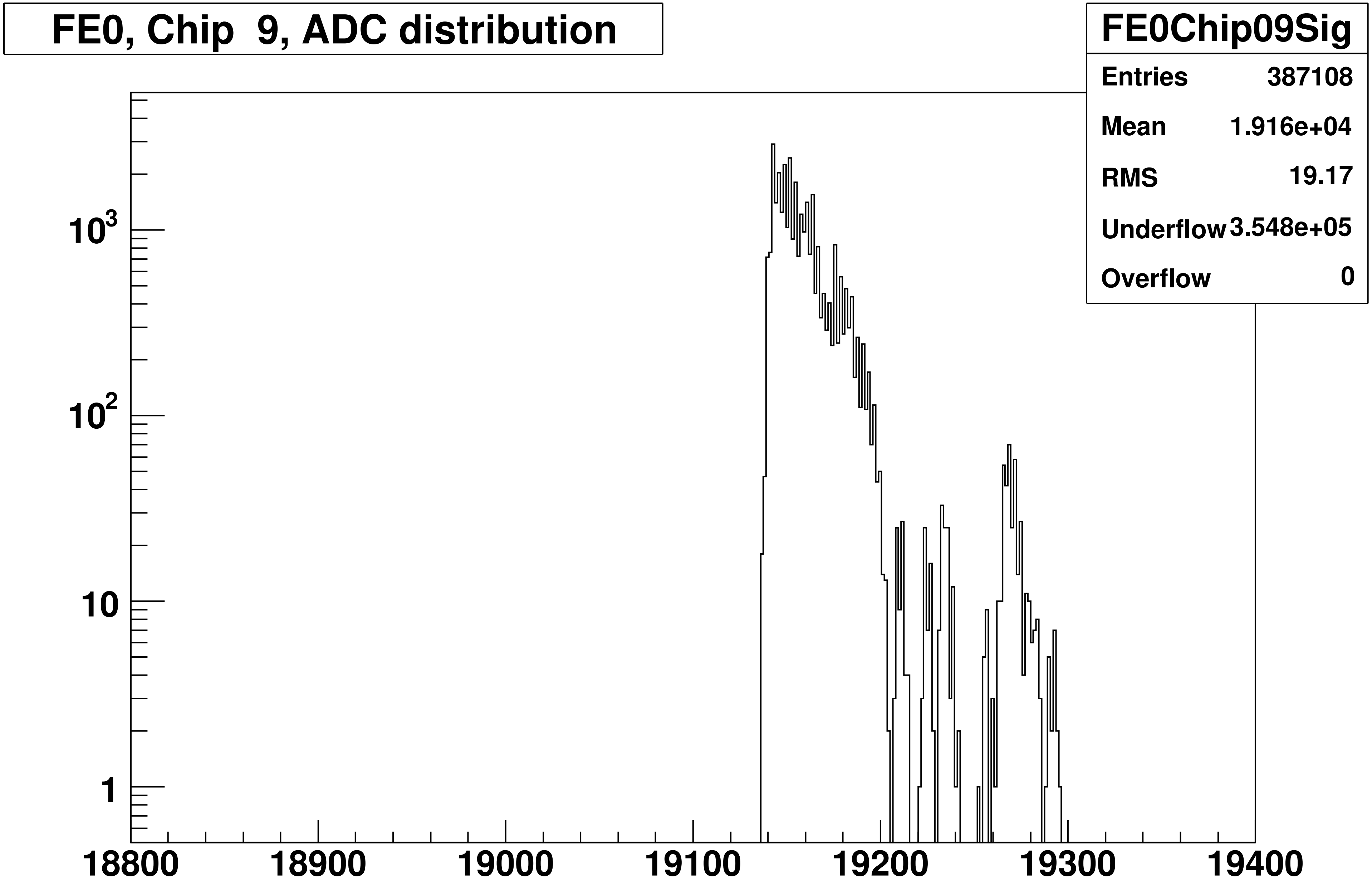


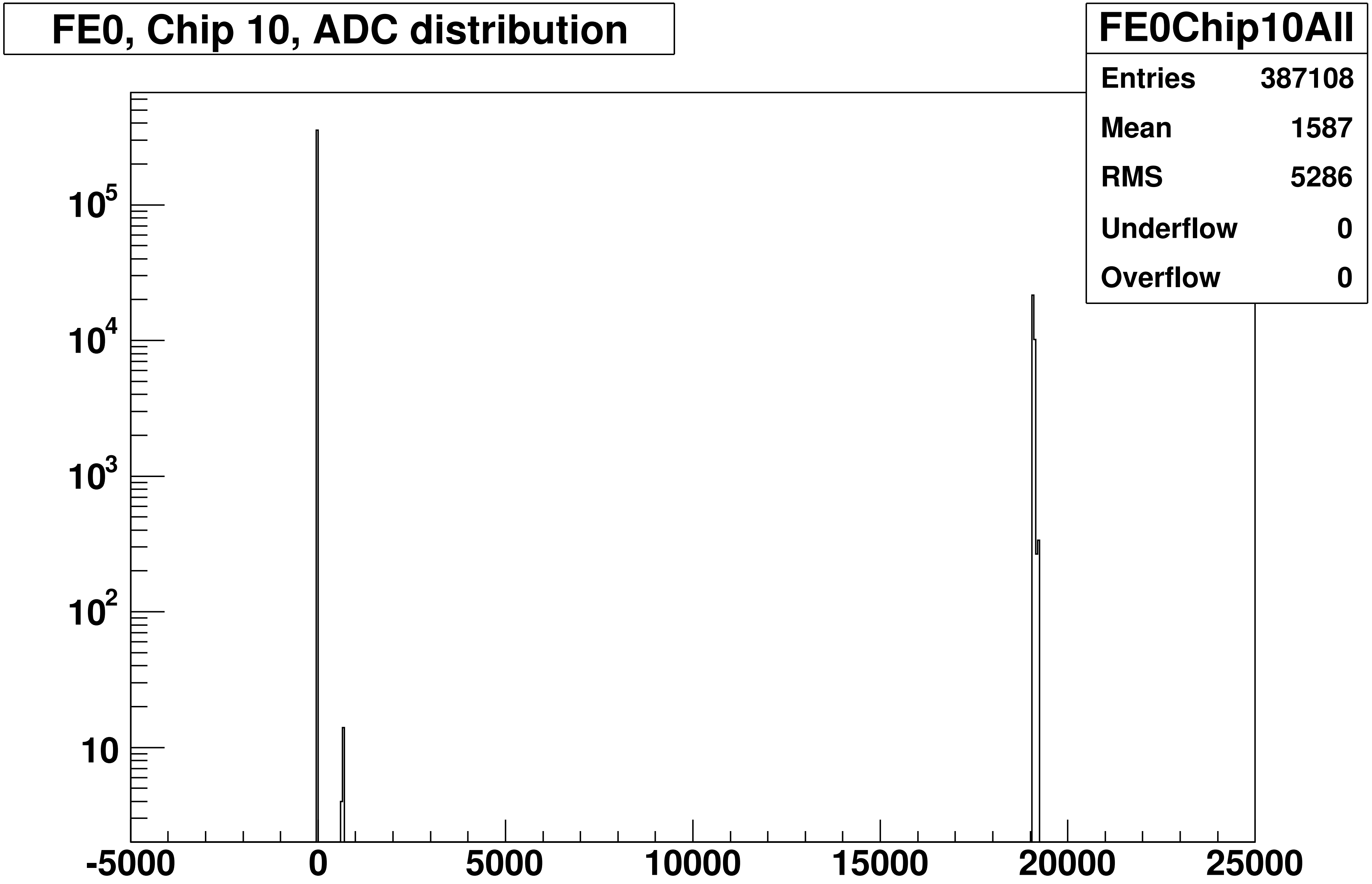
FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 387108
Mean 1.908e+04
RMS 18.81
Underflow 3.548e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

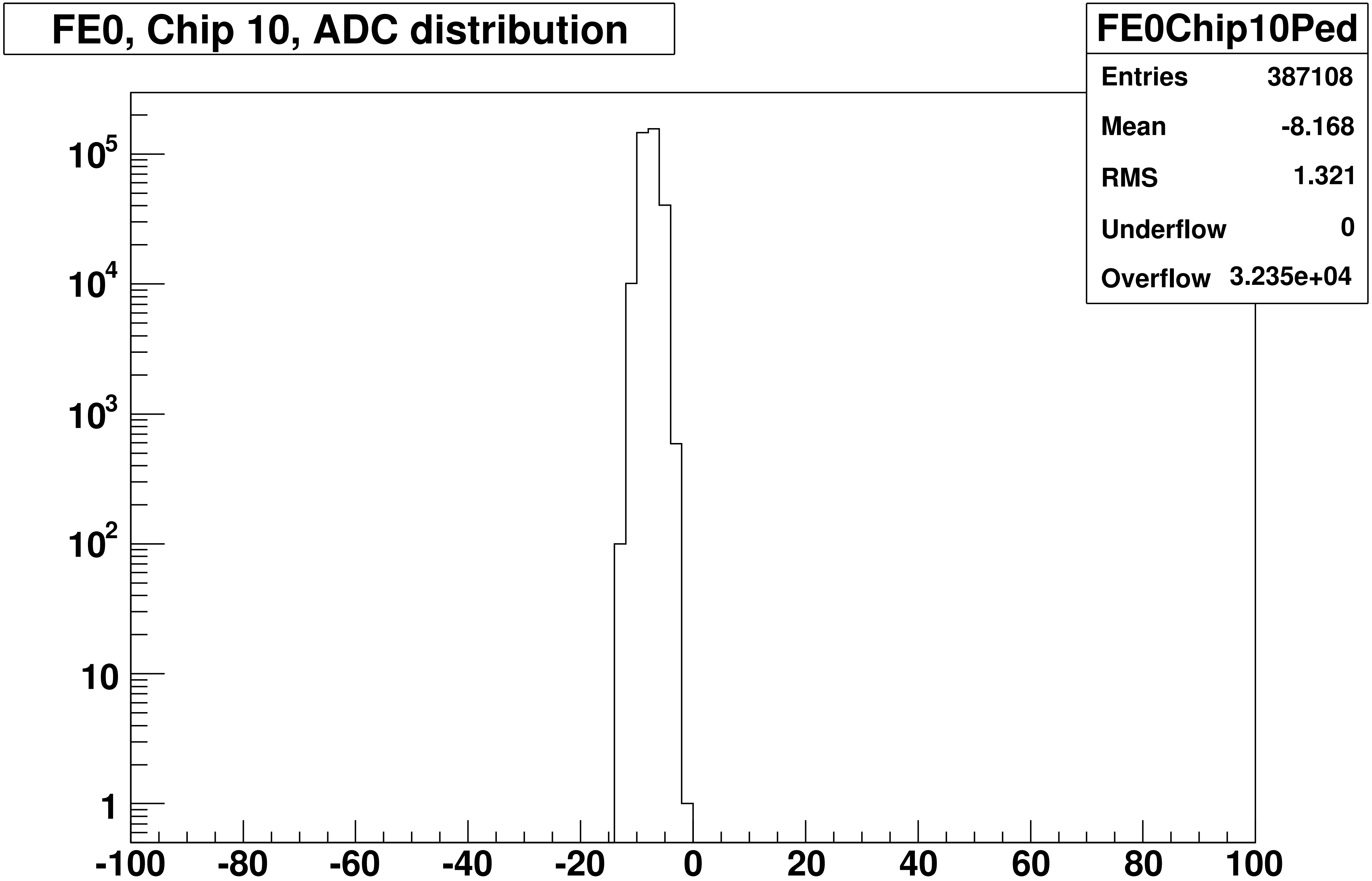

FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 387108
Mean 1594
RMS 5302
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

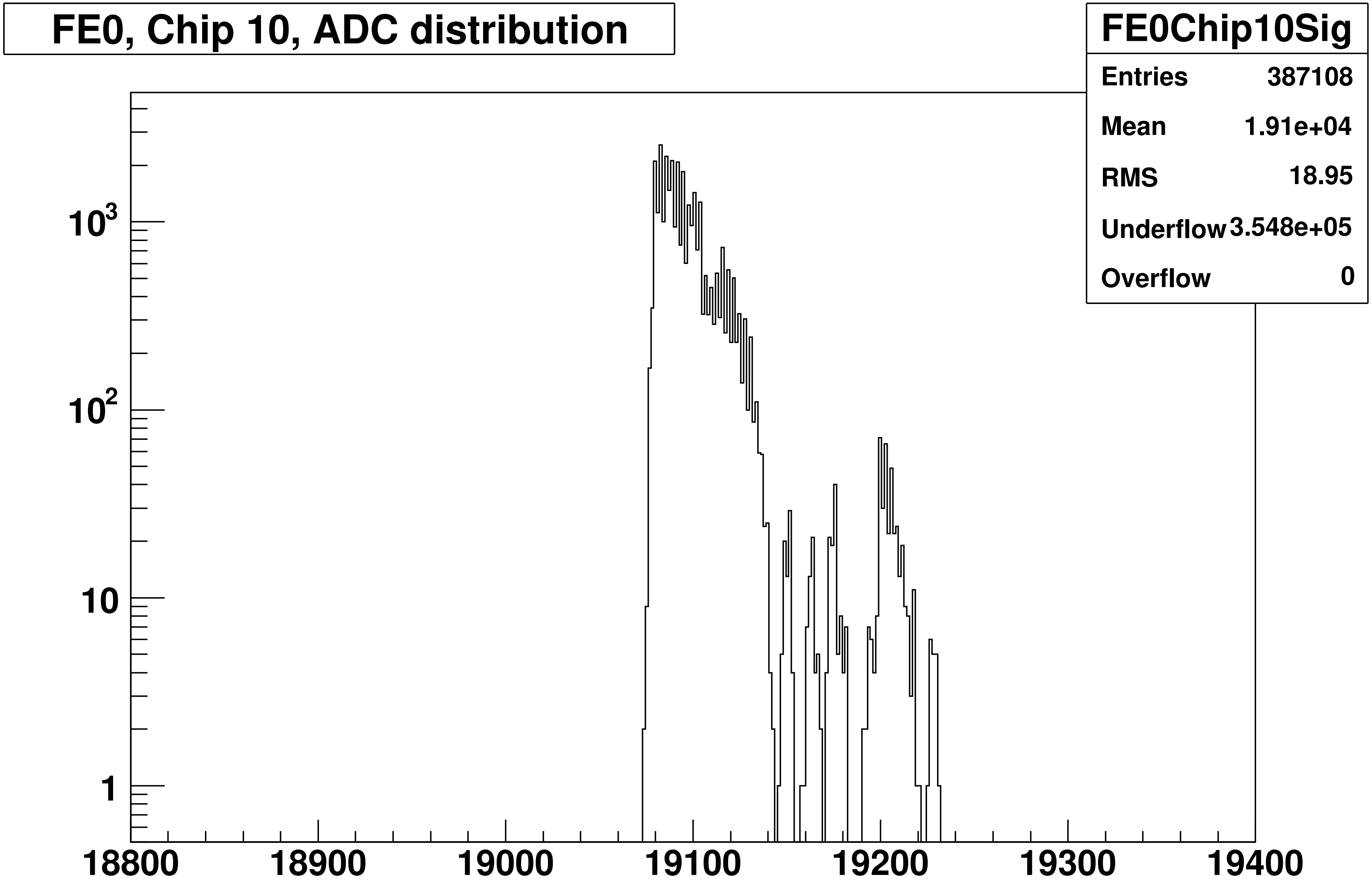


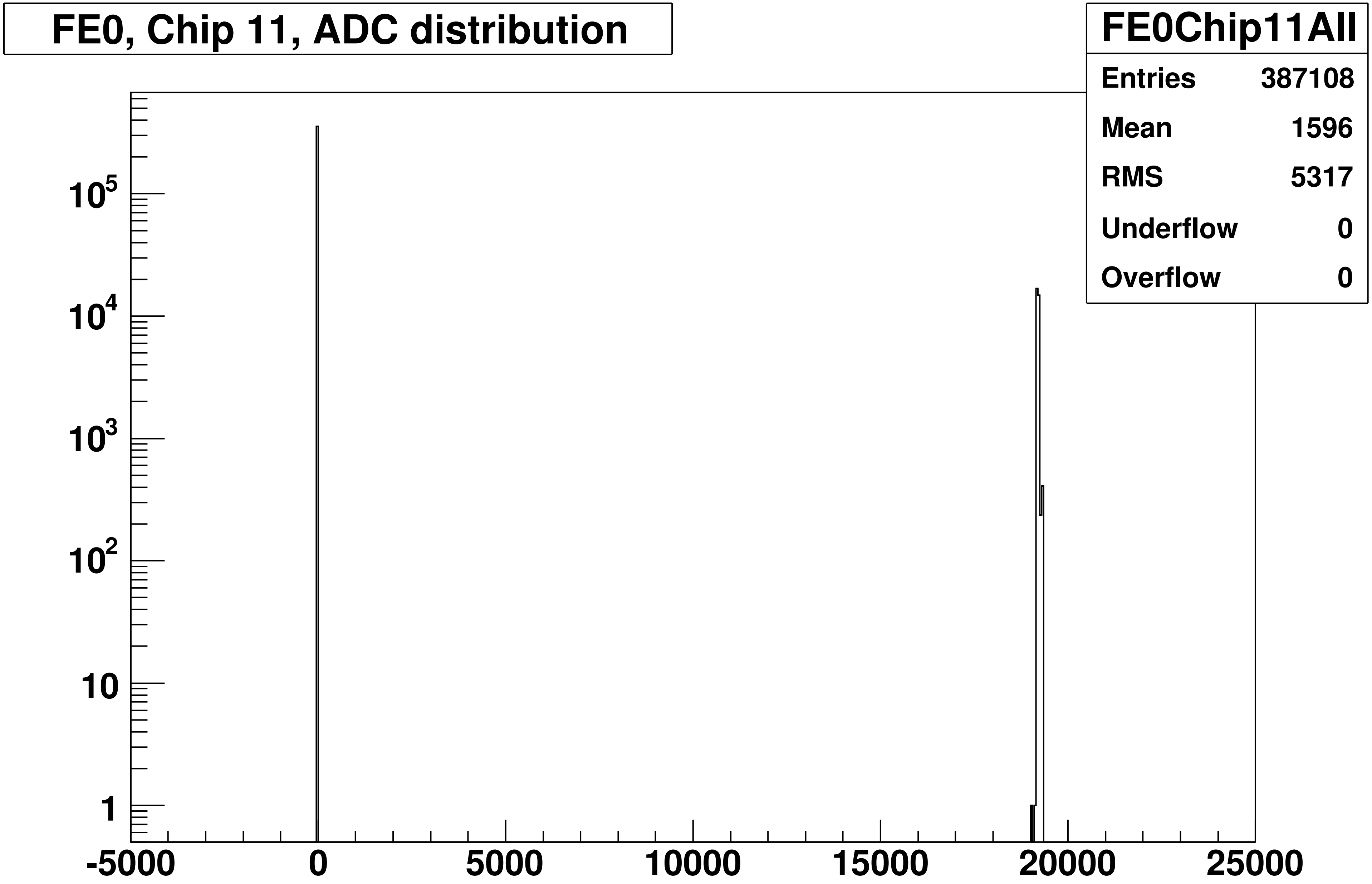
FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 387108
Mean -6.688
RMS 1.33
Underflow 0
Overflow 3.233e+04
10^5
10^4
10^3
-100 -80 -60 -40 -20 0 20 40 60 80 100

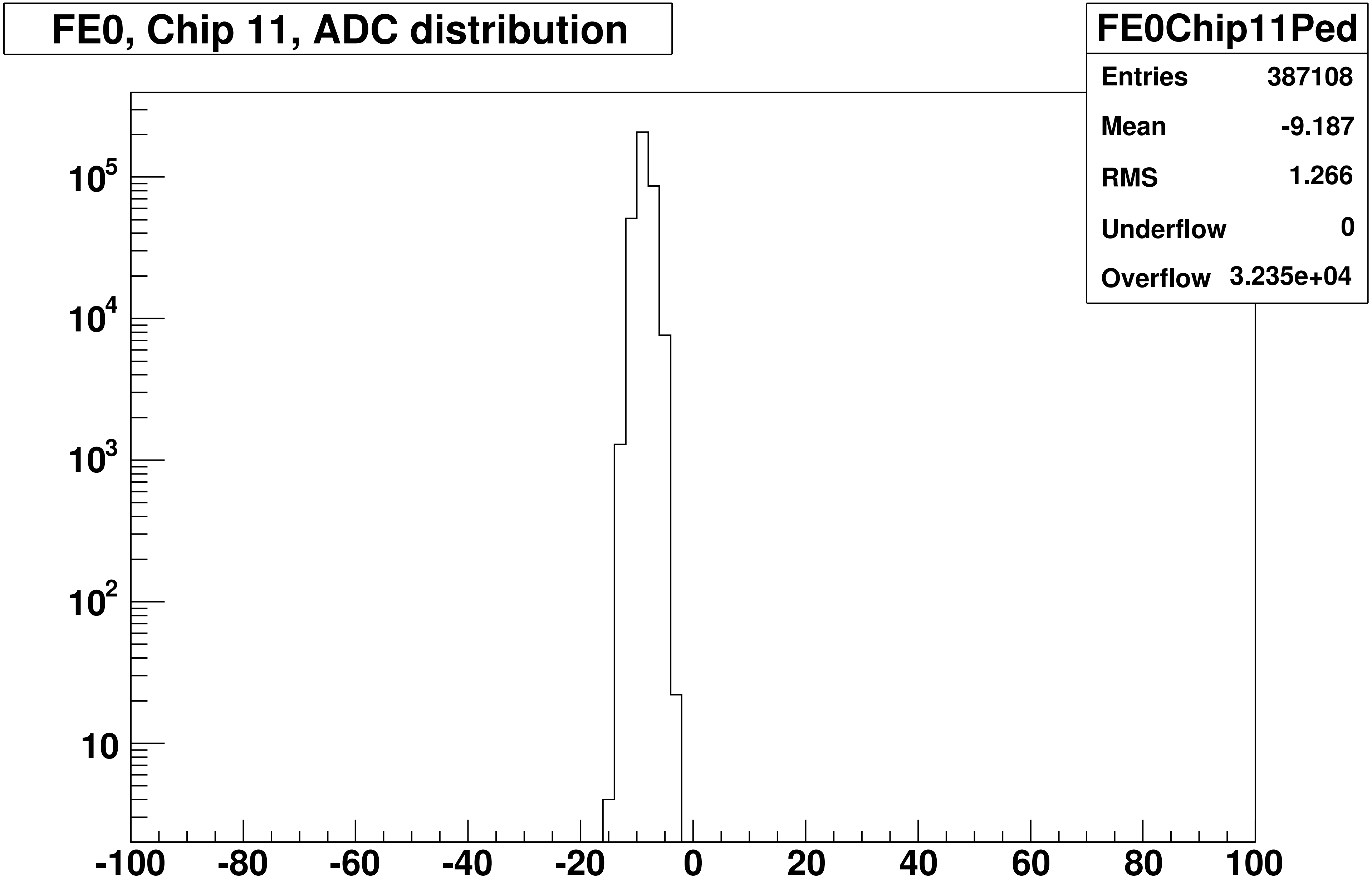

FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 387108
Mean 1.916e+04
RMS 19.17
Underflow 3.548e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

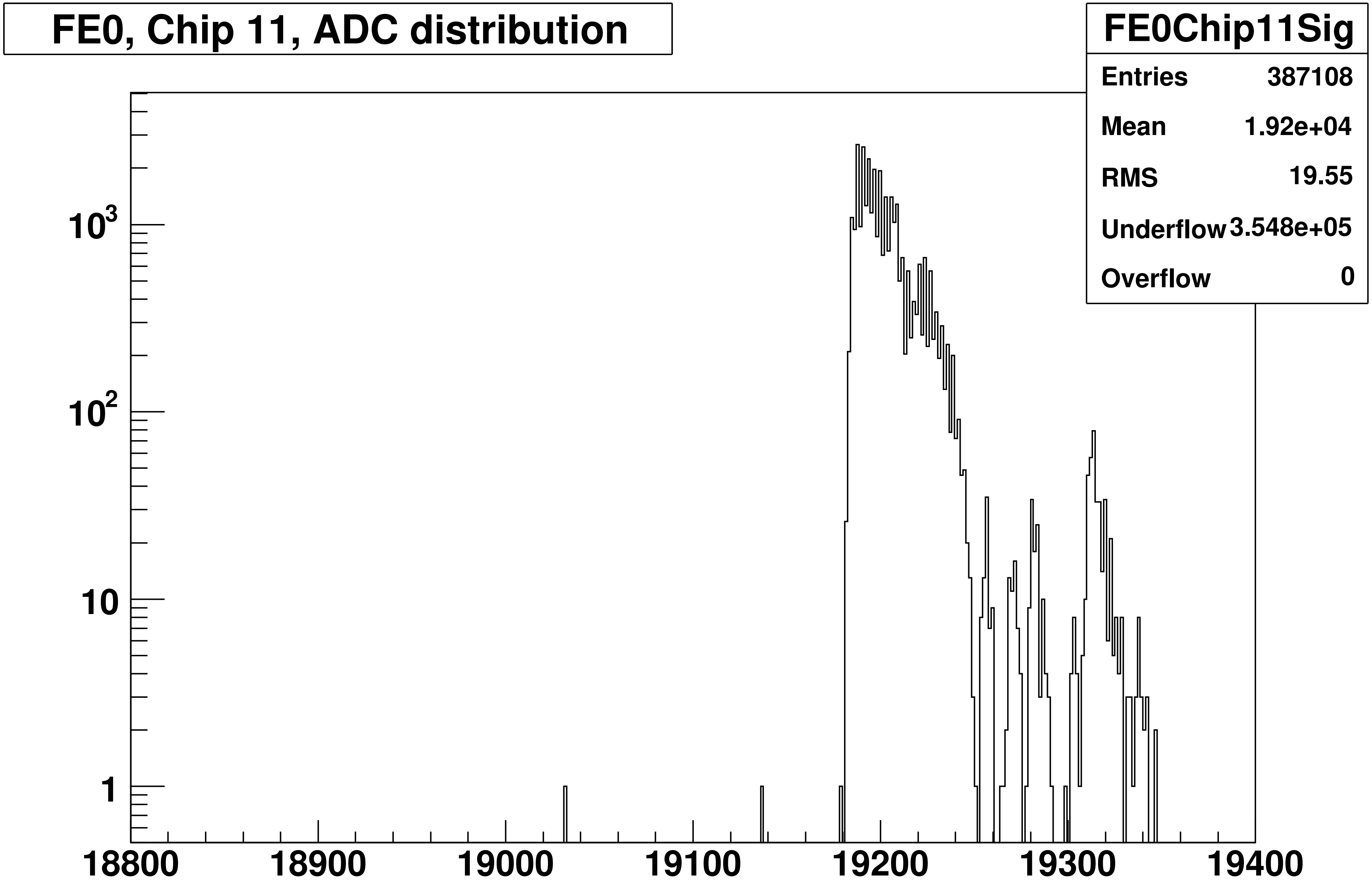

FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 387108
Mean 1587
RMS 5286
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

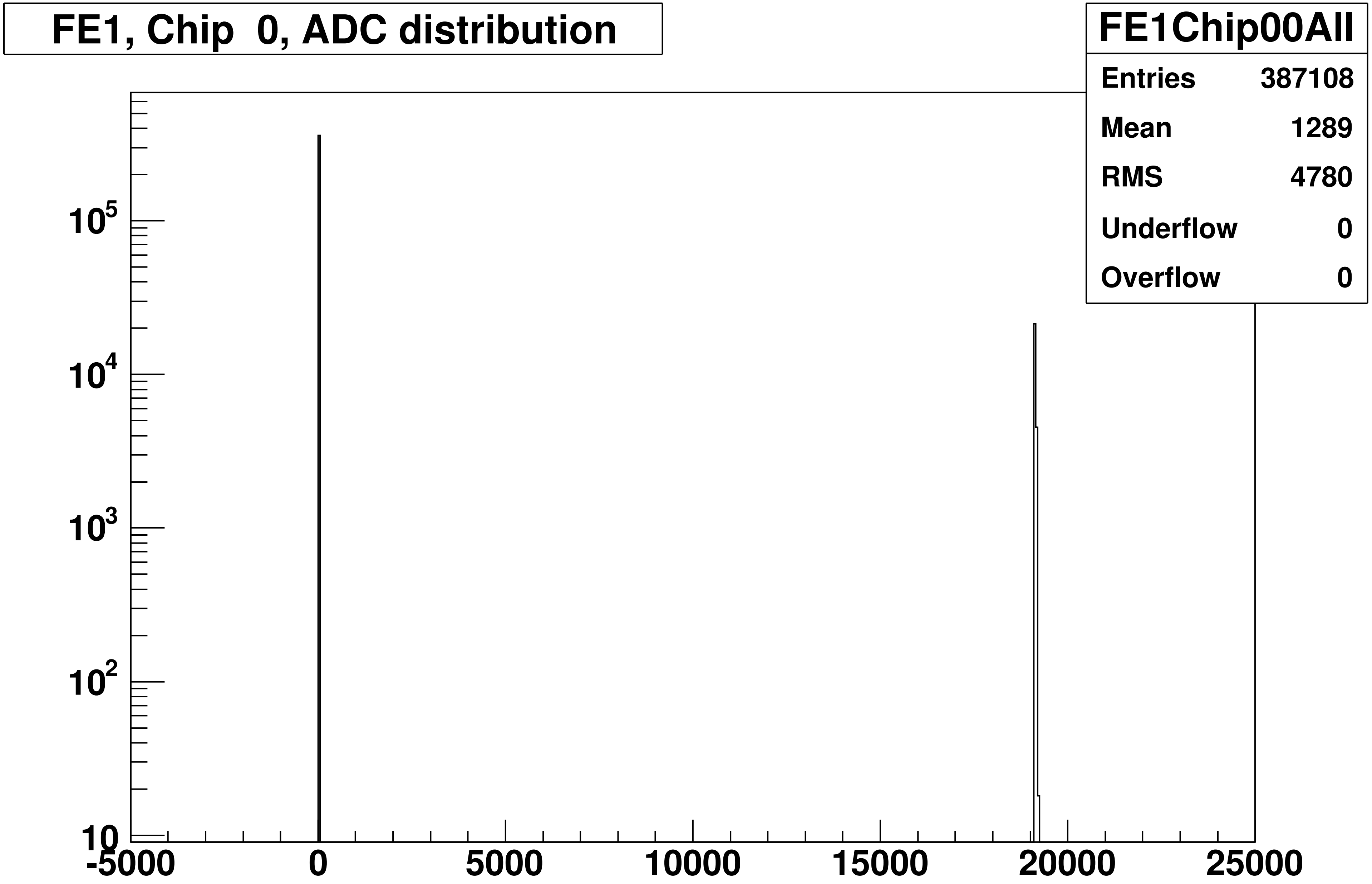

FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 387108
Mean -8.168
RMS 1.321
Underflow 0
Overflow 3.235e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

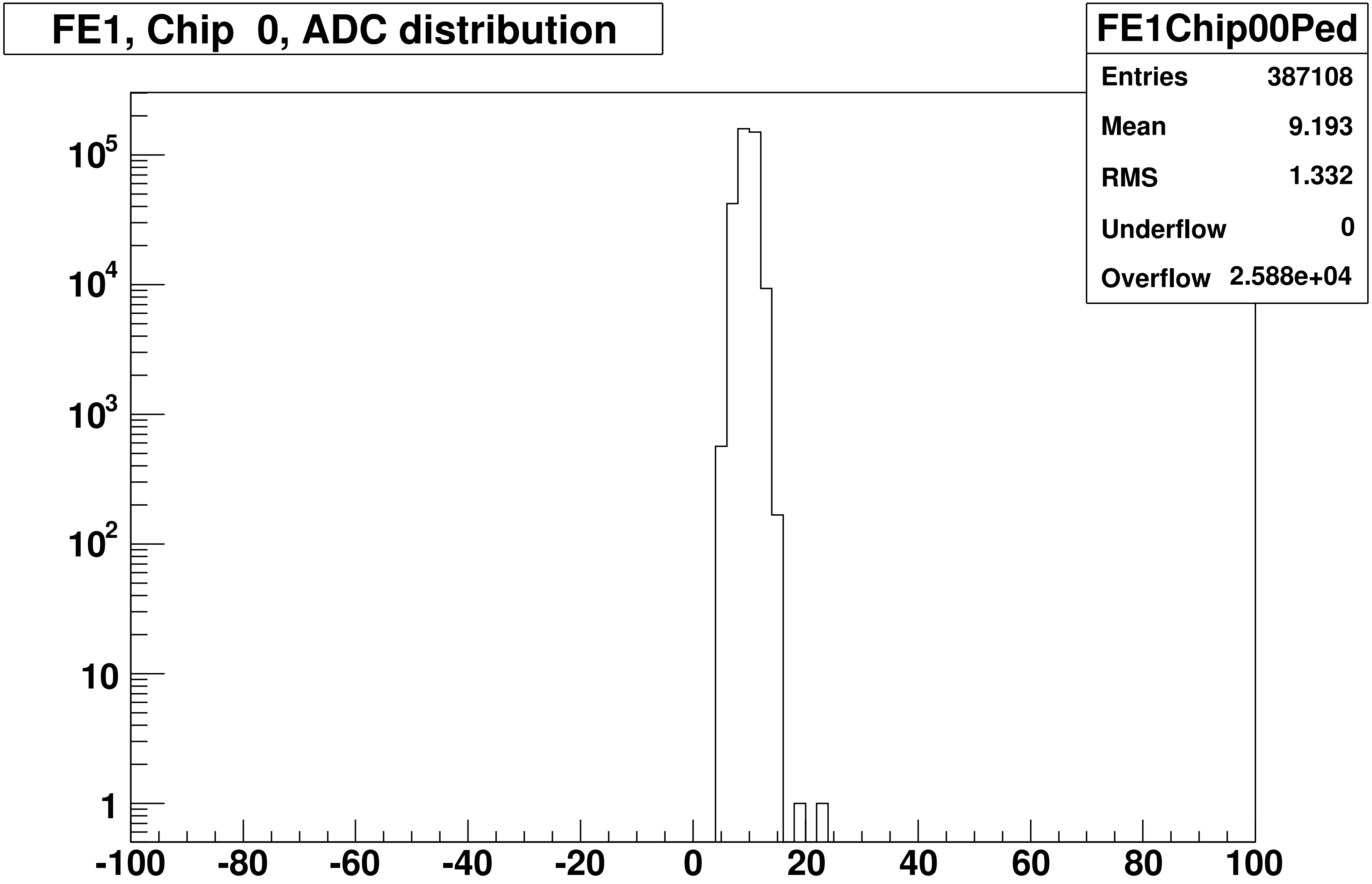

FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 387108
Mean 1.91e+04
RMS 18.95
Underflow 3.548e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

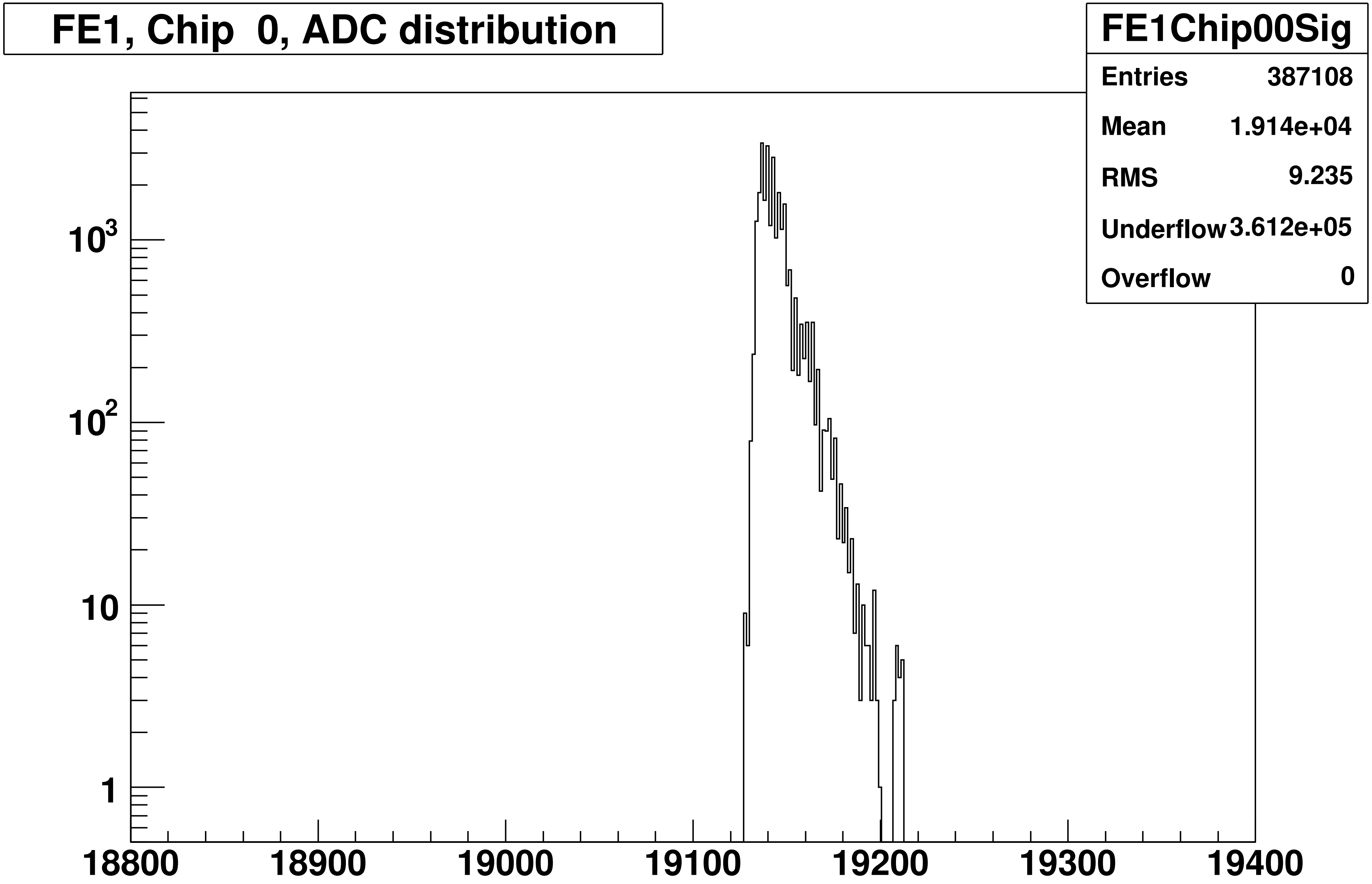

FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 387108
Mean 1596
RMS 5317
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

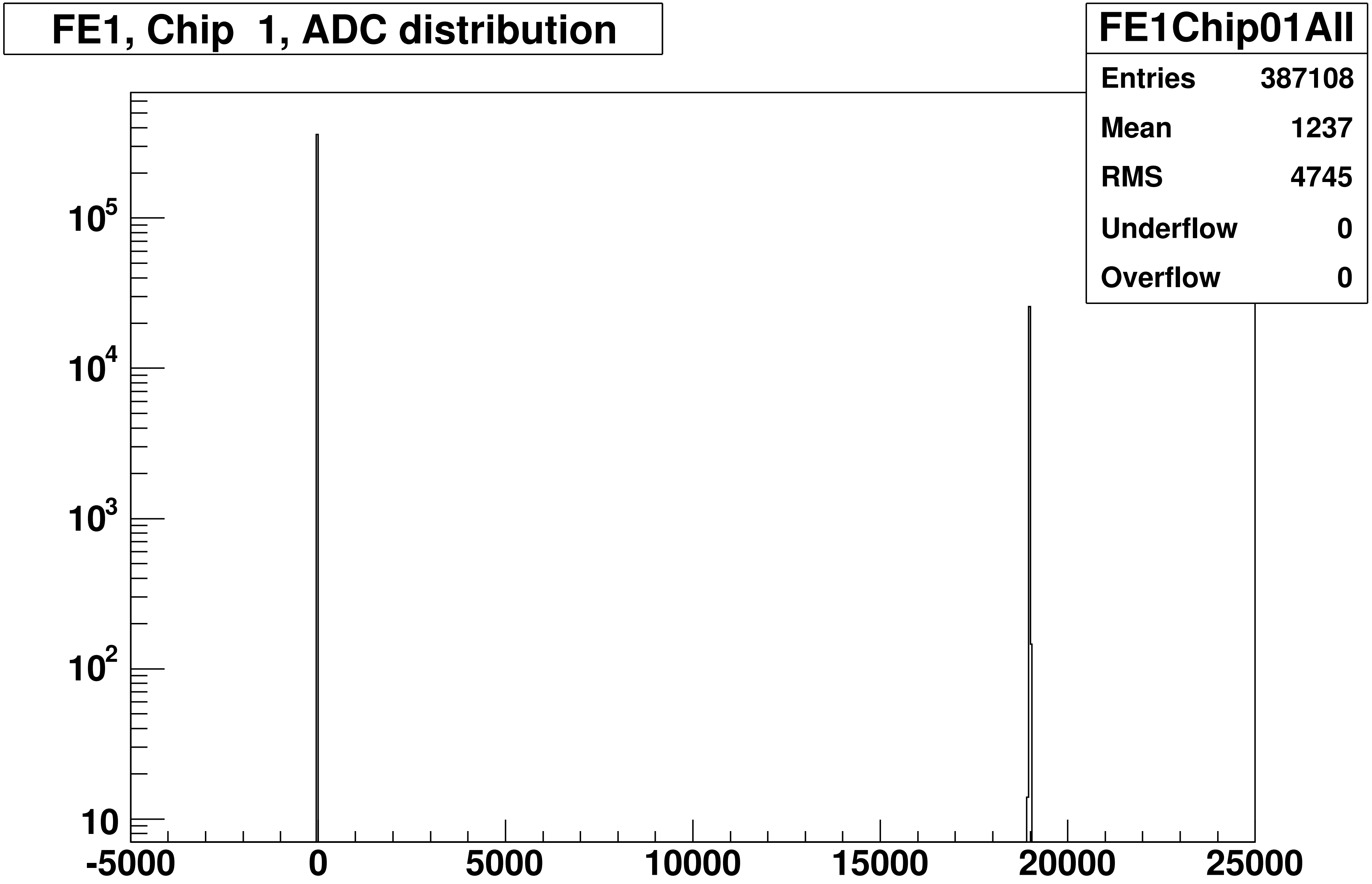

FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 387108
Mean -9.187
RMS 1.266
Underflow 0
Overflow 3.235e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 387108
Mean 1.92e+04
RMS 19.55
Underflow 3.548e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

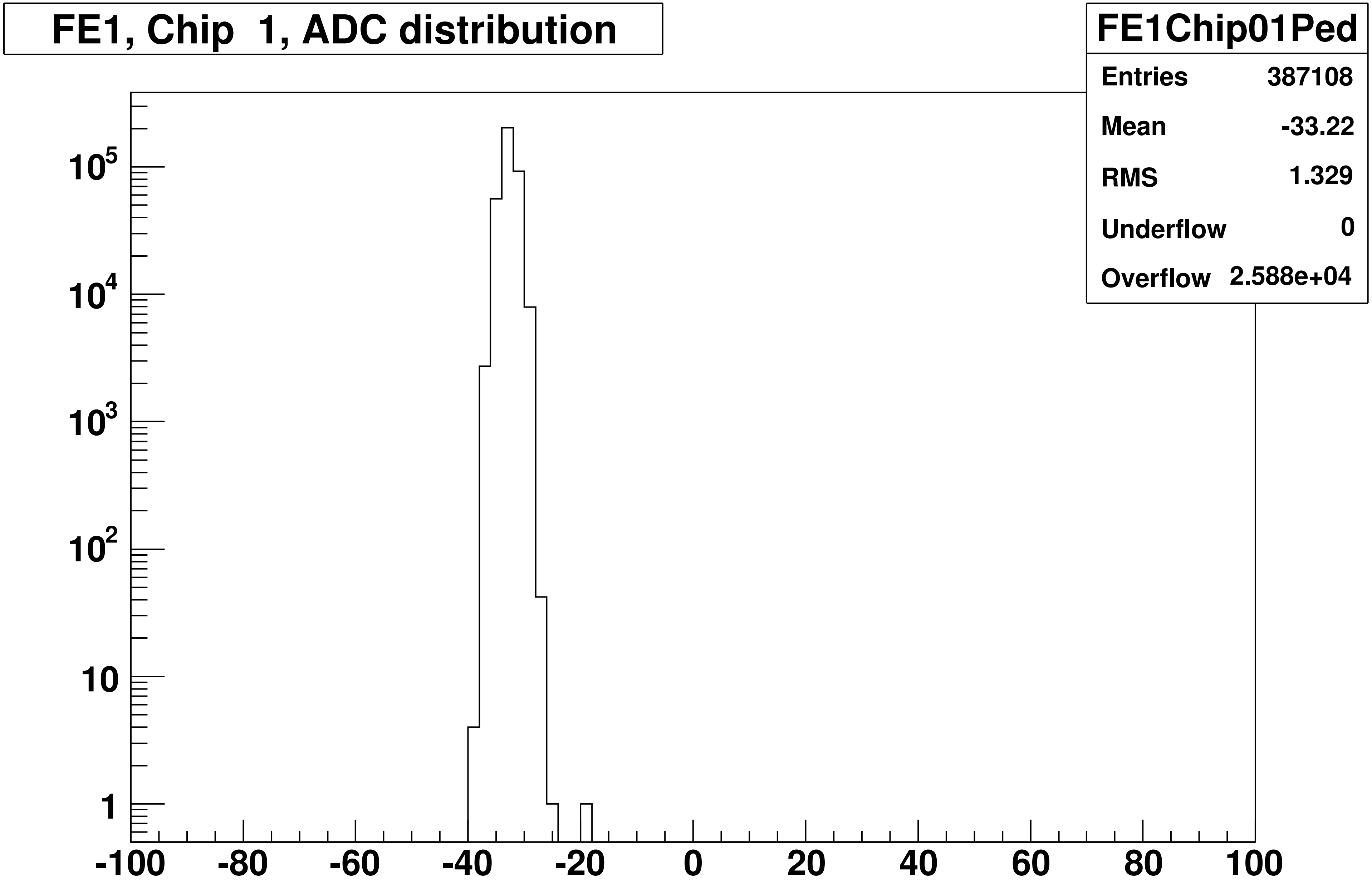

FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 387108
Mean 1289
RMS 4780
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

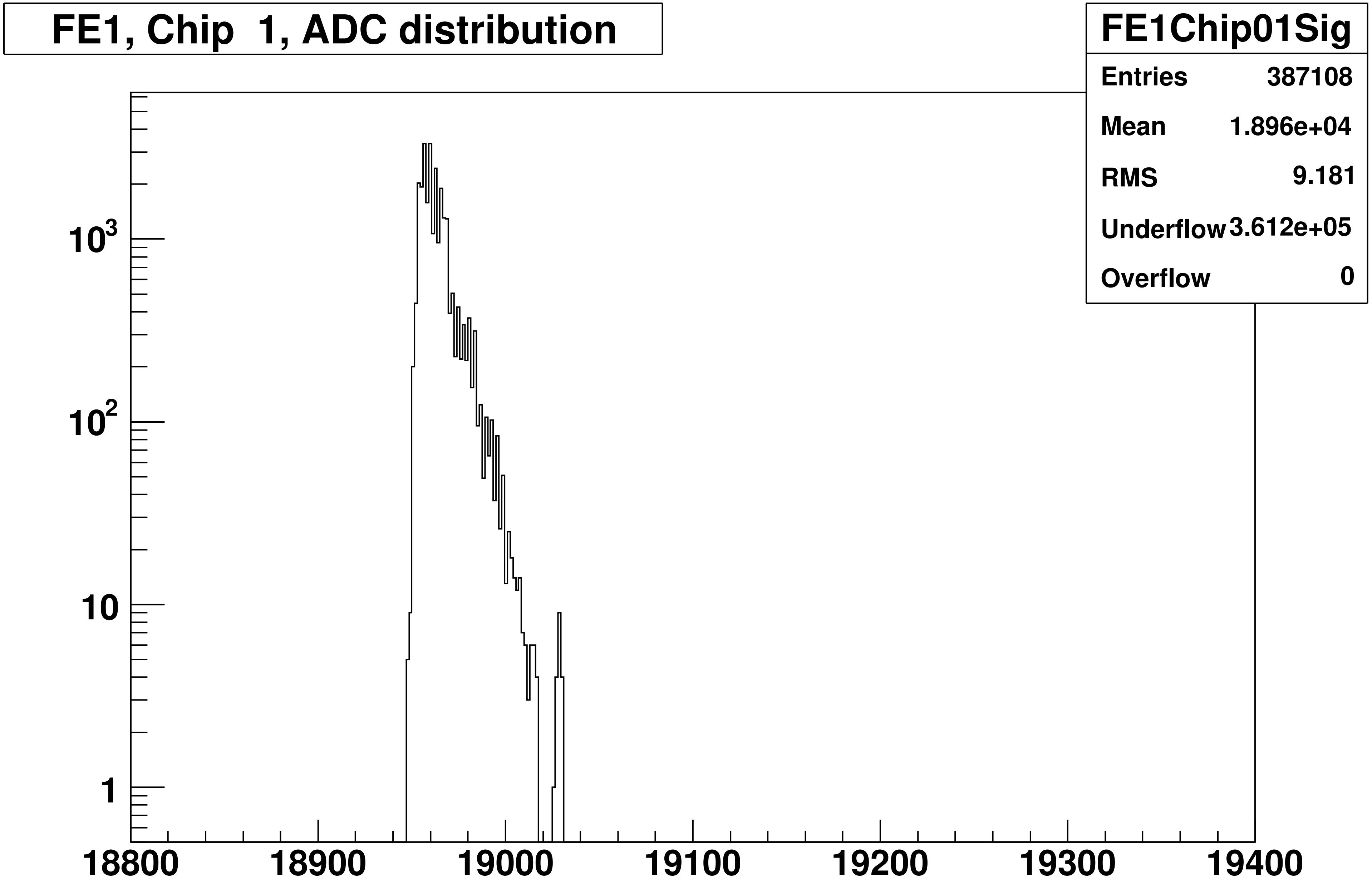

FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 387108
Mean 9.193
RMS 1.332
Underflow 0
Overflow 2.588e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 387108
Mean 1.914e+04
RMS 9.235
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

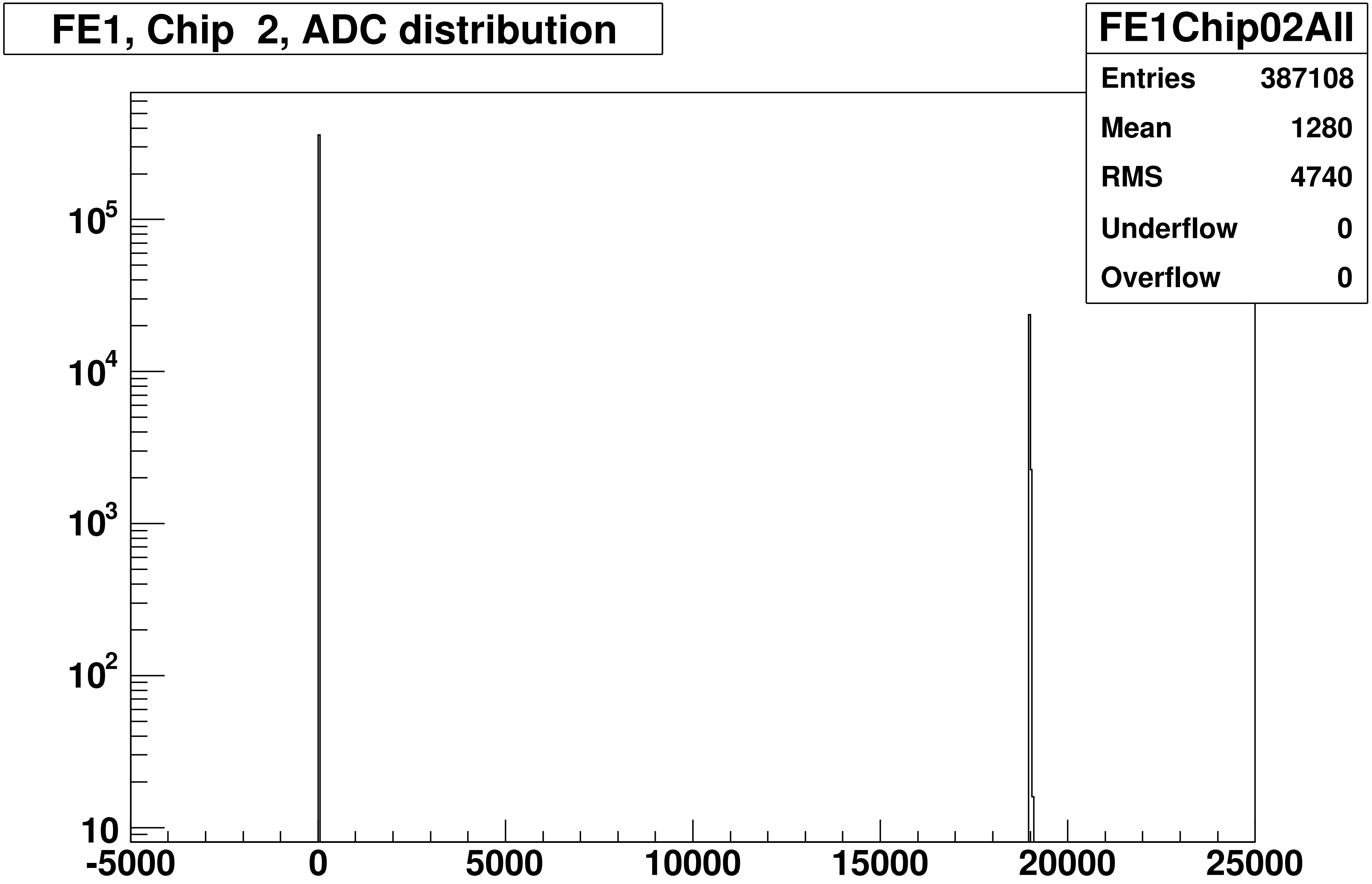


FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 387108
Mean 1237
RMS 4745
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

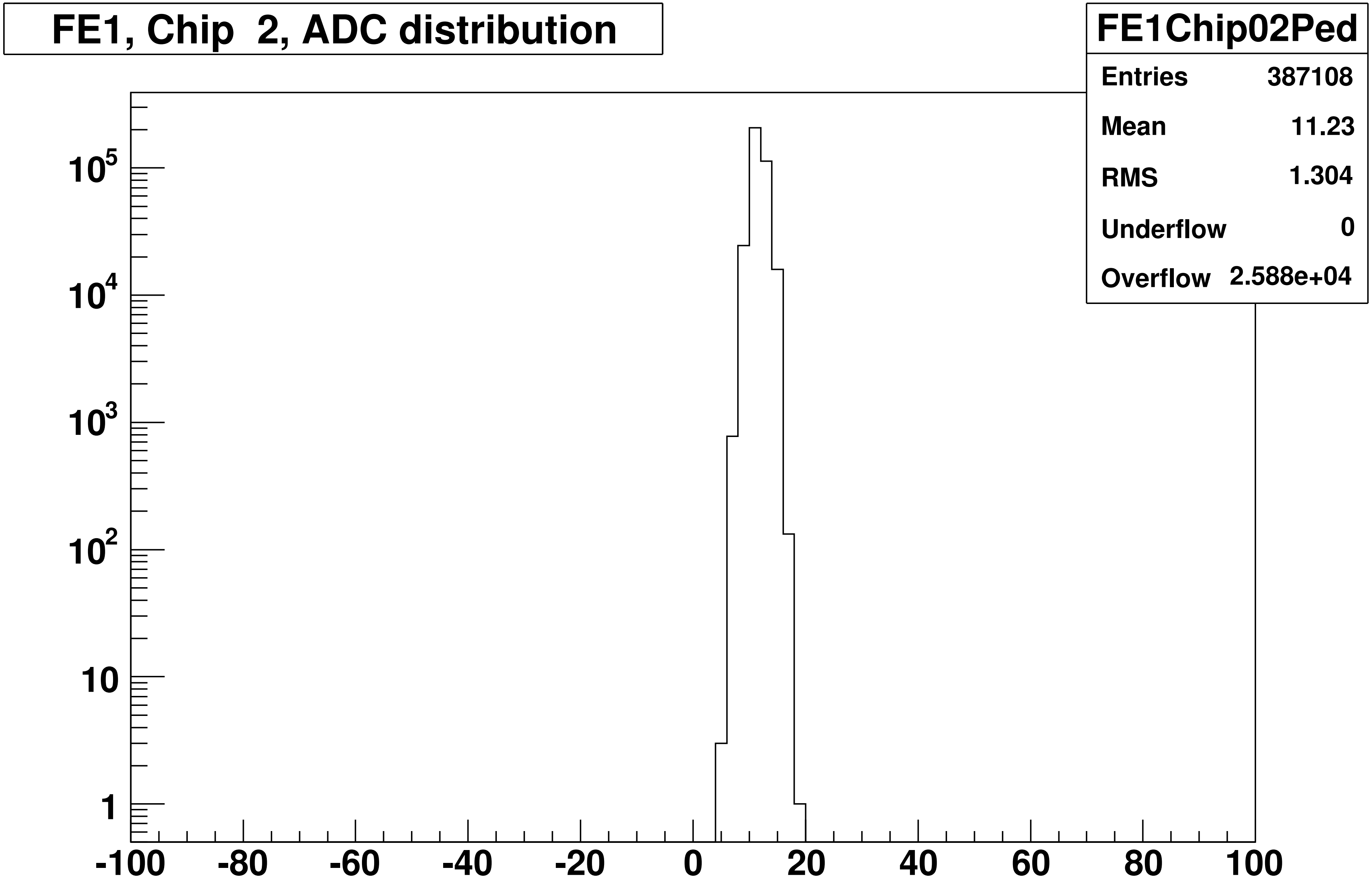

FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 387108
Mean -33.22
RMS 1.329
Underflow 0
Overflow 2.588e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

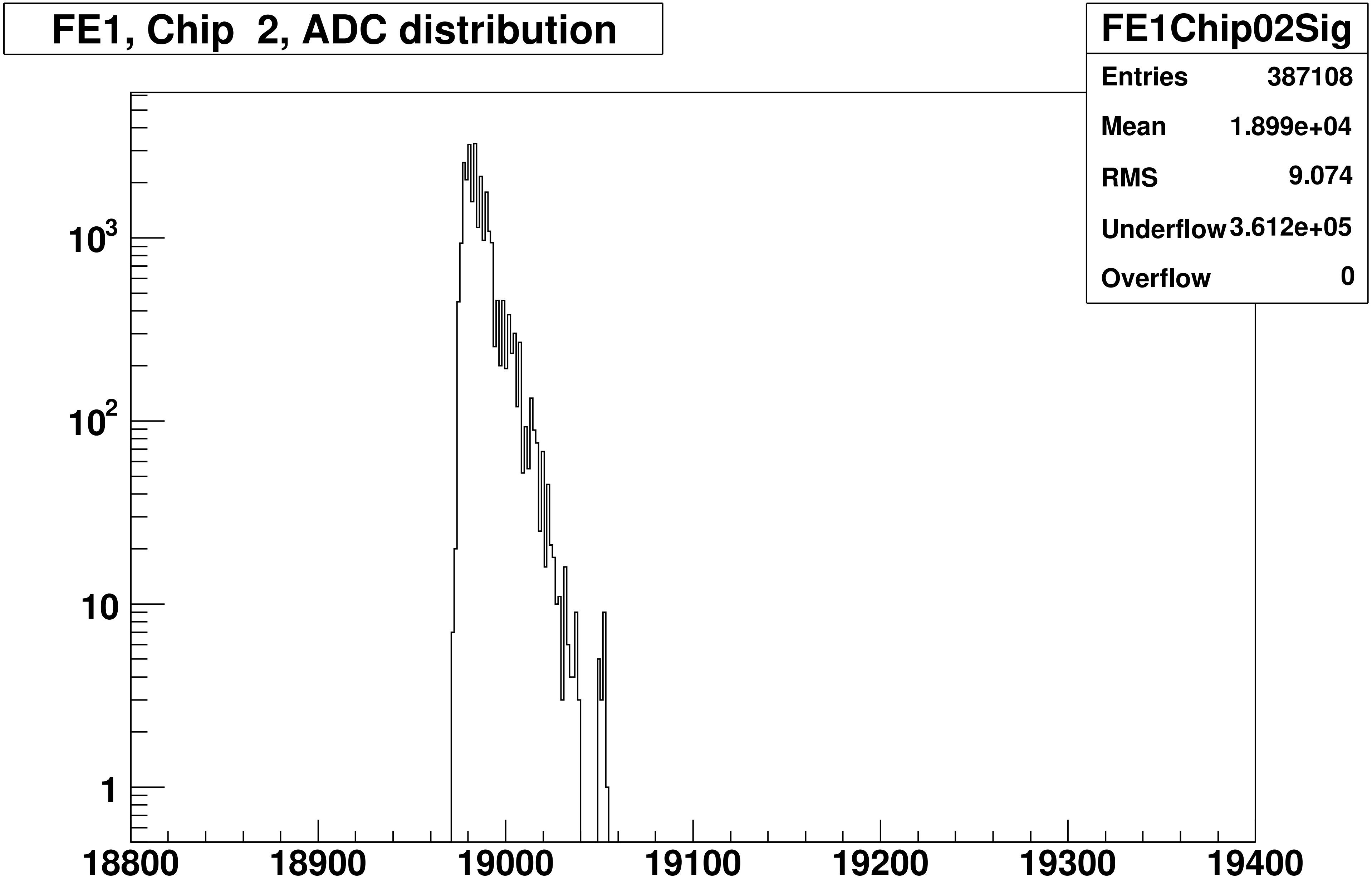


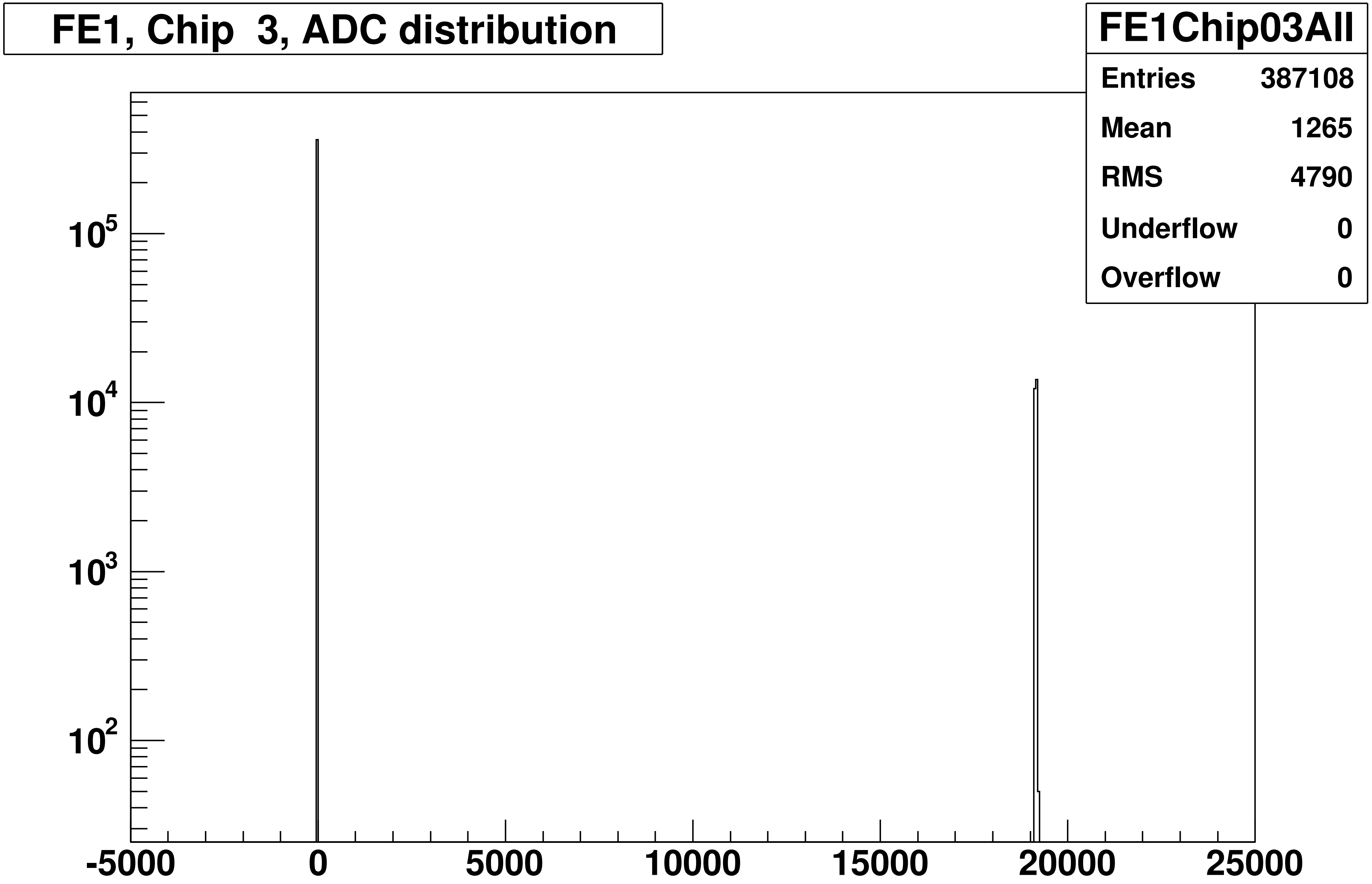
FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 387108
Mean 1.896e+04
RMS 9.181
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



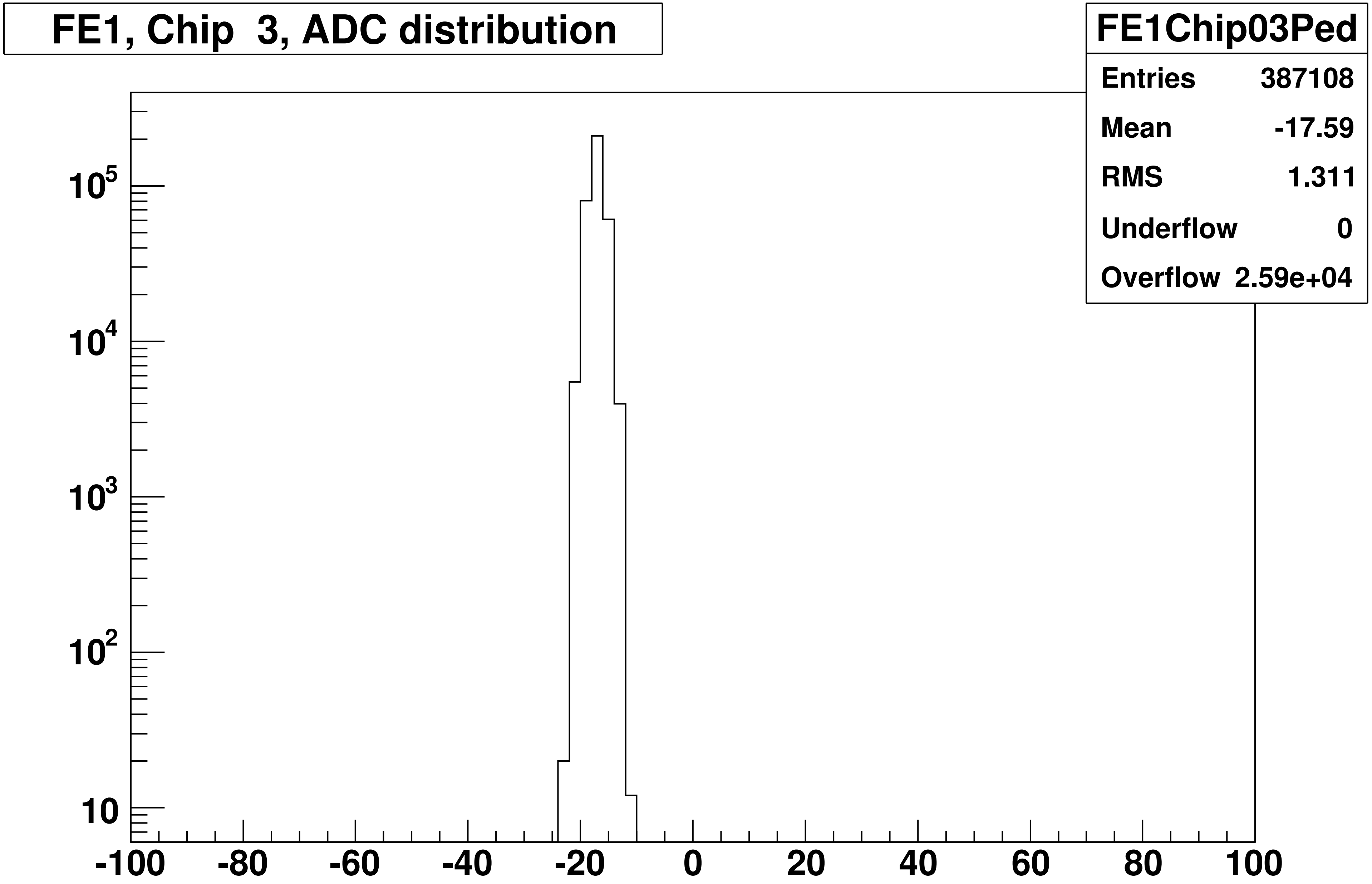
FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 387108
Mean 1280
RMS 4740
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 387108
Mean 11.23
RMS 1.304
Underflow 0
Overflow 2.588e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

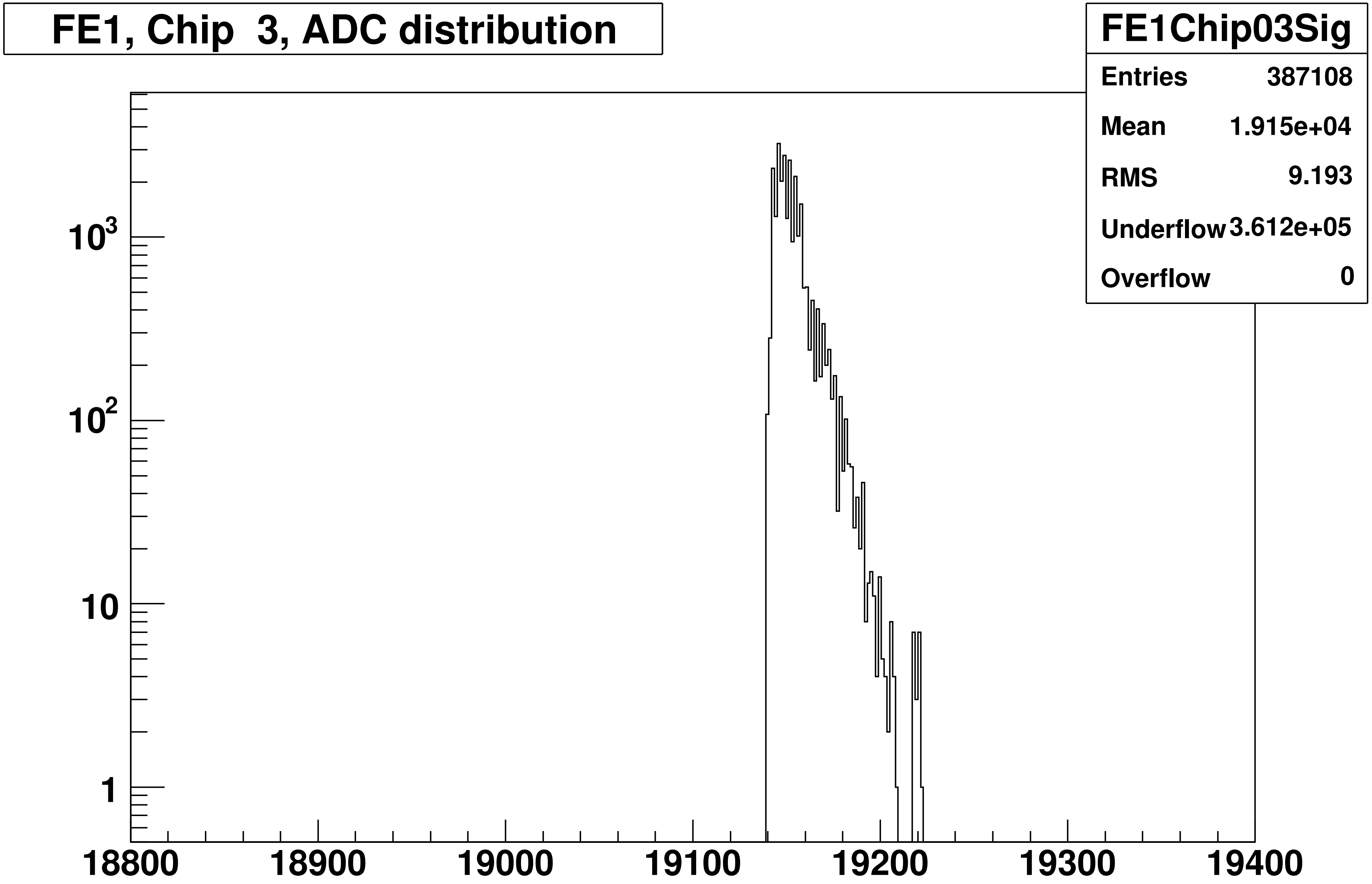

FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 387108
Mean 1.899e+04
RMS 9.074
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



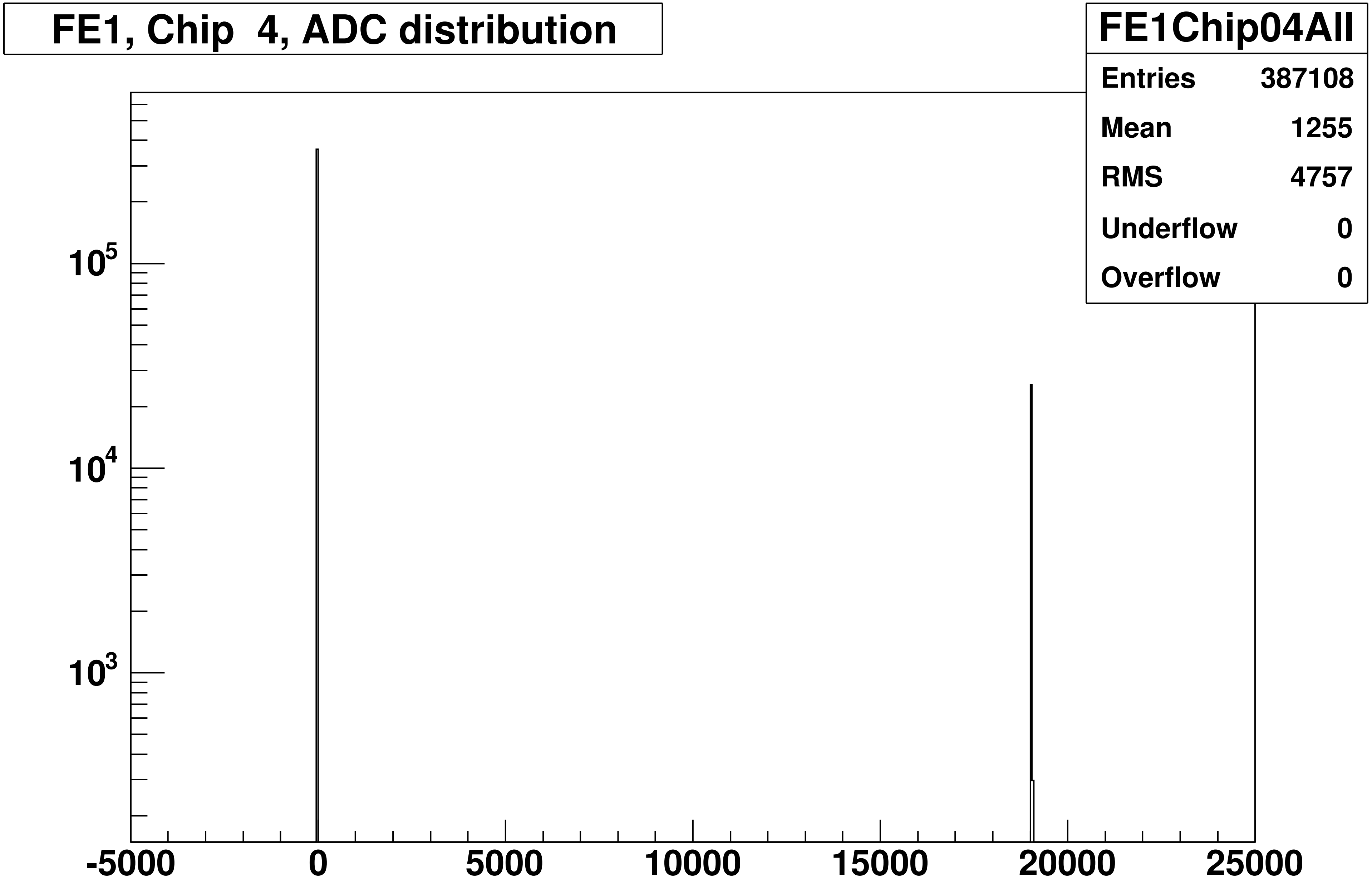
FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 387108
Mean 1265
RMS 4790
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

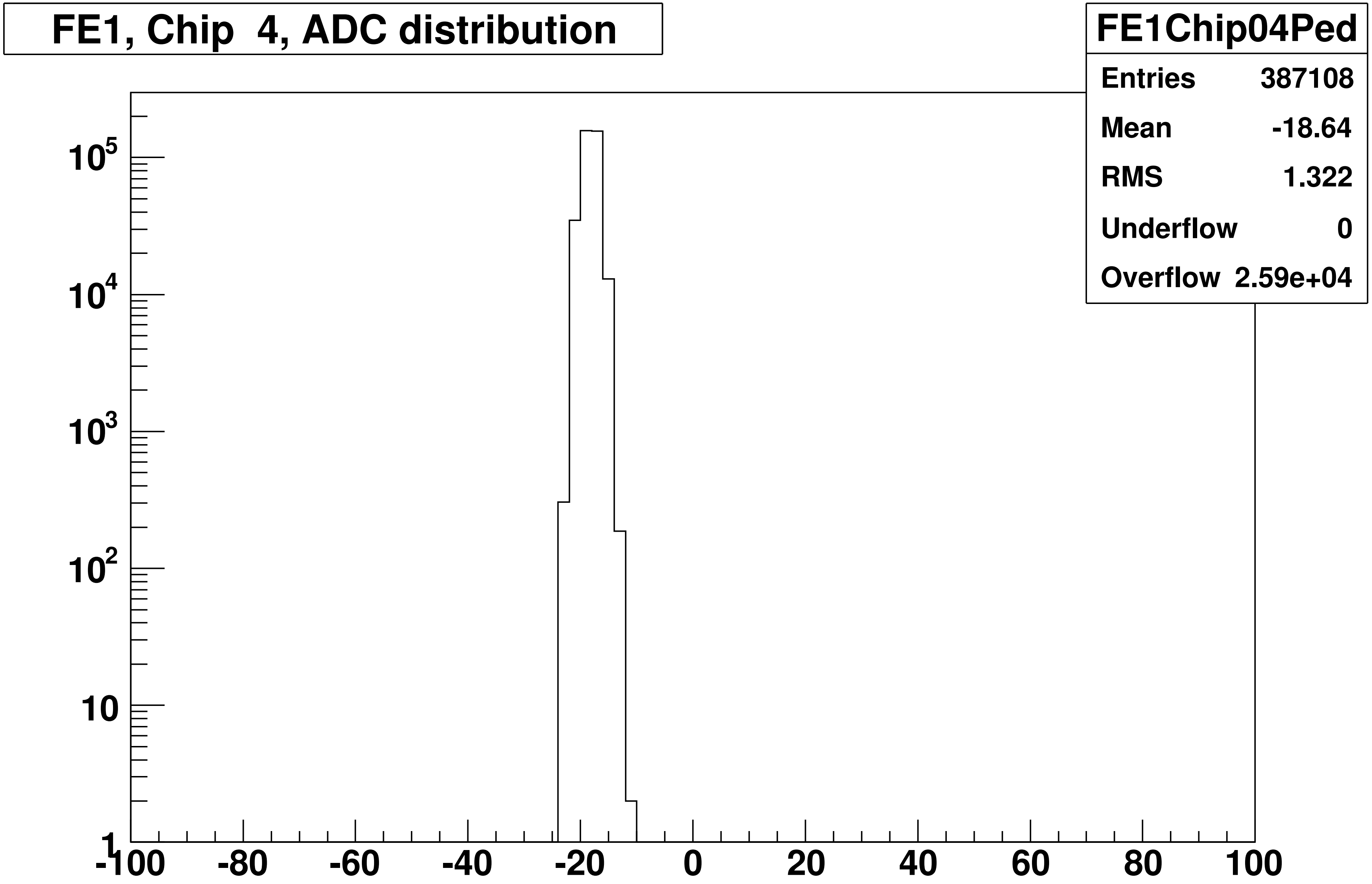


FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 387108
Mean -17.59
RMS 1.311
Underflow 0
Overflow 2.59e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

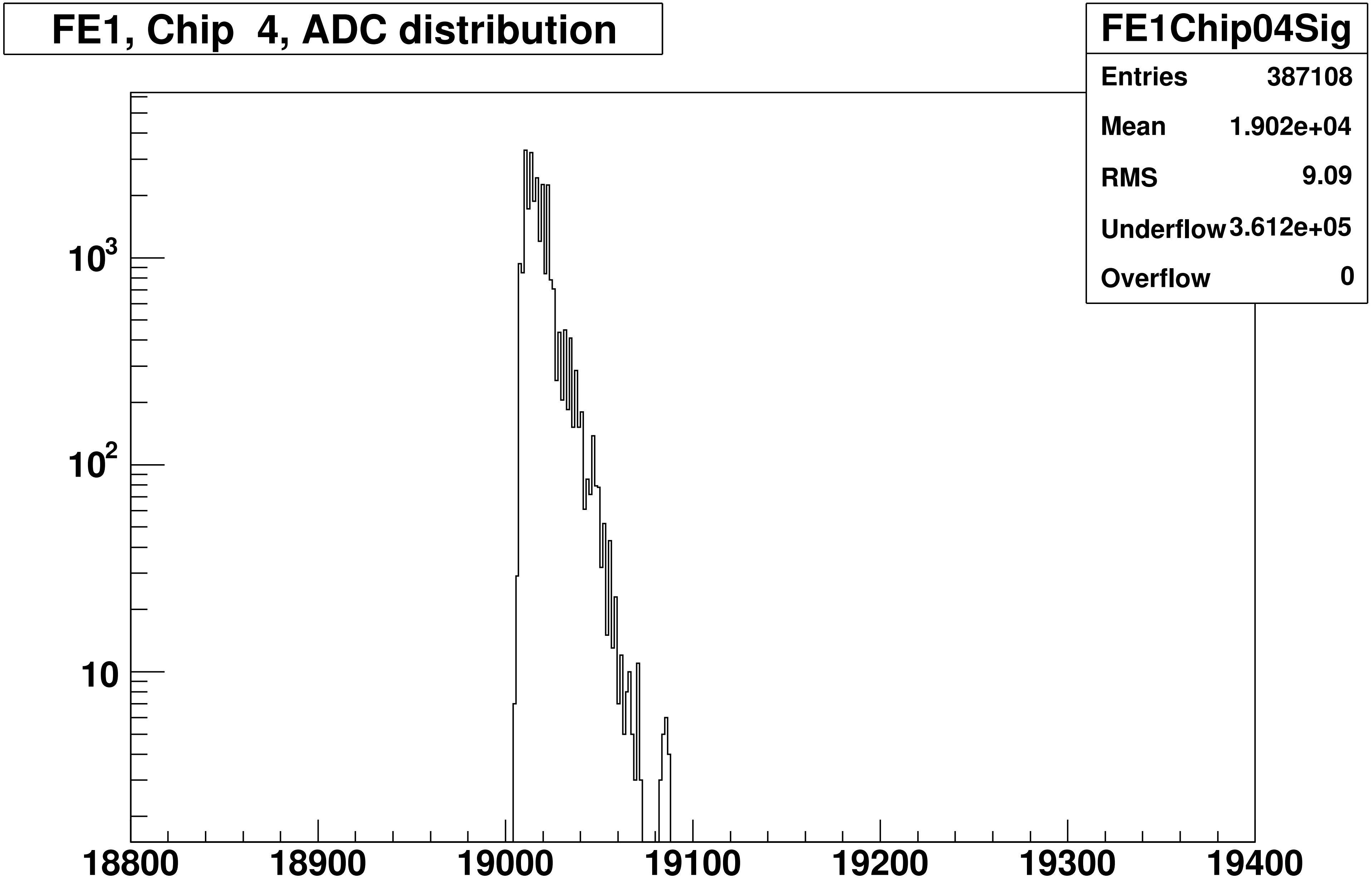


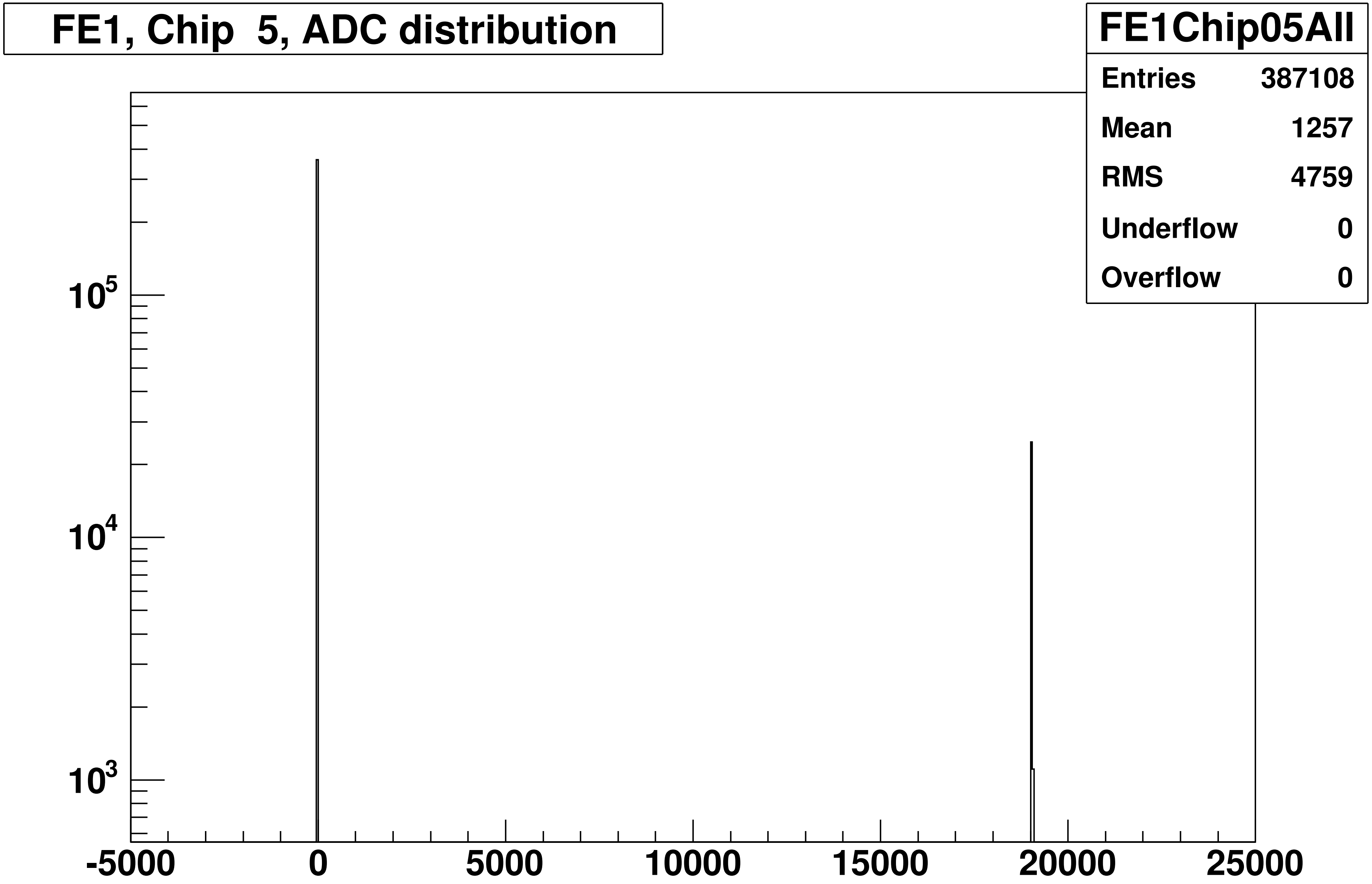
FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 387108
Mean 1.915e+04
RMS 9.193
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 387108
Mean 1255
RMS 4757
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

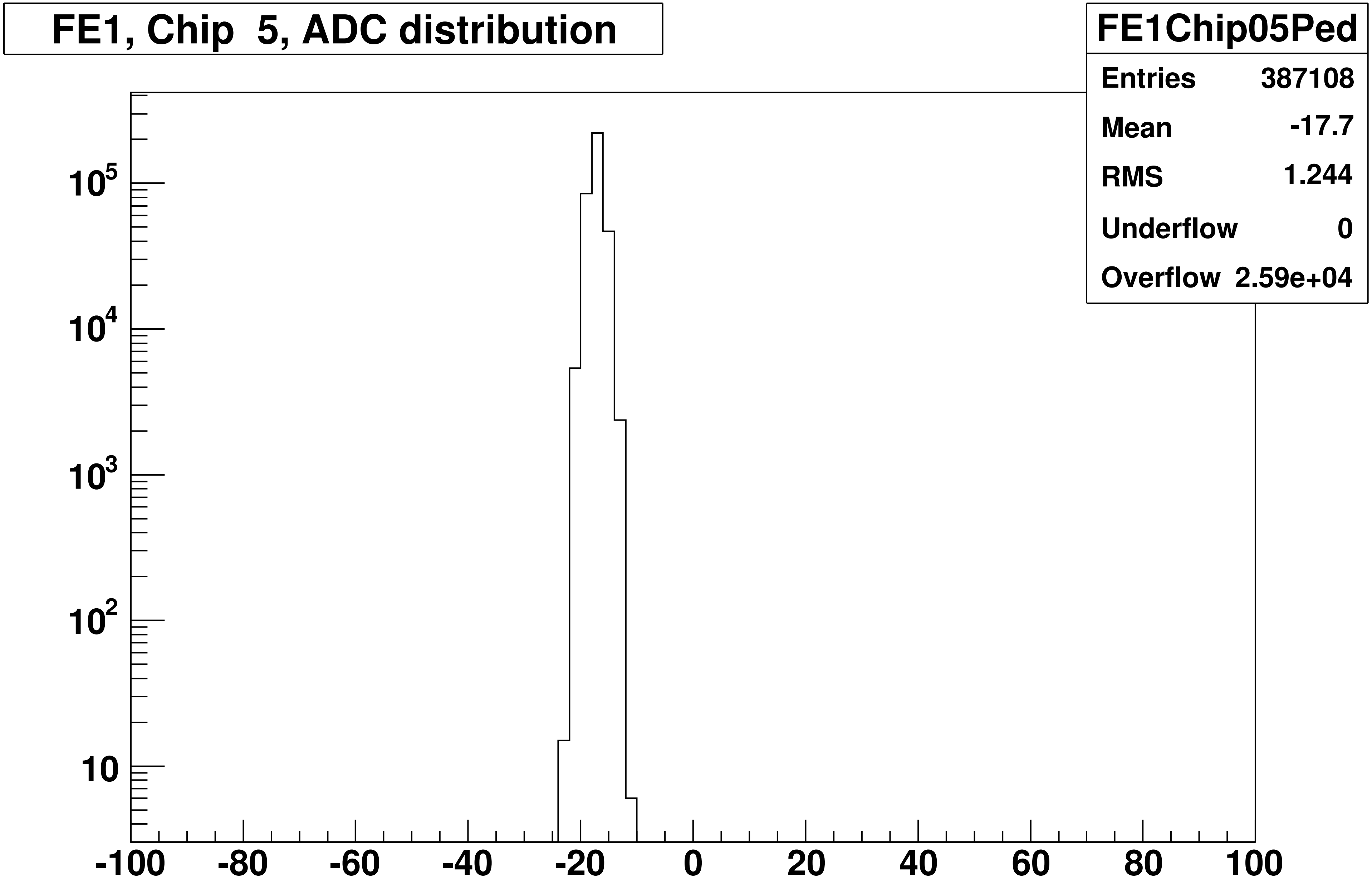


FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 387108
Mean -18.64
RMS 1.322
Underflow 0
Overflow 2.59e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

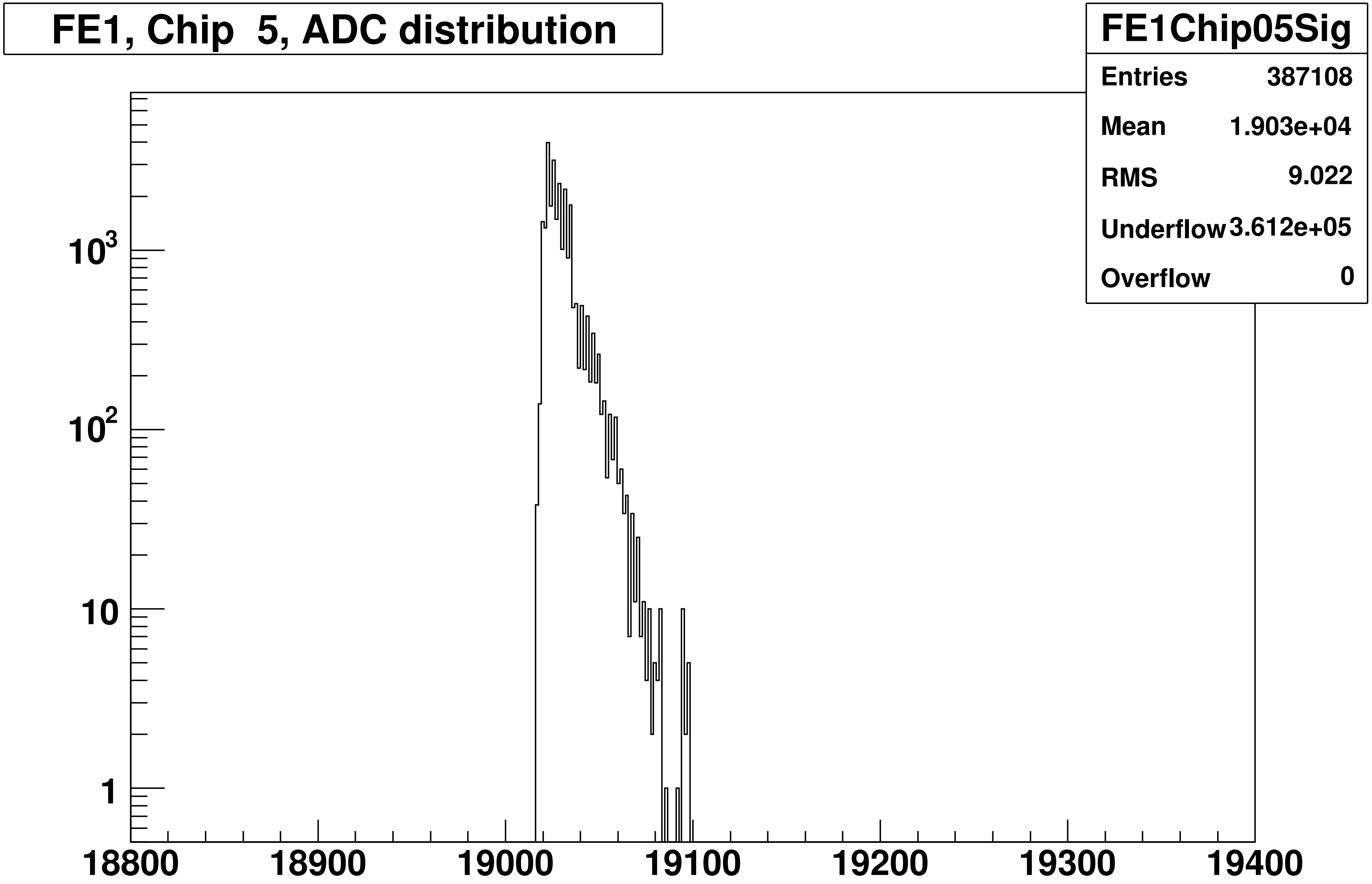

FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 387108
Mean 1.902e+04
RMS 9.09
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400

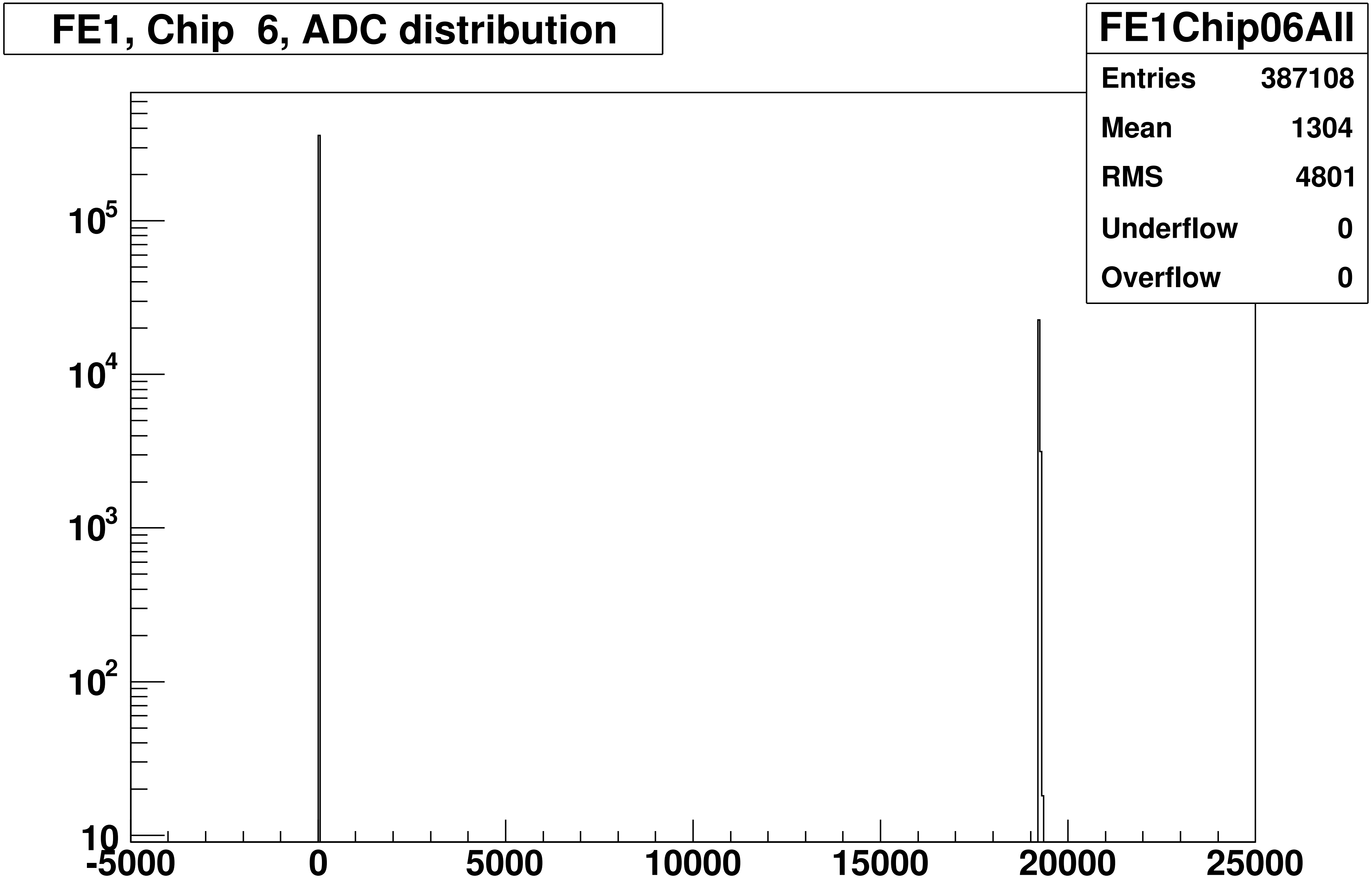


FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 387108
Mean 1257
RMS 4759
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

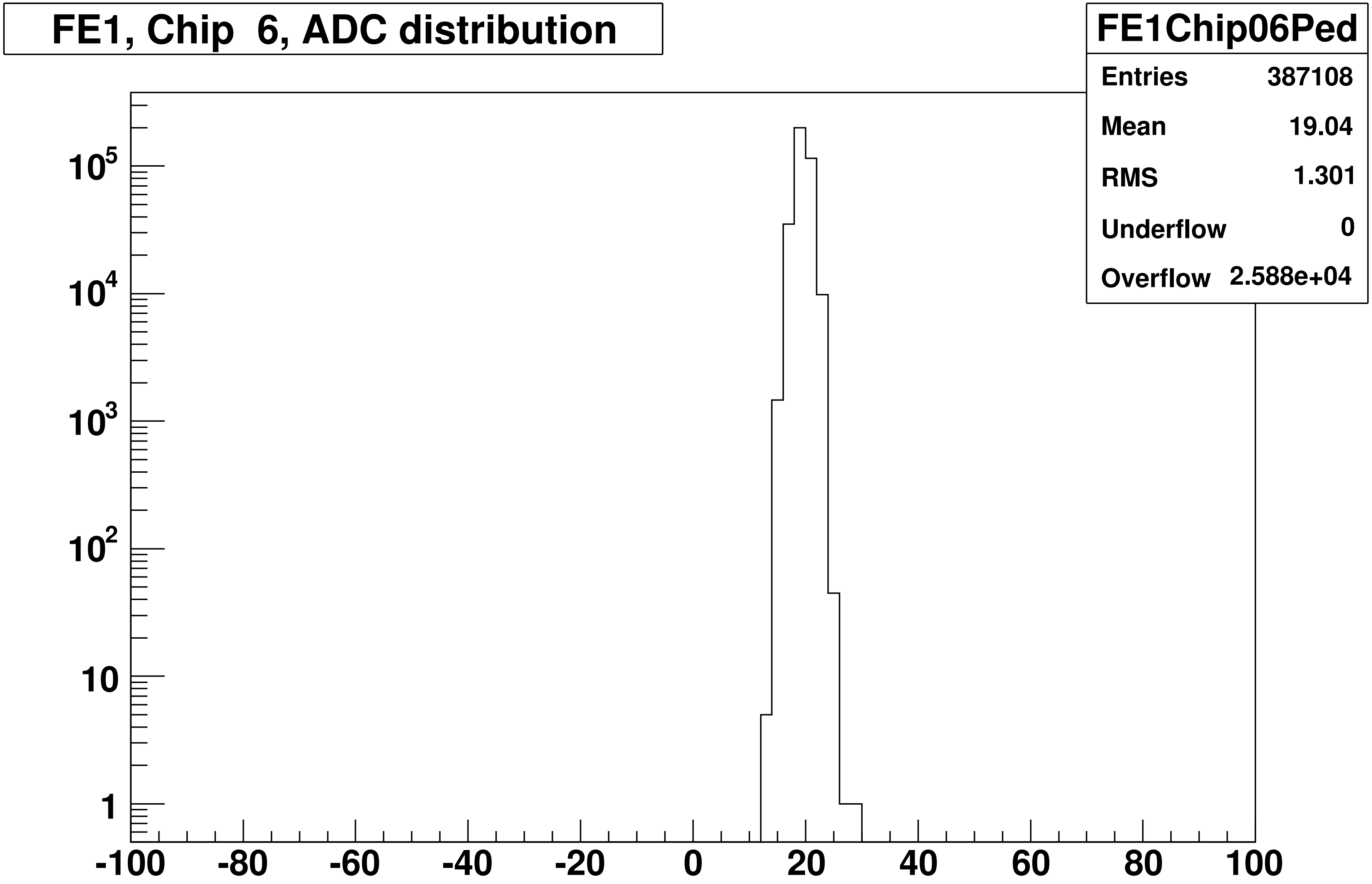


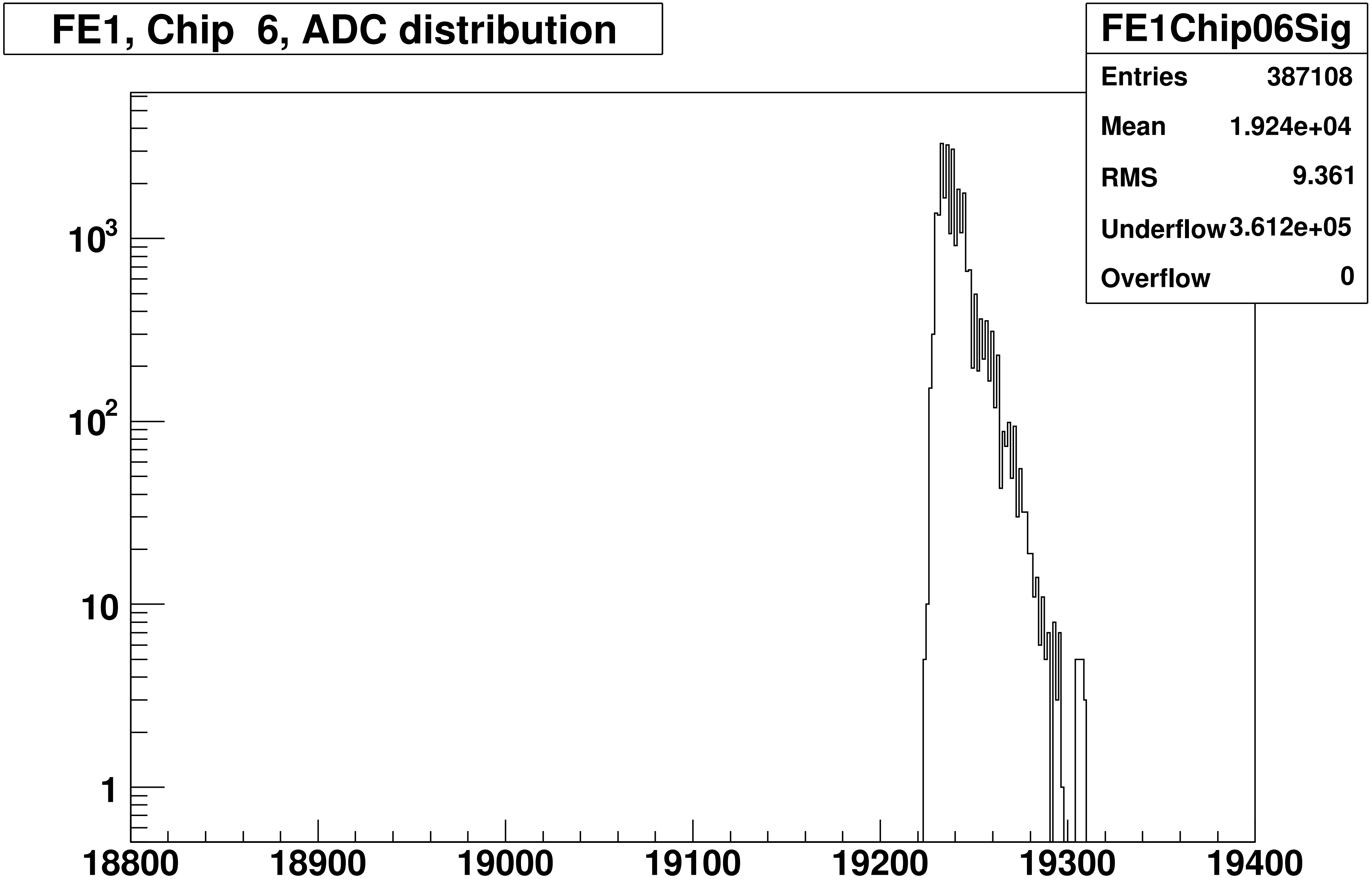
FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 387108
Mean -17.7
RMS 1.244
Underflow 0
Overflow 2.59e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 387108
Mean 1.903e+04
RMS 9.022
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

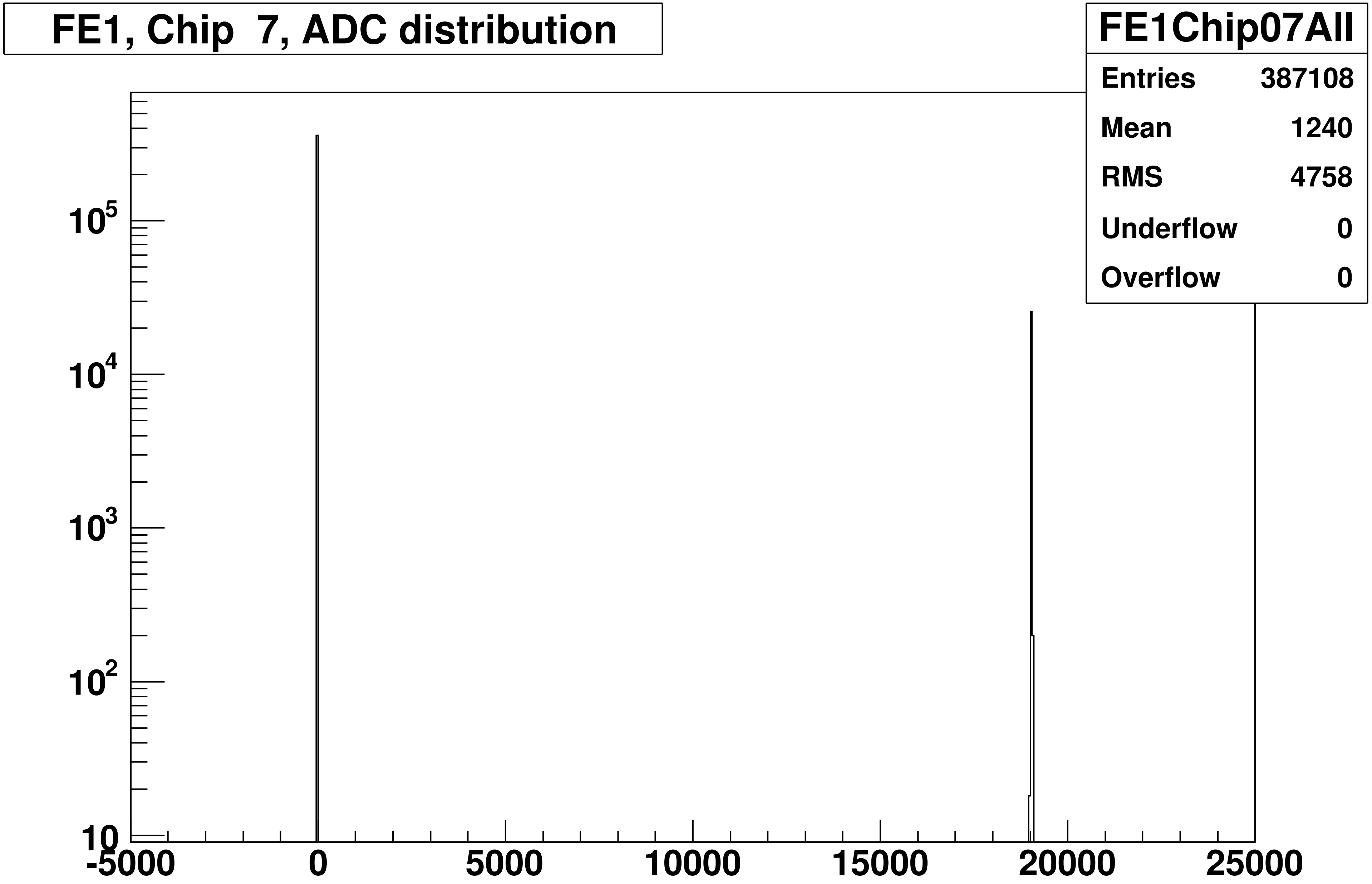


FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 387108
Mean 1304
RMS 4801
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

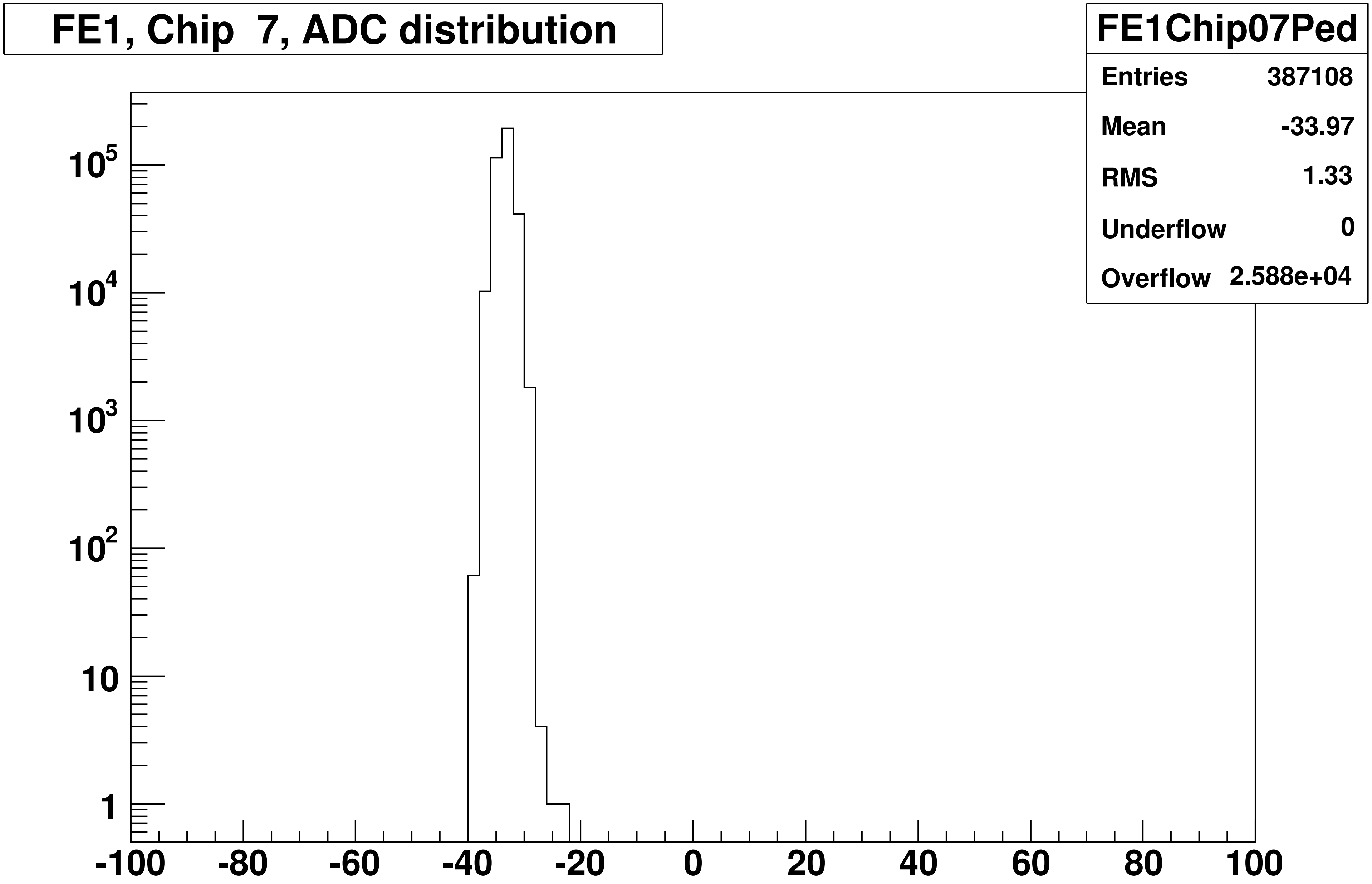

FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 387108
Mean 19.04
RMS 1.301
Underflow 0
Overflow 2.588e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



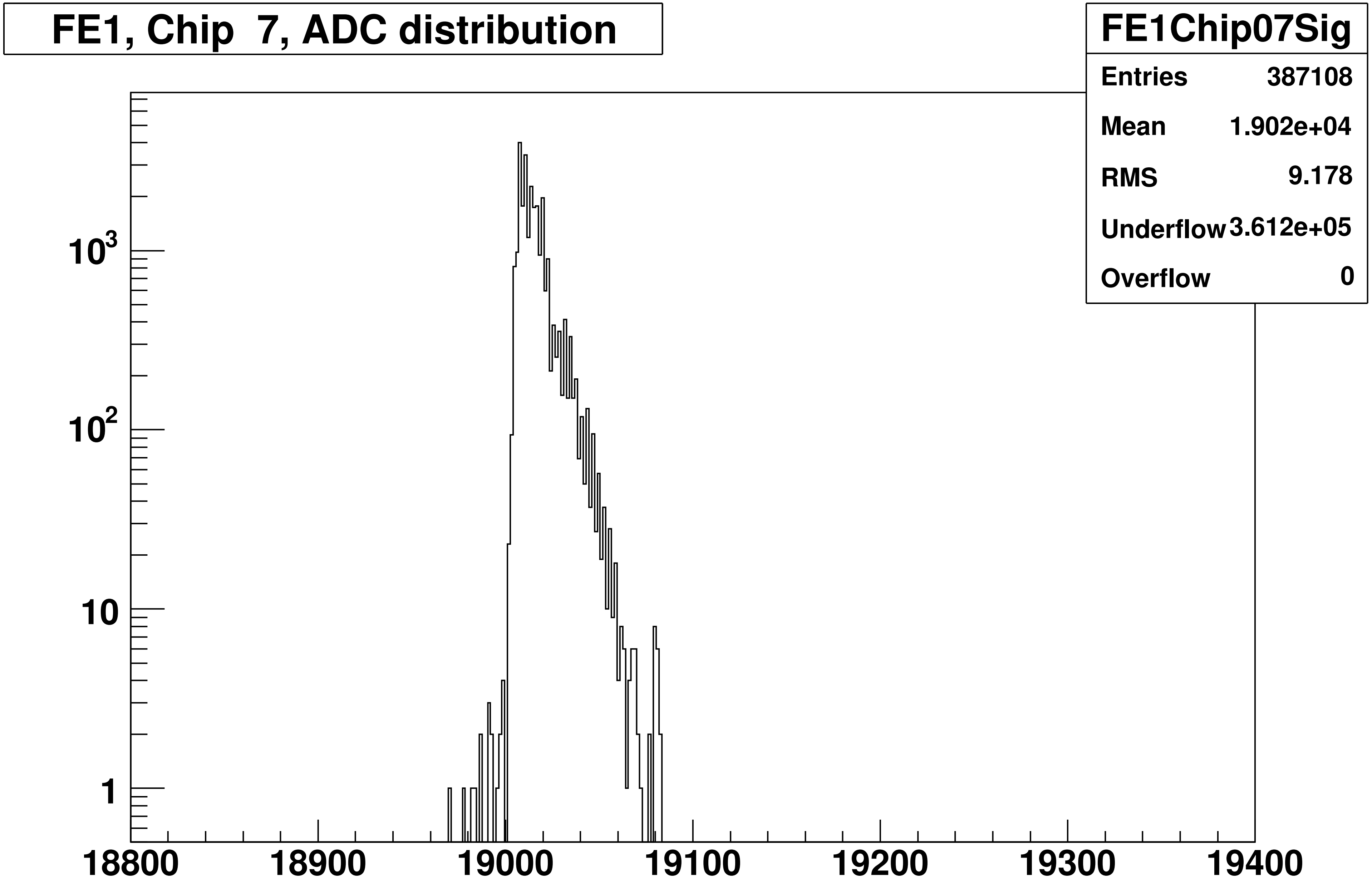
FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 387108
Mean 1.924e+04
RMS 9.361
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



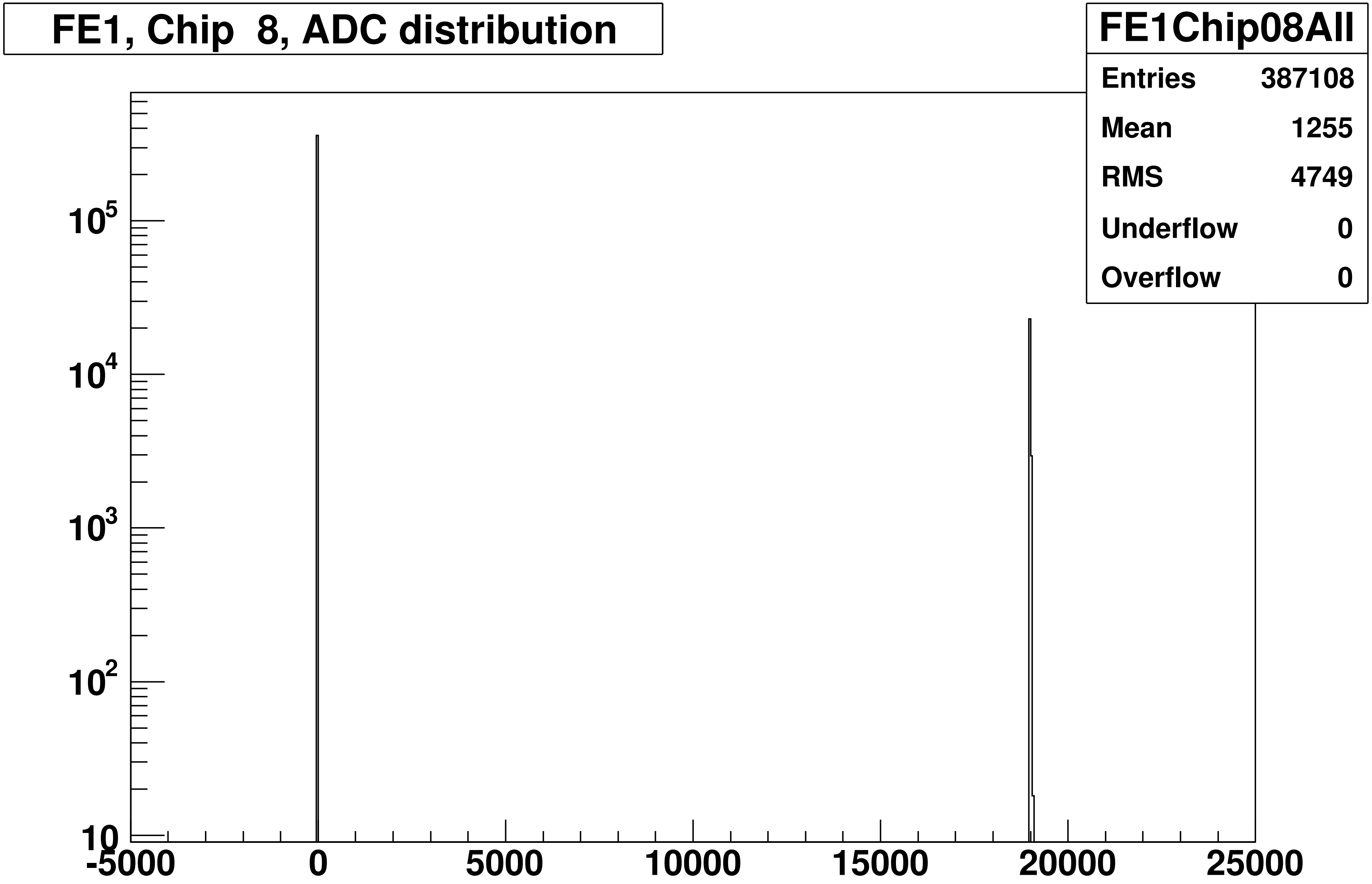
FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 387108
Mean 1240
RMS 4758
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



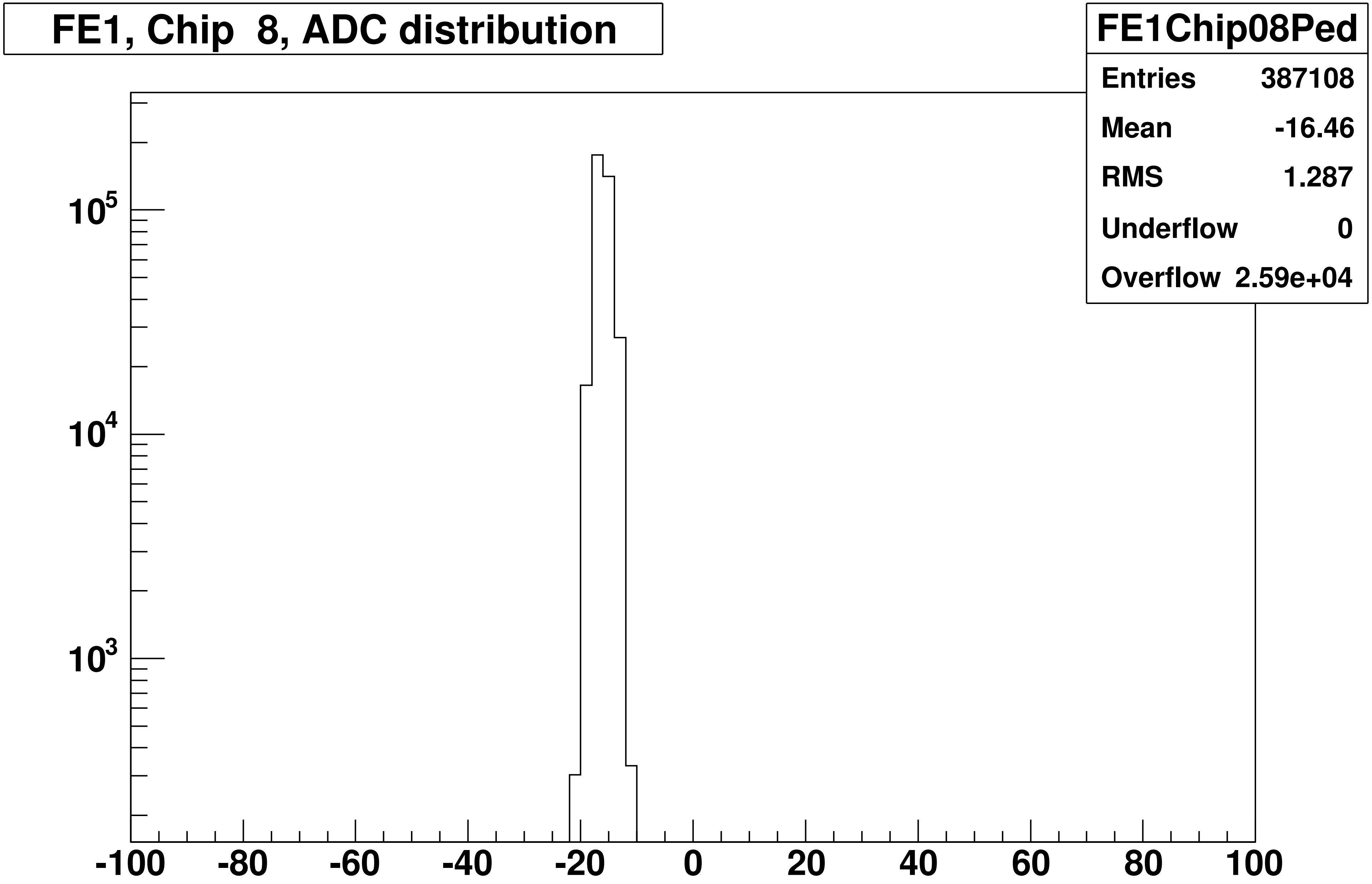
FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 387108
Mean -33.97
RMS 1.33
Underflow 0
Overflow 2.588e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



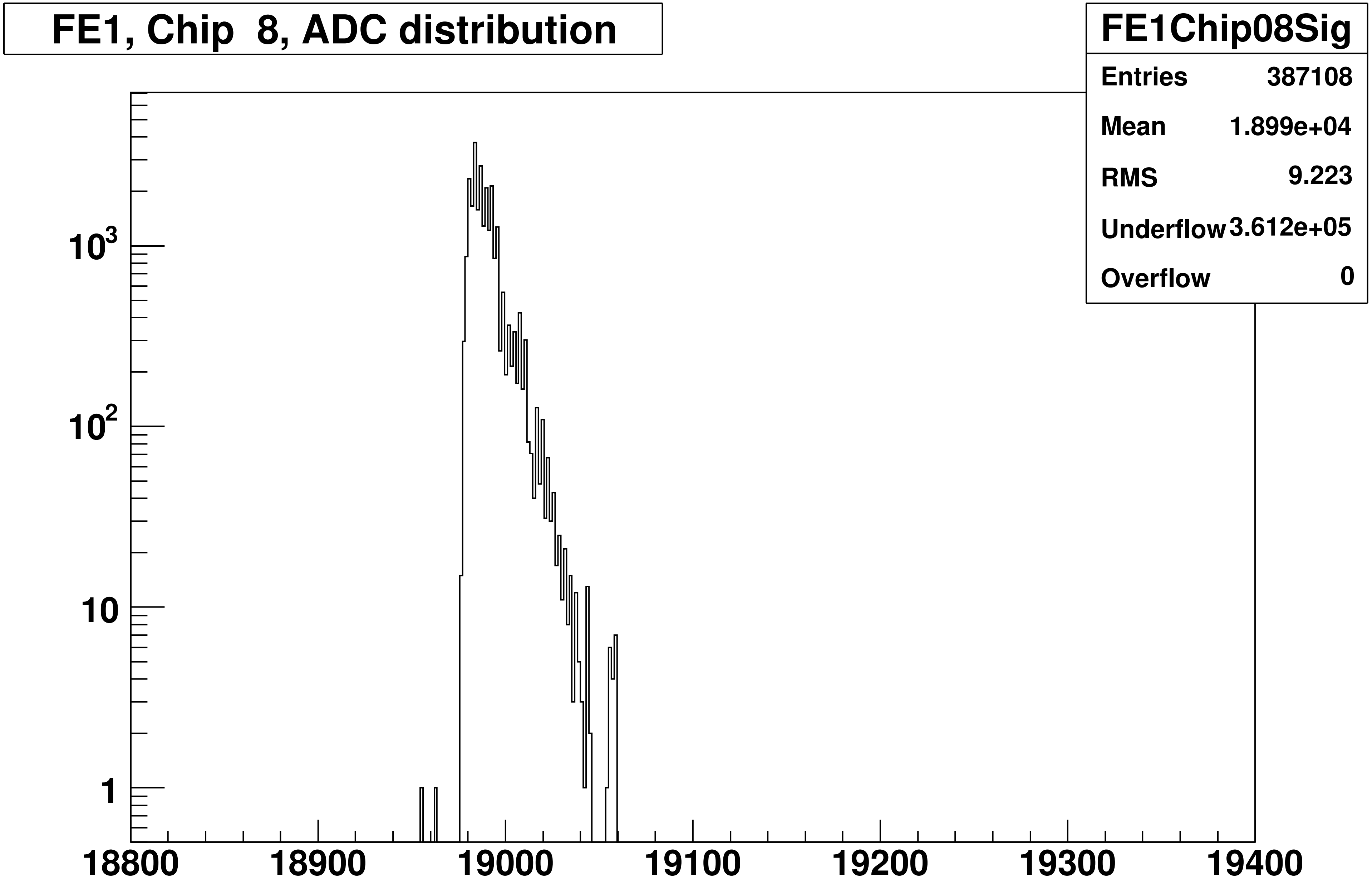
FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 387108
Mean 1.902e+04
RMS 9.178
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

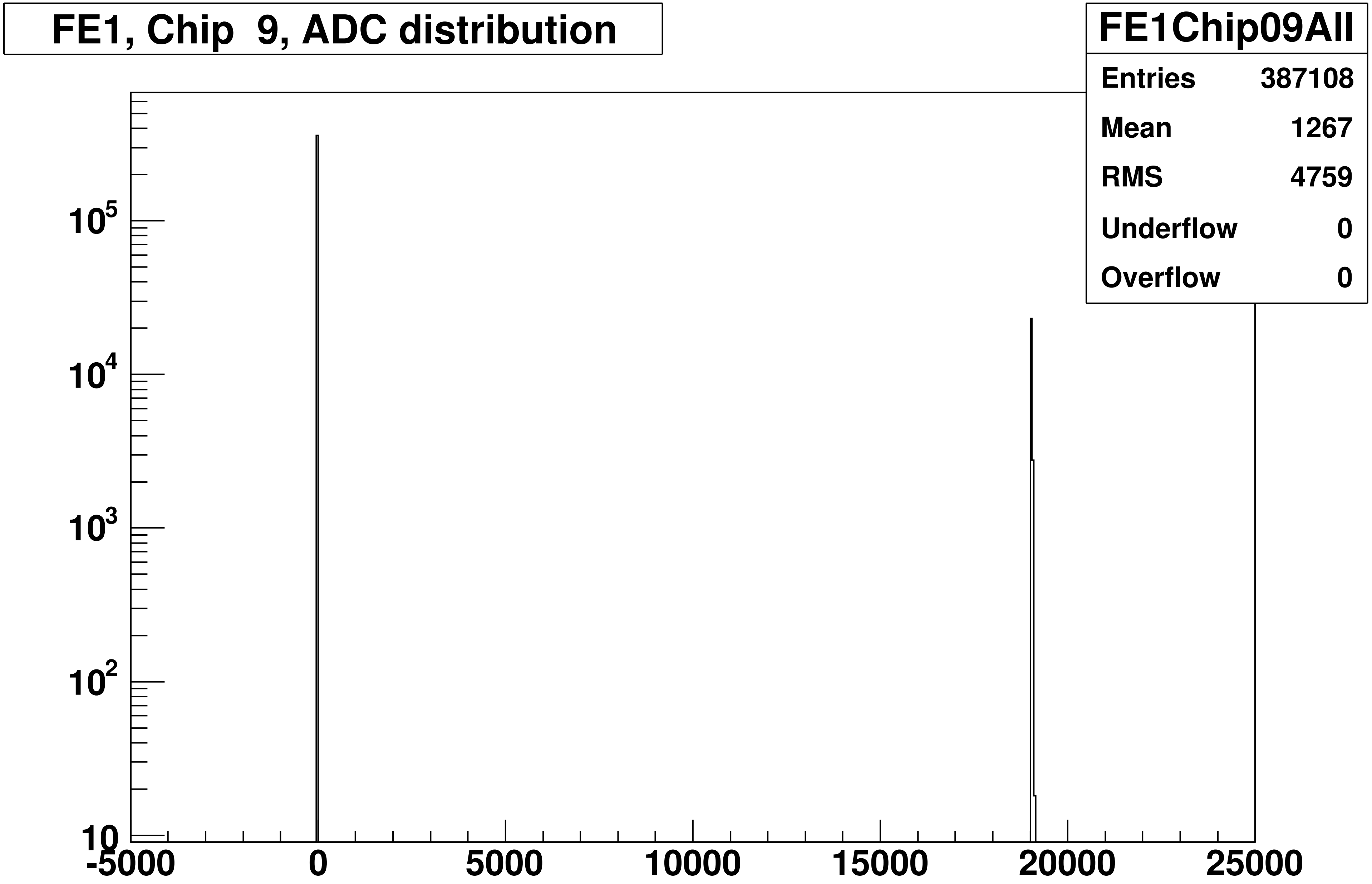


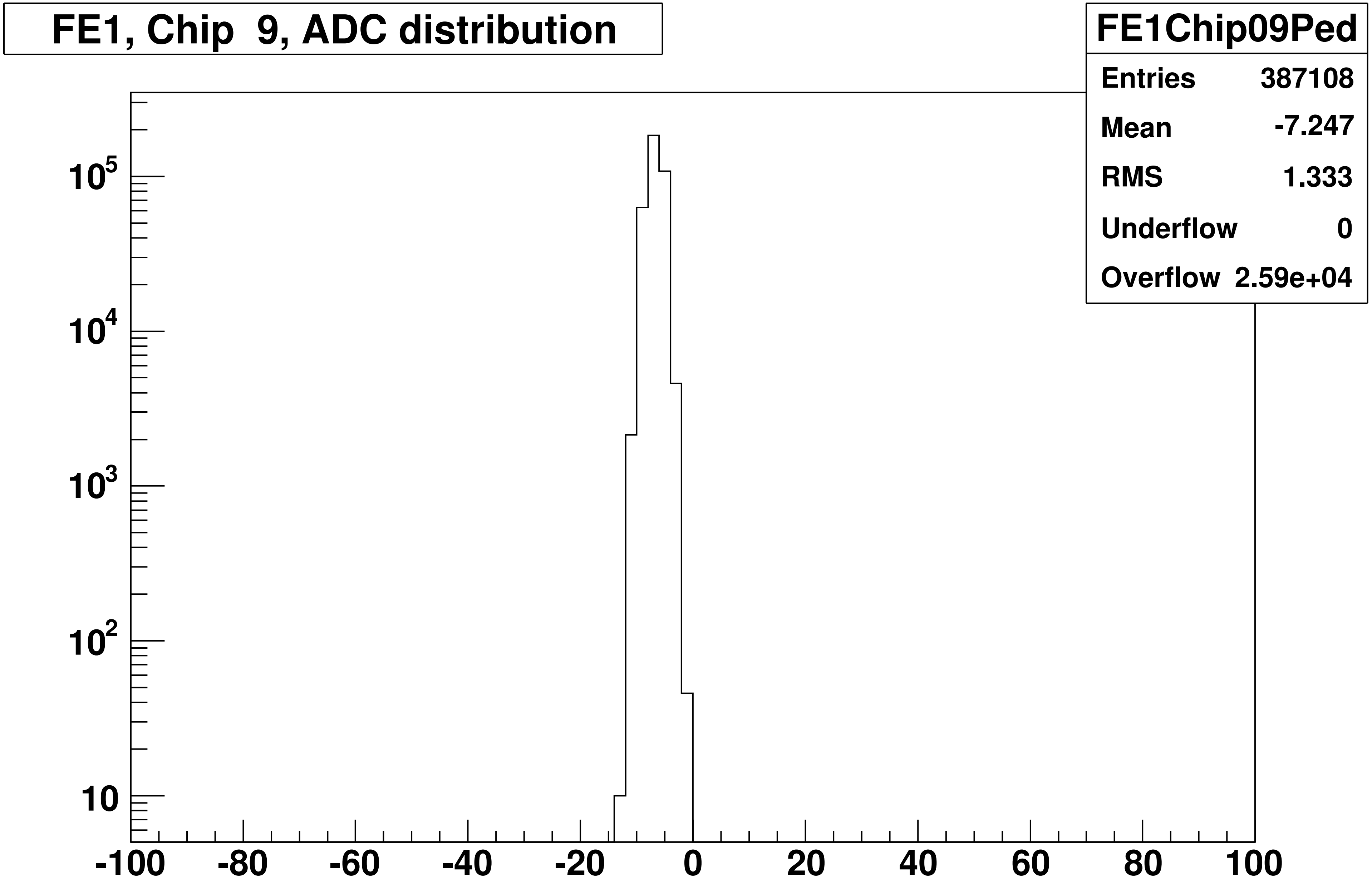
FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 387108
Mean 1255
RMS 4749
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



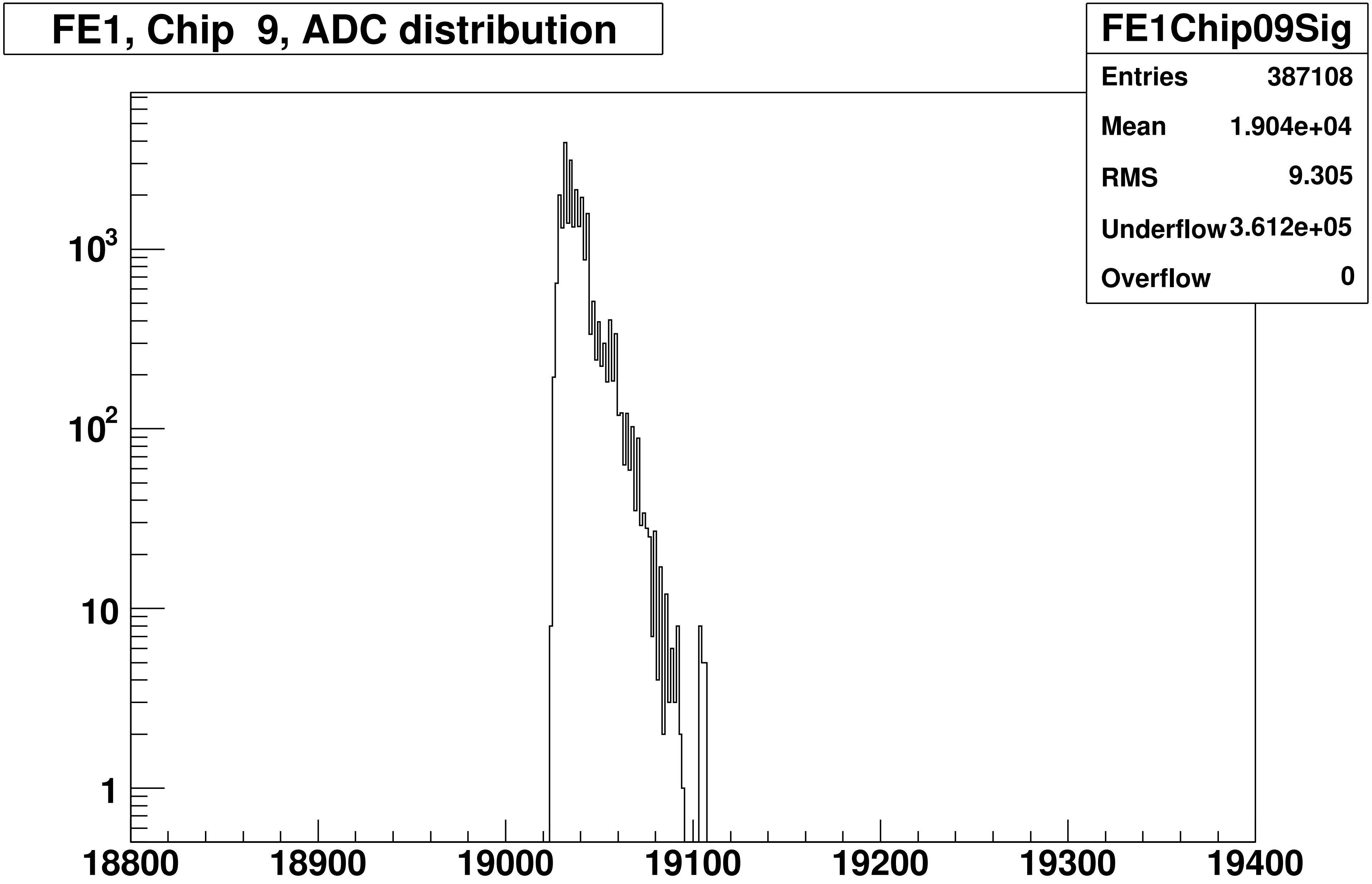
FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 387108
Mean -16.46
RMS 1.287
Underflow 0
Overflow 2.59e+04
10^5
10^4
10^3
-100 -80 -60 -40 -20 0 20 40 60 80 100

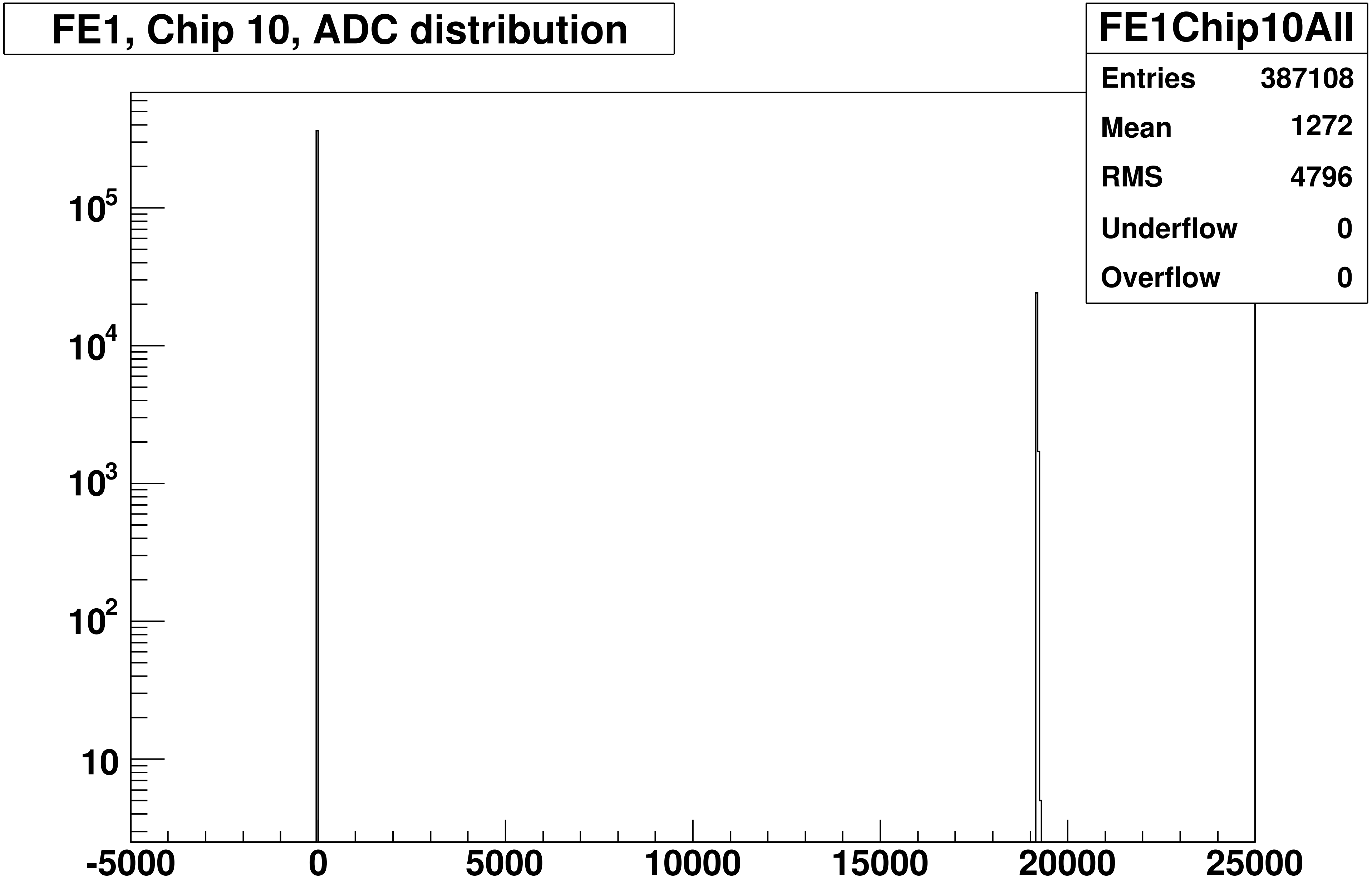

FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 387108
Mean 1.899e+04
RMS 9.223
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

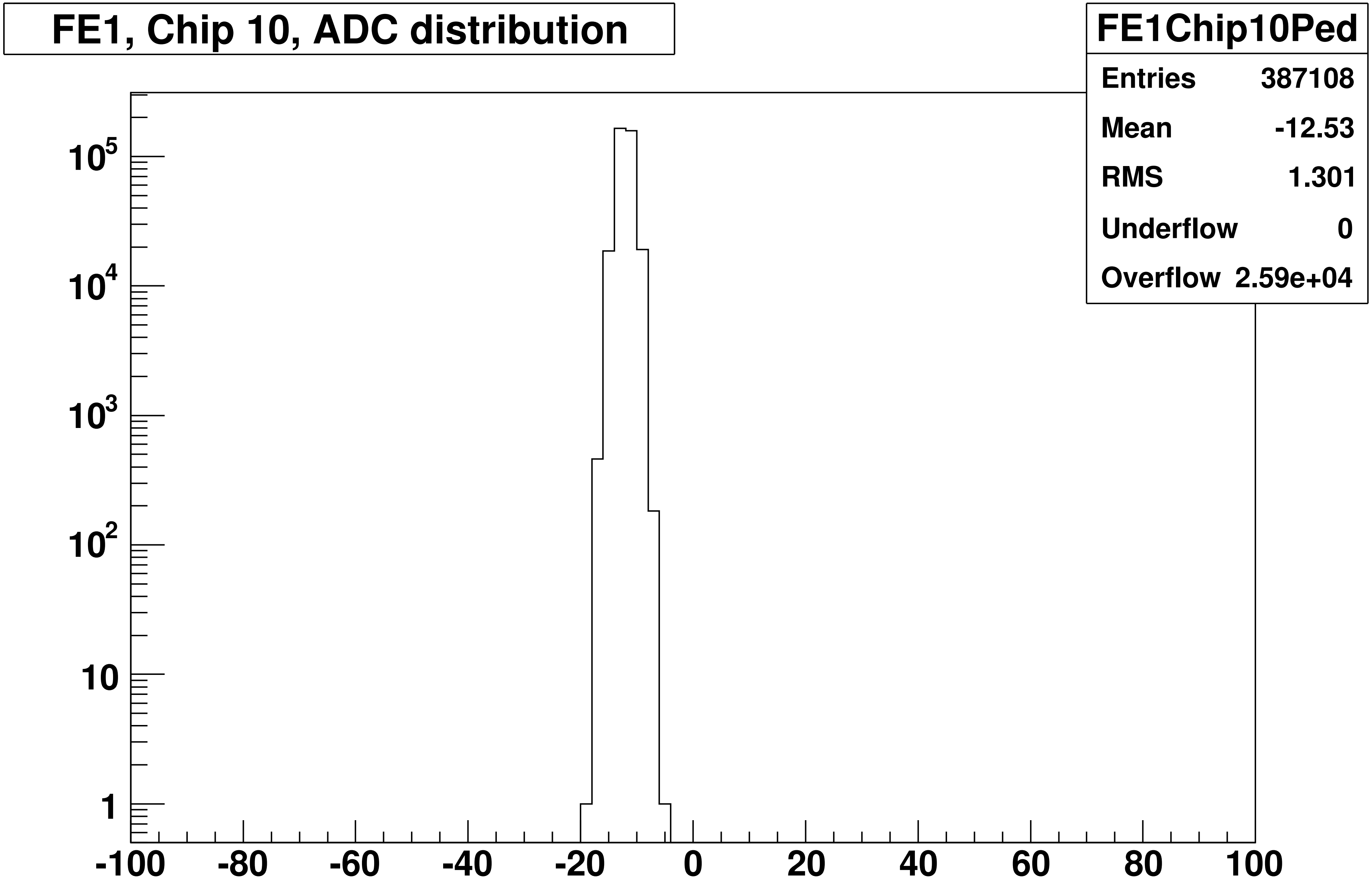

FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 387108
Mean 1267
RMS 4759
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



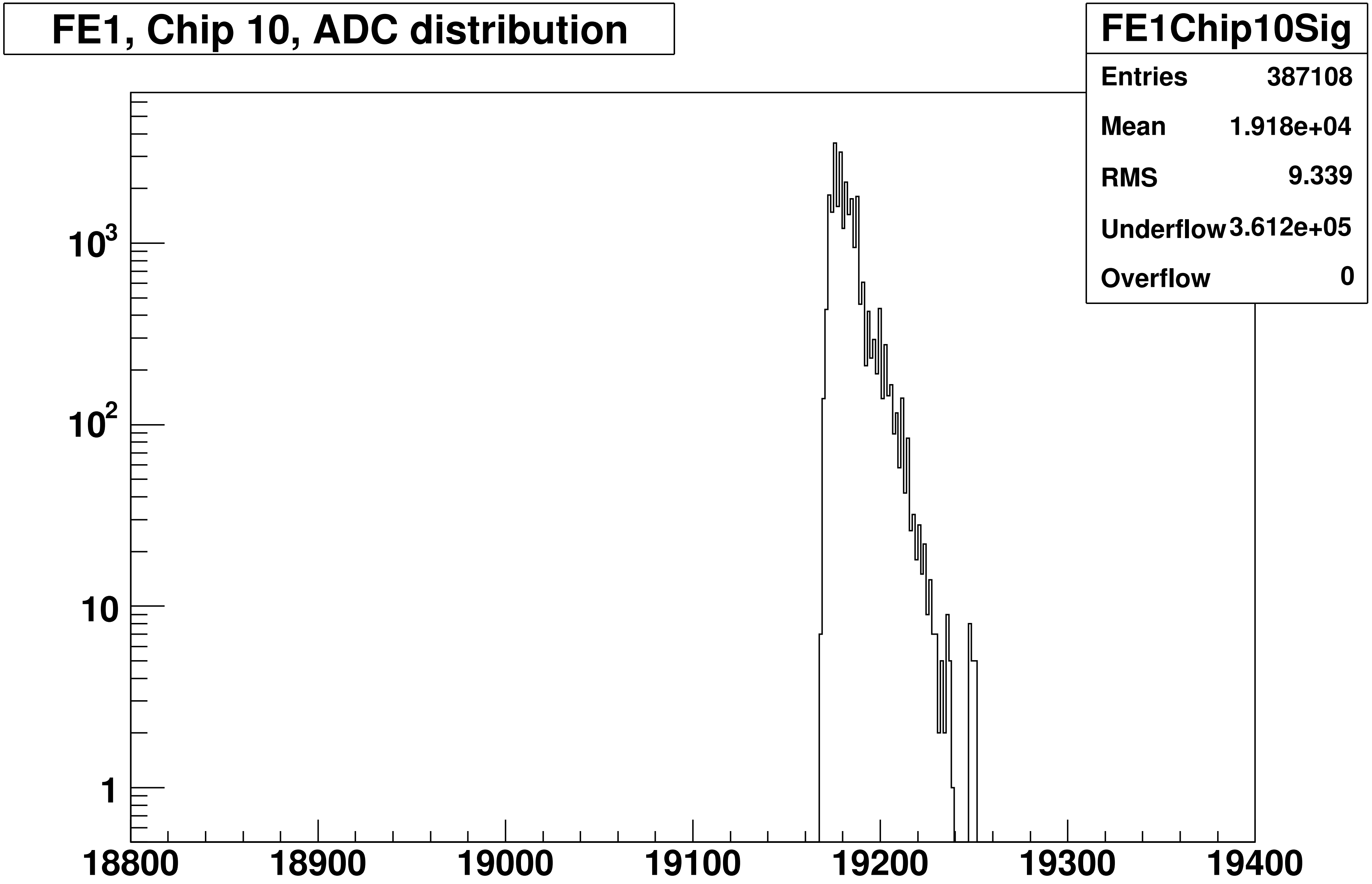
FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 387108
Mean -7.247
RMS 1.333
Underflow 0
Overflow 2.59e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 387108
Mean 1.904e+04
RMS 9.305
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

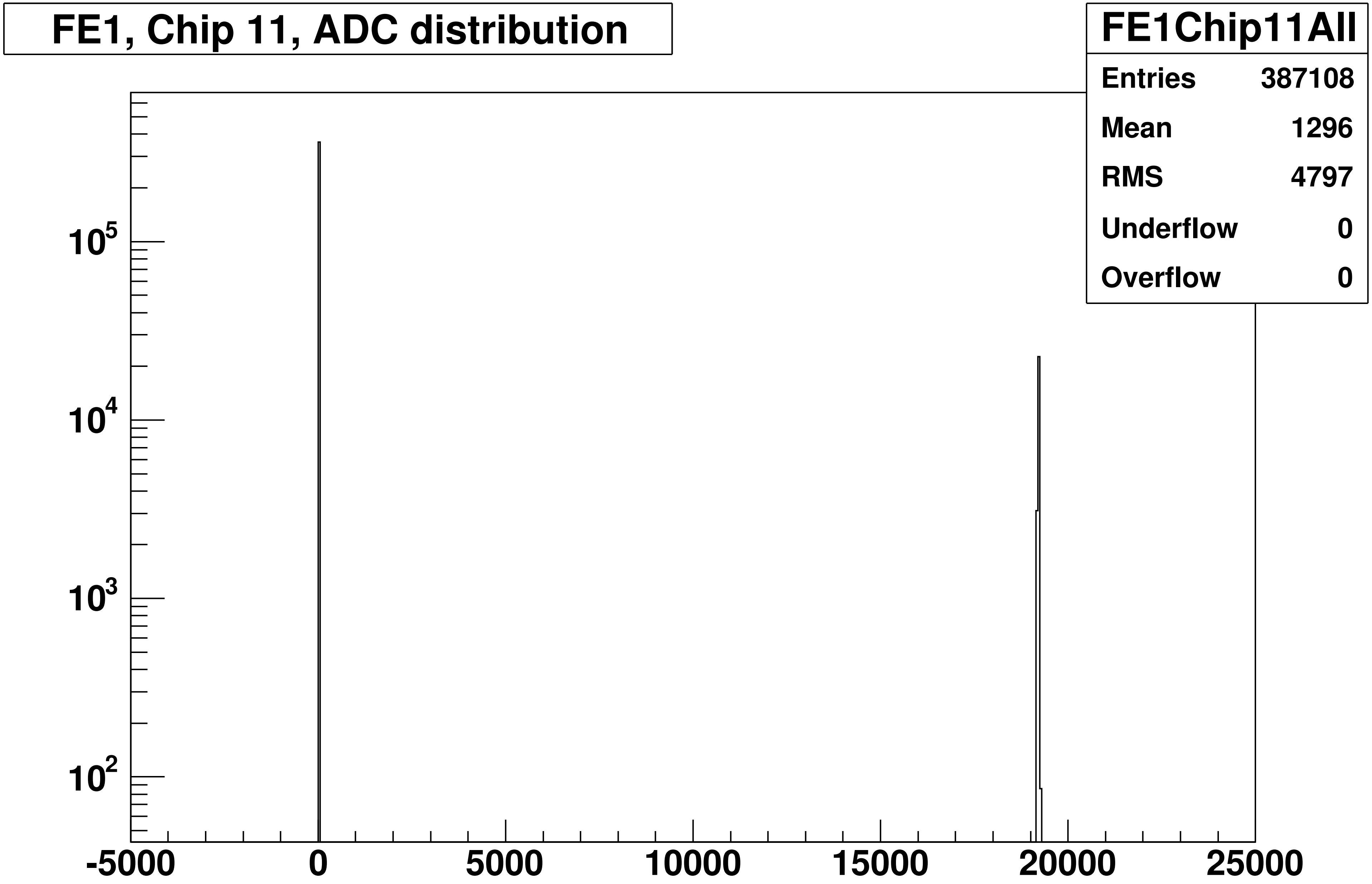

FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 387108
Mean 1272
RMS 4796
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

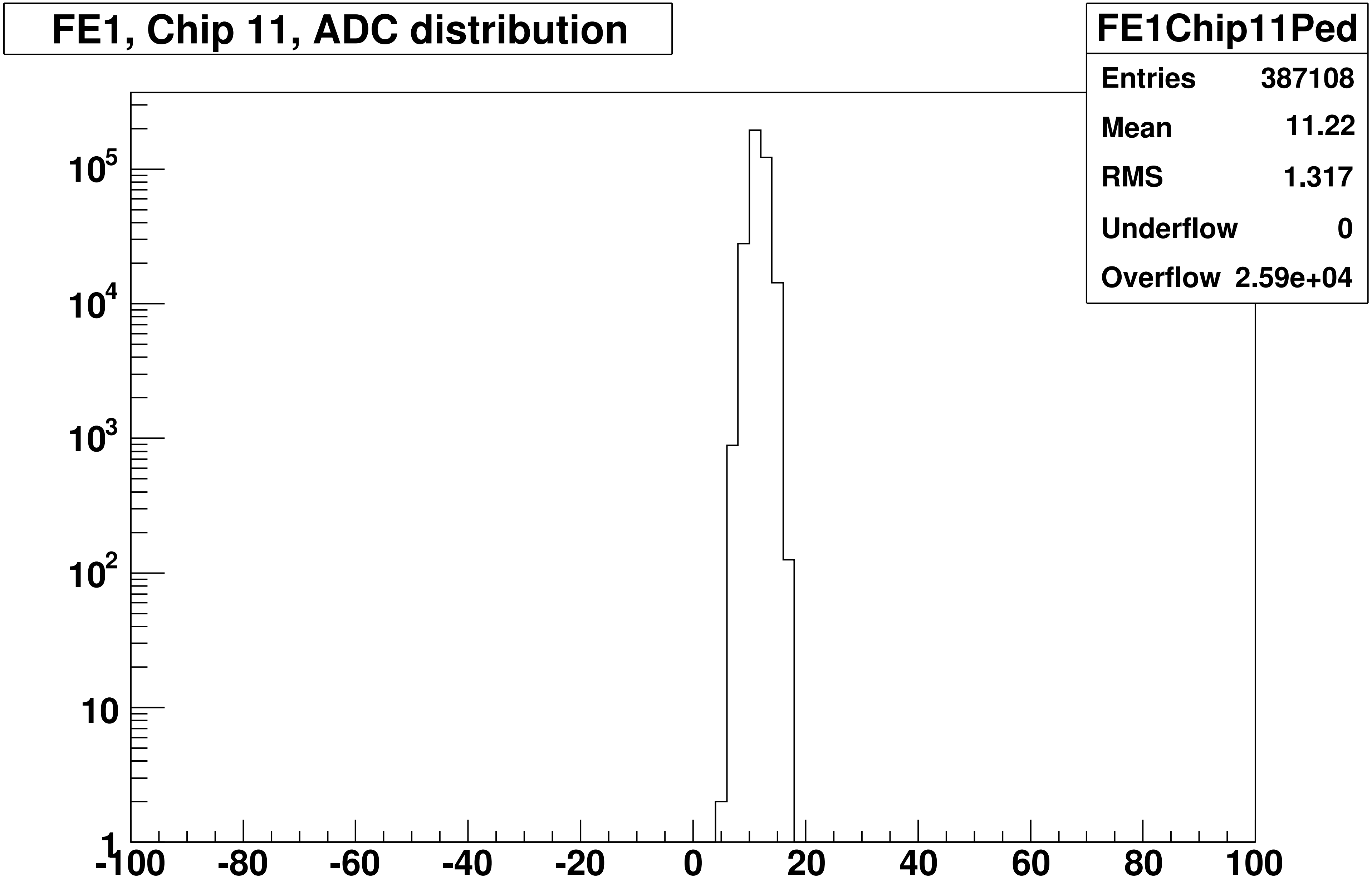


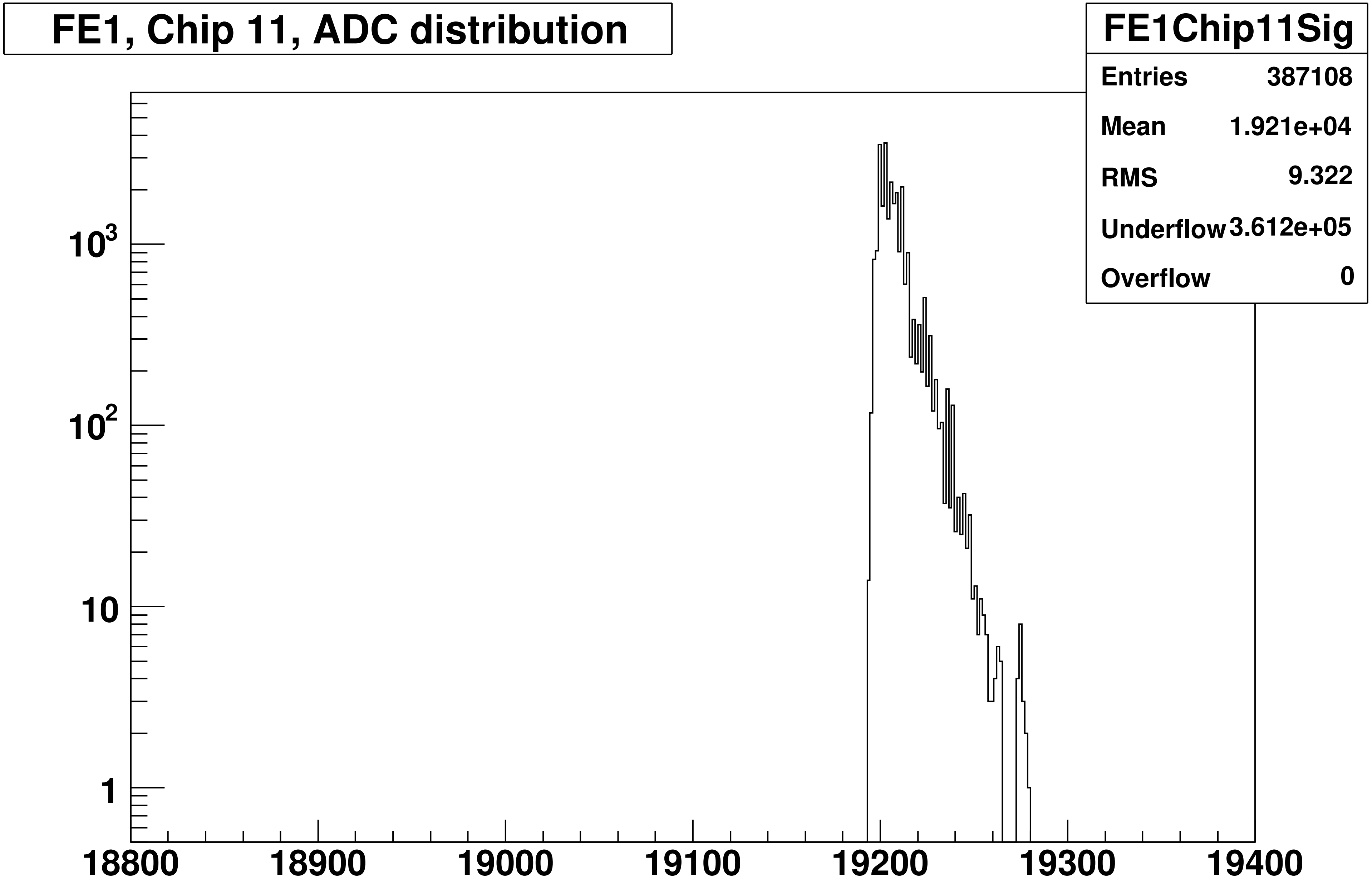
FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 387108
Mean -12.53
RMS 1.301
Underflow 0
Overflow 2.59e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

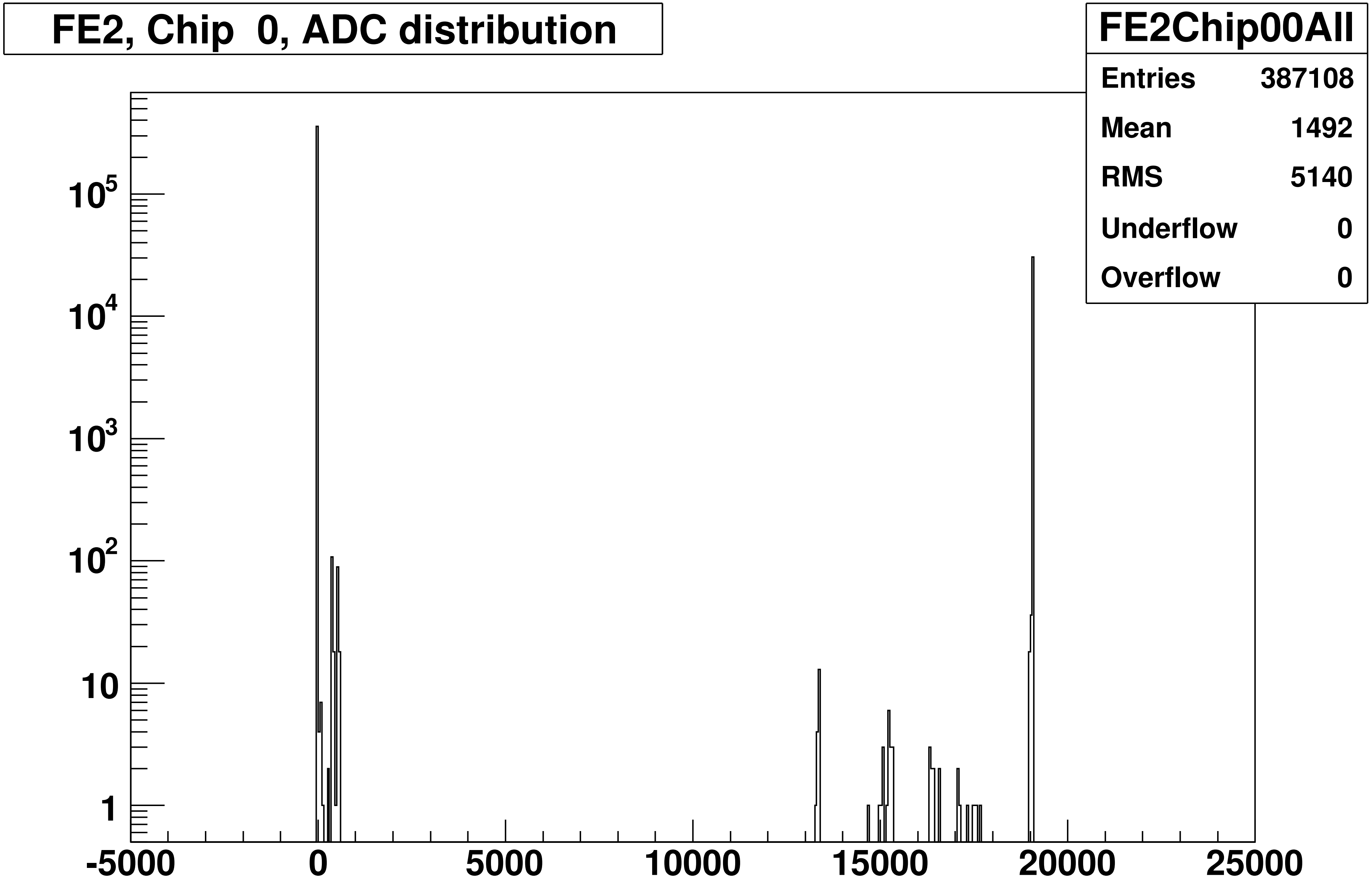


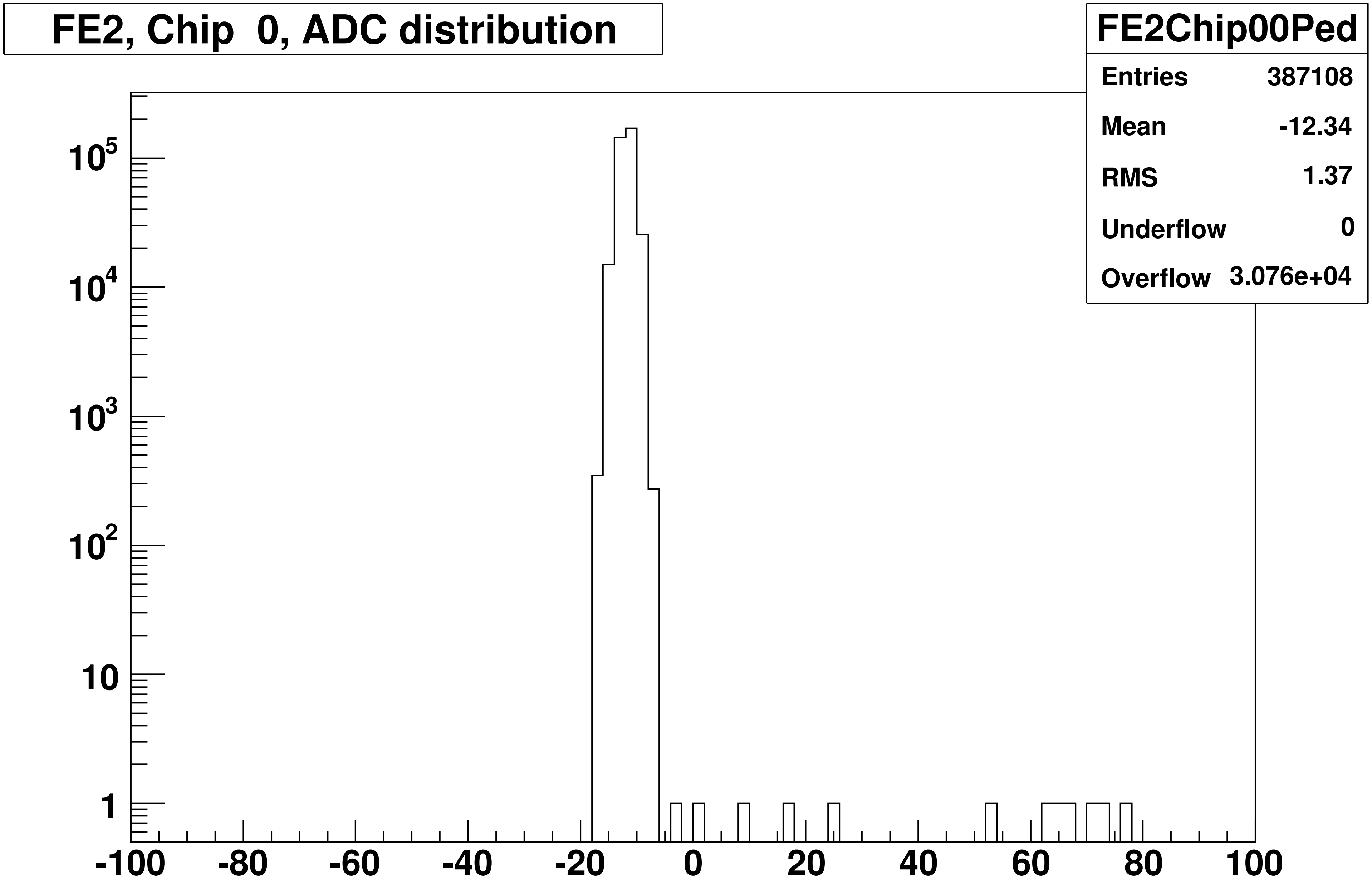
FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 387108
Mean 1.918e+04
RMS 9.339
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

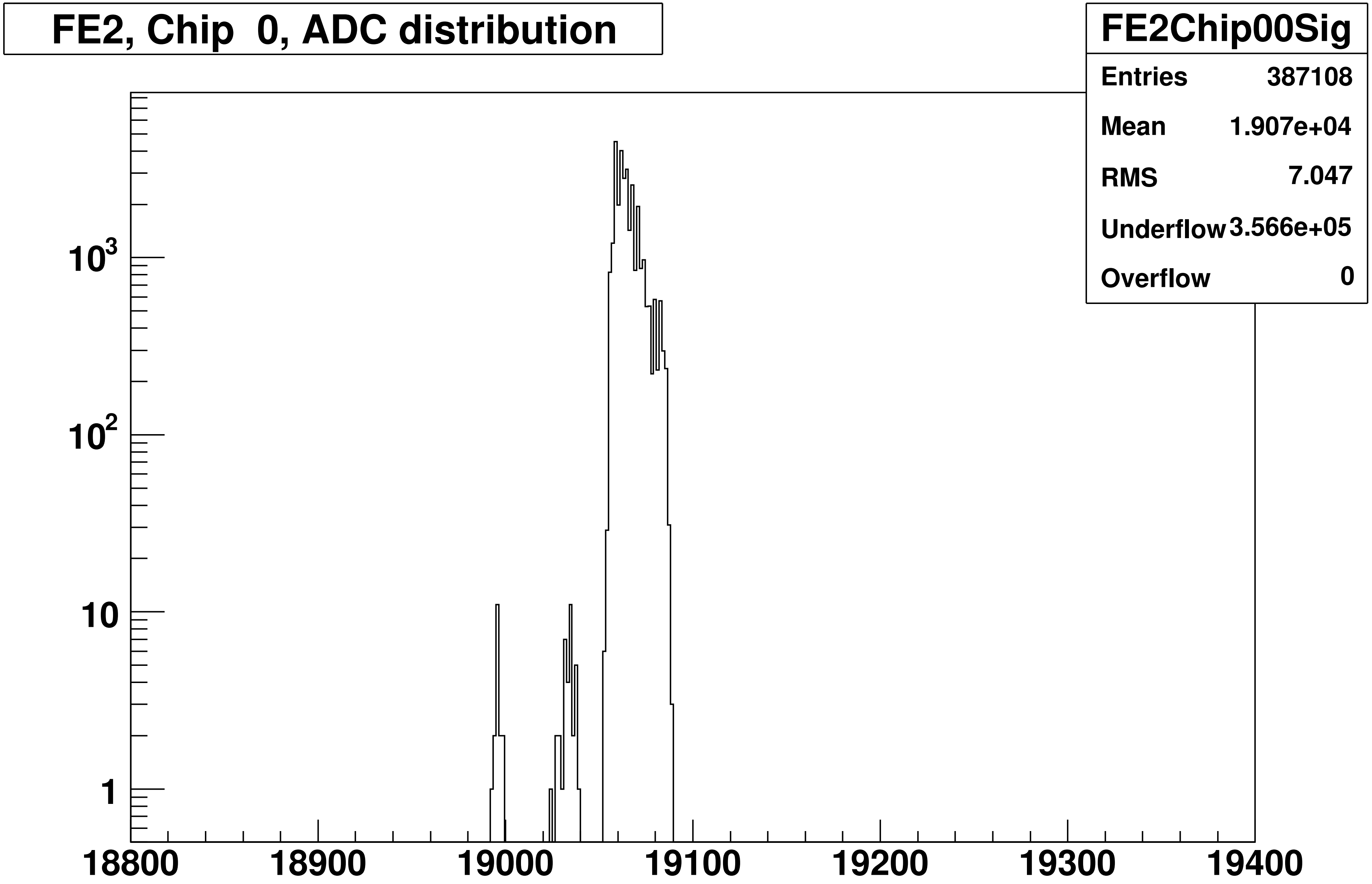

FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 387108
Mean 1296
RMS 4797
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

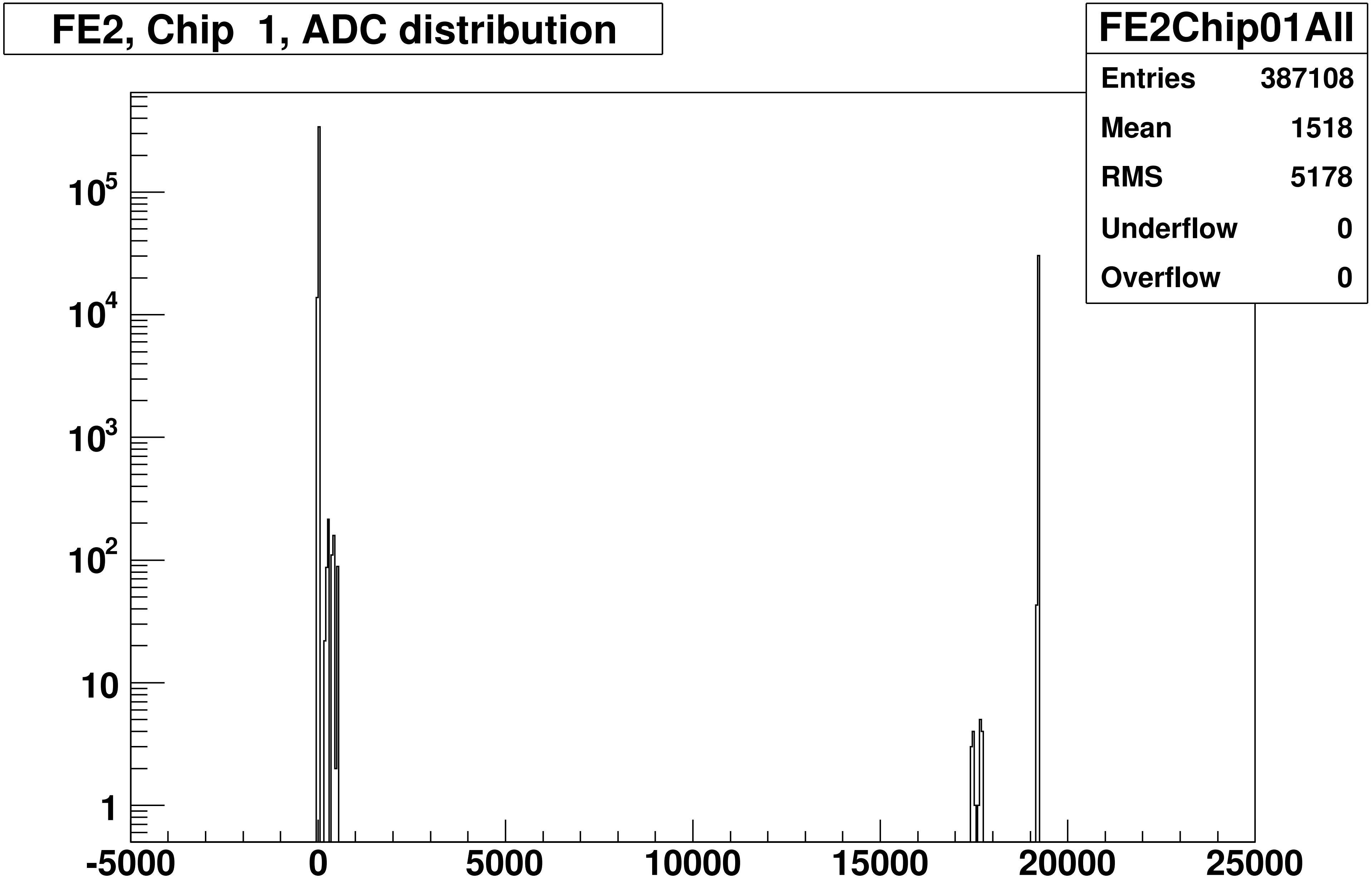

FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 387108
Mean 11.22
RMS 1.317
Underflow 0
Overflow 2.59e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

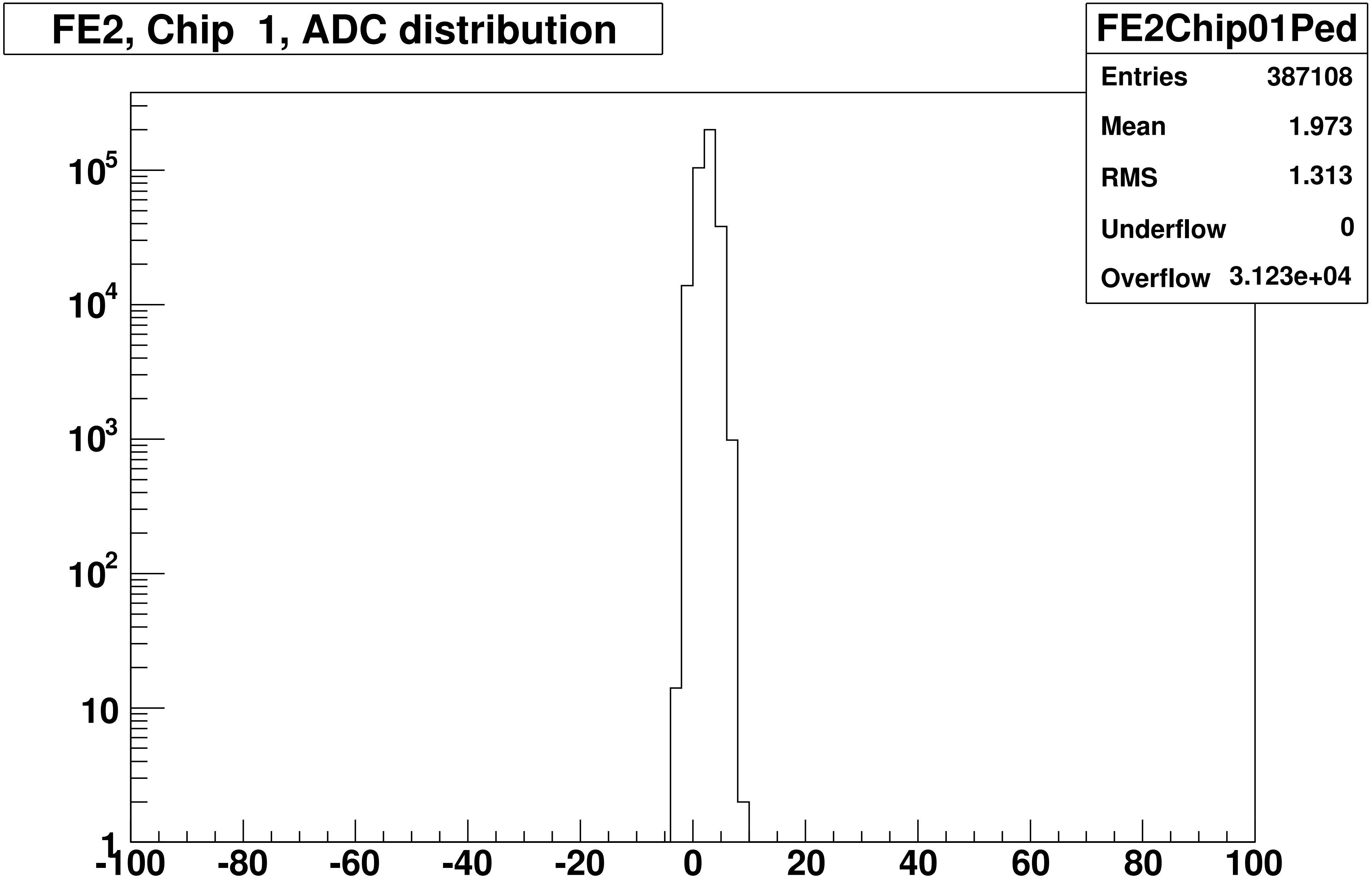

FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 387108
Mean 1.921e+04
RMS 9.322
Underflow 3.612e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 387108
Mean 1492
RMS 5140
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

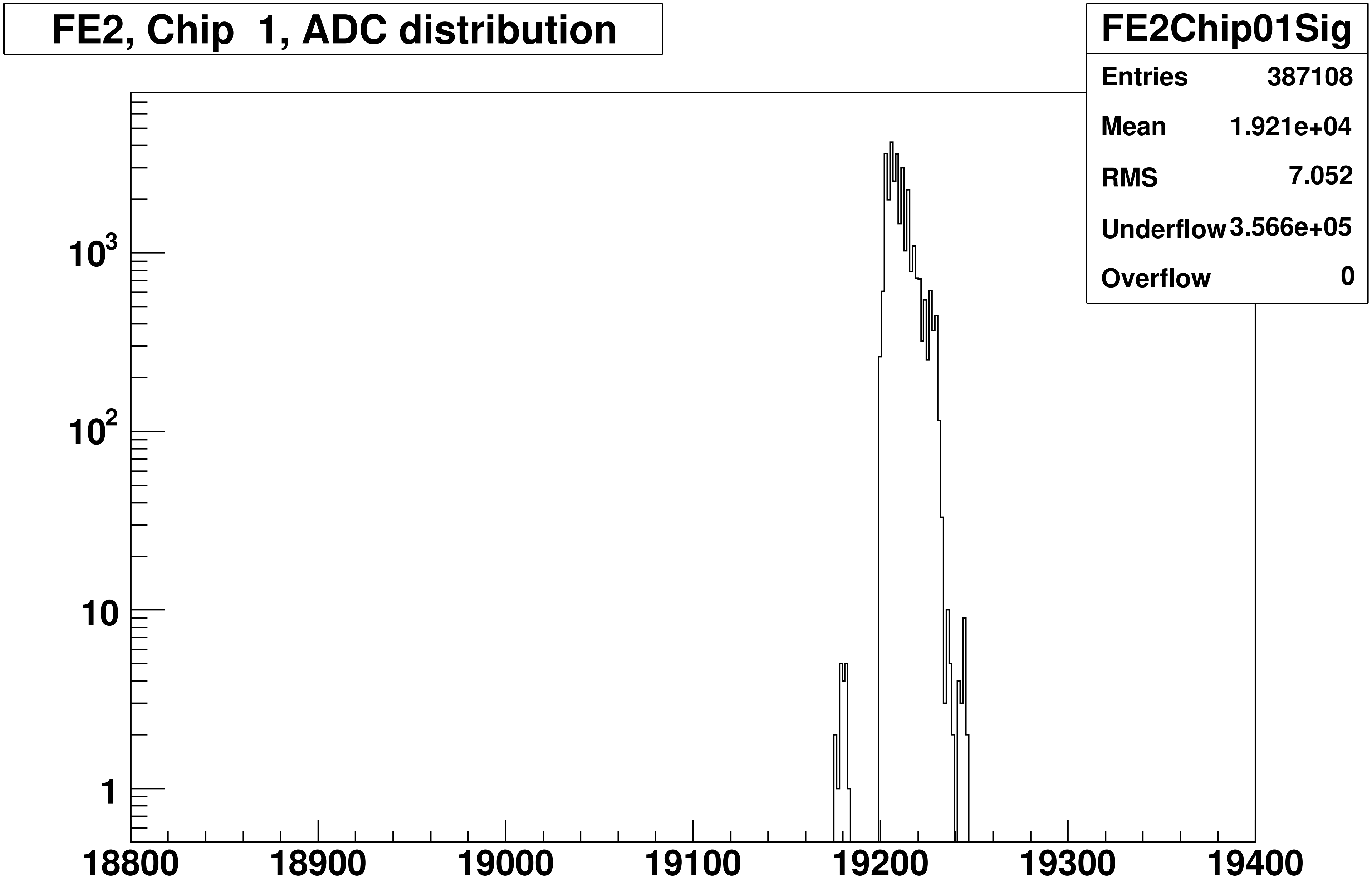

FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 387108
Mean -12.34
RMS 1.37
Underflow 0
Overflow 3.076e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

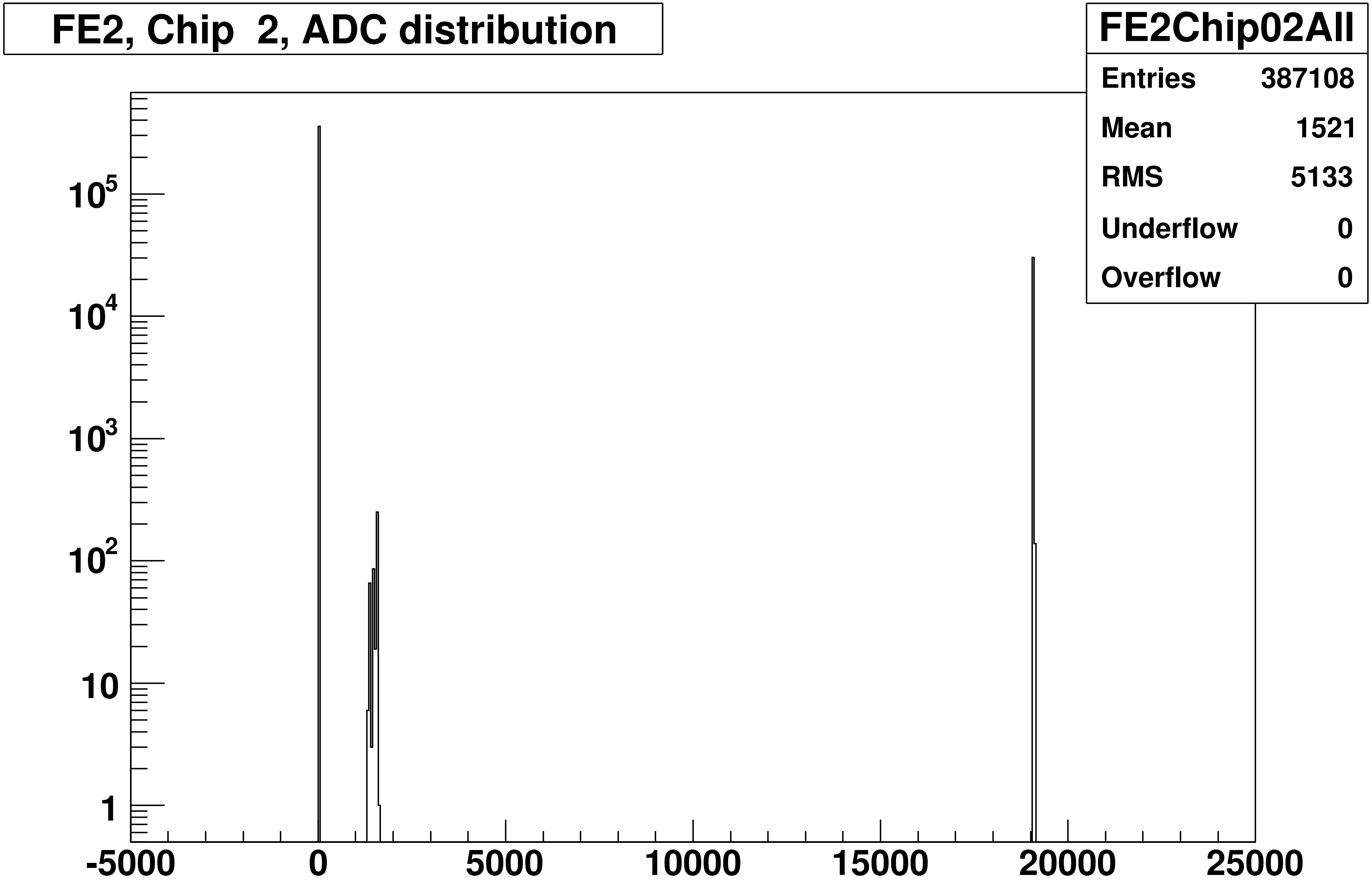

FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 387108
Mean 1.907e+04
RMS 7.047
Underflow 3.566e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 387108
Mean 1518
RMS 5178
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

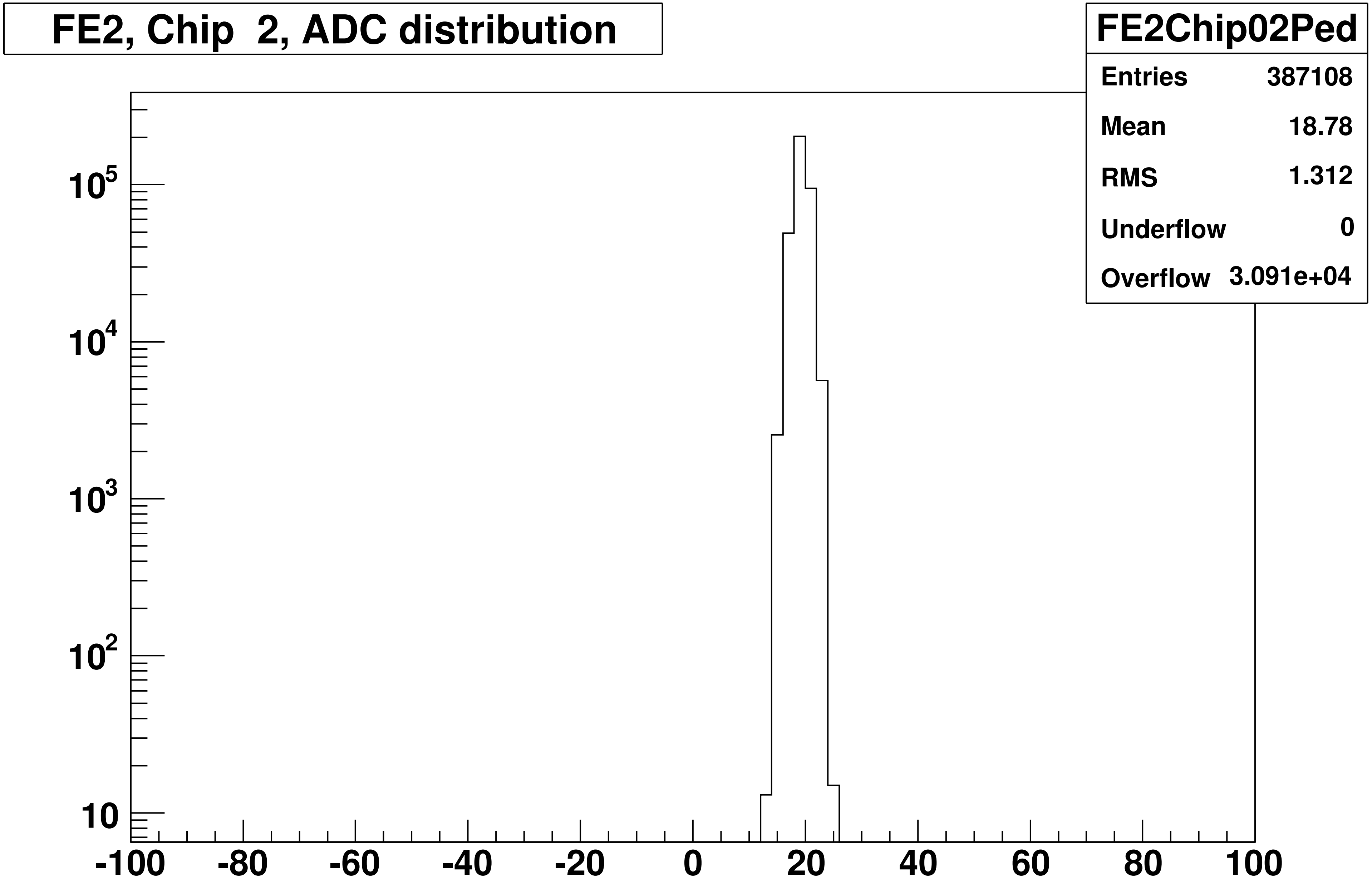


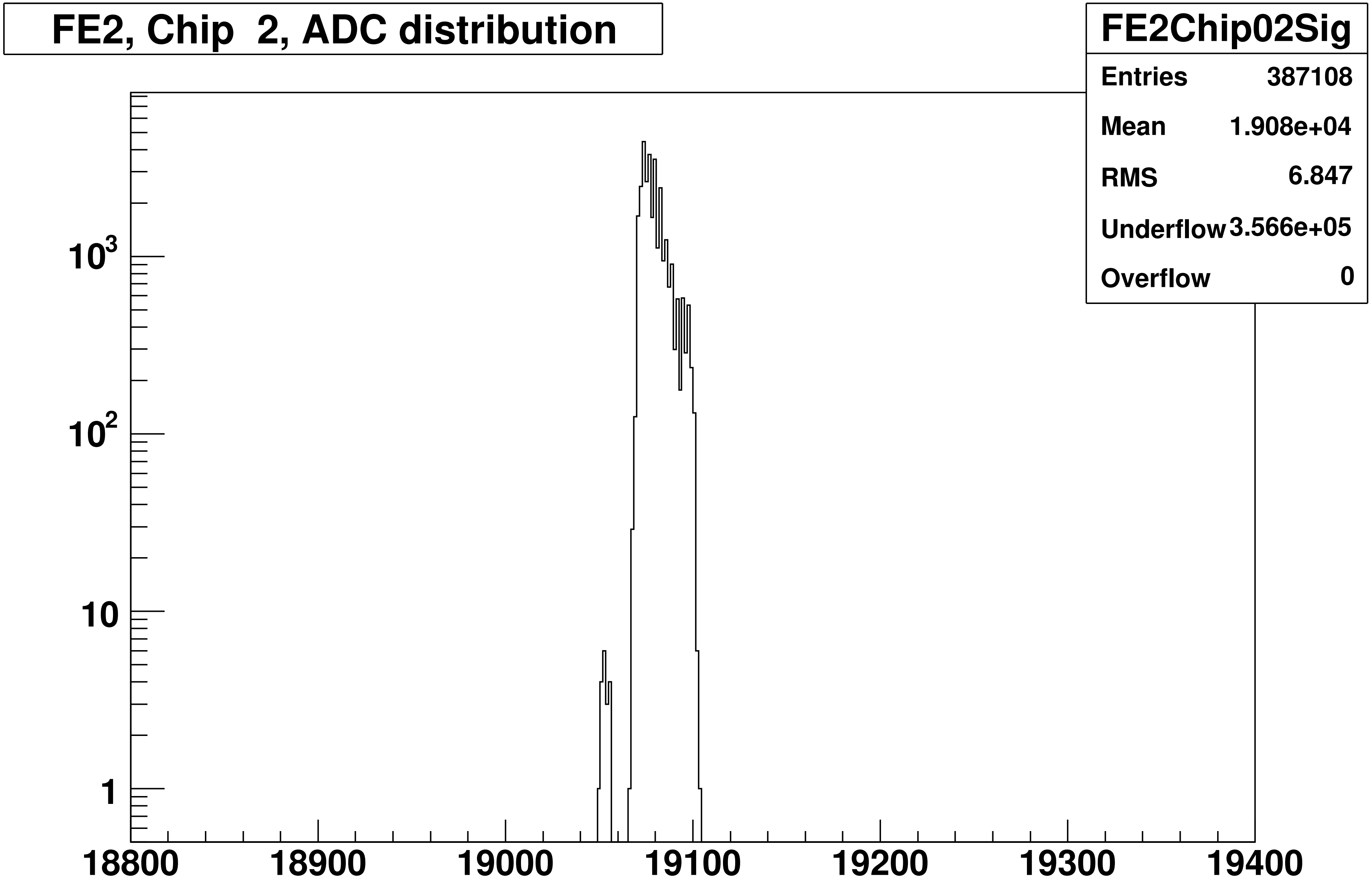
FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 387108
Mean 1.973
RMS 1.313
Underflow 0
Overflow 3.123e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 387108
Mean 1.921e+04
RMS 7.052
Underflow 3.566e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

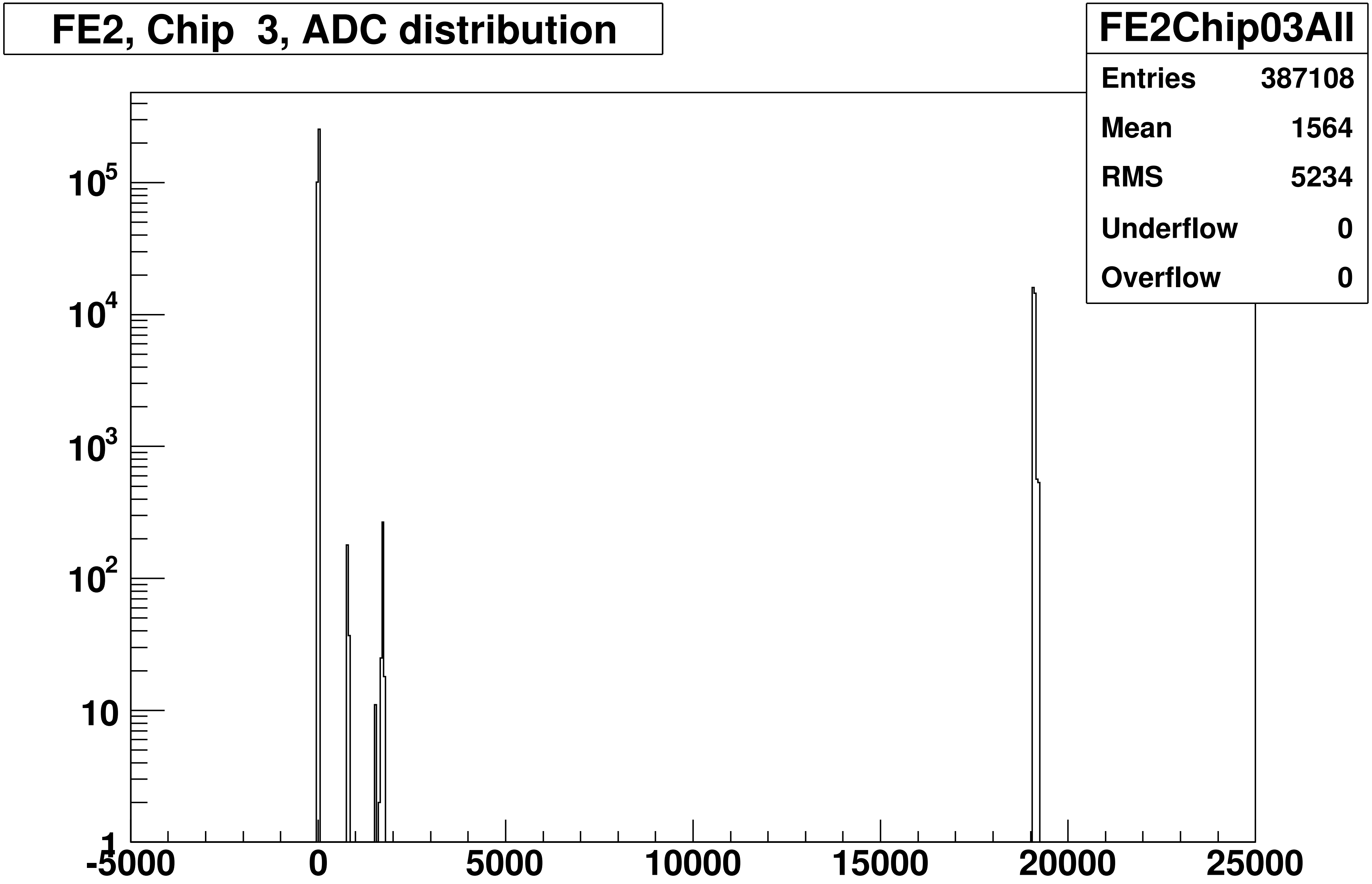


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 387108
Mean 1521
RMS 5133
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

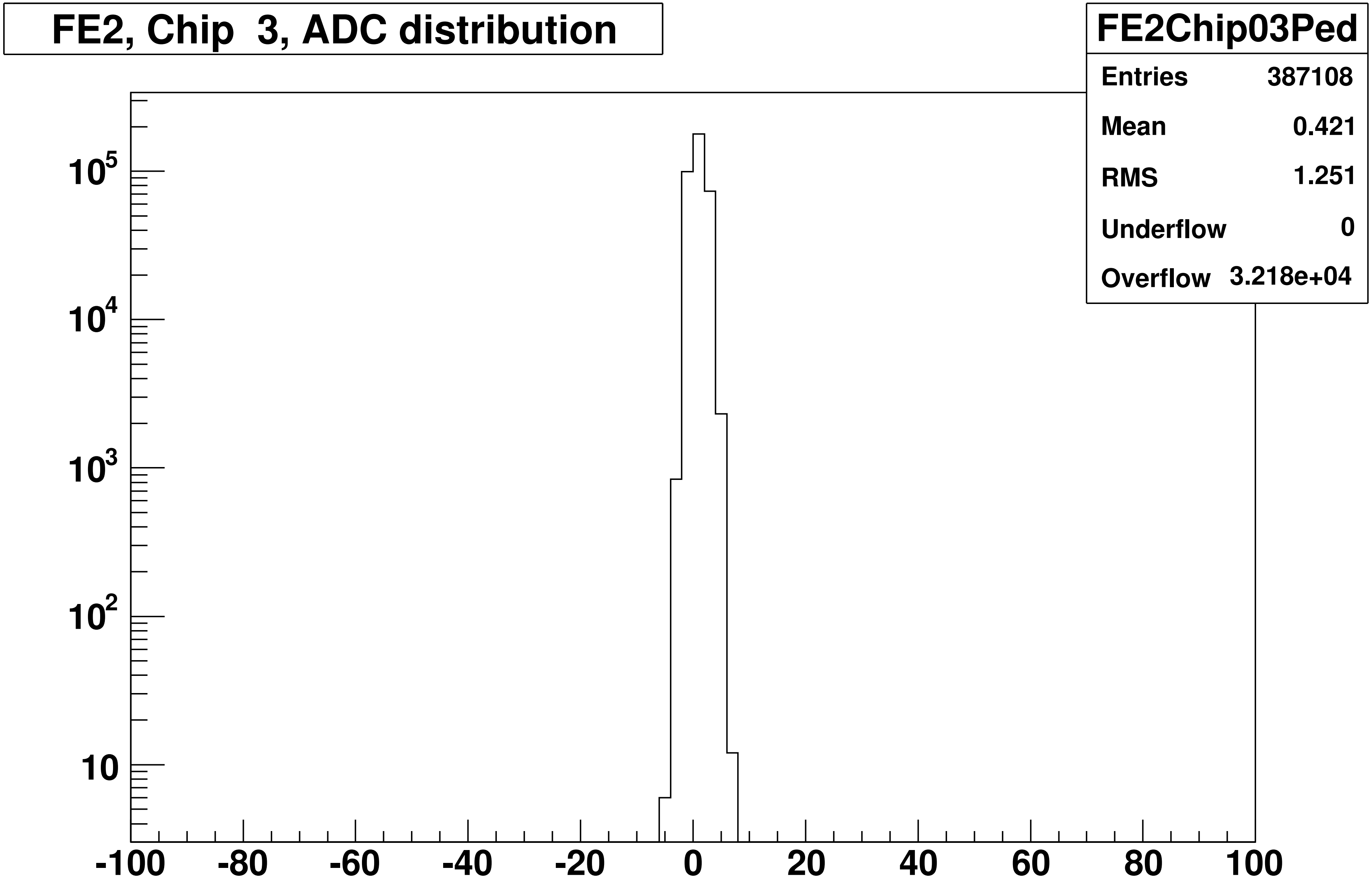

FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 387108
Mean 18.78
RMS 1.312
Underflow 0
Overflow 3.091e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

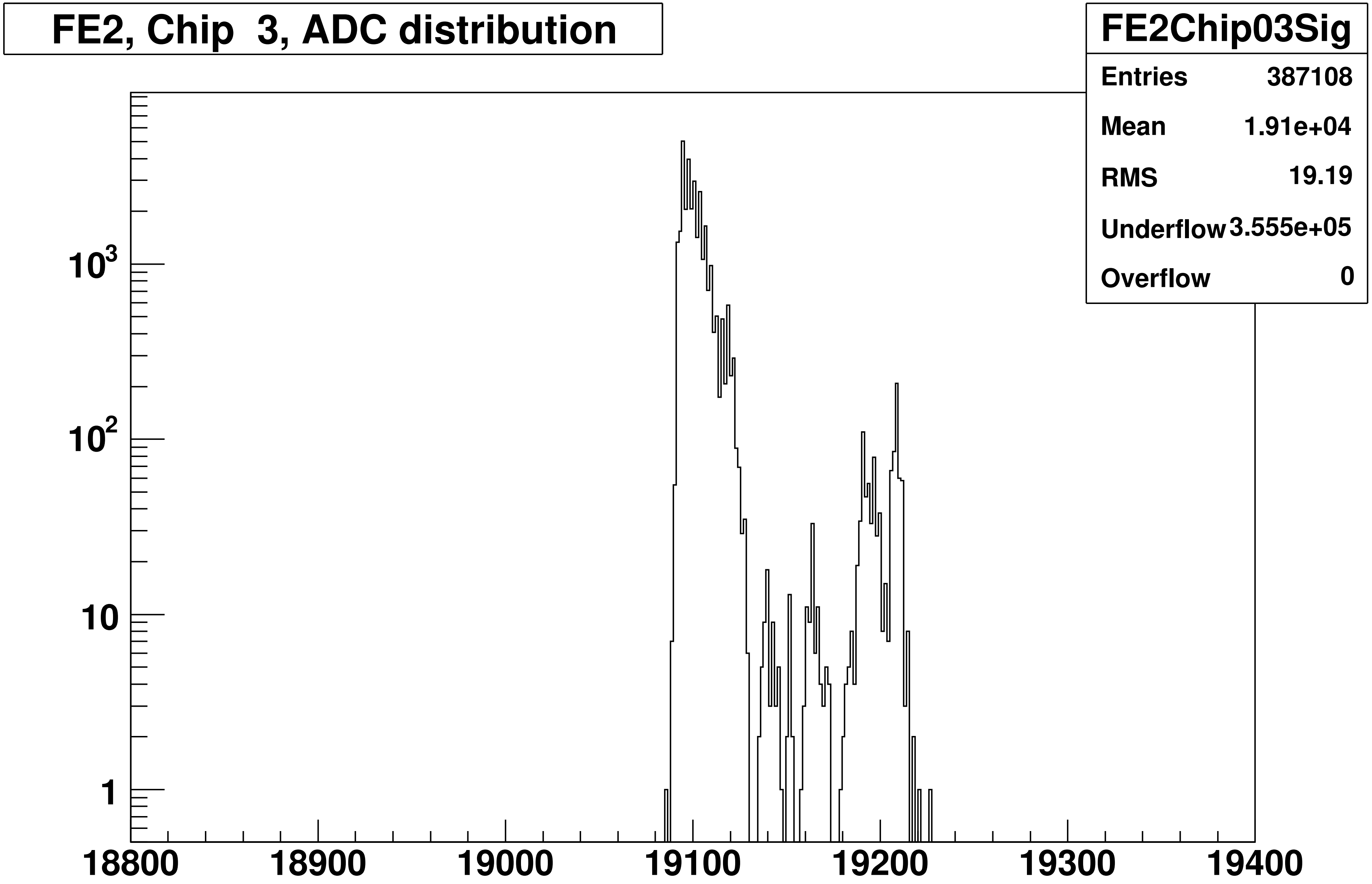


FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 387108
Mean 1.908e+04
RMS 6.847
Underflow 3.566e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

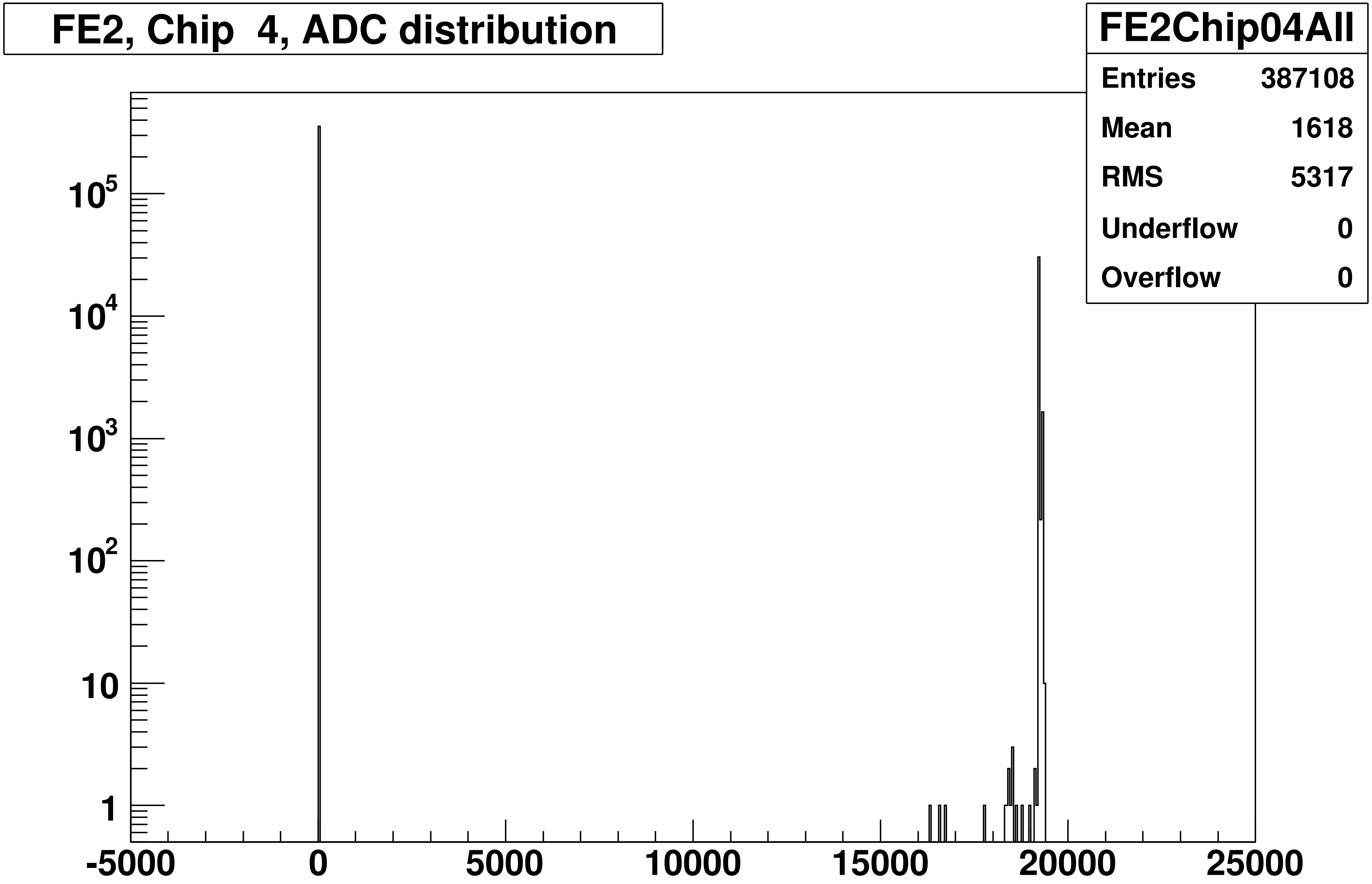


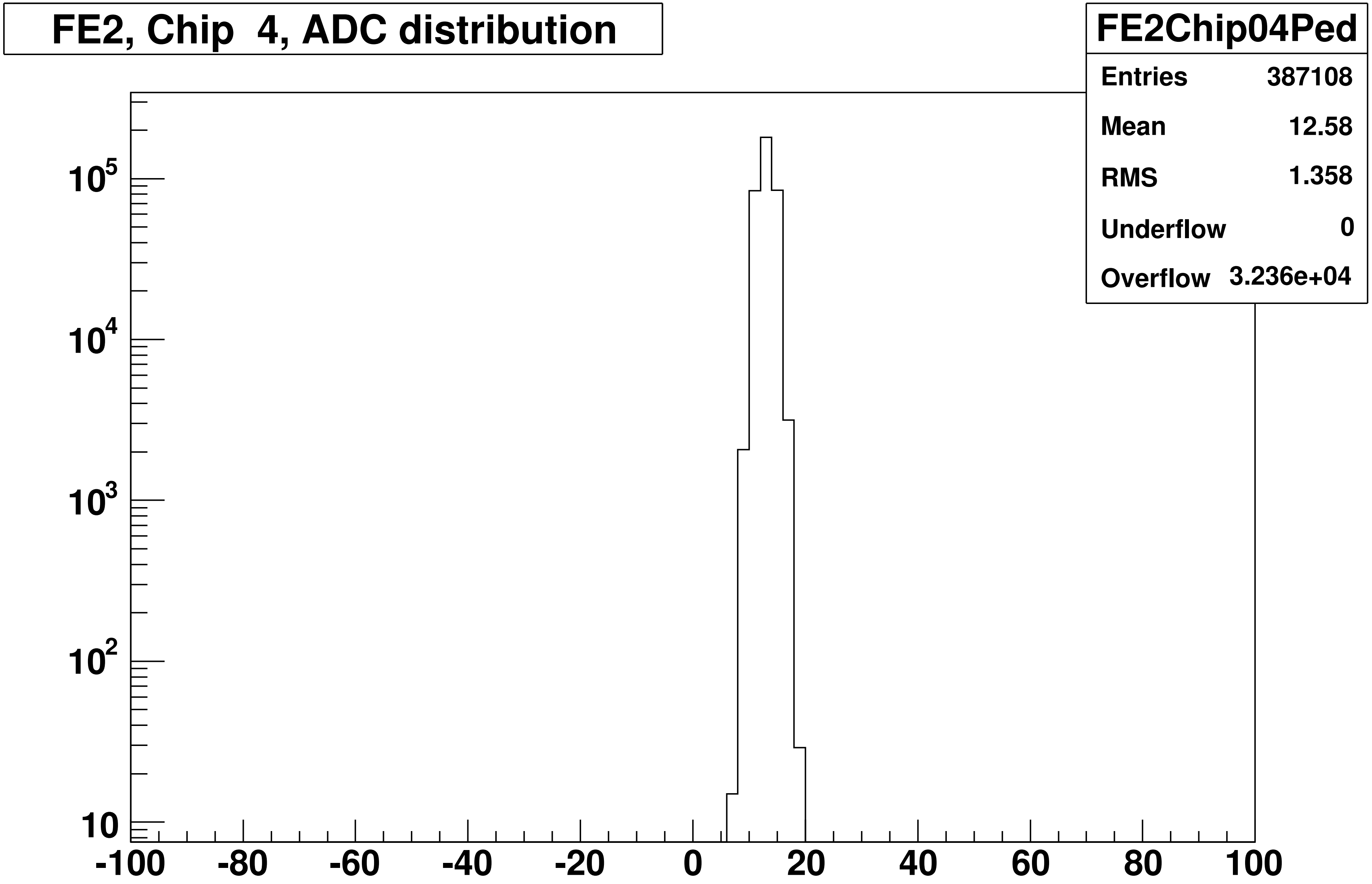
FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 387108
Mean 1564
RMS 5234
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 387108
Mean 0.421
RMS 1.251
Underflow 0
Overflow 3.218e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

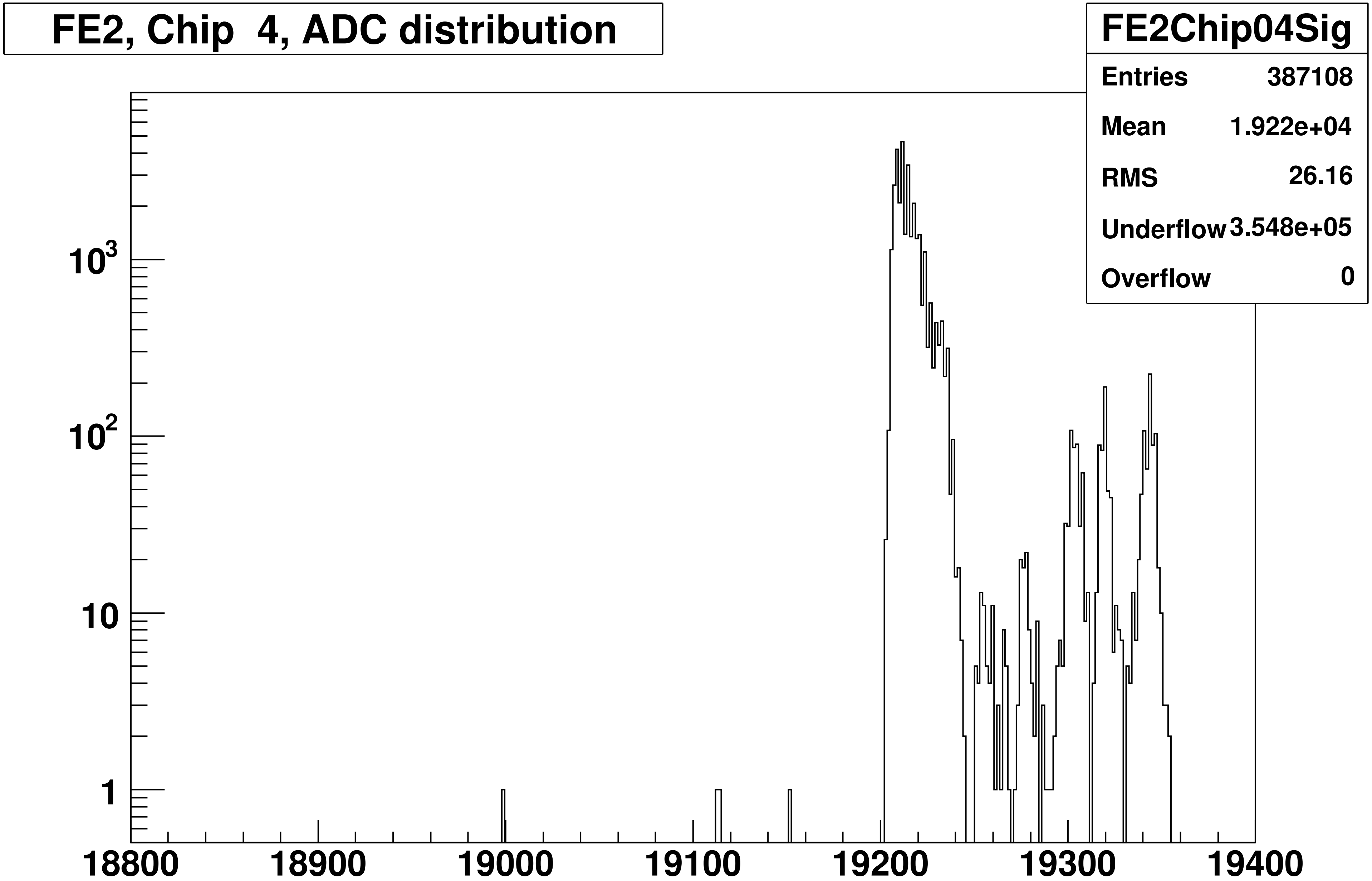


FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 387108
Mean 1.91e+04
RMS 19.19
Underflow 3.555e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

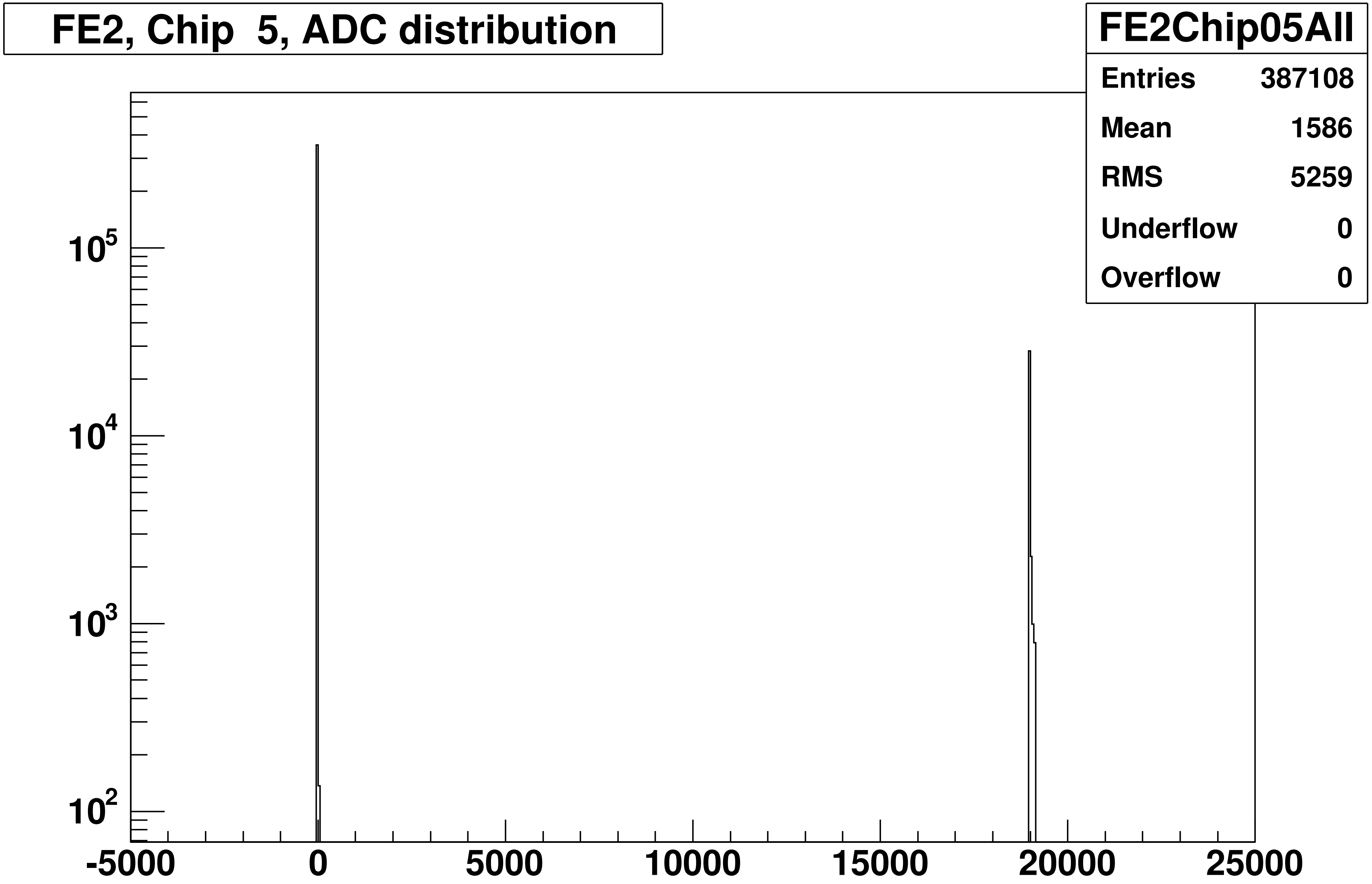

FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 387108
Mean 1618
RMS 5317
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



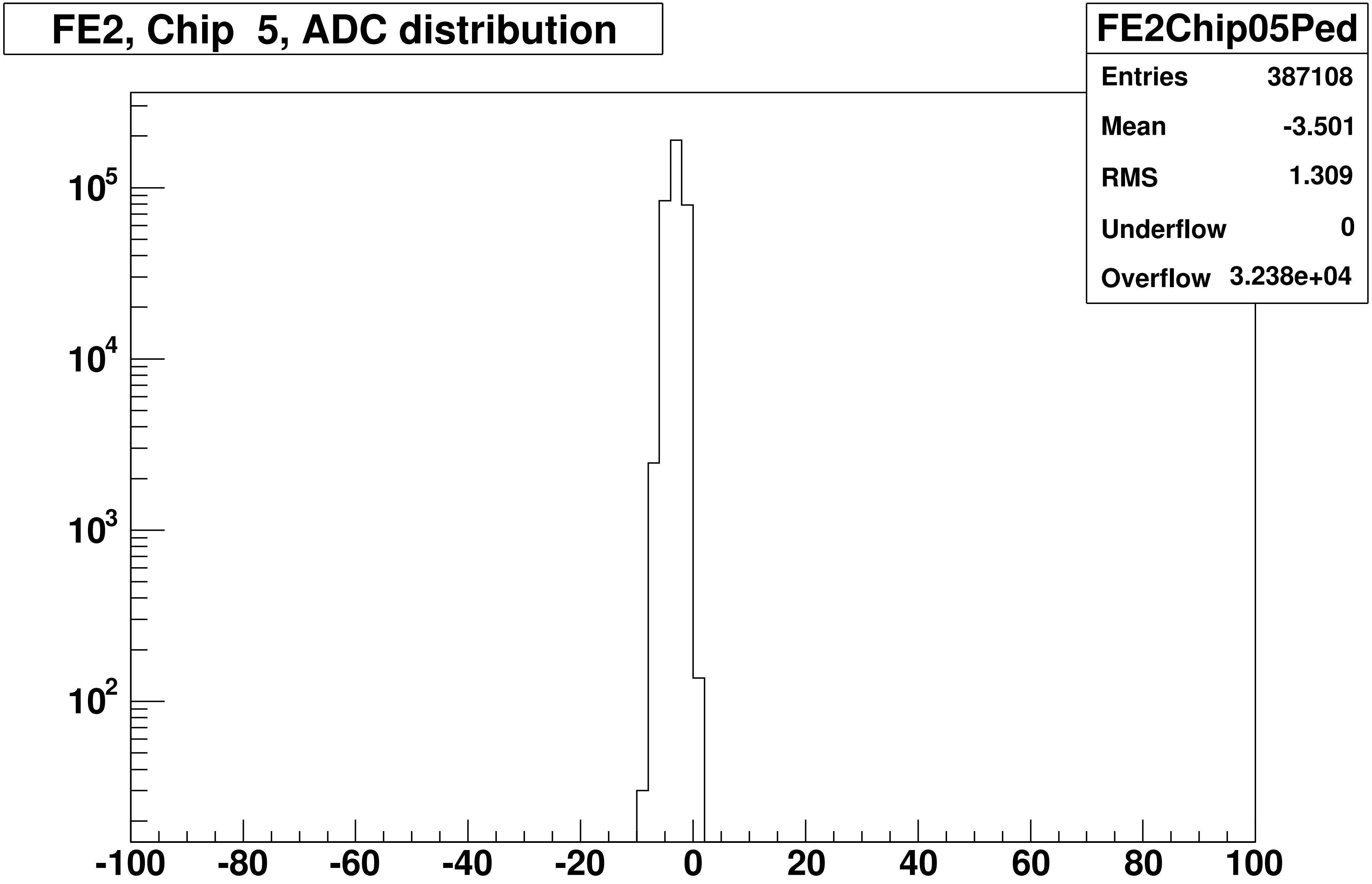
FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 387108
Mean 12.58
RMS 1.358
Underflow 0
Overflow 3.236e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



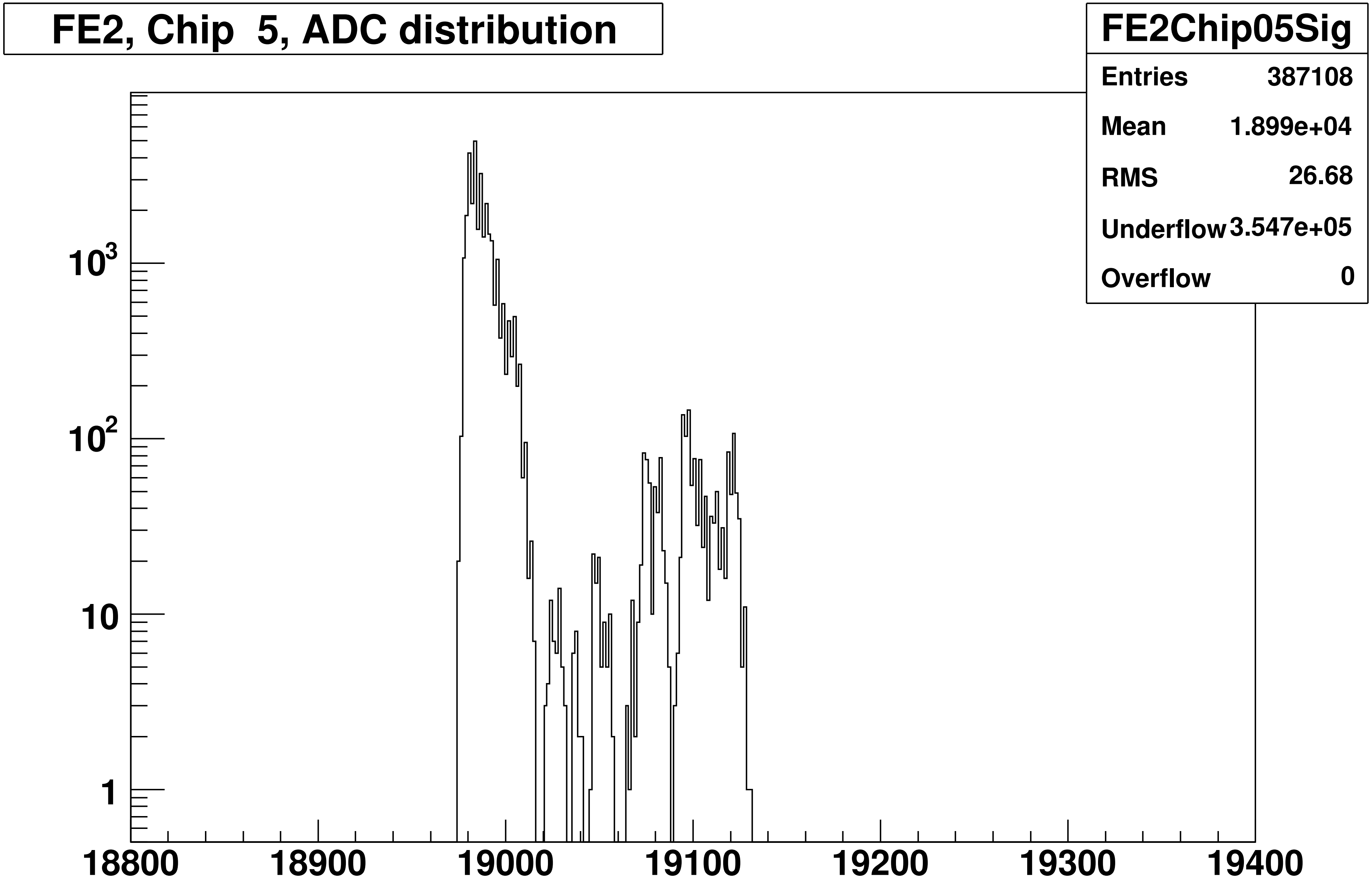
FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 387108
Mean 1.922e+04
RMS 26.16
Underflow 3.548e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

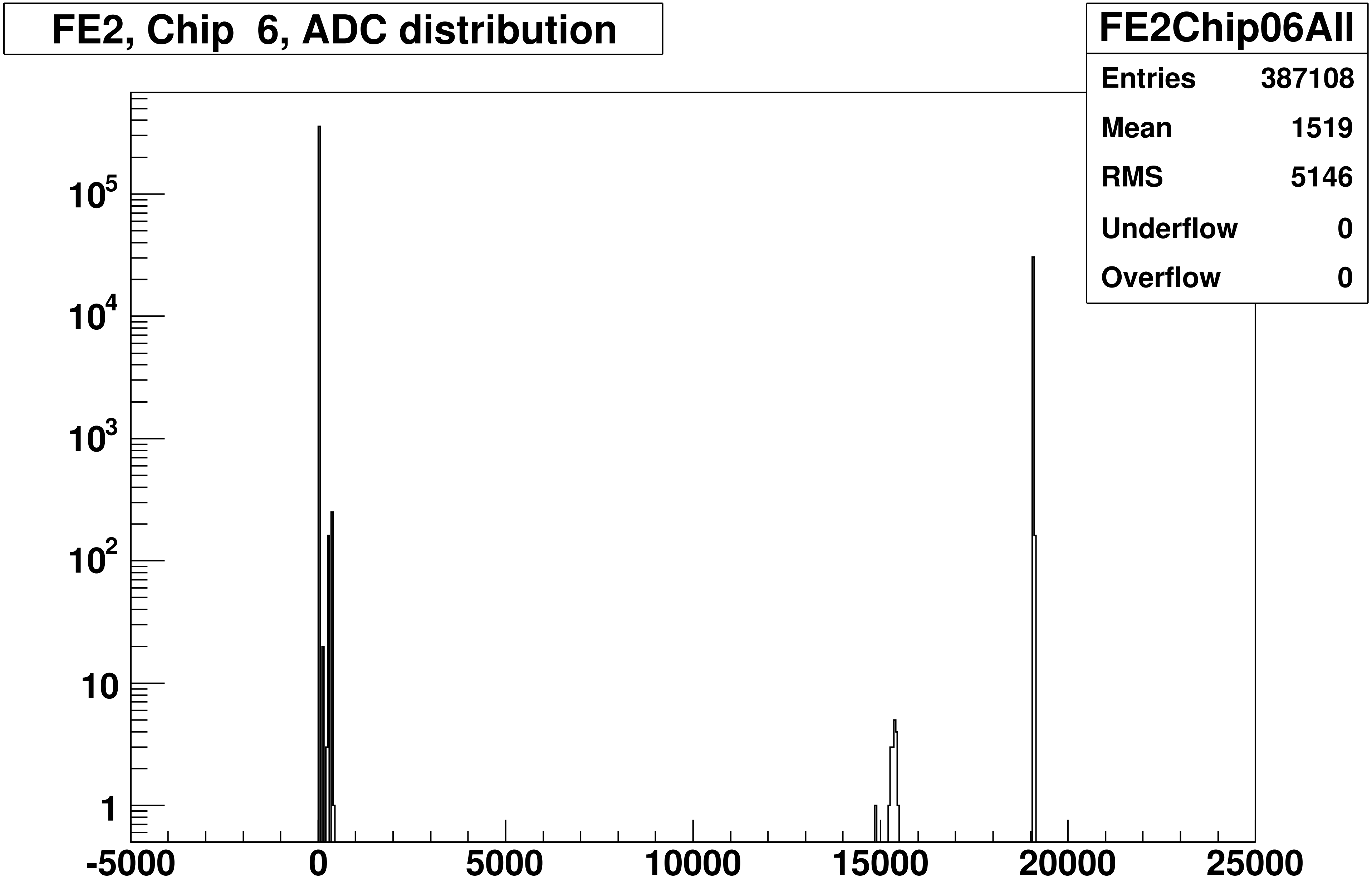


FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 387108
Mean 1586
RMS 5259
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

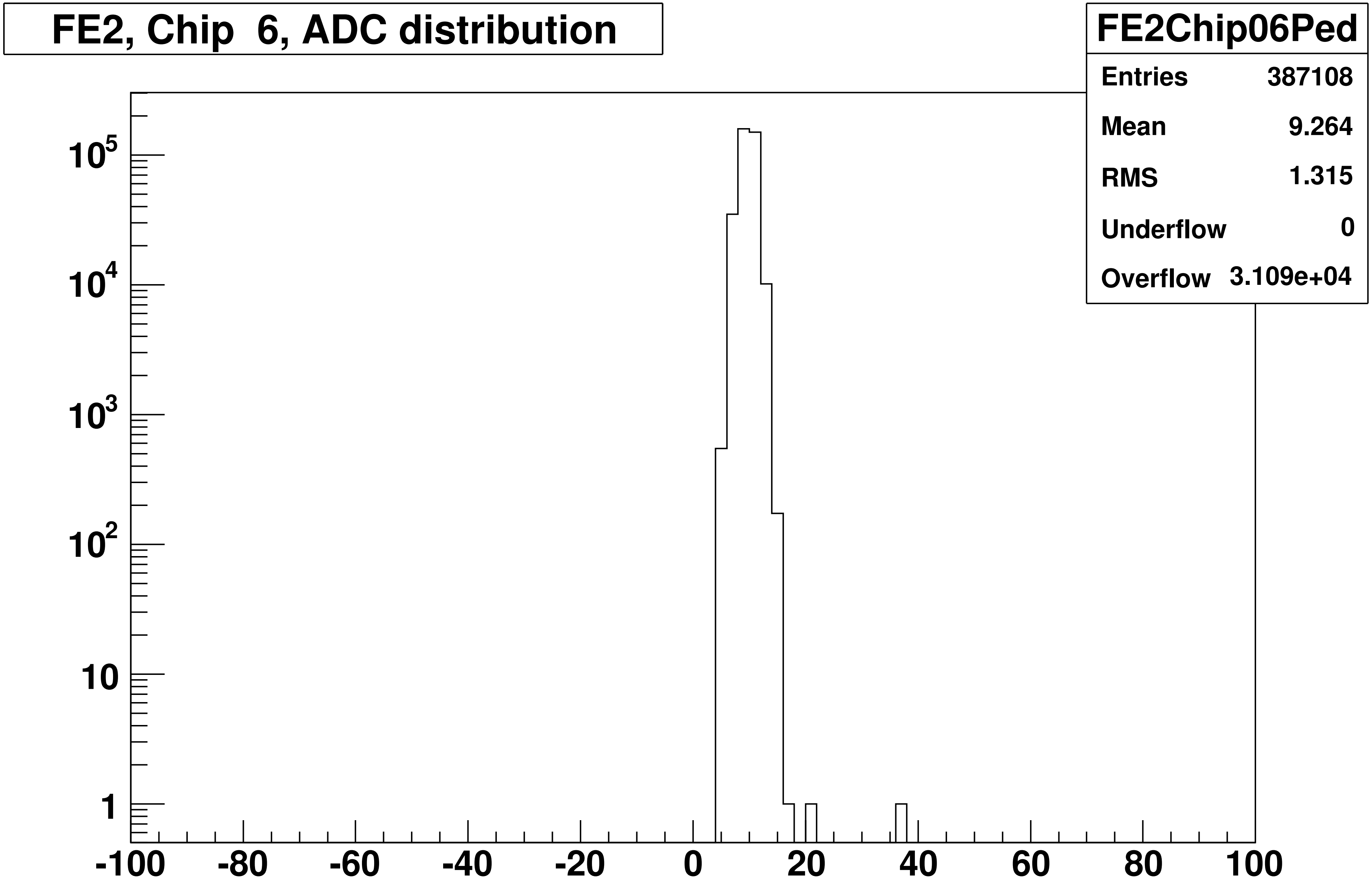


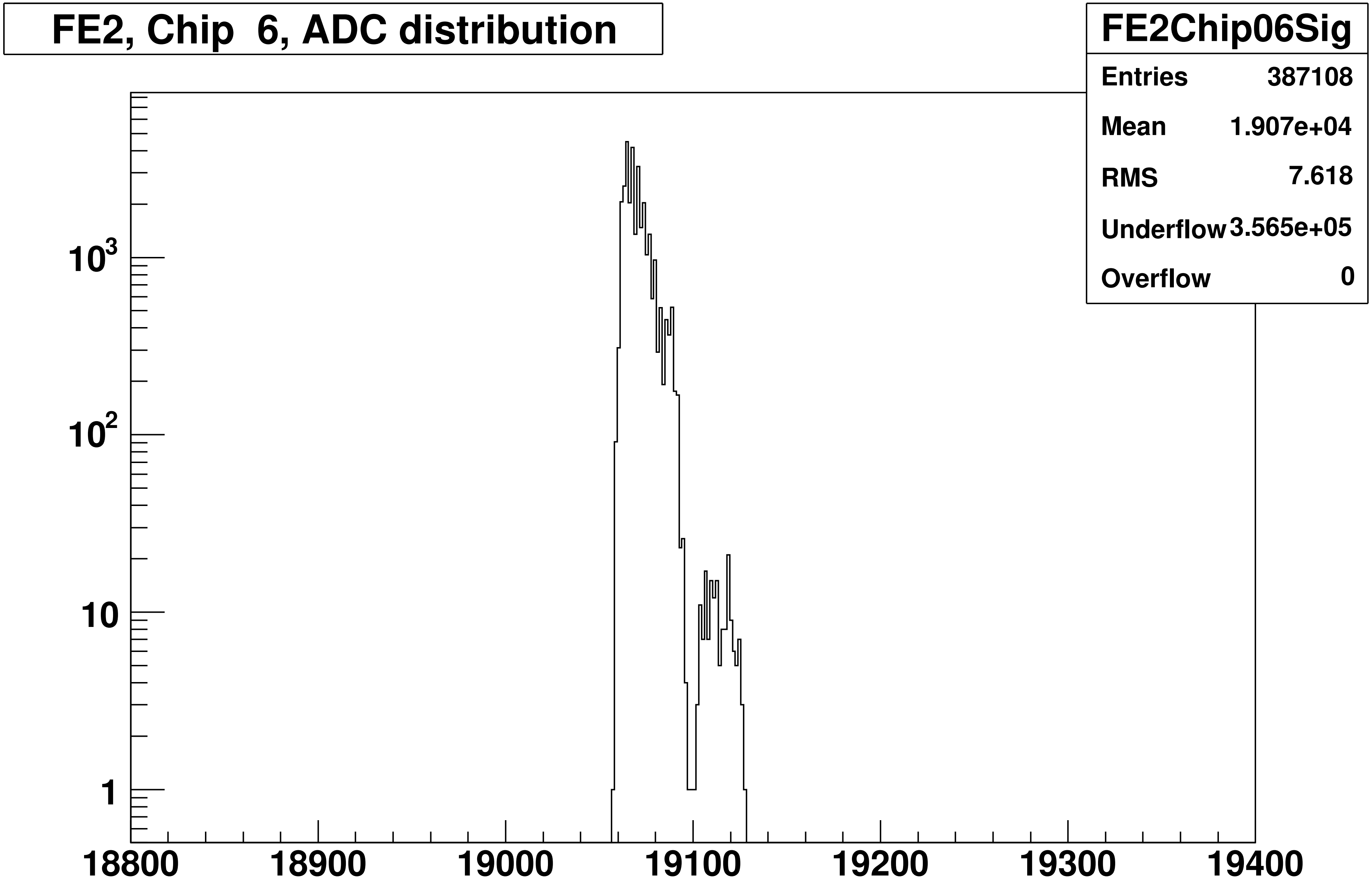
FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 387108
Mean -3.501
RMS 1.309
Underflow 0
Overflow 3.238e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

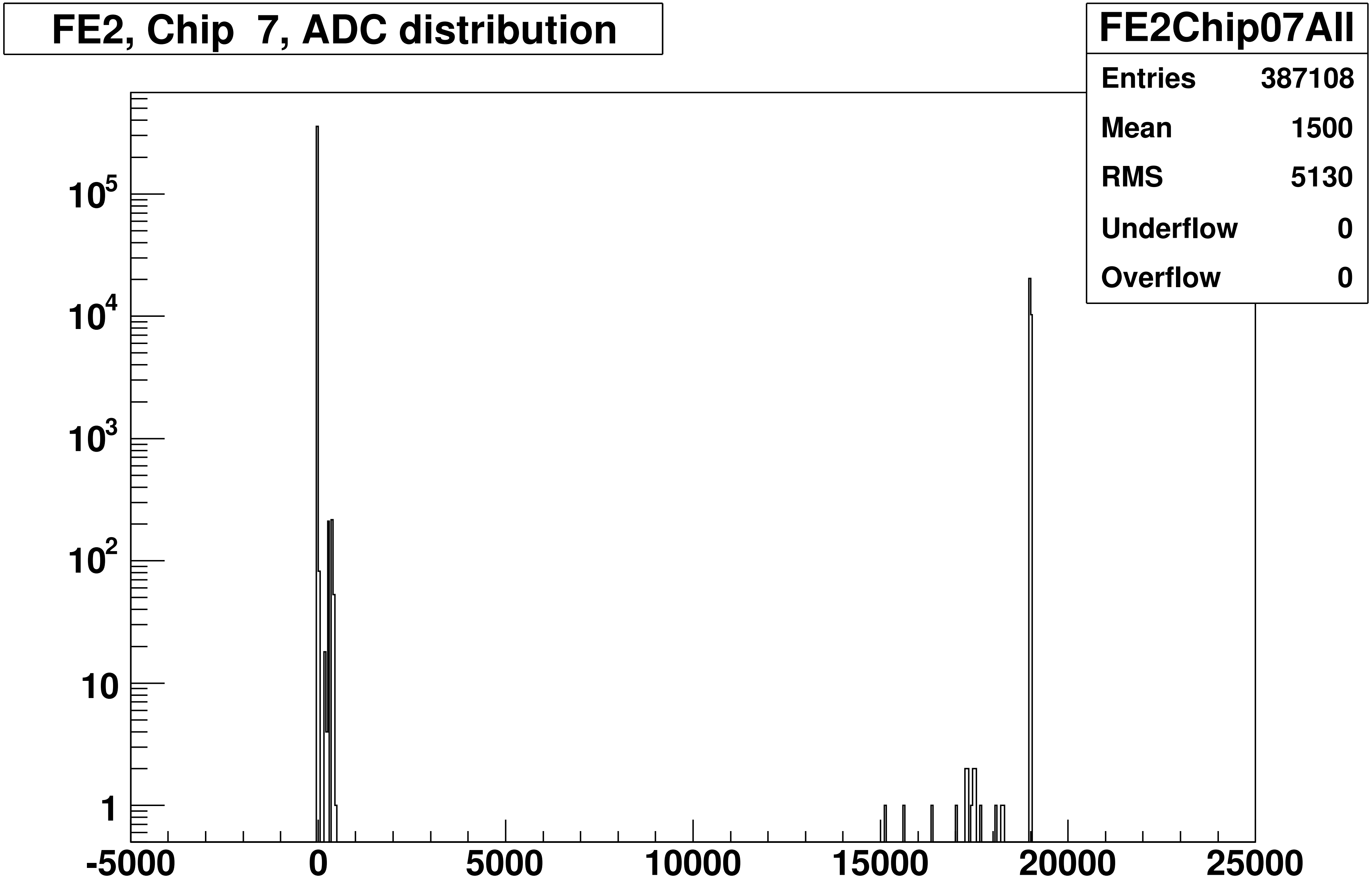

FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 387108
Mean 1.899e+04
RMS 26.68
Underflow 3.547e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

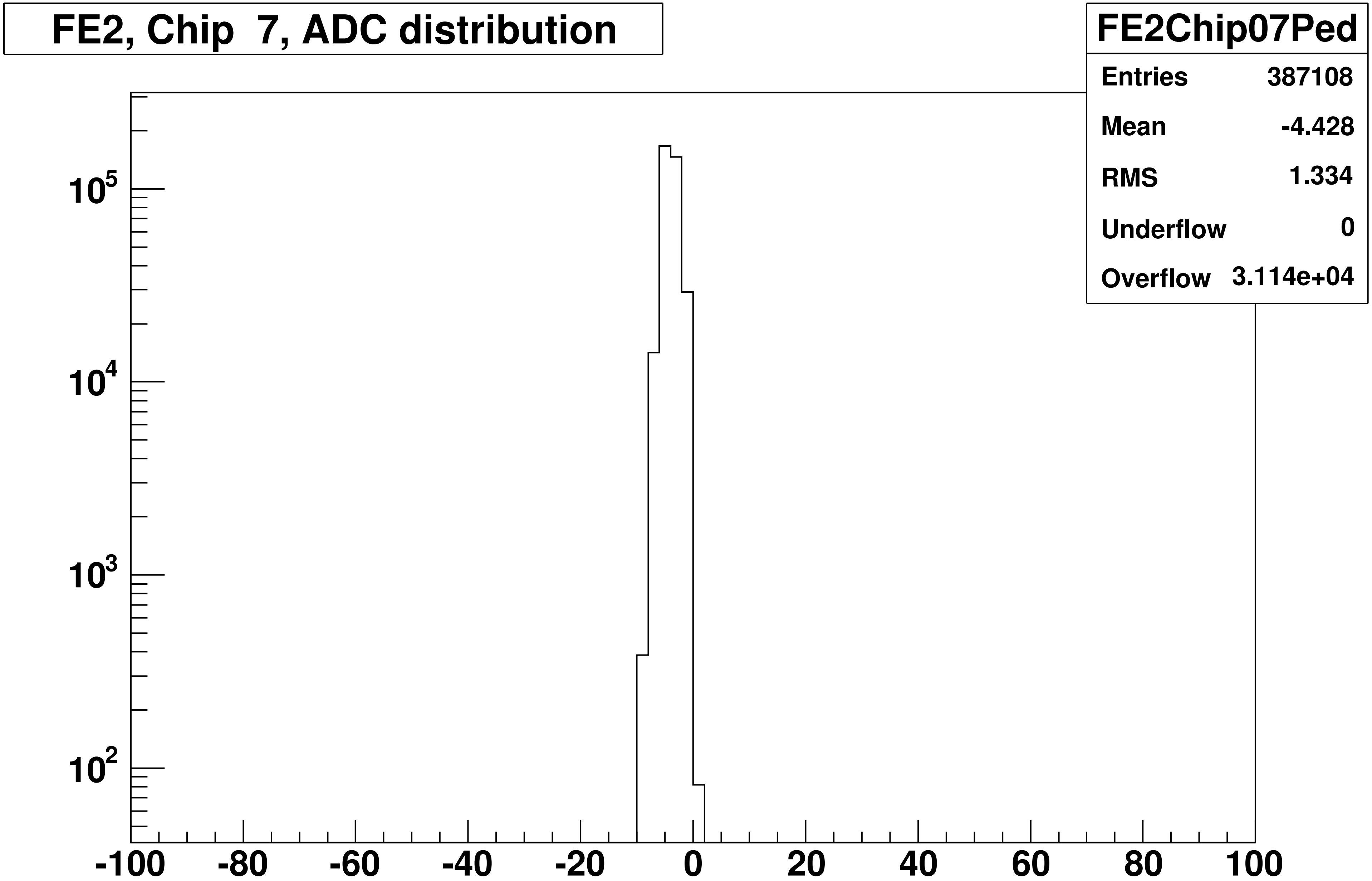


FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 387108
Mean 1519
RMS 5146
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

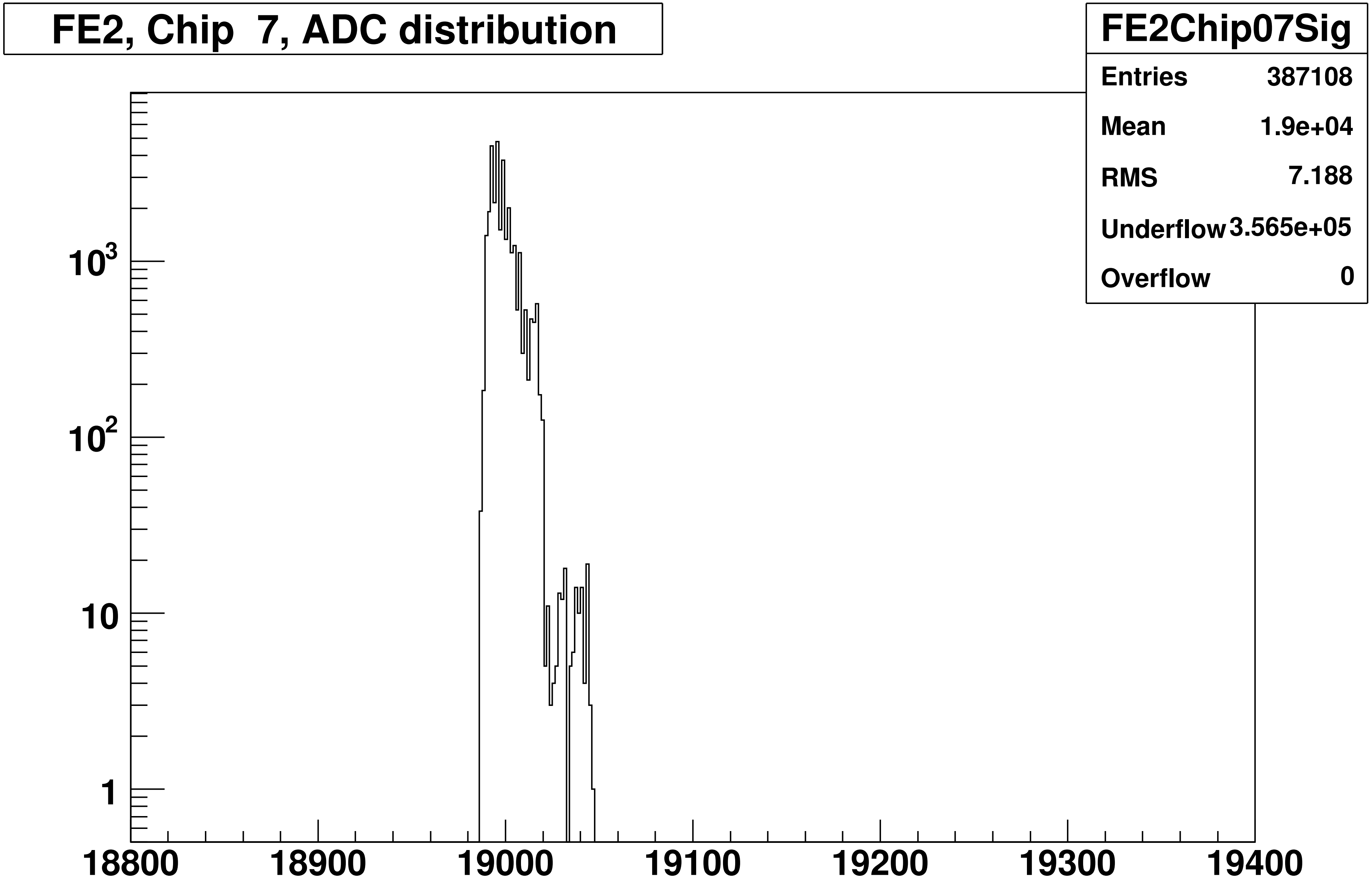

FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 387108
Mean 9.264
RMS 1.315
Underflow 0
Overflow 3.109e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 387108
Mean 1.907e+04
RMS 7.618
Underflow 3.565e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

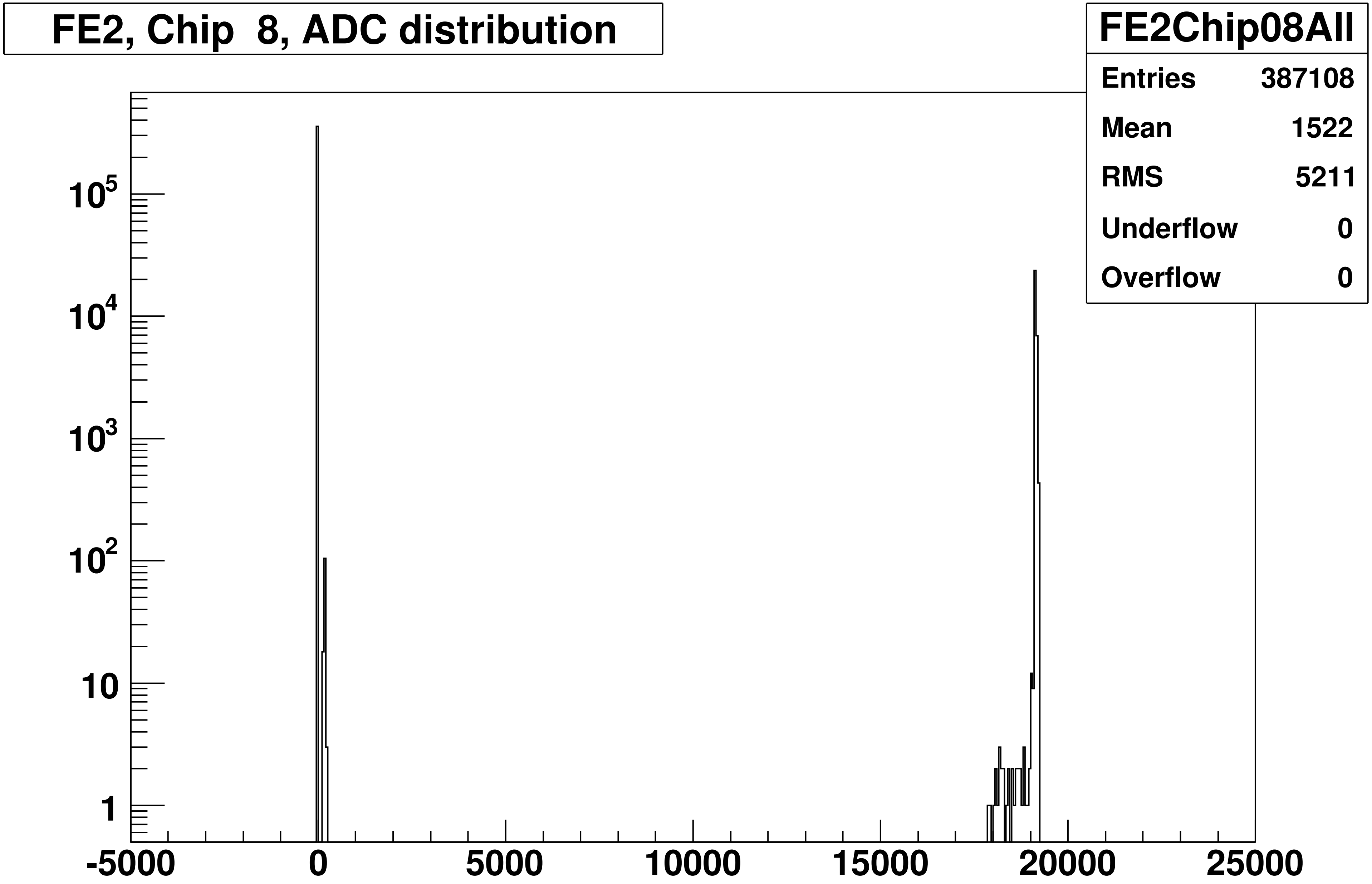

FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 387108
Mean 1500
RMS 5130
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



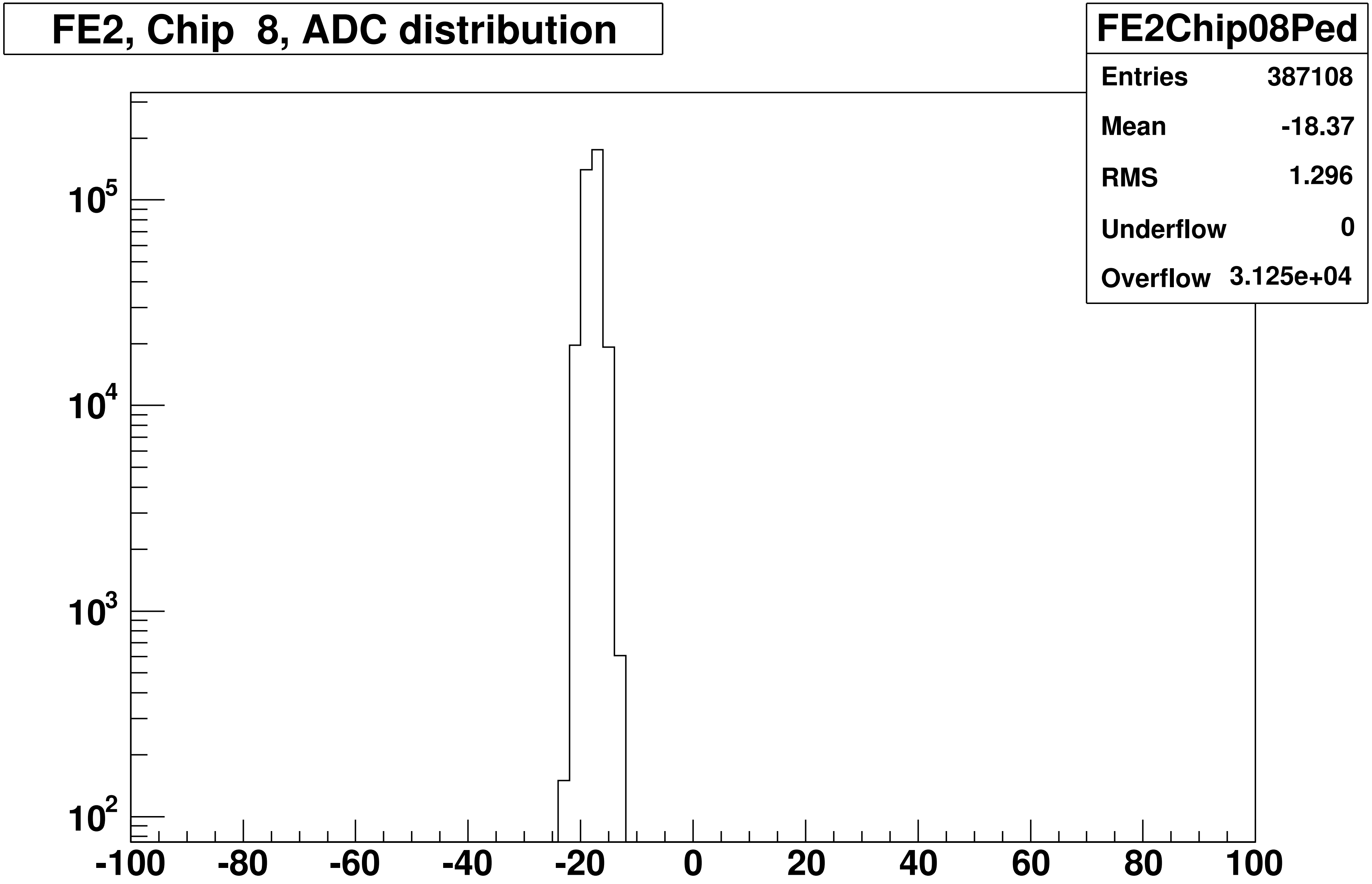
FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 387108
Mean -4.428
RMS 1.334
Underflow 0
Overflow 3.114e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

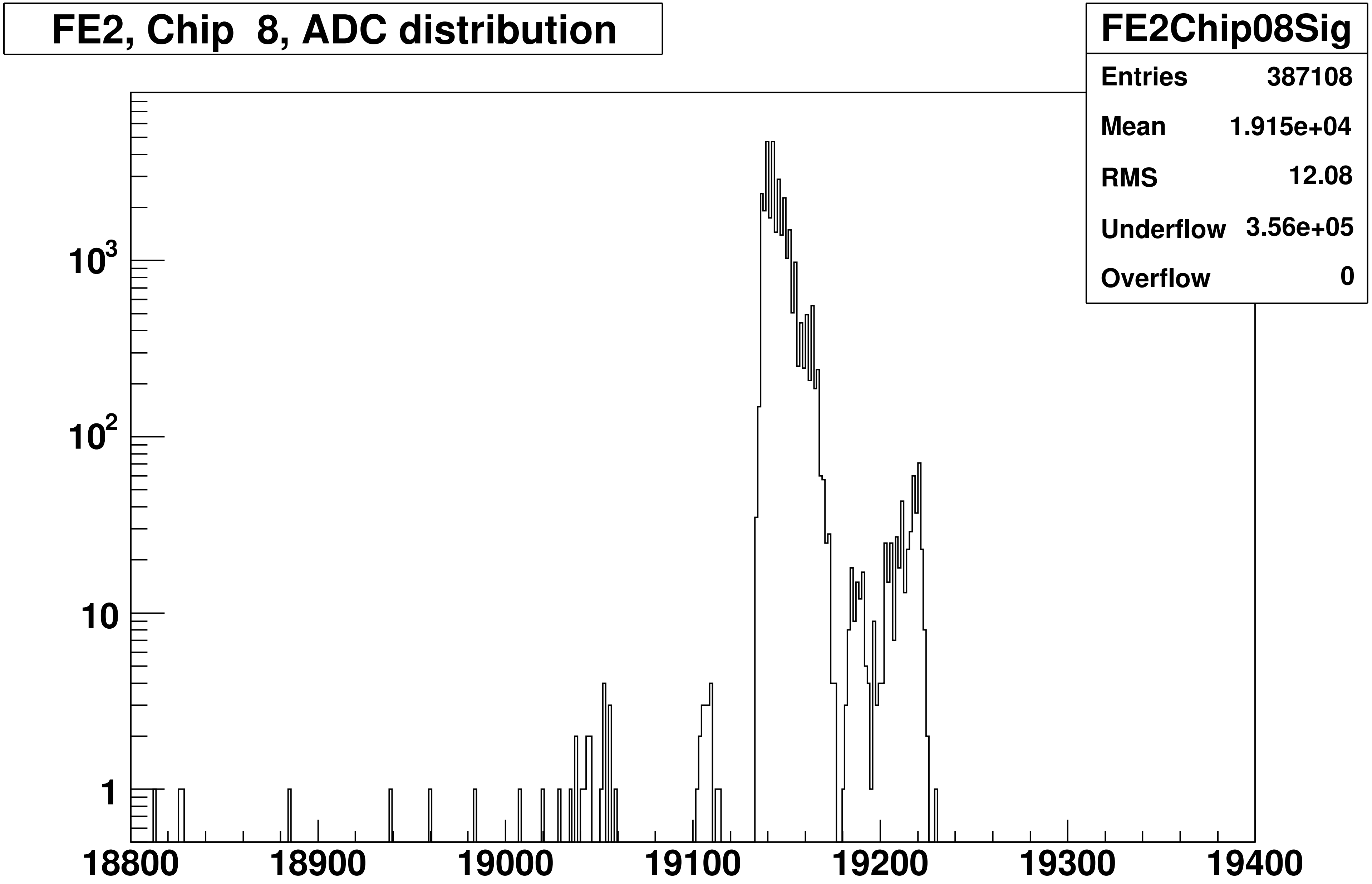


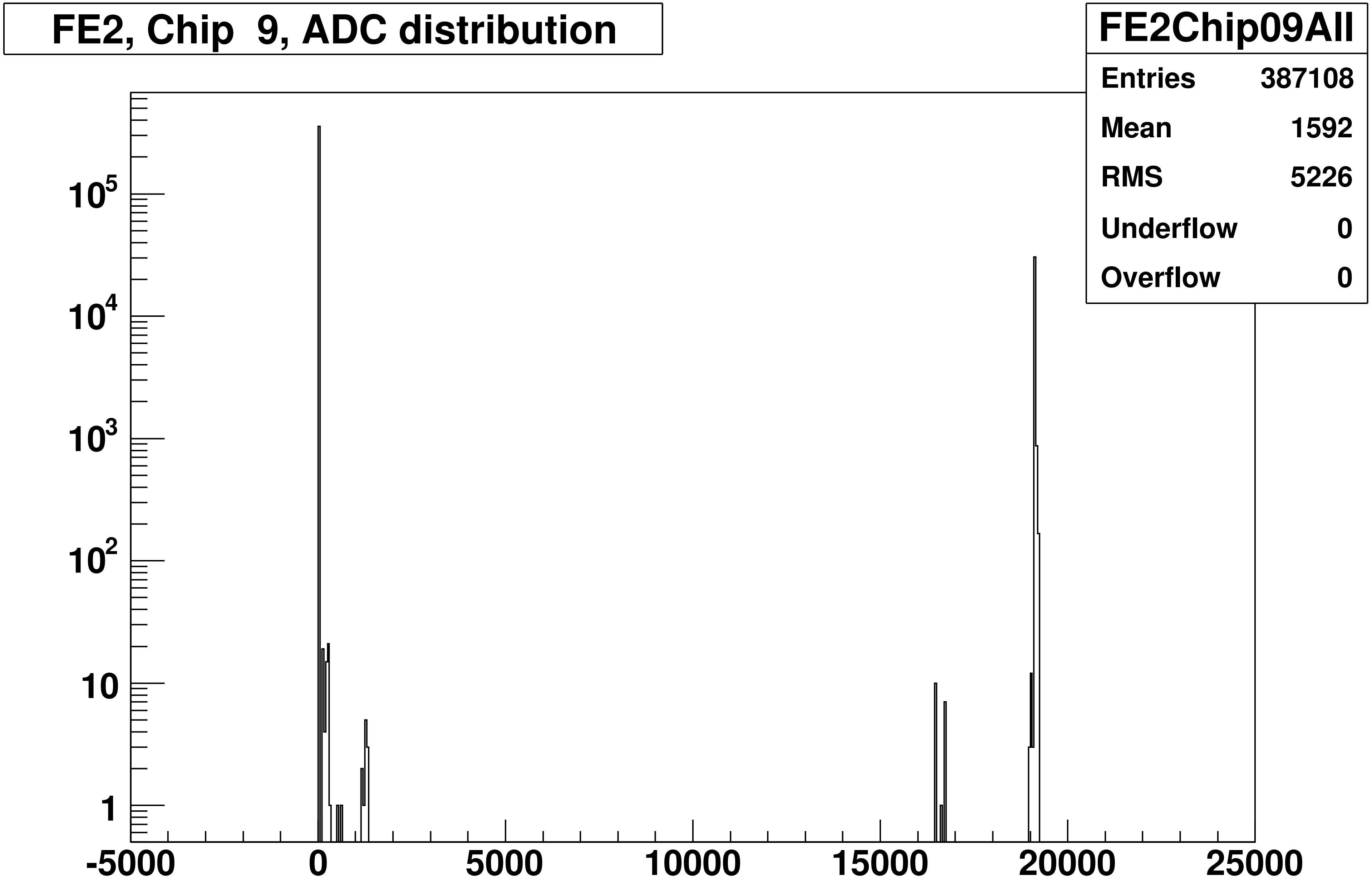
FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 387108
Mean 1.9e+04
RMS 7.188
Underflow 3.565e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

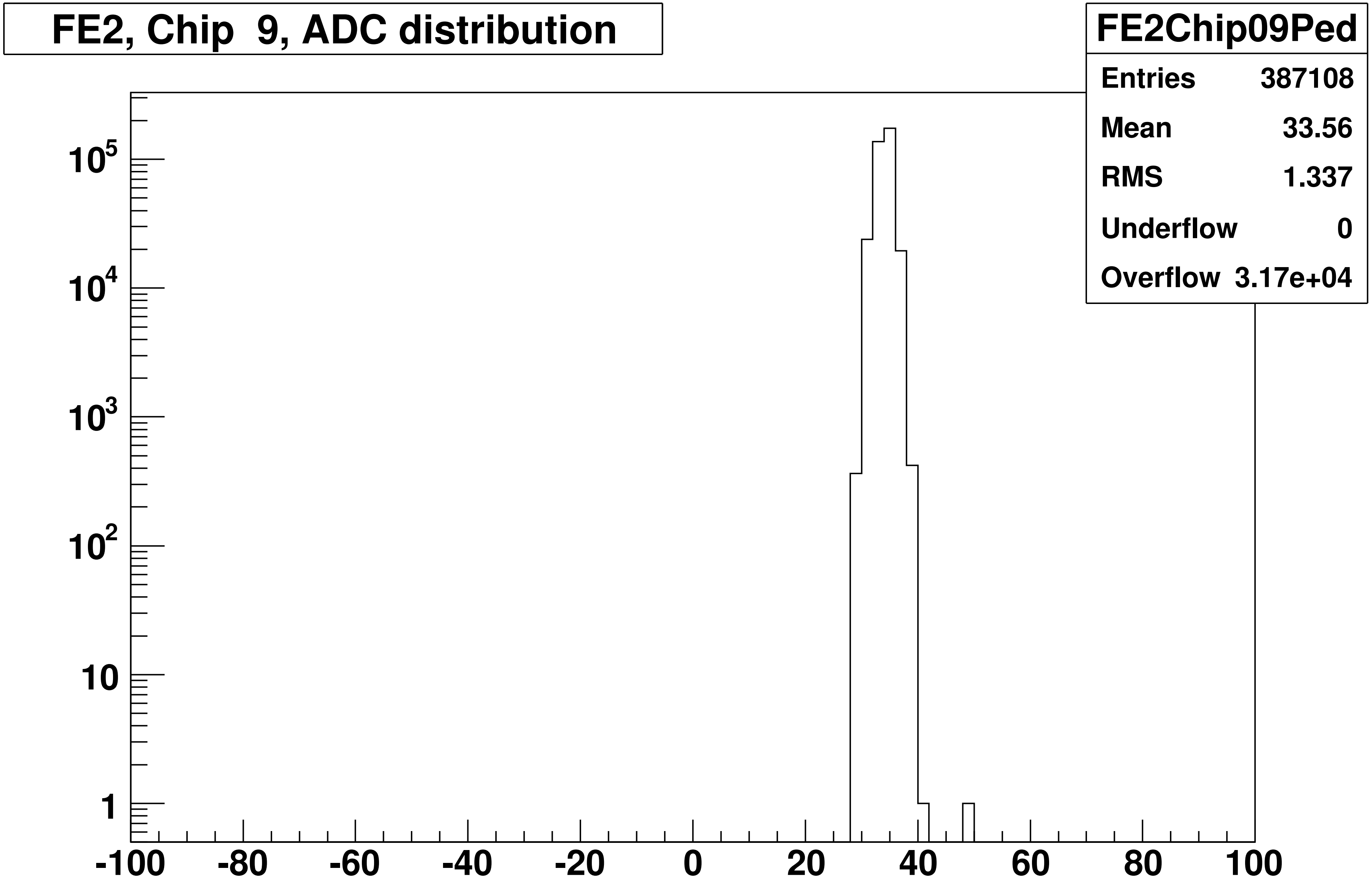


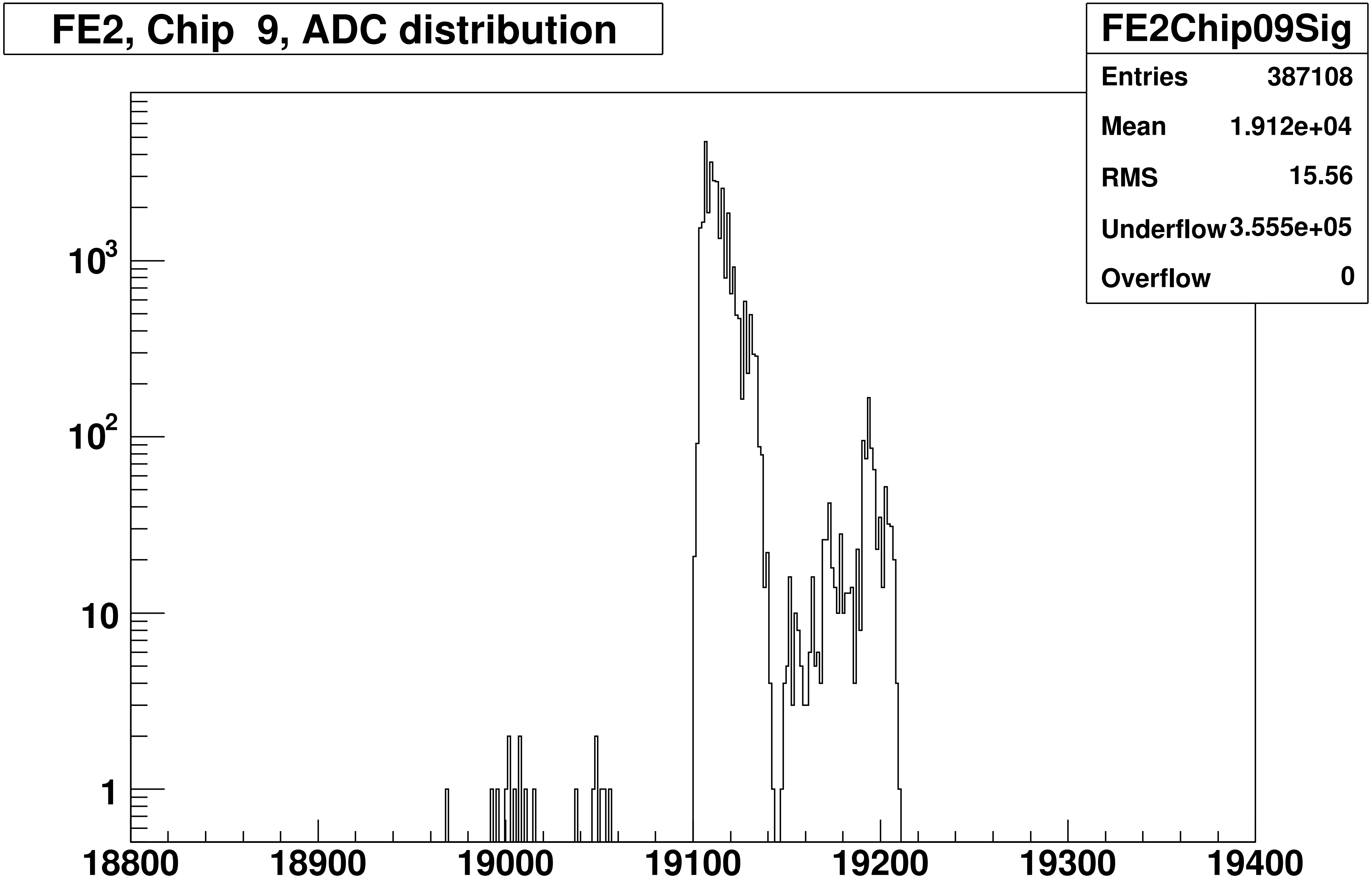
FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 387108
Mean 1522
RMS 5211
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

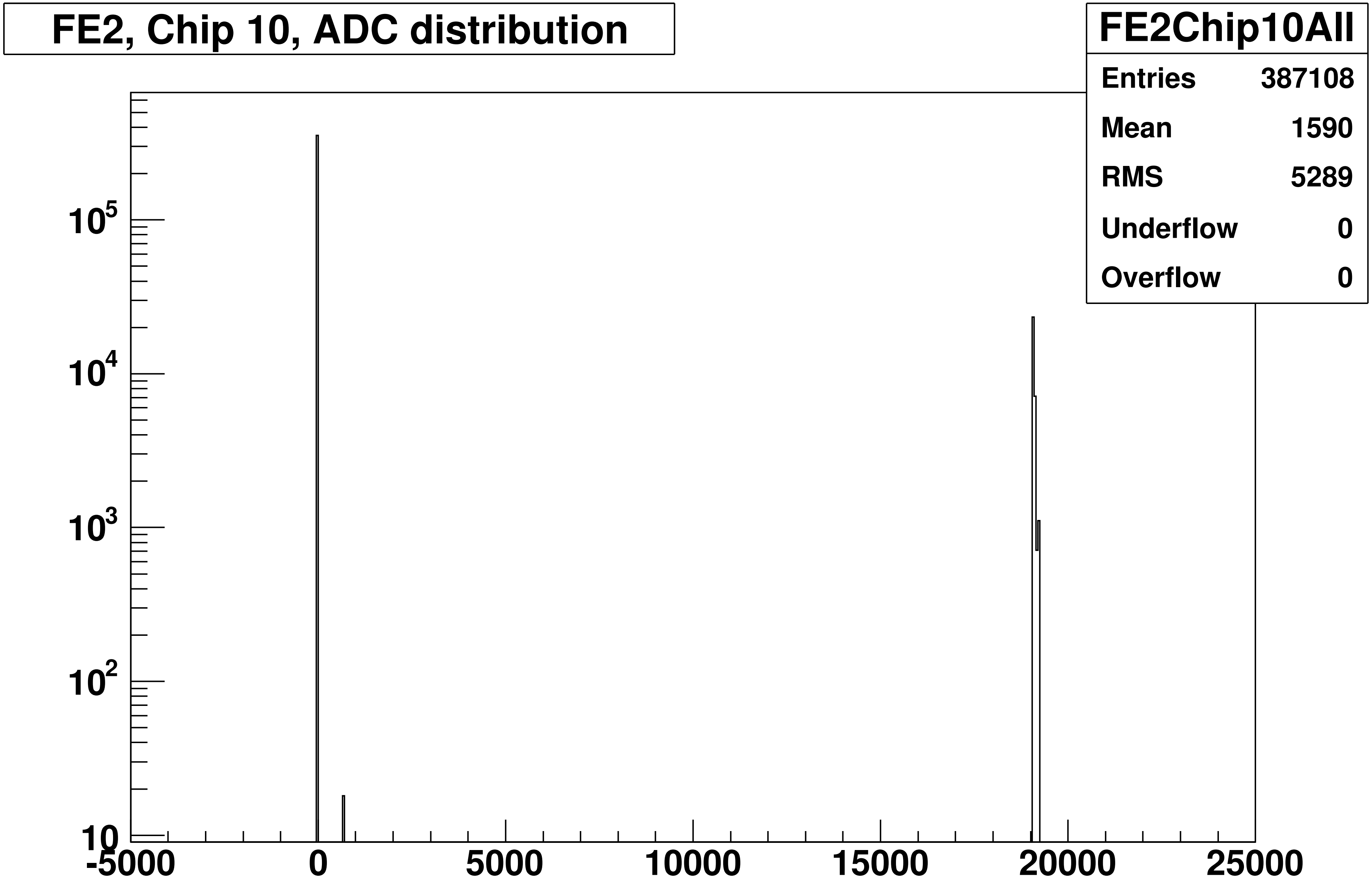

FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 387108
Mean -18.37
RMS 1.296
Underflow 0
Overflow 3.125e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 387108
Mean 1.915e+04
RMS 12.08
Underflow 3.56e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

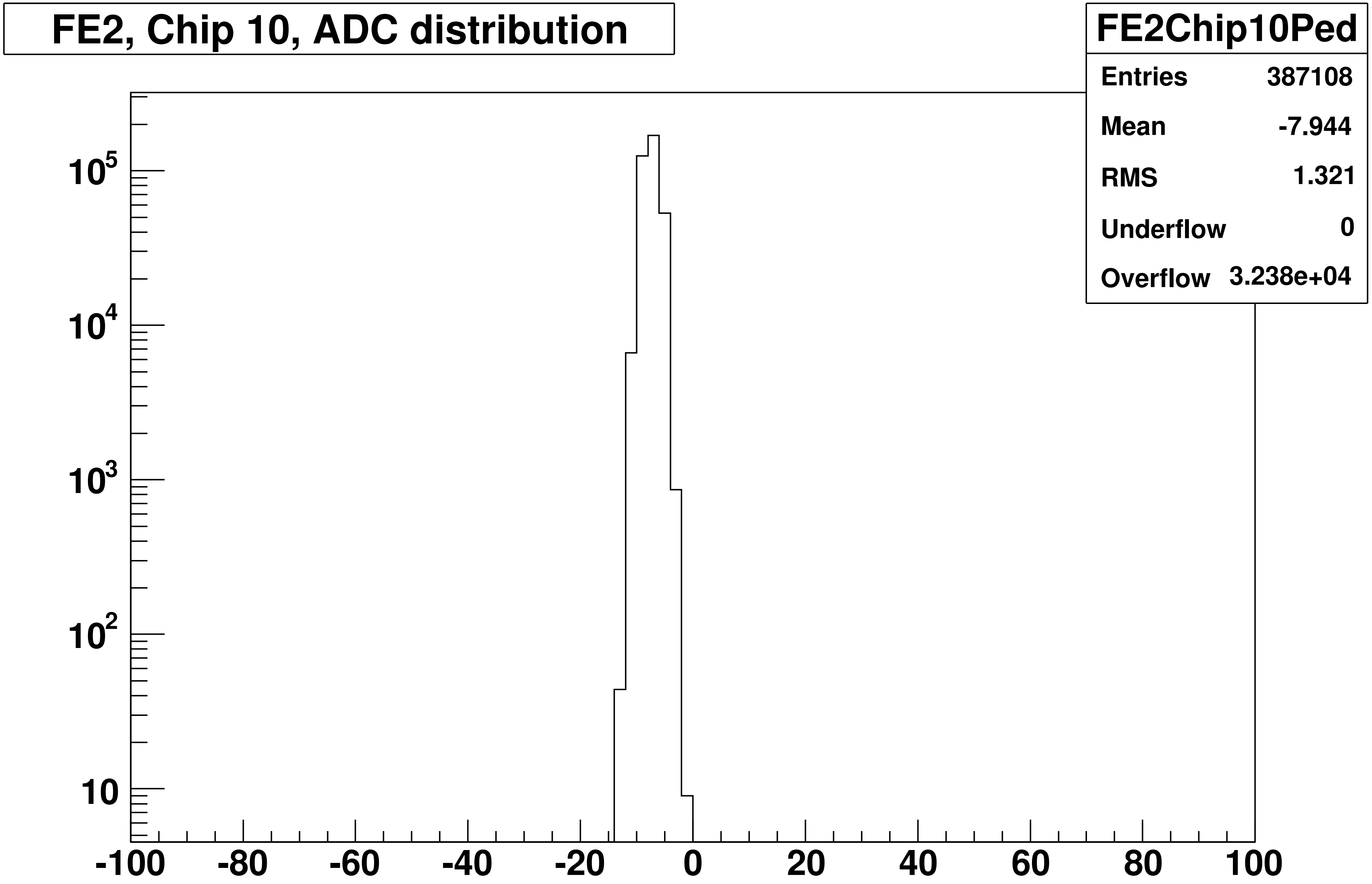

FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 387108
Mean 1592
RMS 5226
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 387108
Mean 33.56
RMS 1.337
Underflow 0
Overflow 3.17e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

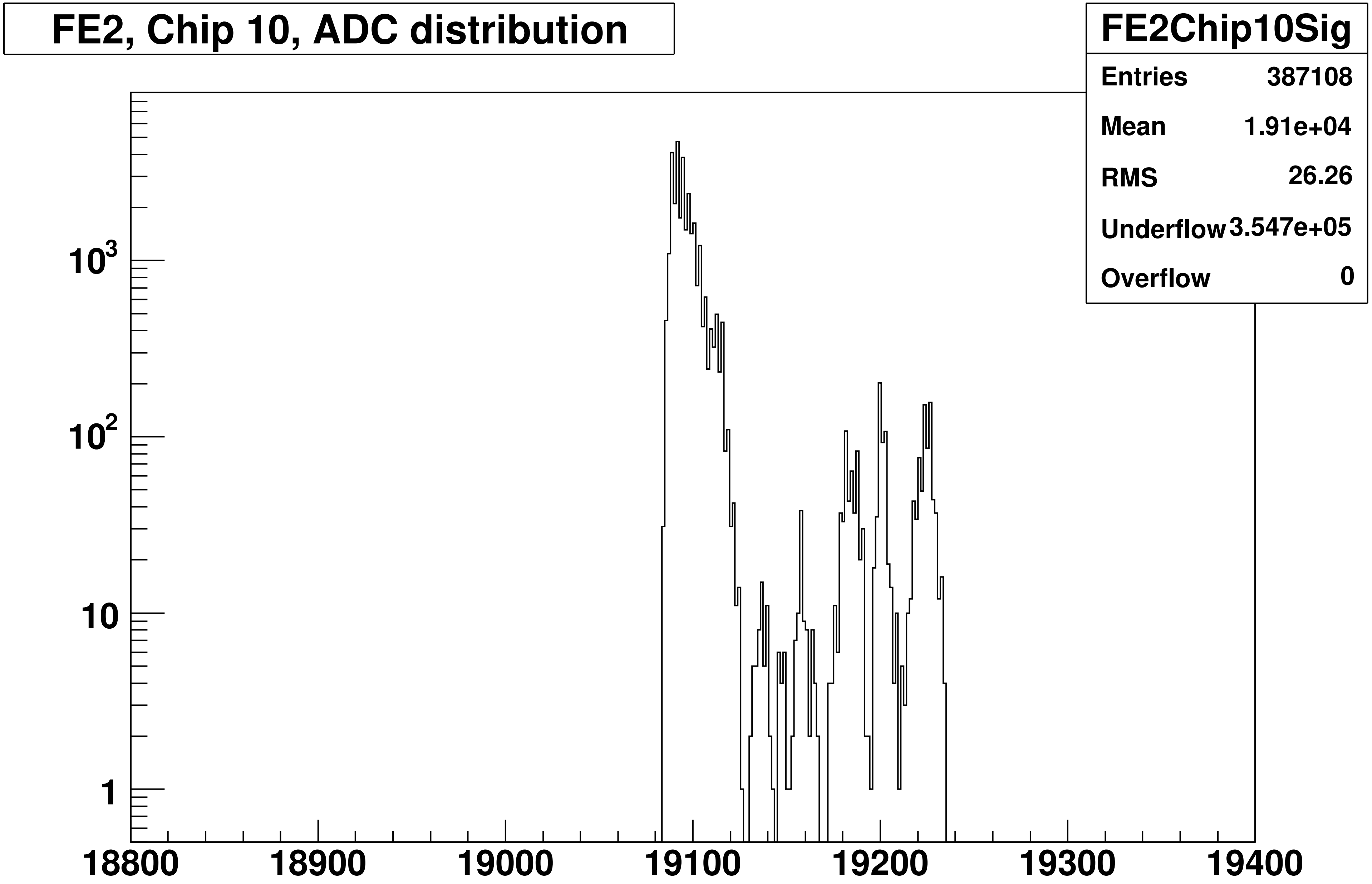

FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 387108
Mean 1.912e+04
RMS 15.56
Underflow 3.555e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



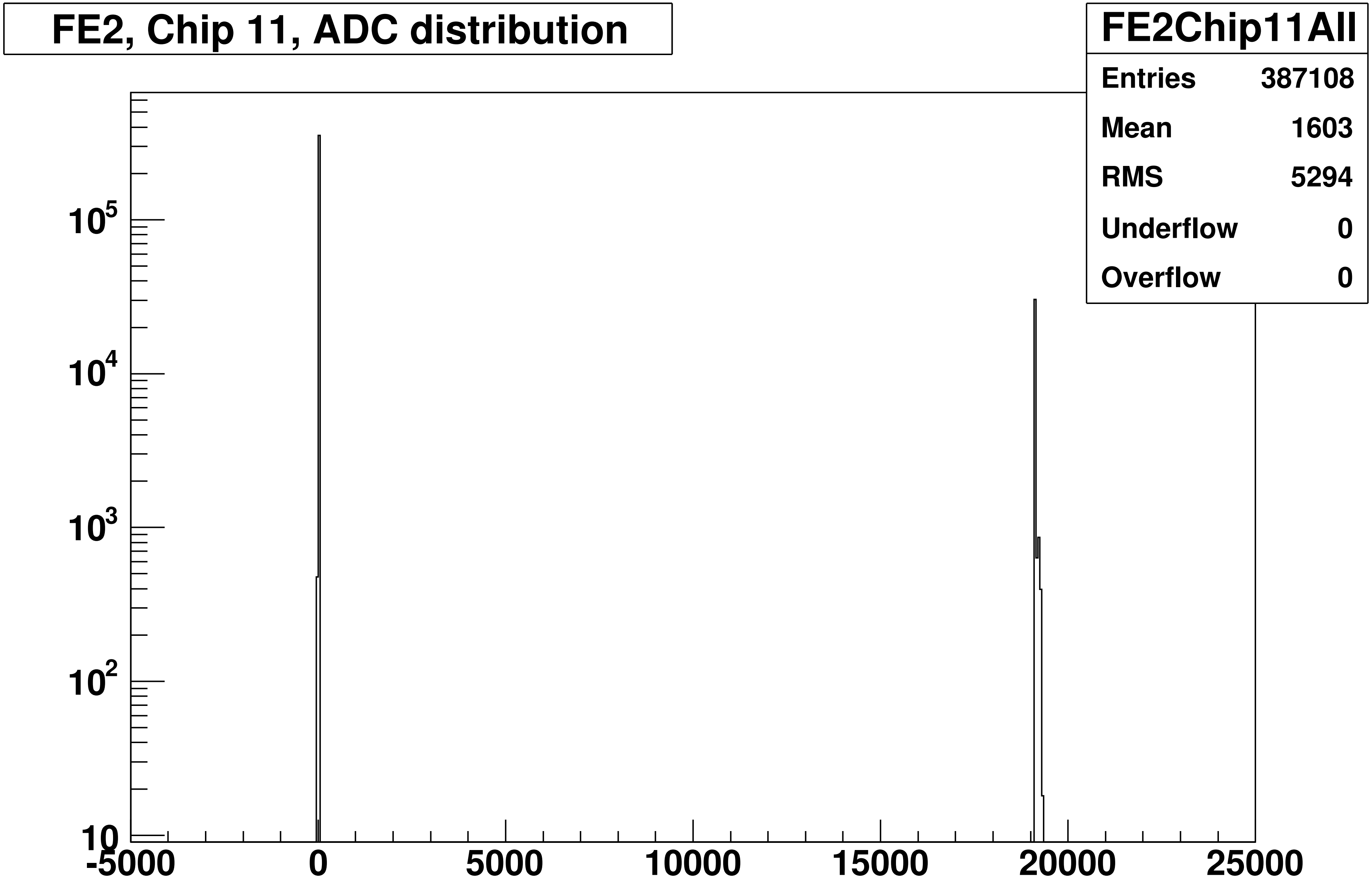
FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 387108
Mean 1590
RMS 5289
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



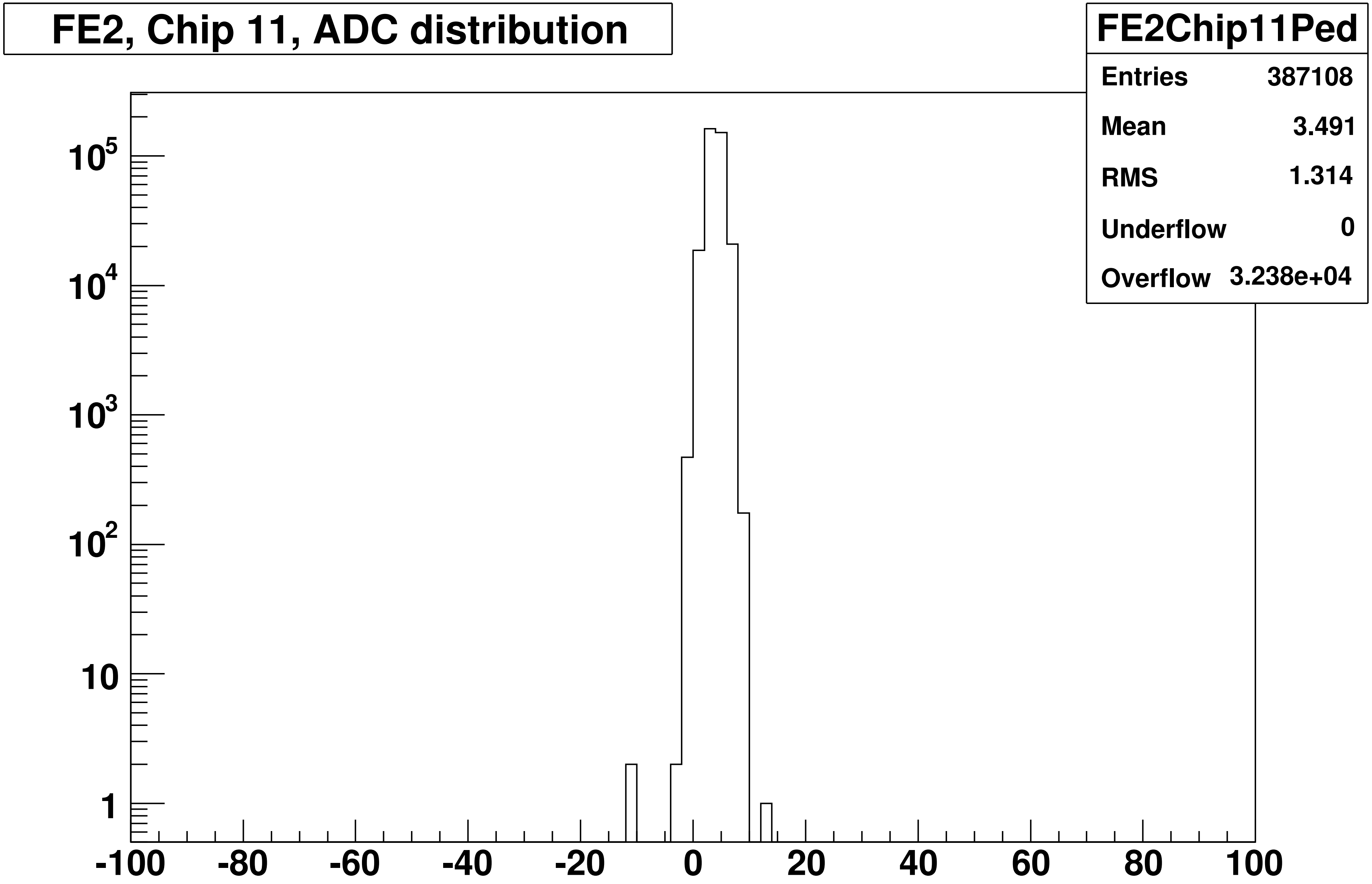
FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 387108
Mean -7.944
RMS 1.321
Underflow 0
Overflow 3.238e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

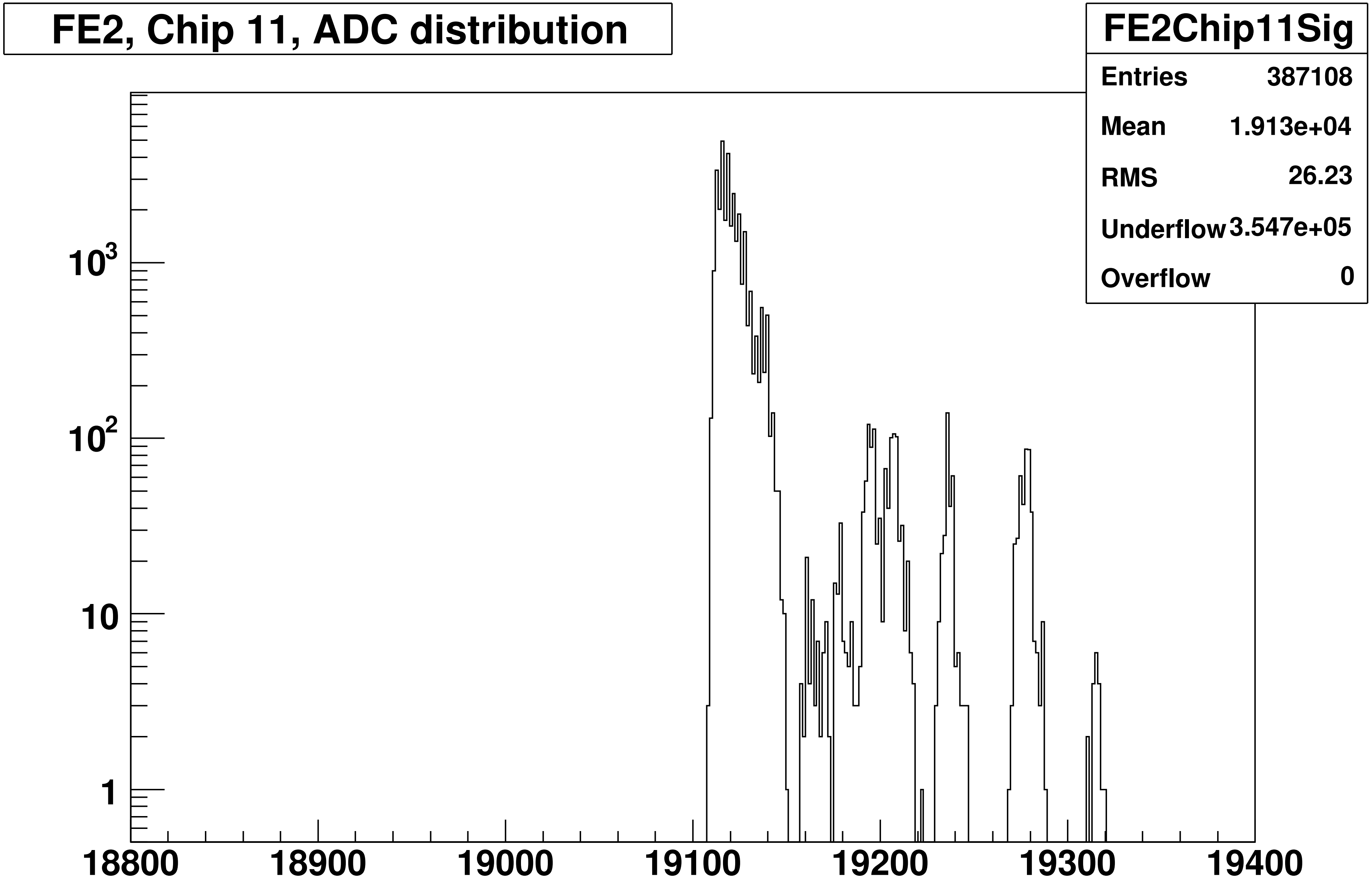


FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 387108
Mean 1.91e+04
RMS 26.26
Underflow 3.547e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

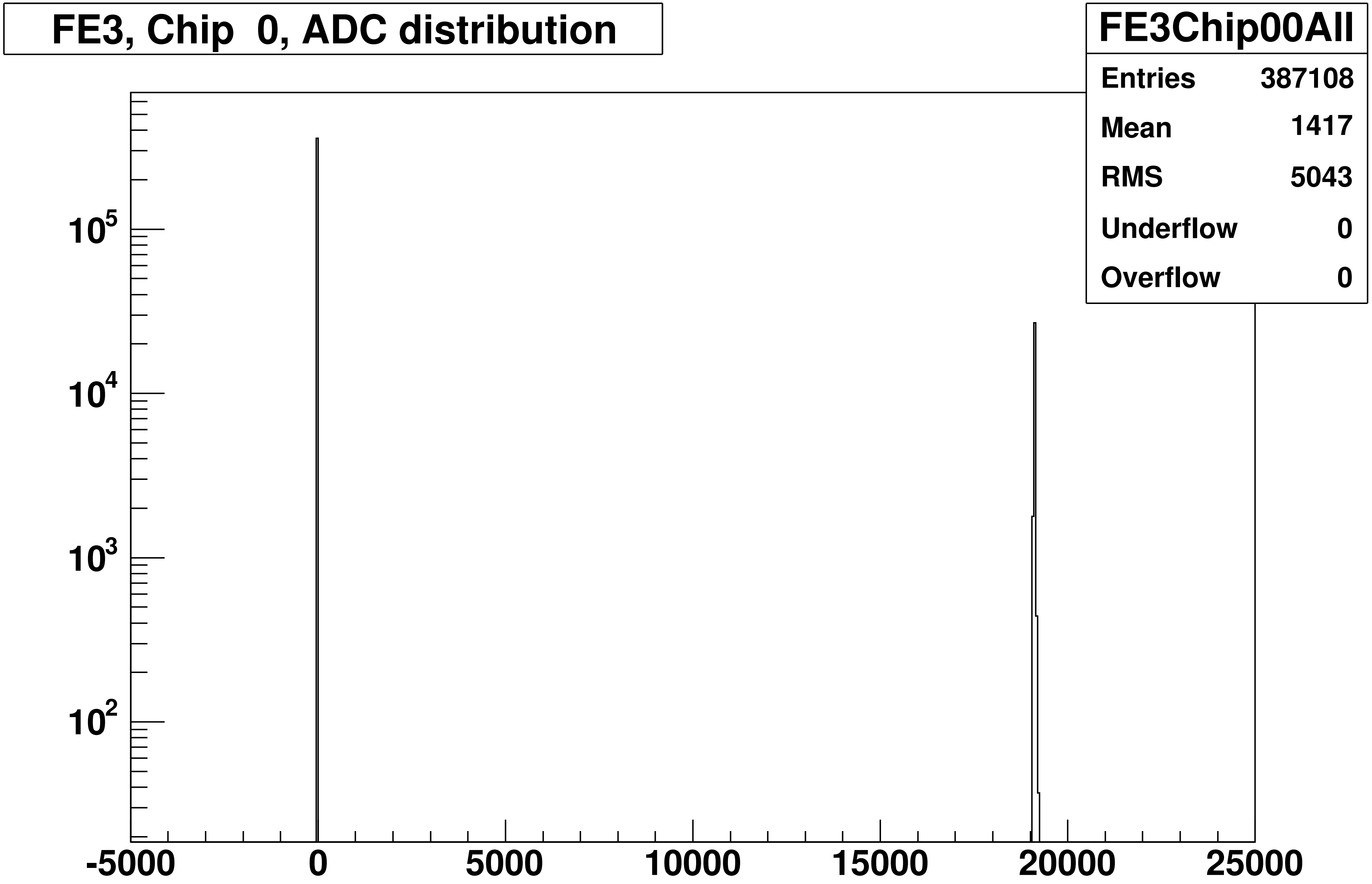


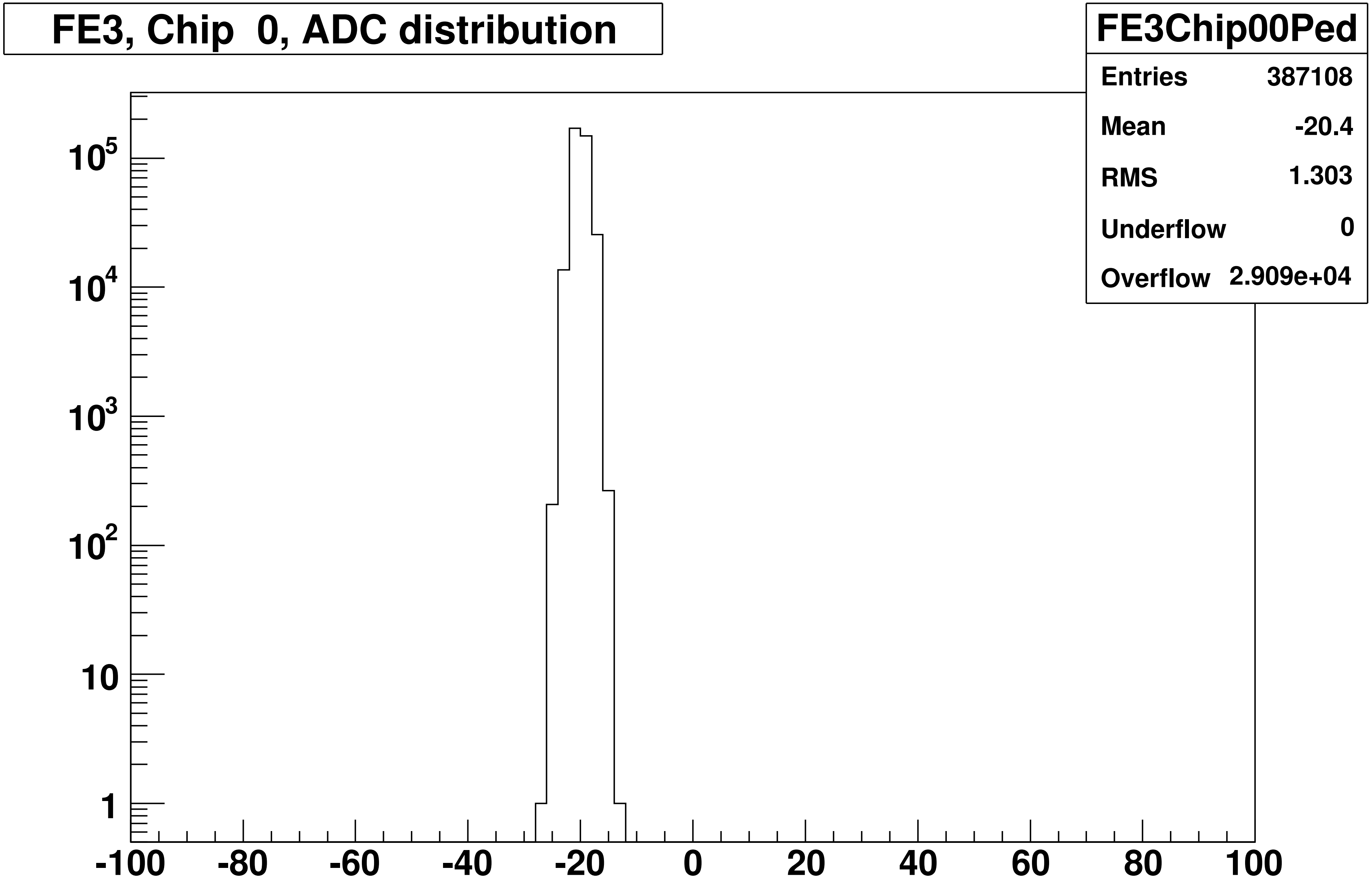
FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 387108
Mean 1603
RMS 5294
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

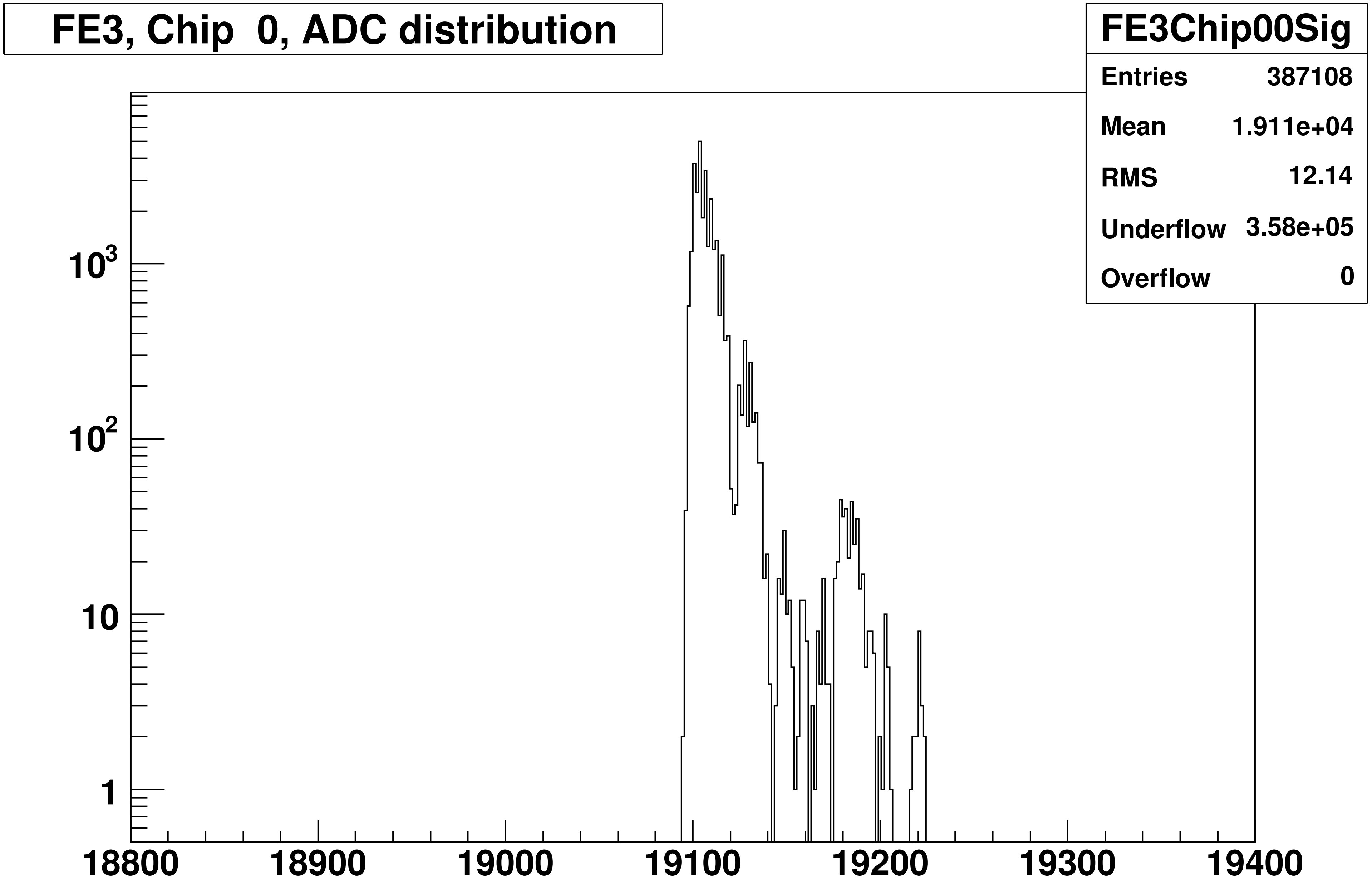

FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 387108
Mean 3.491
RMS 1.314
Underflow 0
Overflow 3.238e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



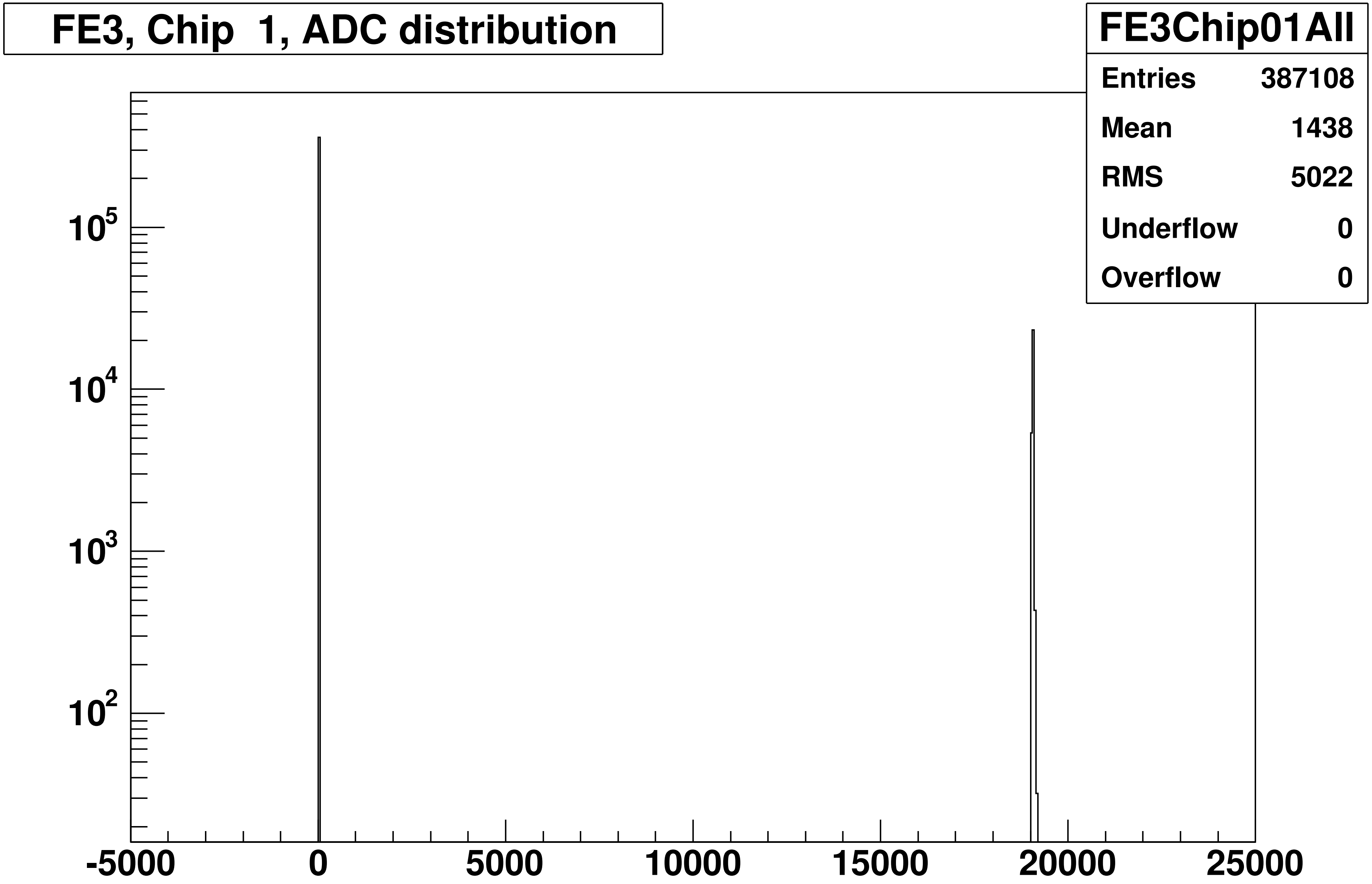
FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 387108
Mean 1.913e+04
RMS 26.23
Underflow 3.547e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 387108
Mean 1417
RMS 5043
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

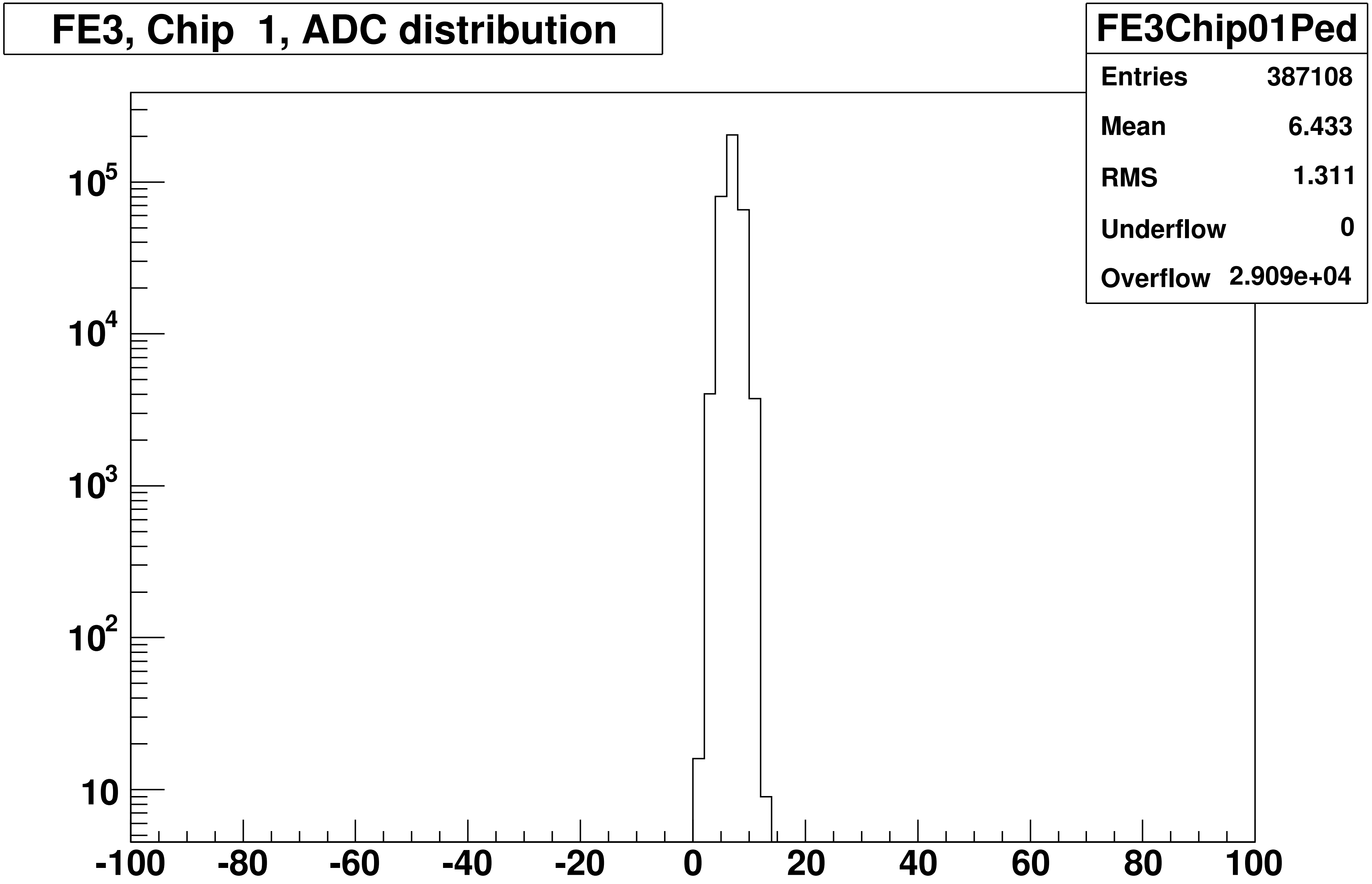

FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 387108
Mean -20.4
RMS 1.303
Underflow 0
Overflow 2.909e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

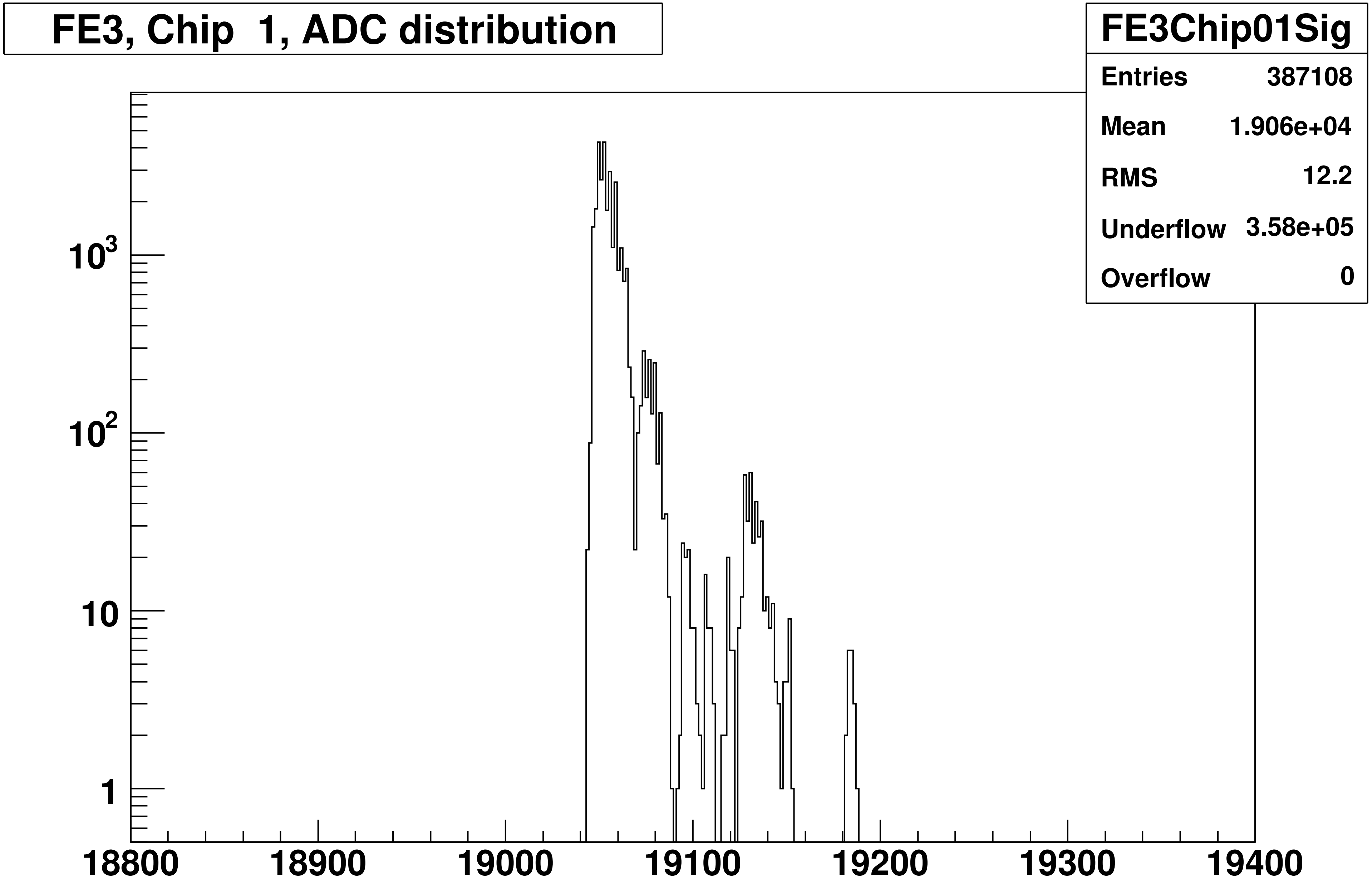


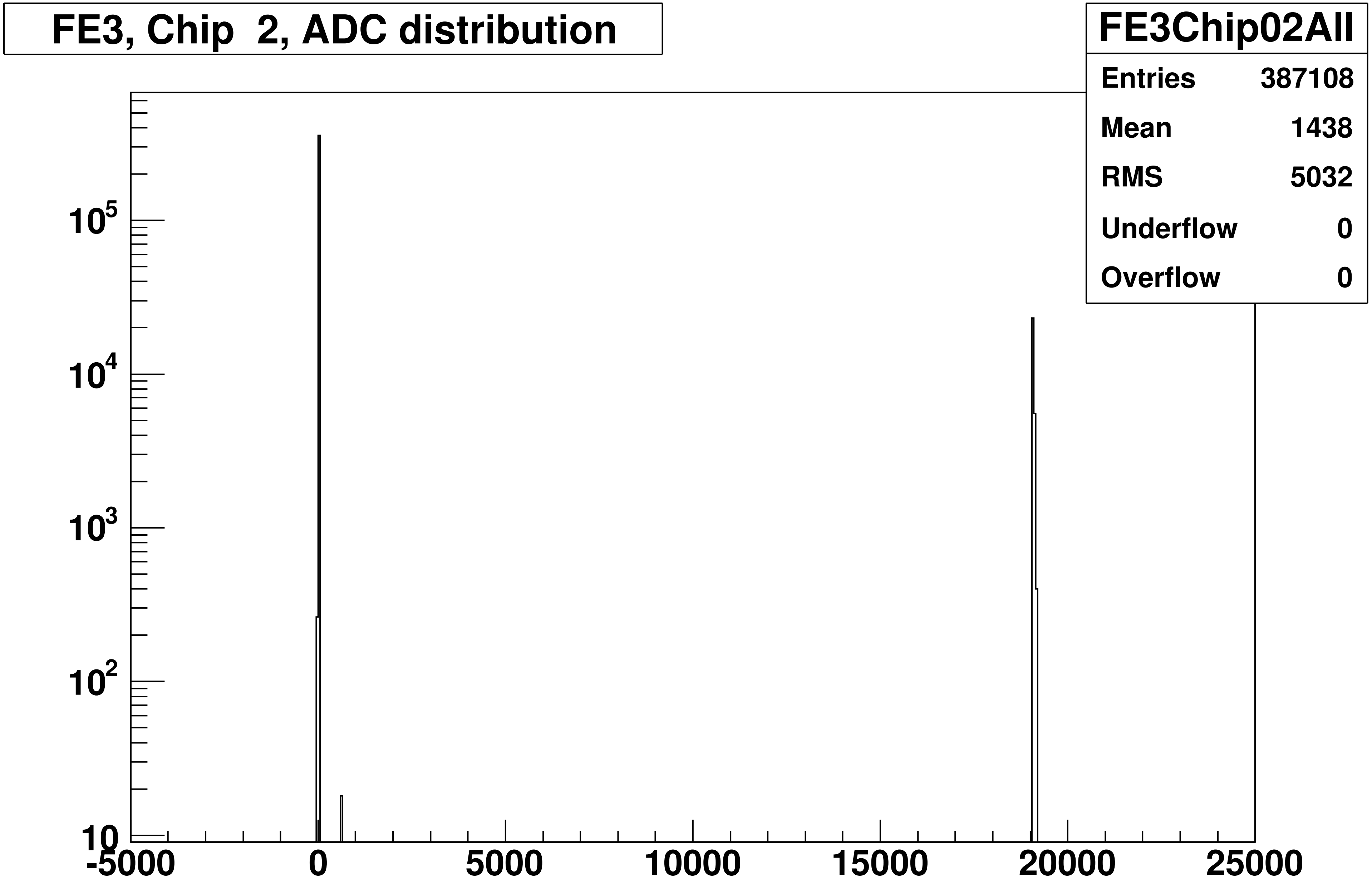
FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 387108
Mean 1.911e+04
RMS 12.14
Underflow 3.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

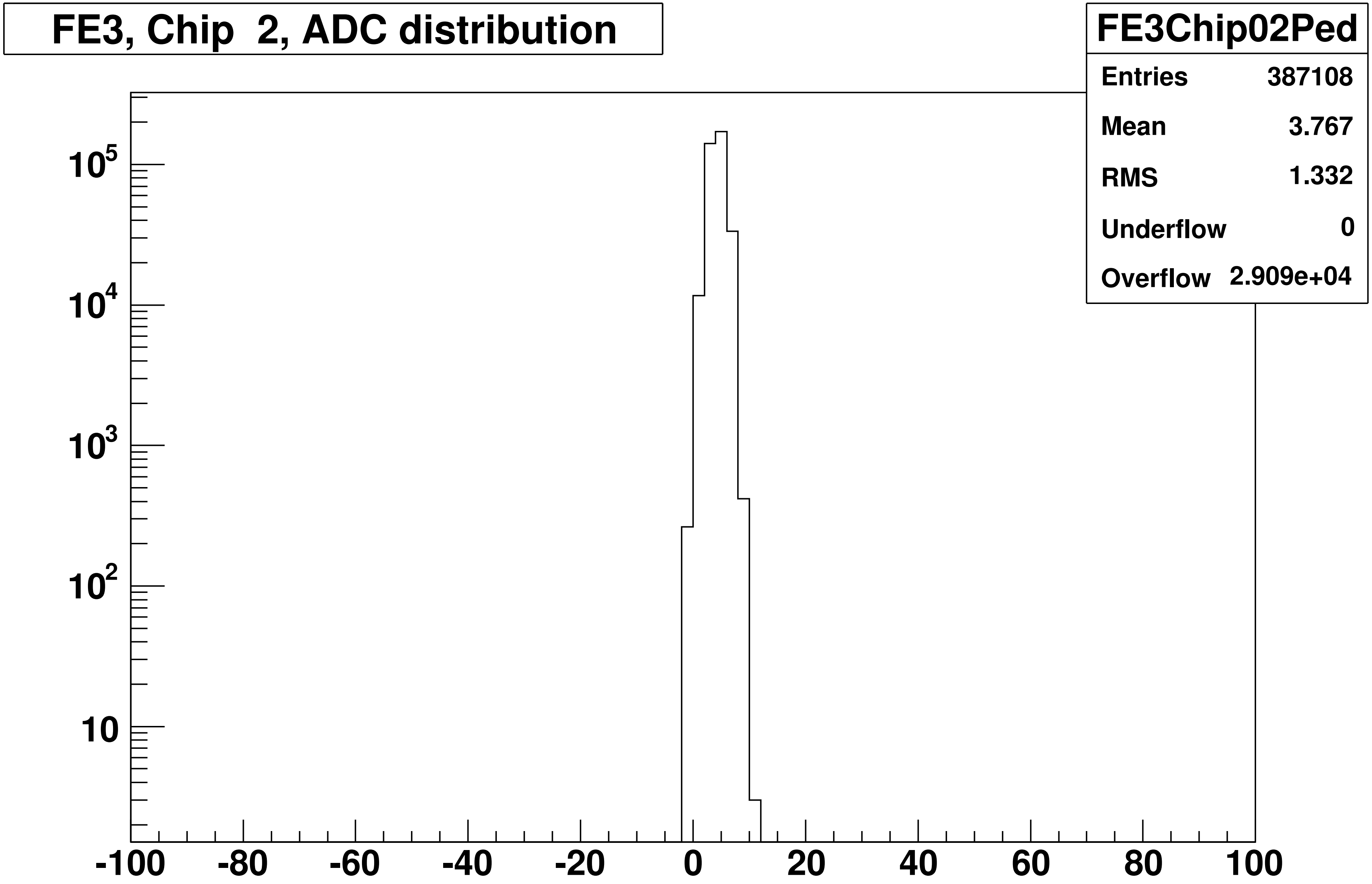


FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 387108
Mean 1438
RMS 5022
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

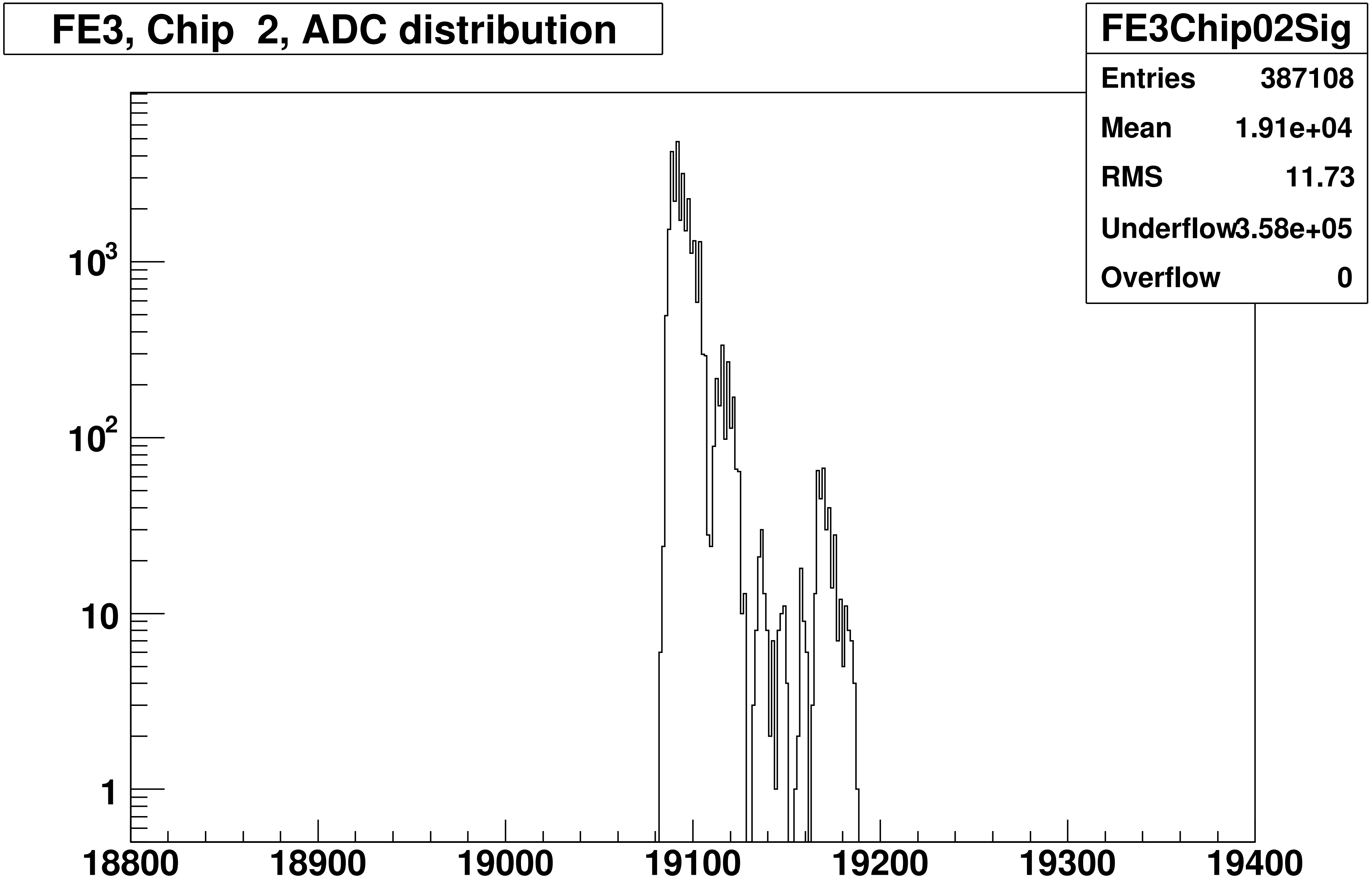

FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 387108
Mean 6.433
RMS 1.311
Underflow 0
Overflow 2.909e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

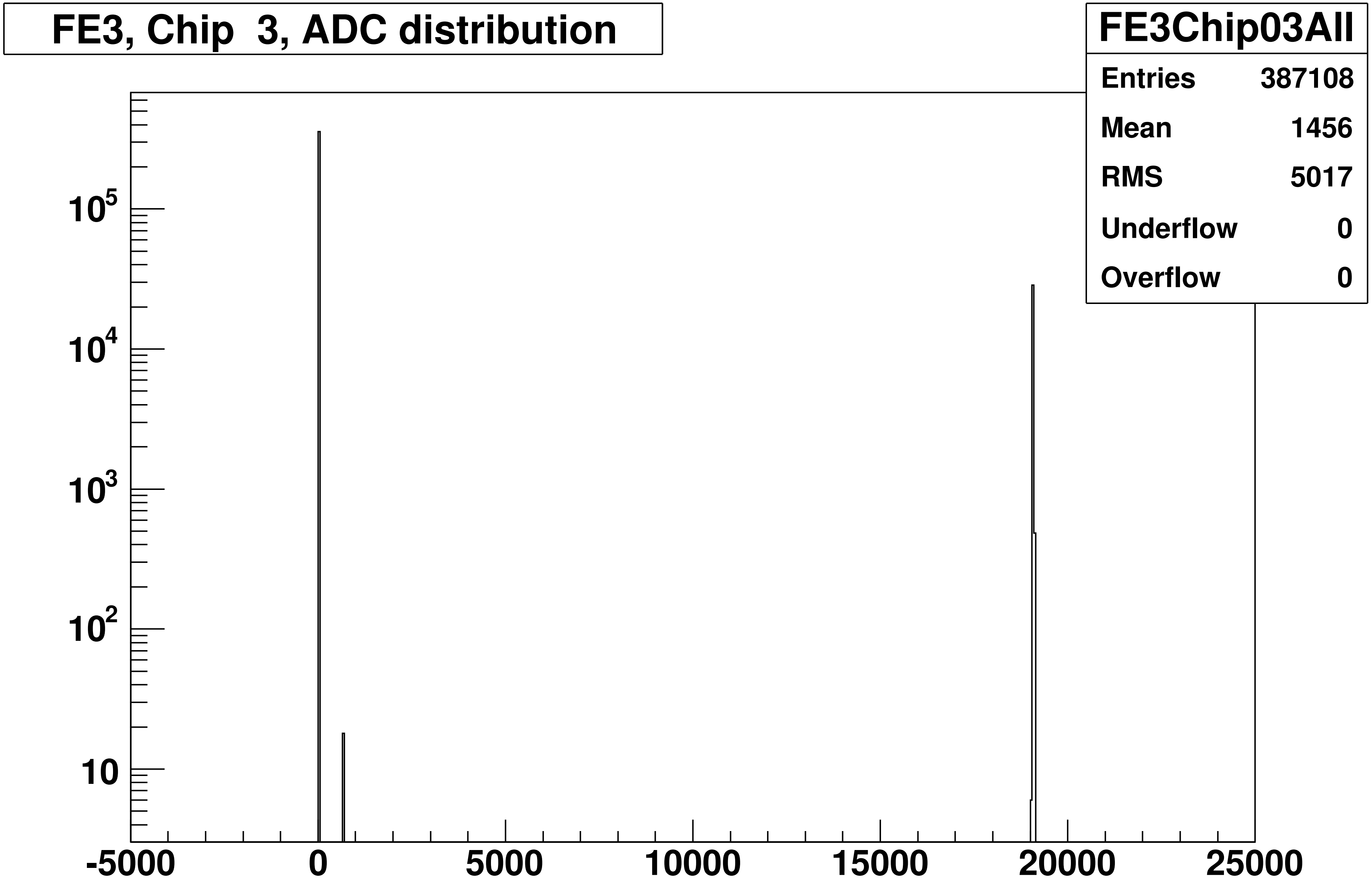

FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 387108
Mean 1.906e+04
RMS 12.2
Underflow 3.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 387108
Mean 1438
RMS 5032
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

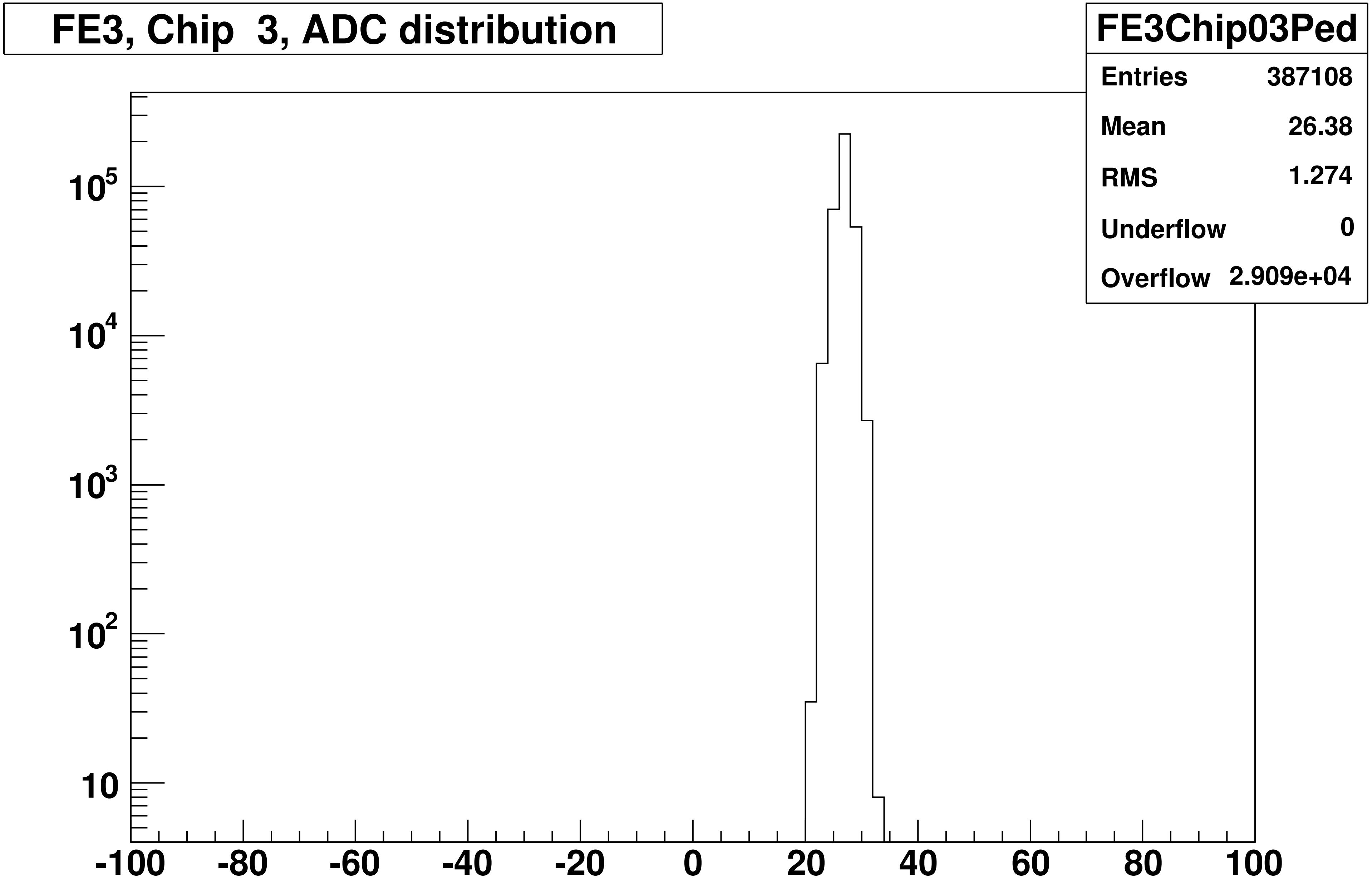


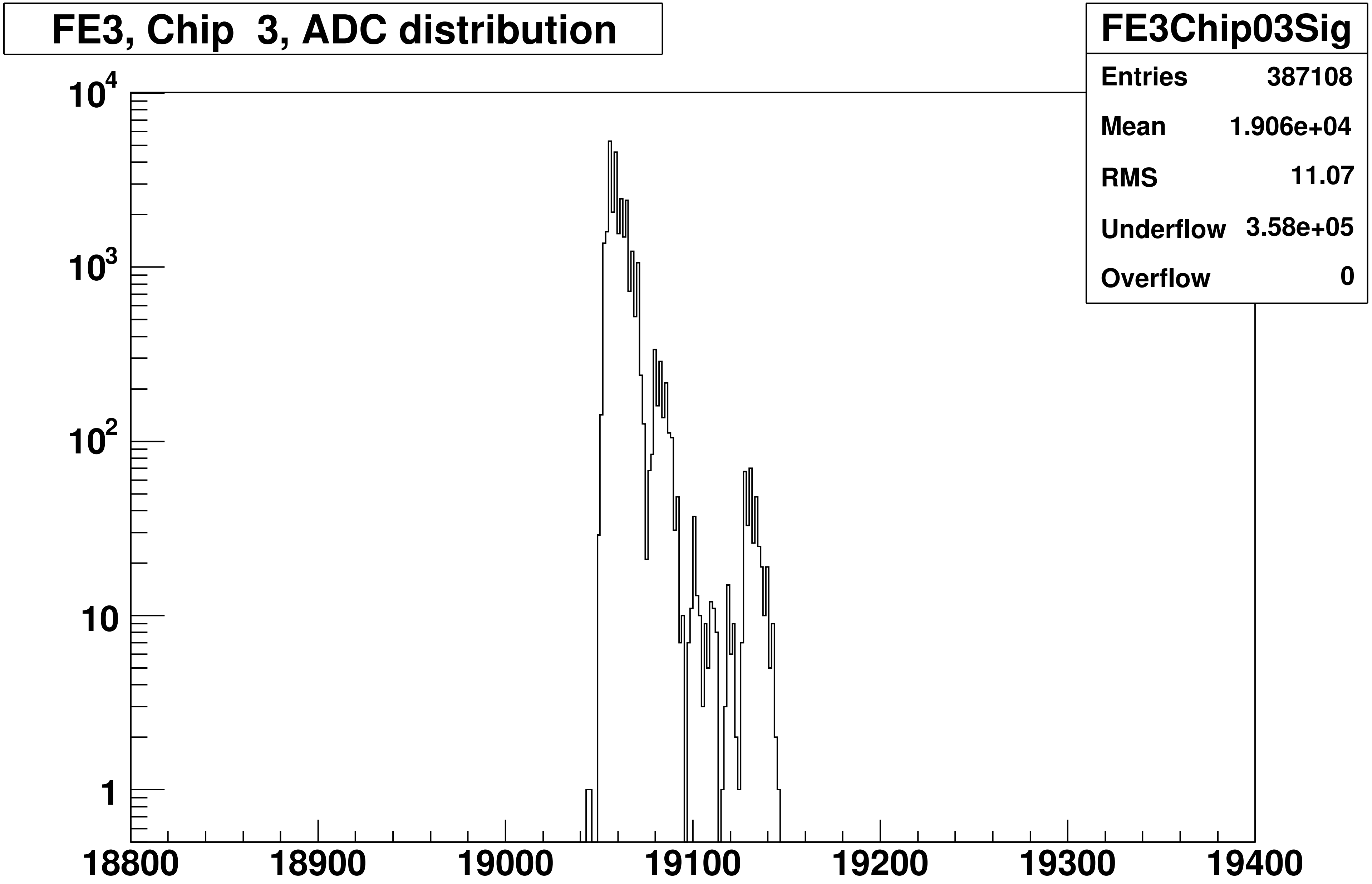
FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 387108
Mean 3.767
RMS 1.332
Underflow 0
Overflow 2.909e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 387108
Mean 1.91e+04
RMS 11.73
Underflow 3.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

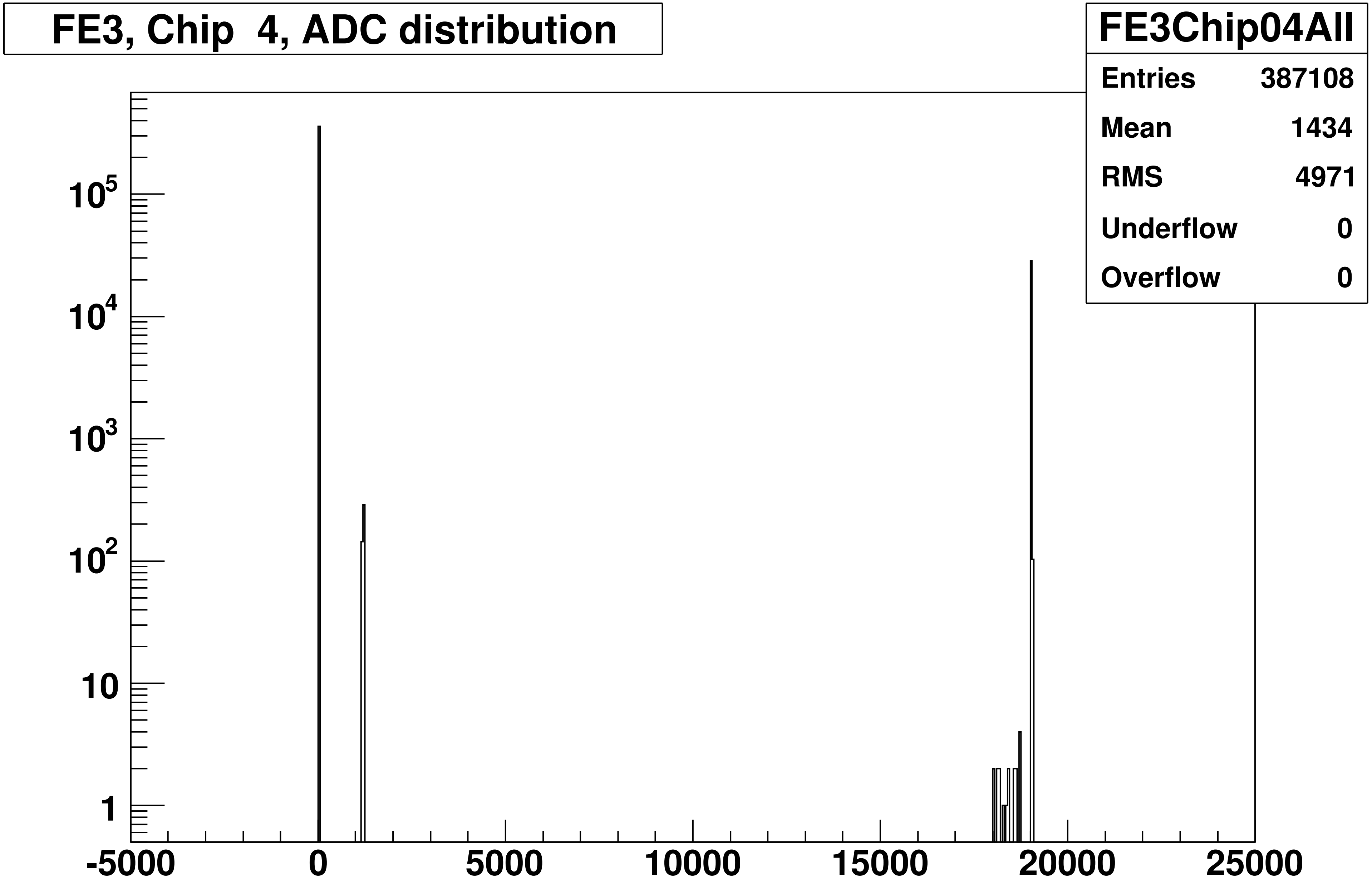


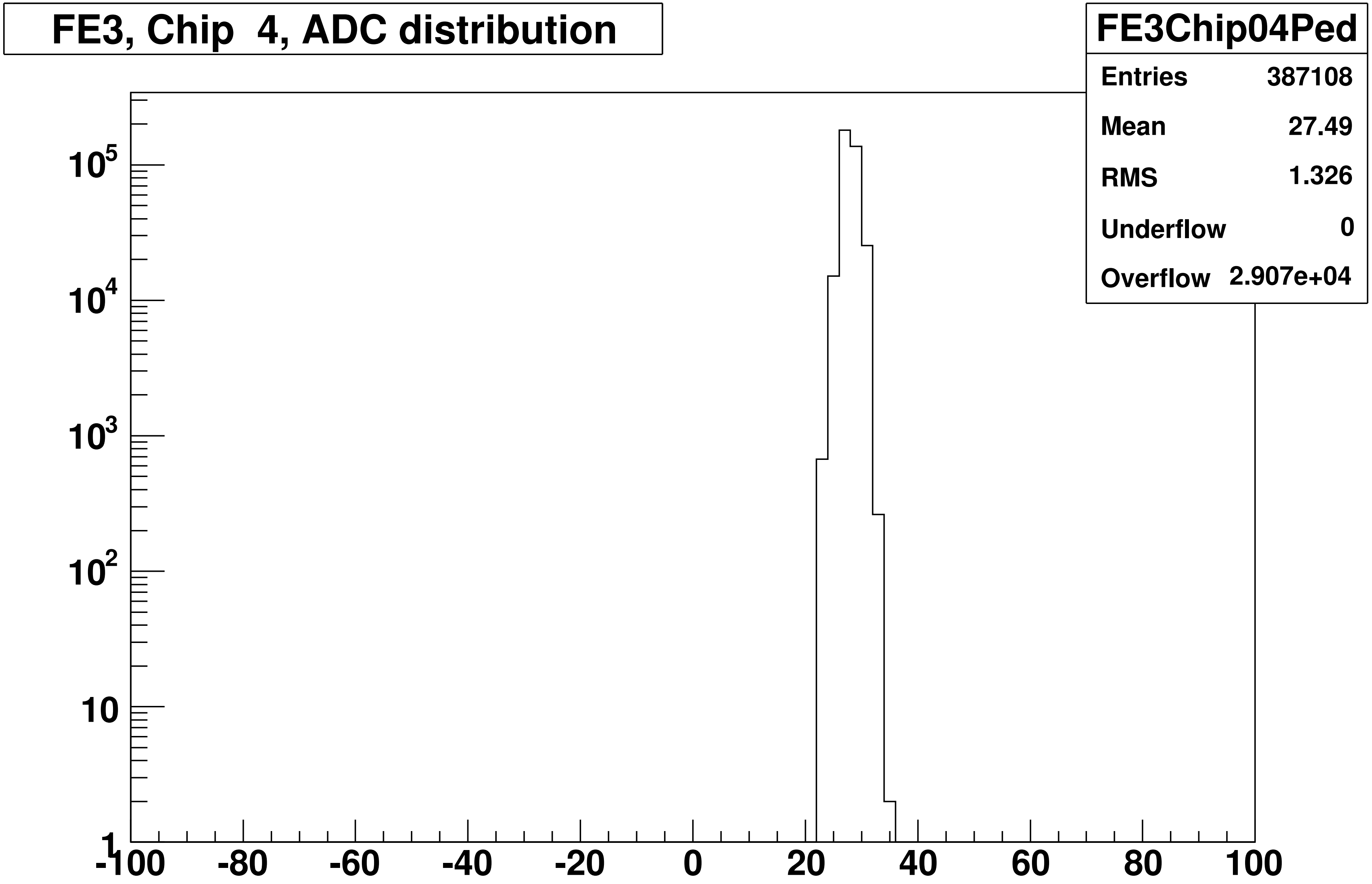
FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 387108
Mean 1456
RMS 5017
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 387108
Mean 26.38
RMS 1.274
Underflow 0
Overflow 2.909e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

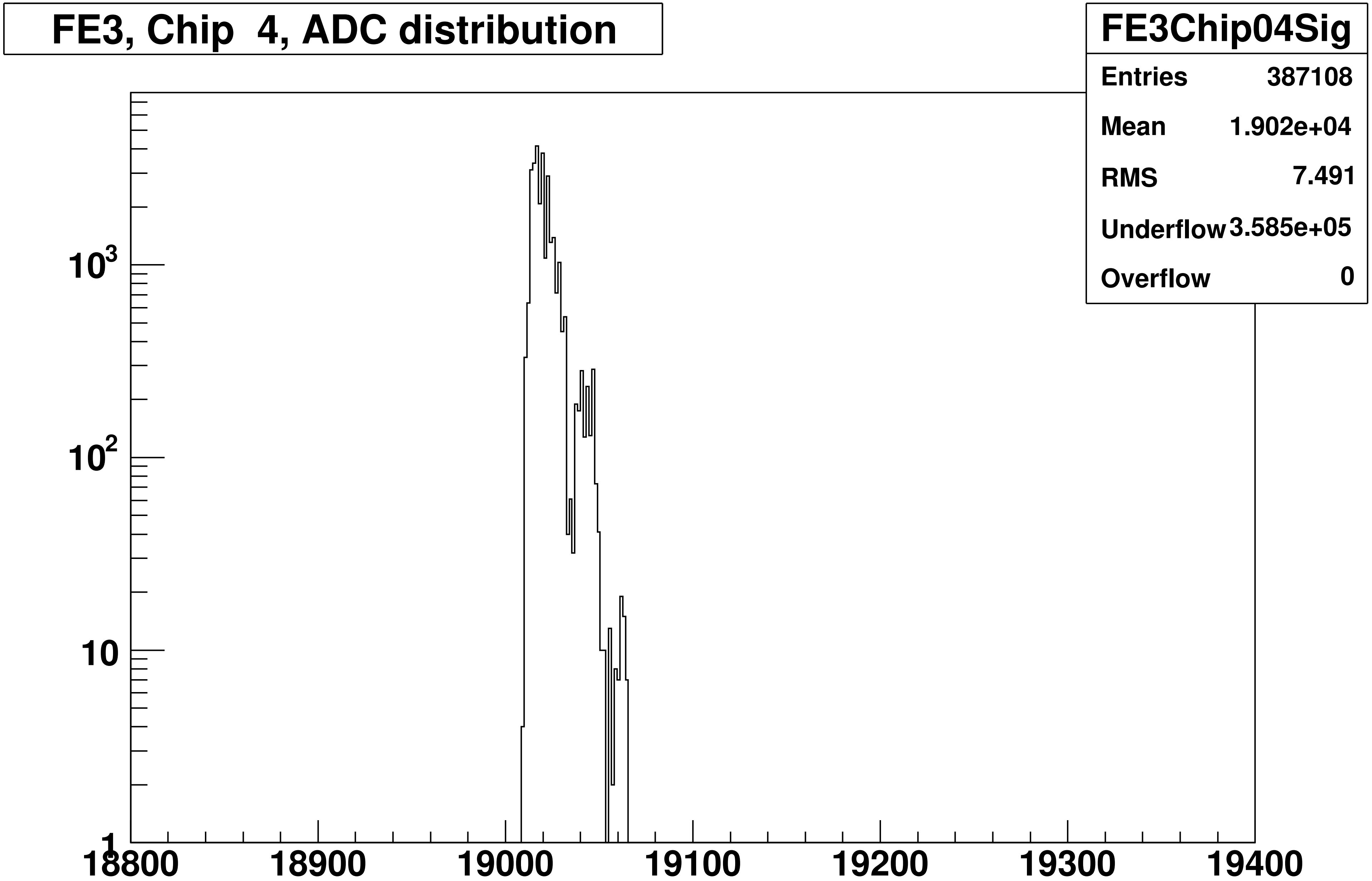


FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 387108
Mean 1.906e+04
RMS 11.07
Underflow 3.58e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

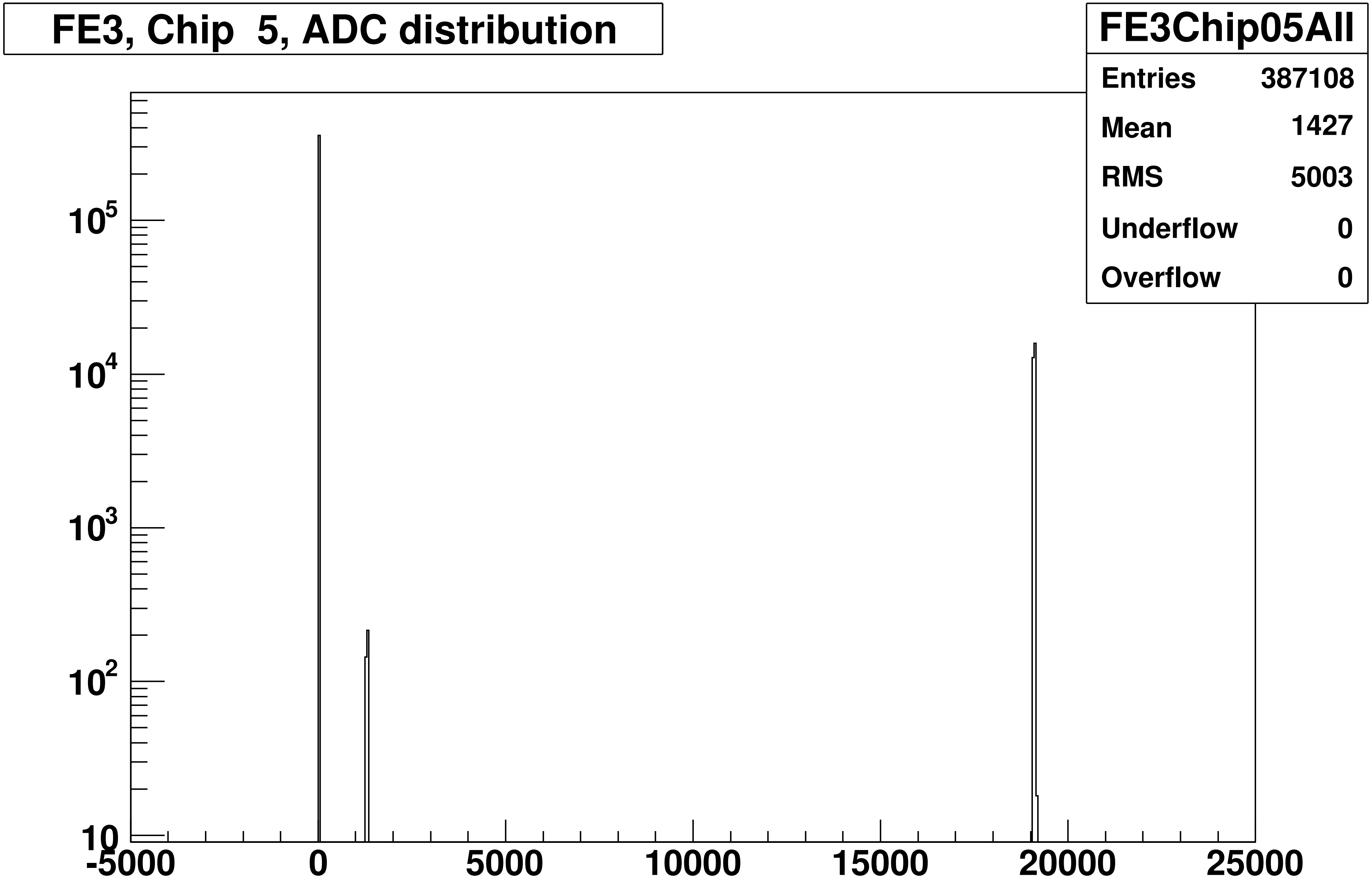

FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 387108
Mean 1434
RMS 4971
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



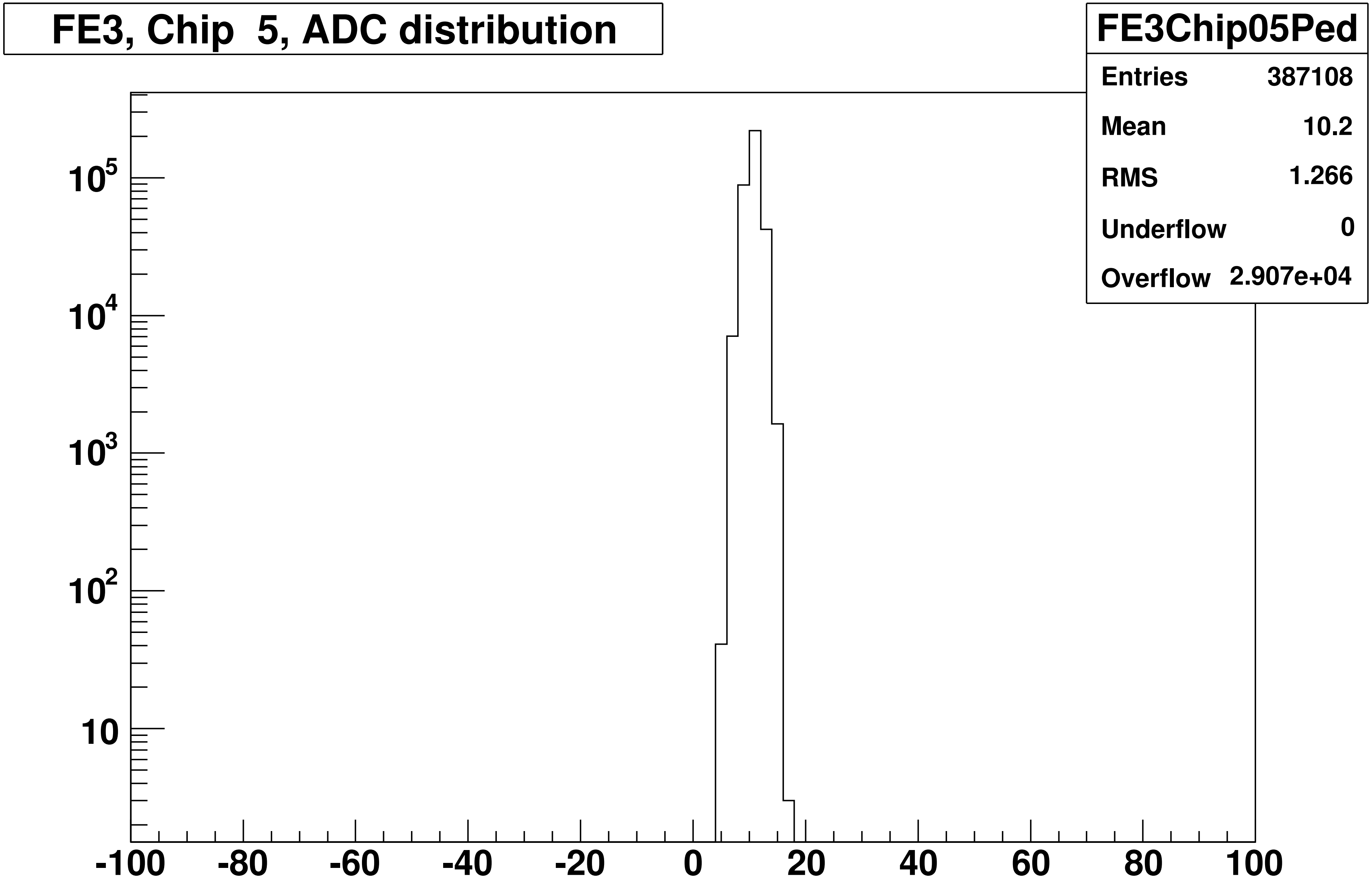
FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 387108
Mean 27.49
RMS 1.326
Underflow 0
Overflow 2.907e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



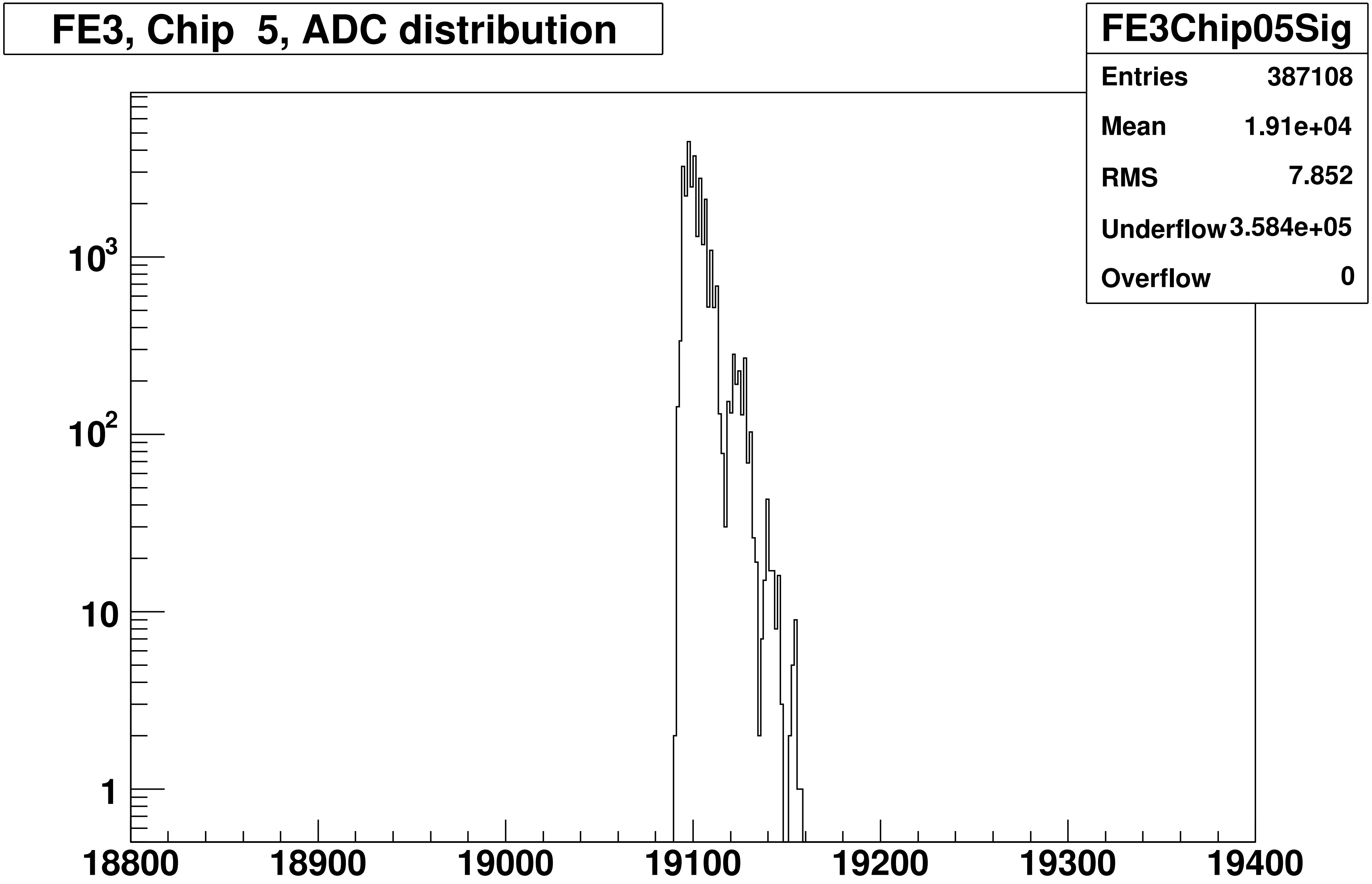
FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 387108
Mean 1.902e+04
RMS 7.491
Underflow 3.585e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

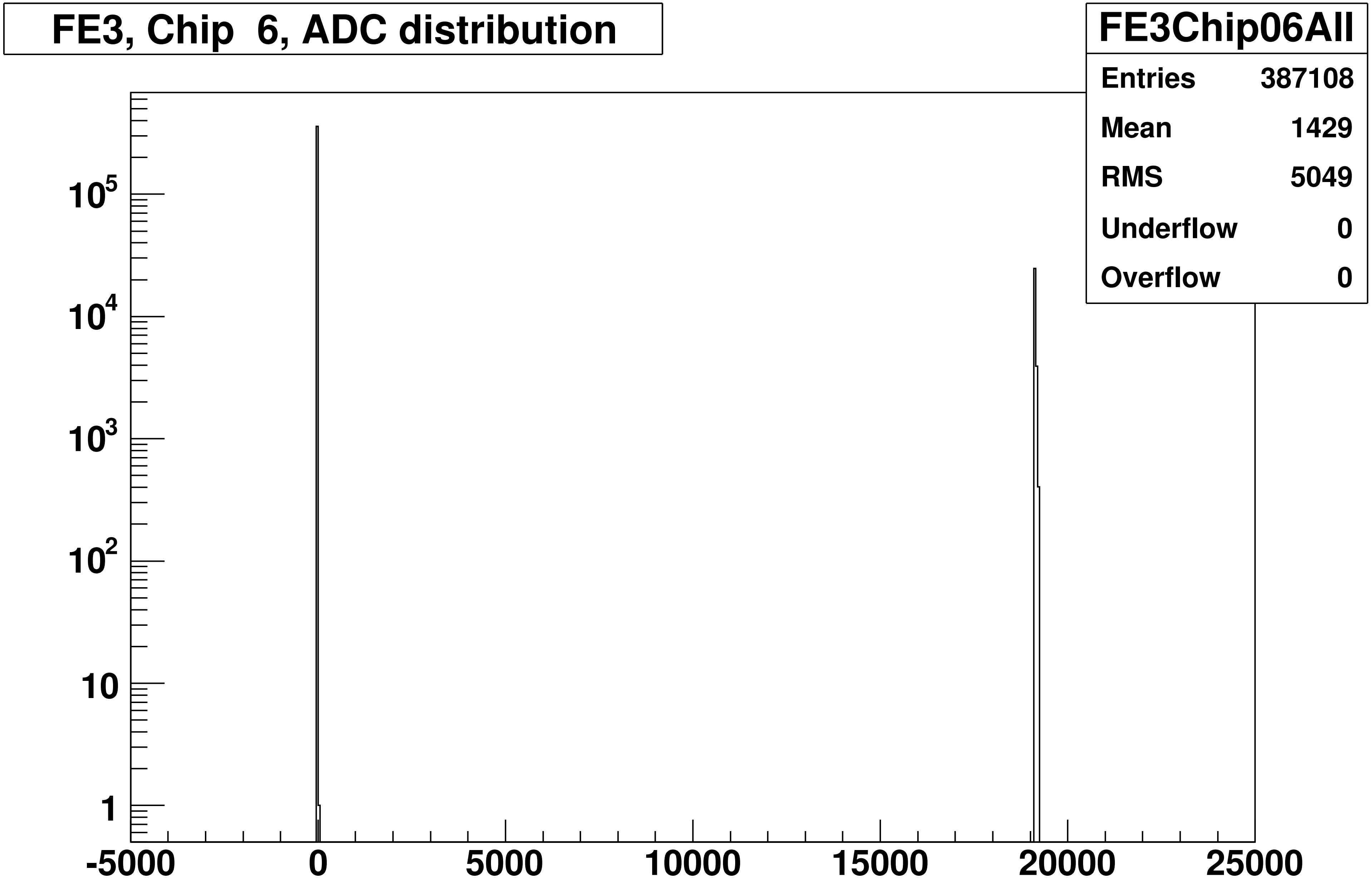


FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 387108
Mean 1427
RMS 5003
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

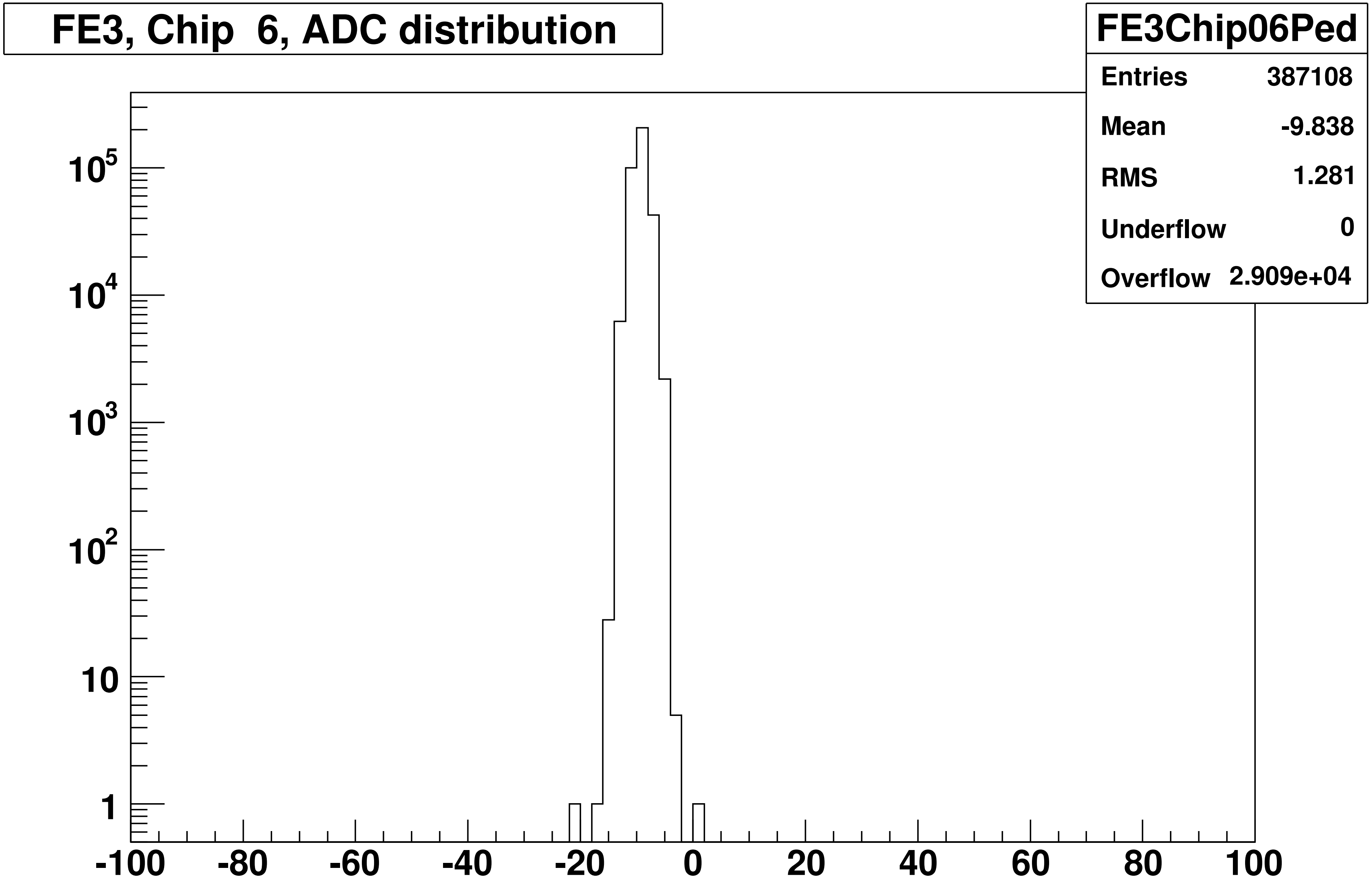


FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 387108
Mean 10.2
RMS 1.266
Underflow 0
Overflow 2.907e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

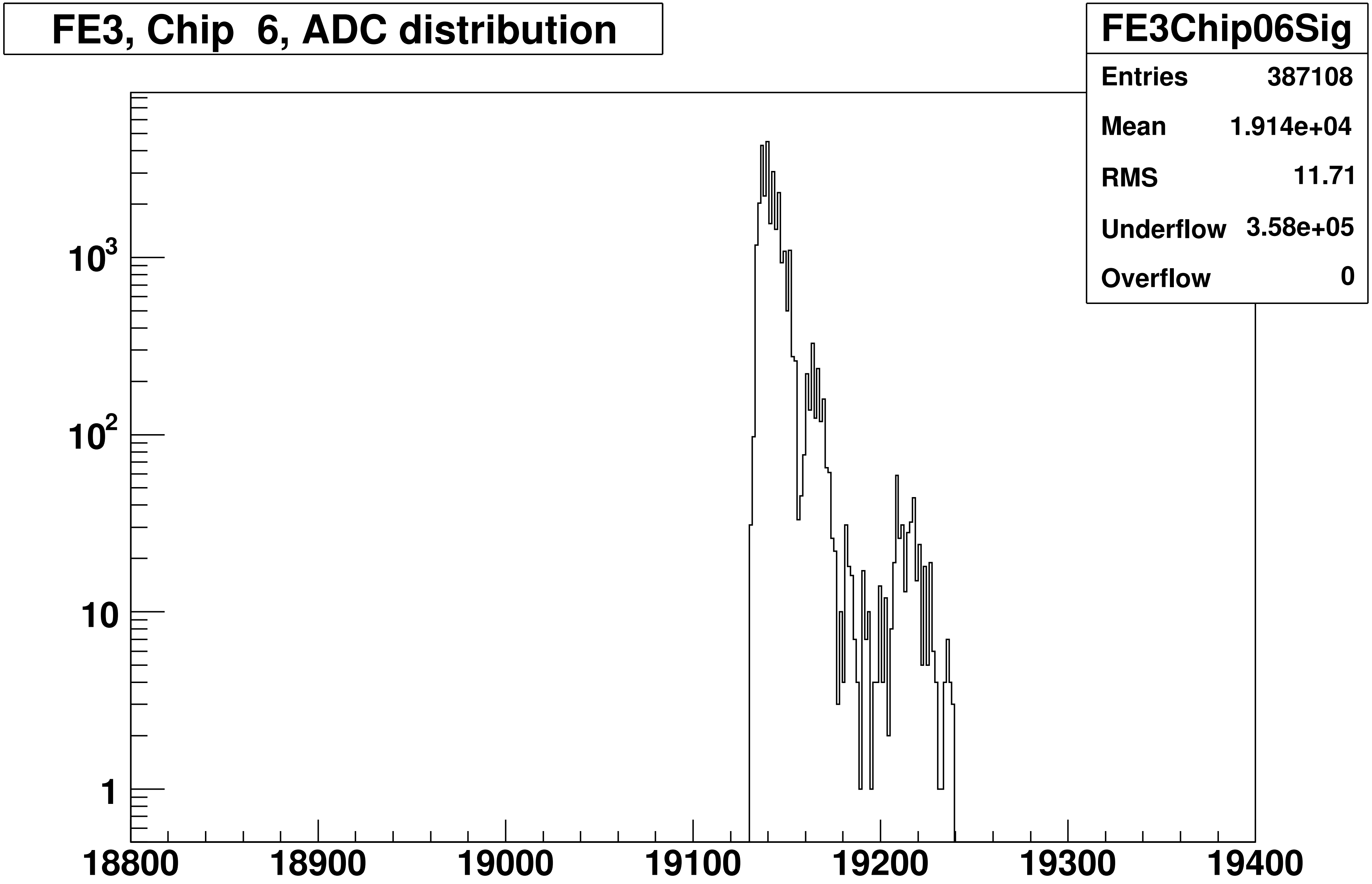

FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 387108
Mean 1.91e+04
RMS 7.852
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



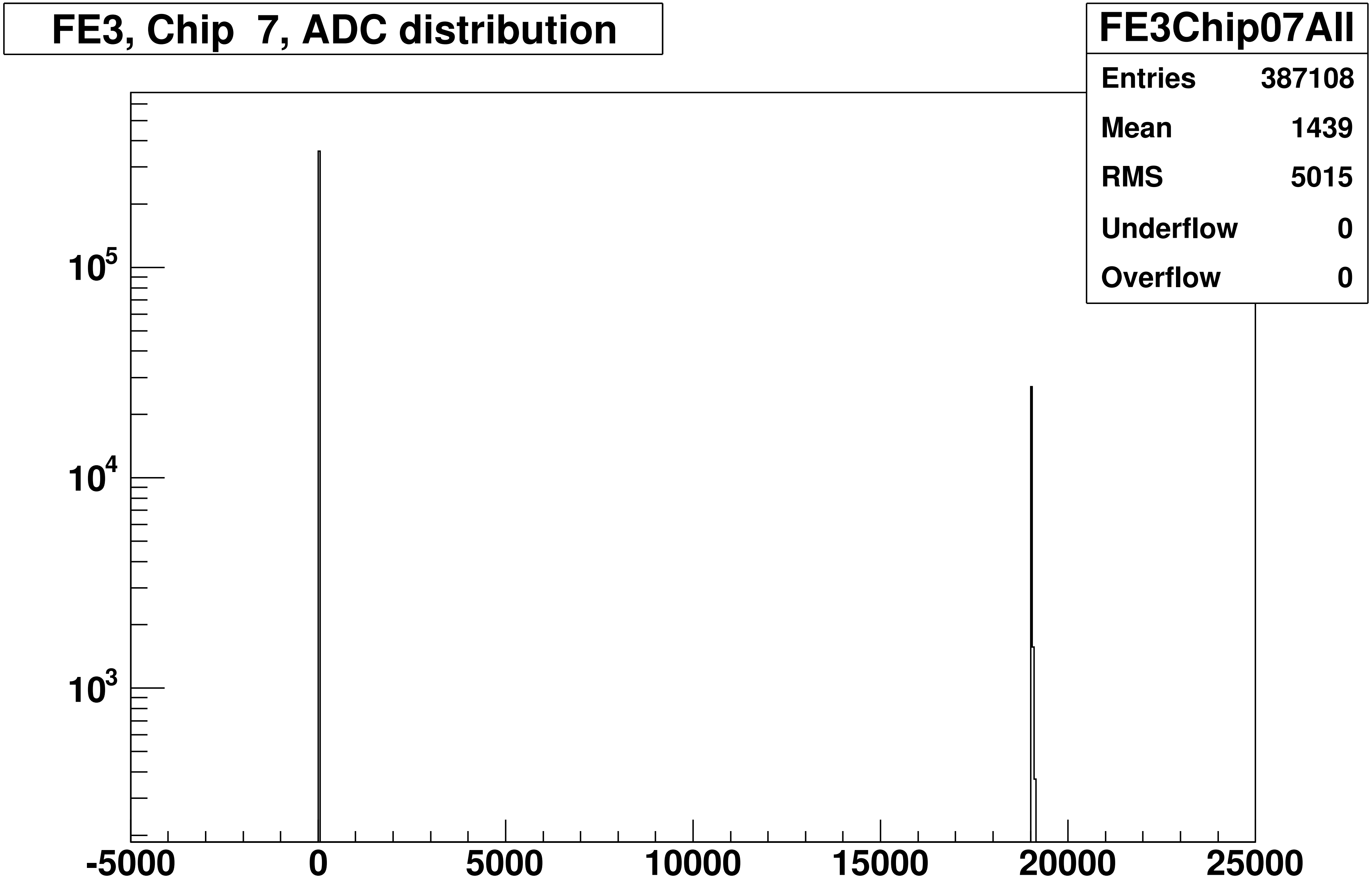
FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 387108
Mean 1429
RMS 5049
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000



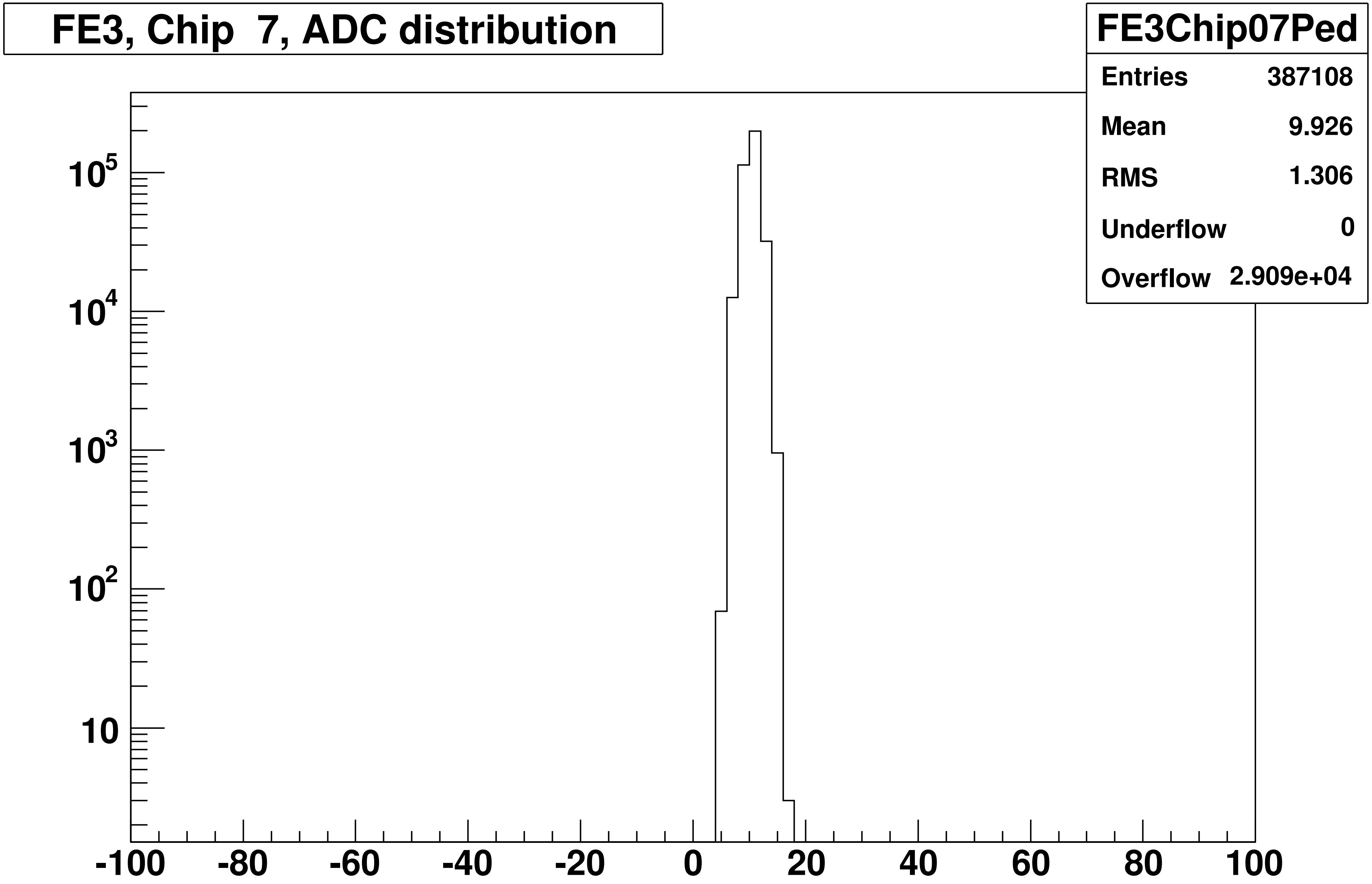
FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 387108
Mean -9.838
RMS 1.281
Underflow 0
Overflow 2.909e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



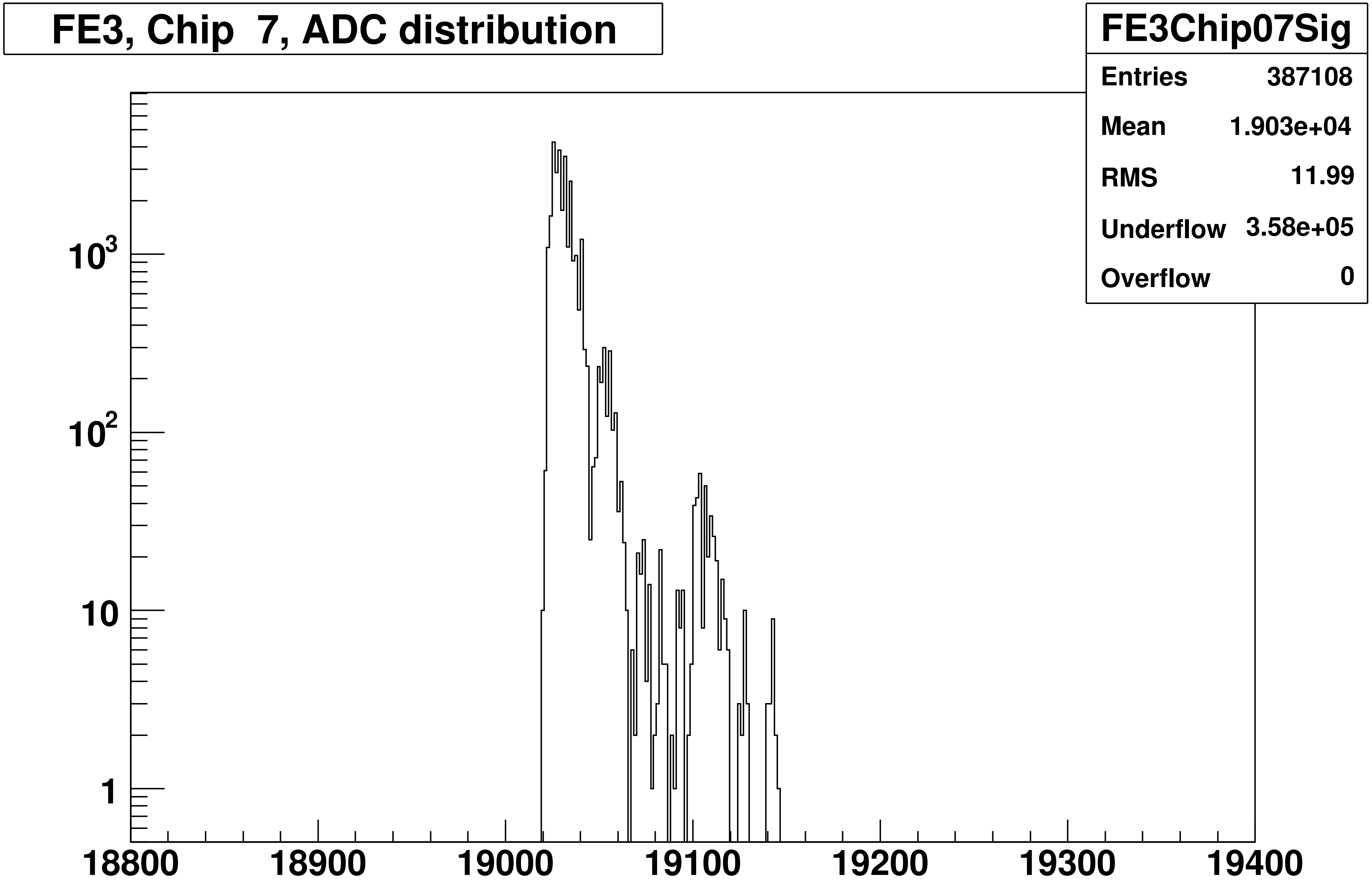
FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 387108
Mean 1.914e+04
RMS 11.71
Underflow 3.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

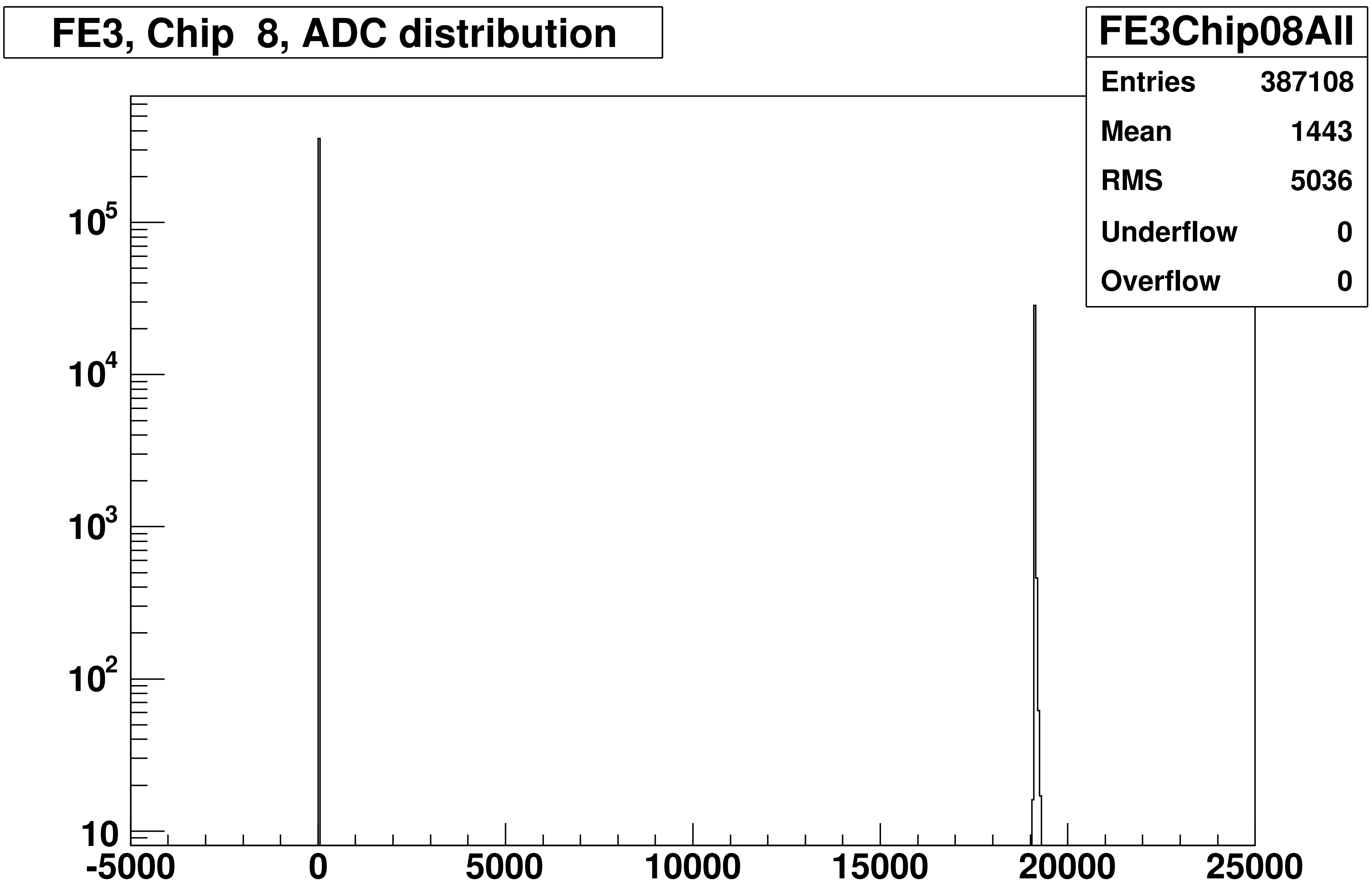


FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 387108
Mean 1439
RMS 5015
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

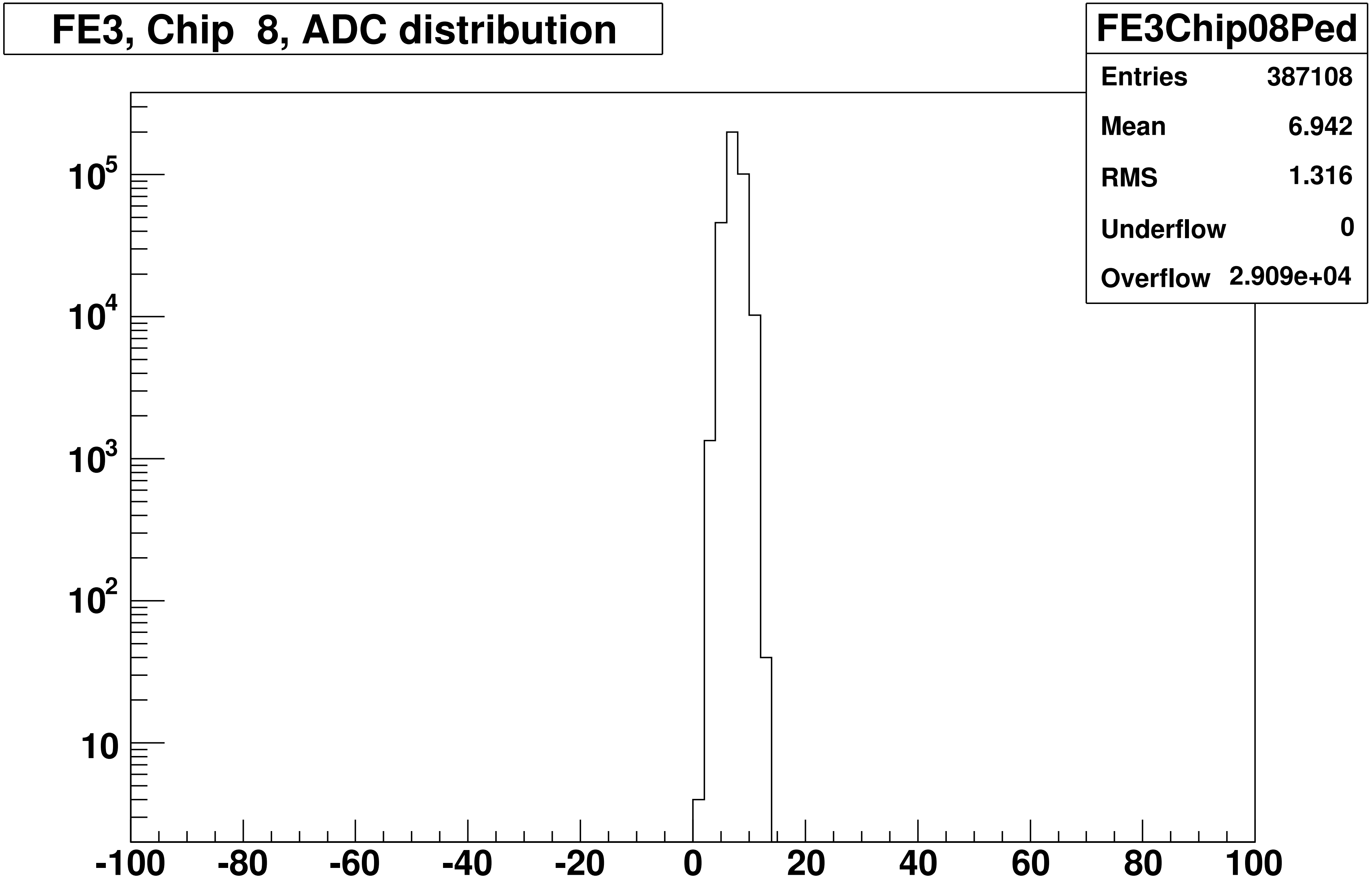


FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 387108
Mean 9.926
RMS 1.306
Underflow 0
Overflow 2.909e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

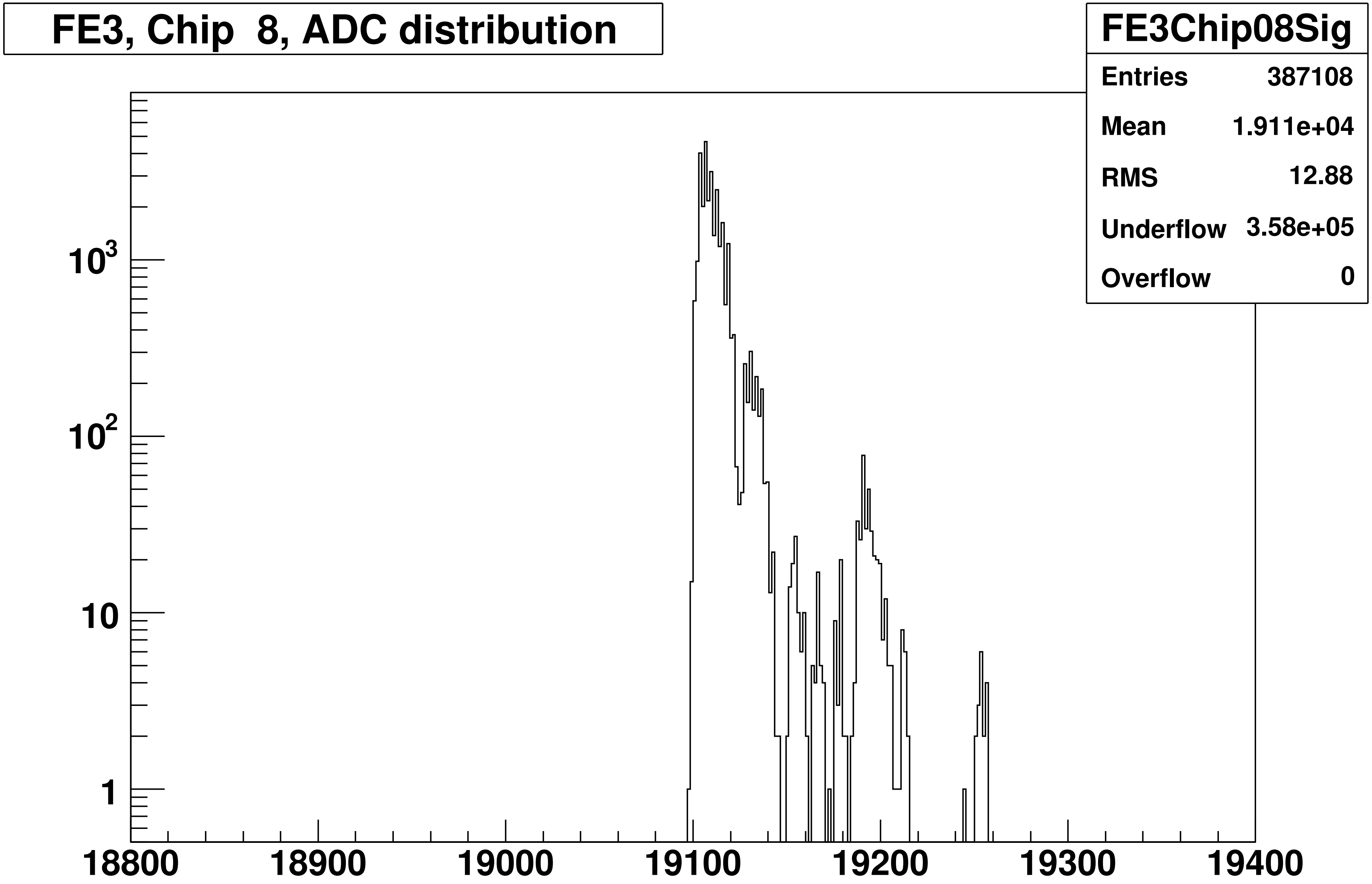

FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 387108
Mean 1.903e+04
RMS 11.99
Underflow 3.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



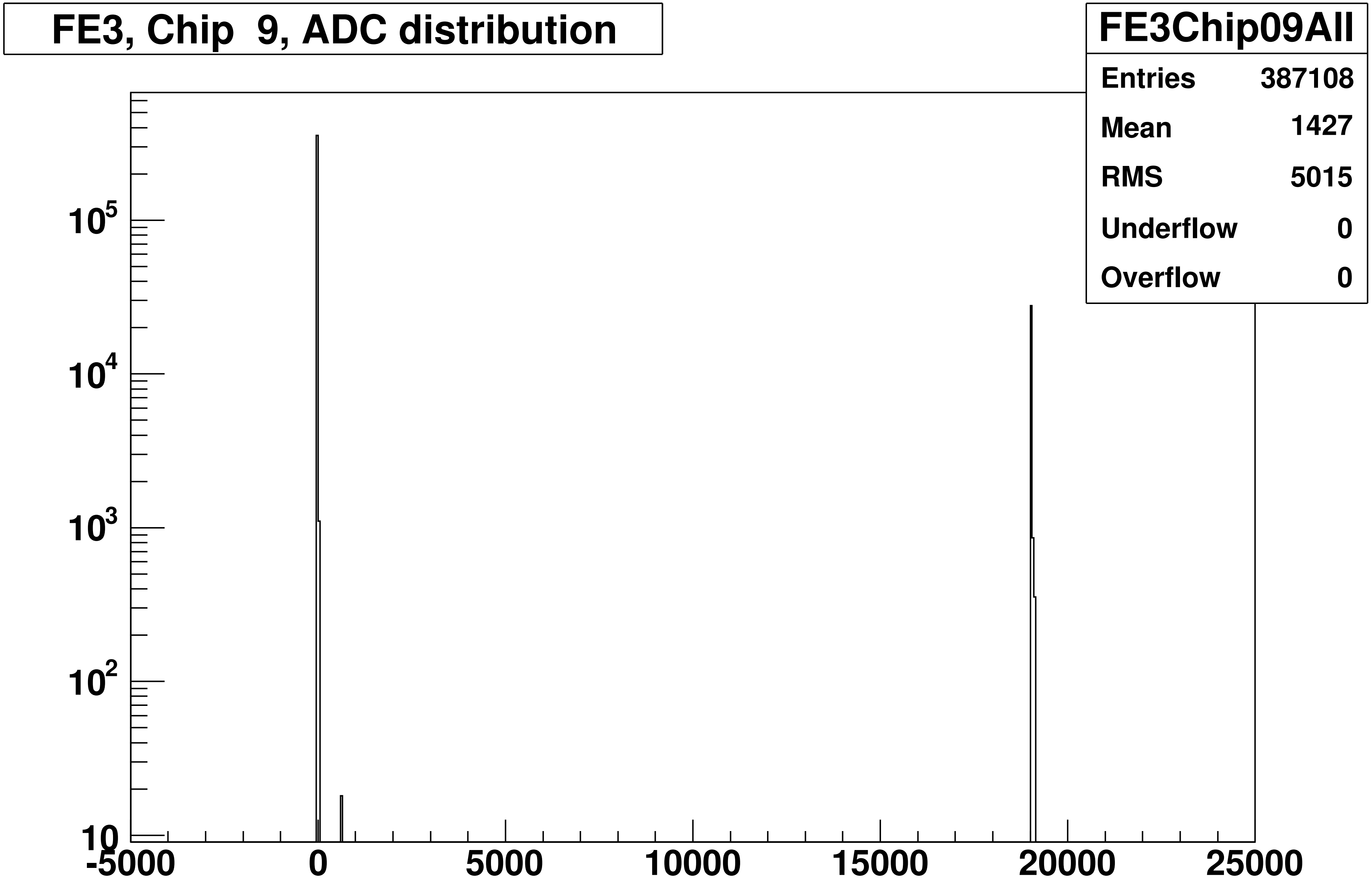
FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 387108
Mean 1443
RMS 5036
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

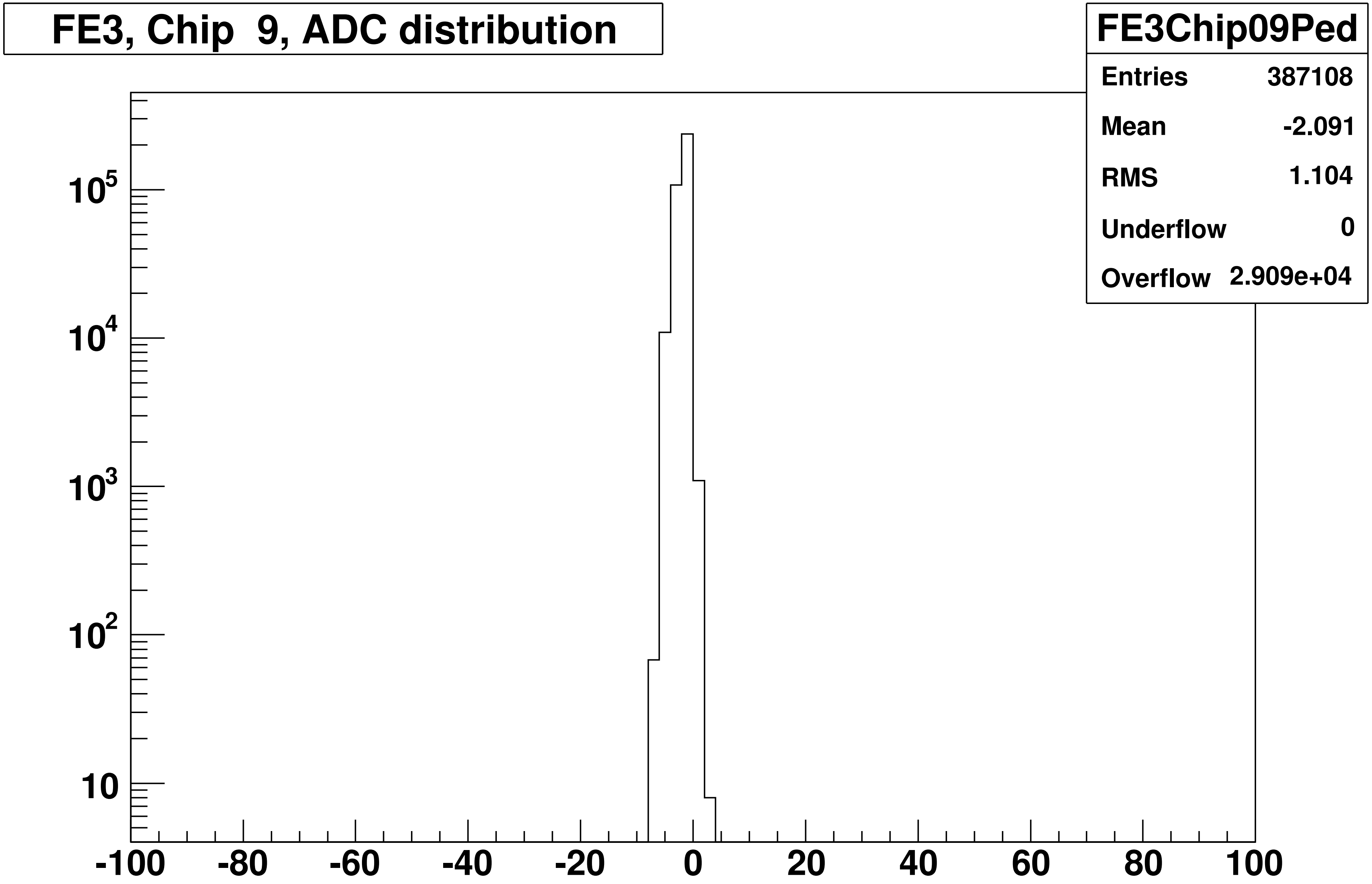


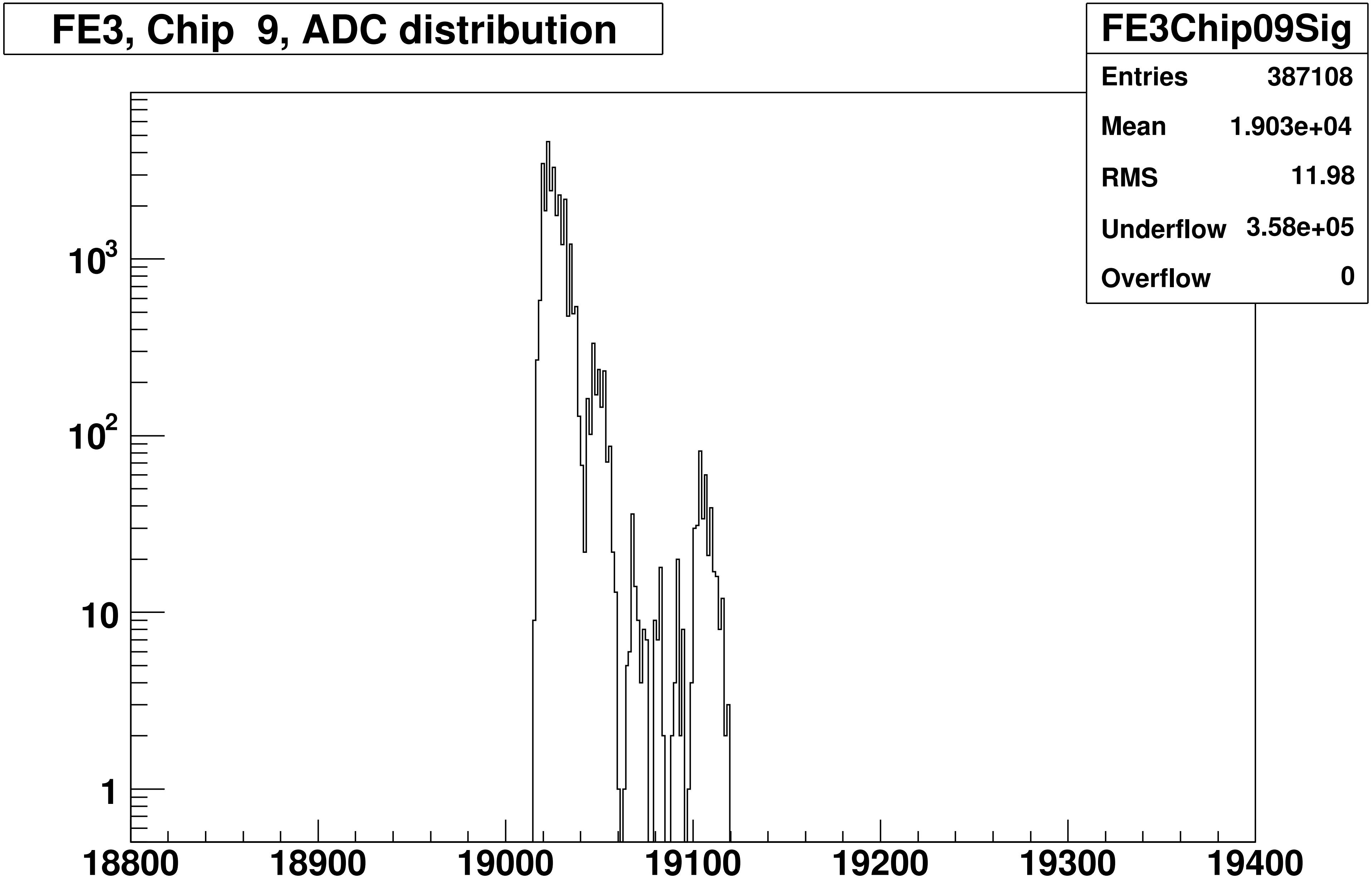
FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 387108
Mean 6.942
RMS 1.316
Underflow 0
Overflow 2.909e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

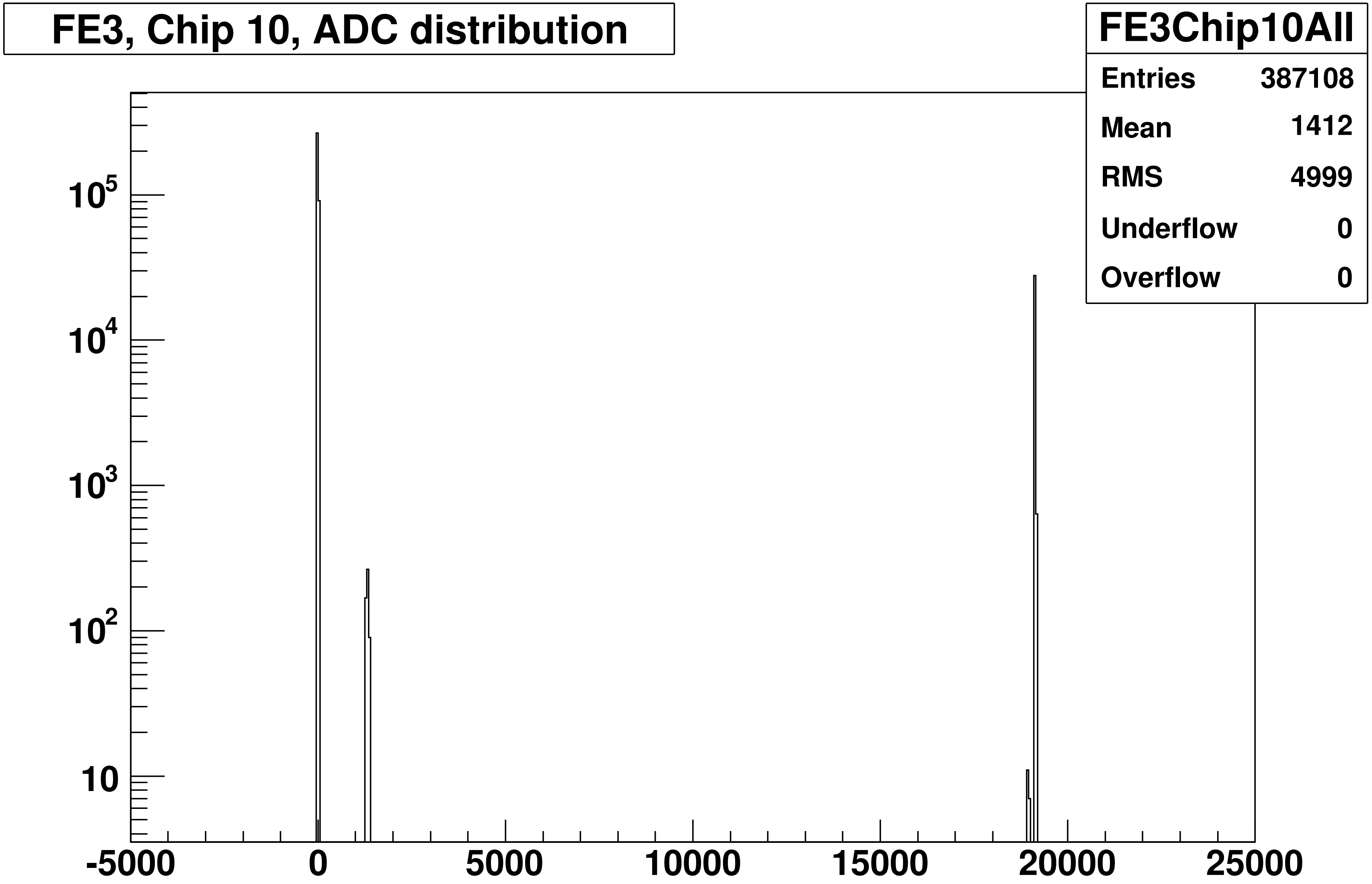


FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 387108
Mean 1.911e+04
RMS 12.88
Underflow 3.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

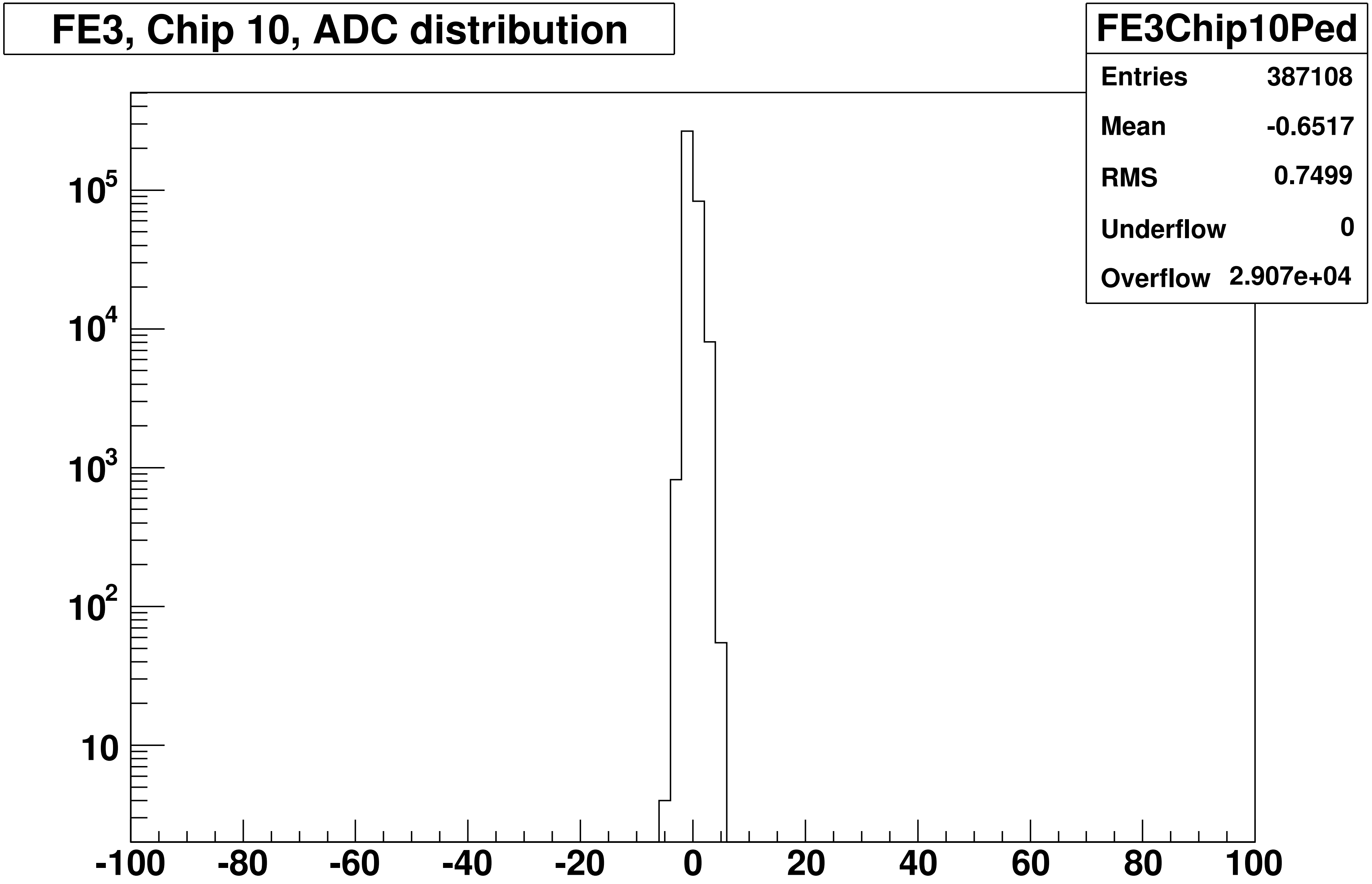

FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 387108
Mean 1427
RMS 5015
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

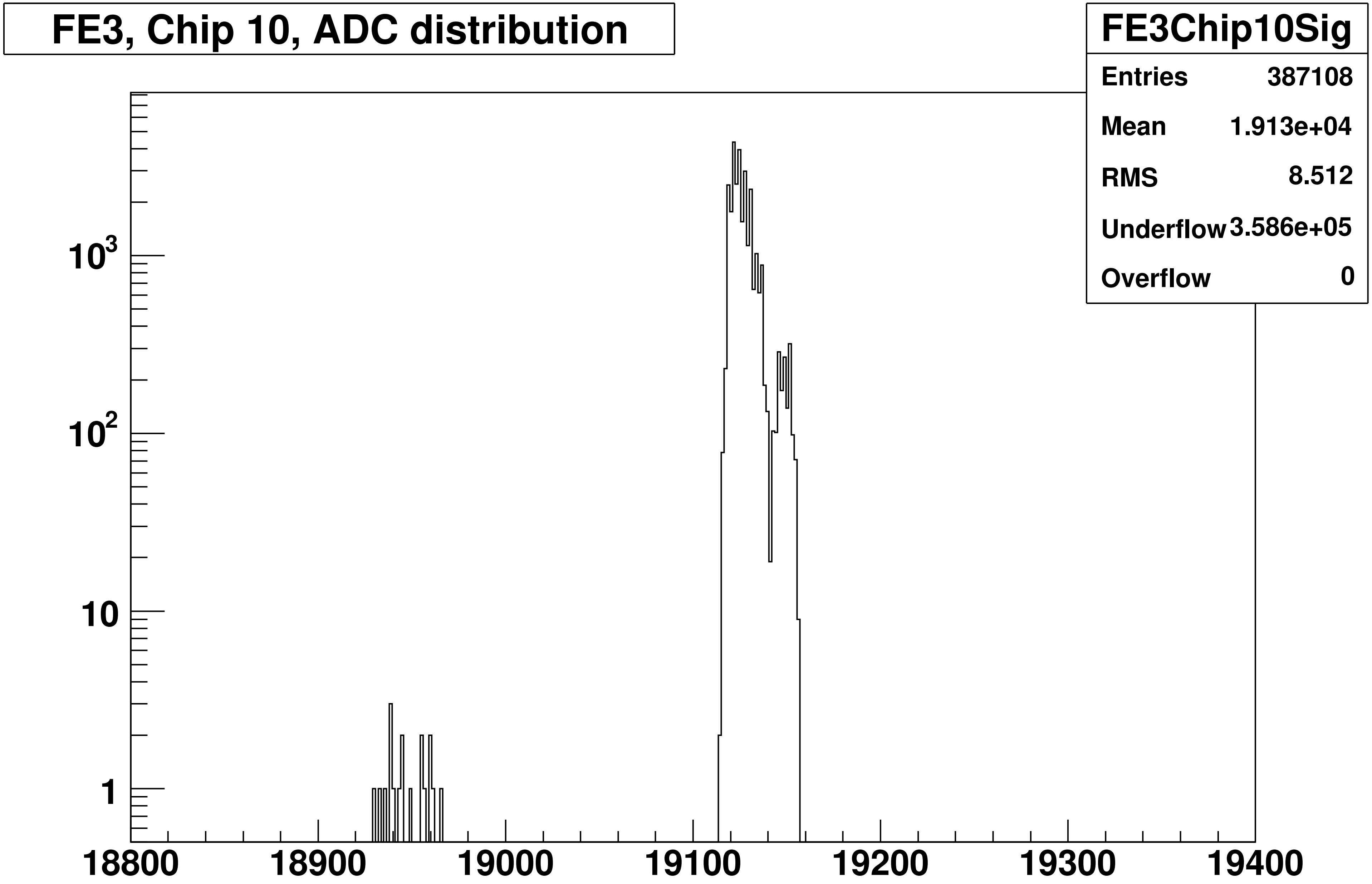

FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 387108
Mean -2.091
RMS 1.104
Underflow 0
Overflow 2.909e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 387108
Mean 1.903e+04
RMS 11.98
Underflow 3.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

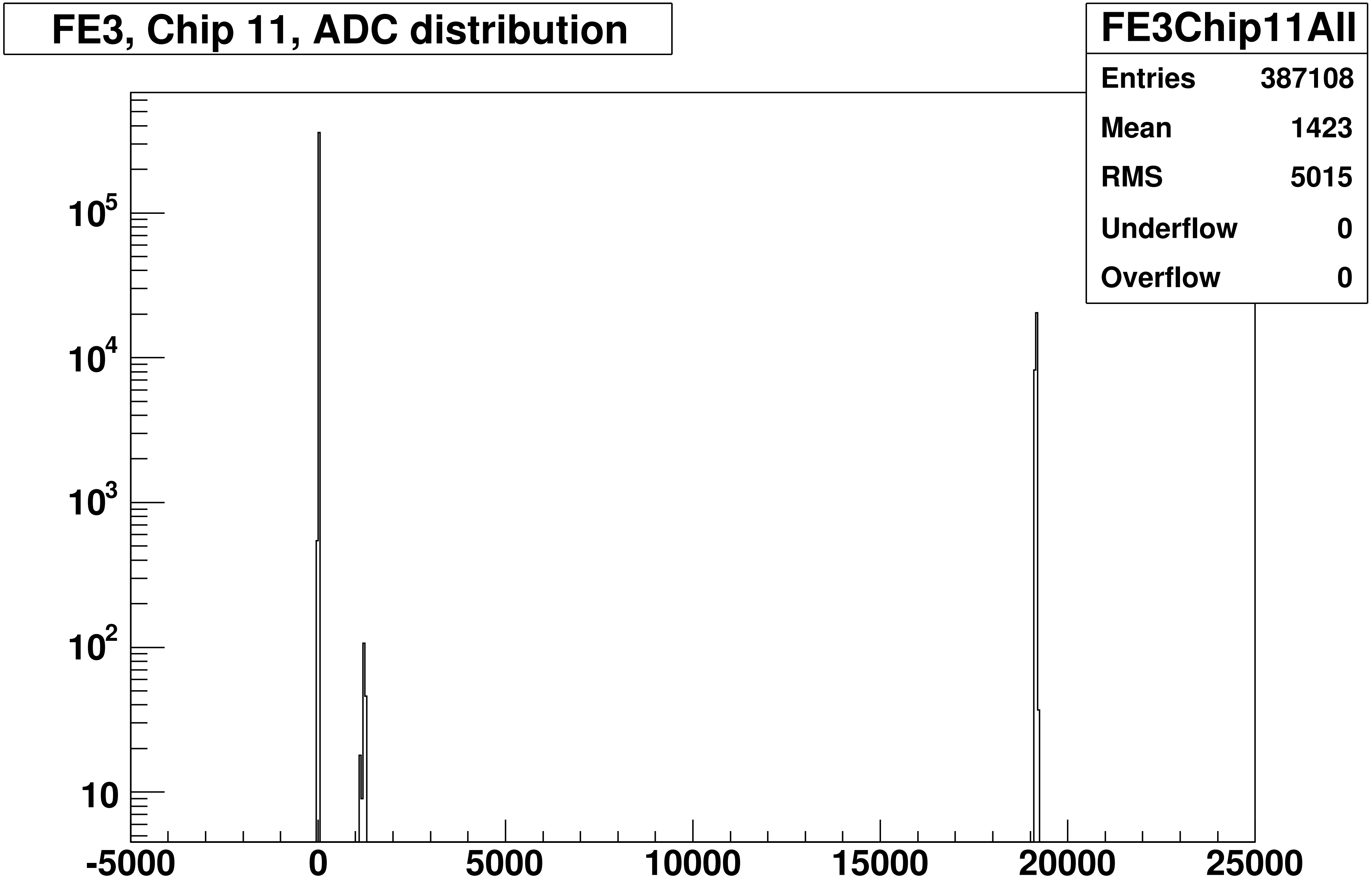


FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 387108
Mean 1412
RMS 4999
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

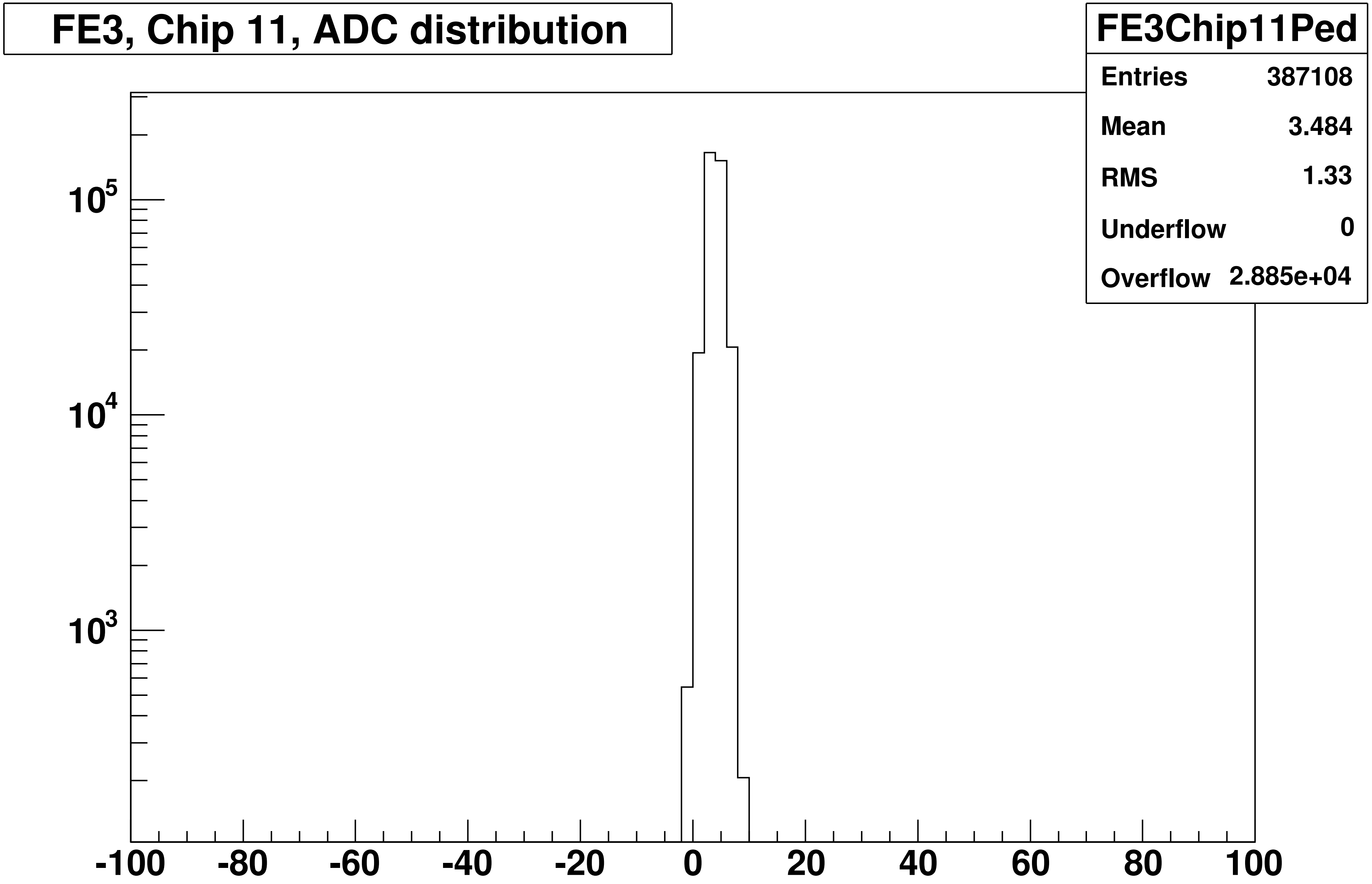

FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 387108
Mean -0.6517
RMS 0.7499
Underflow 0
Overflow 2.907e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



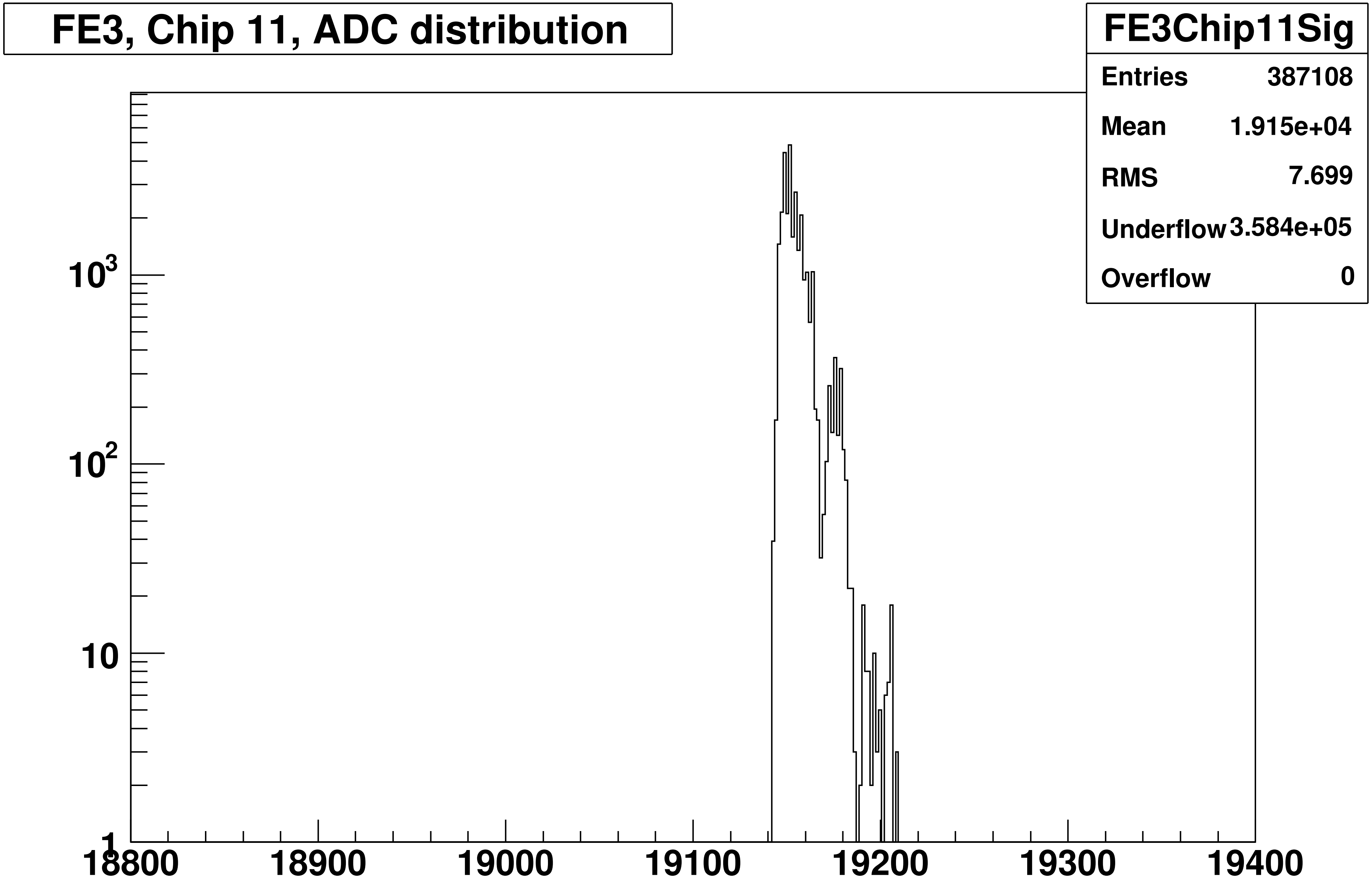
FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 387108
Mean 1.913e+04
RMS 8.512
Underflow 3.586e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



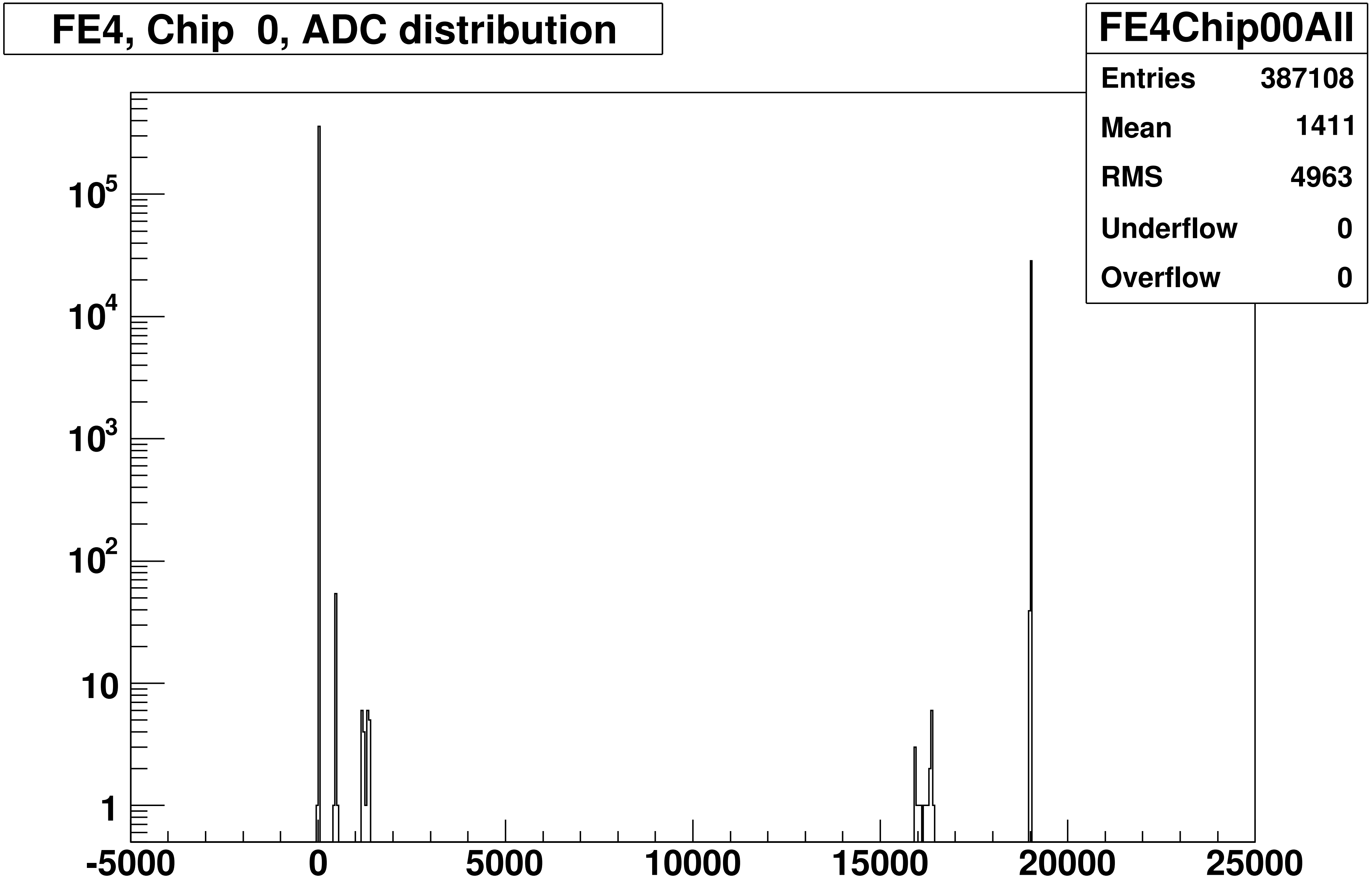
FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 387108
Mean 1423
RMS 5015
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

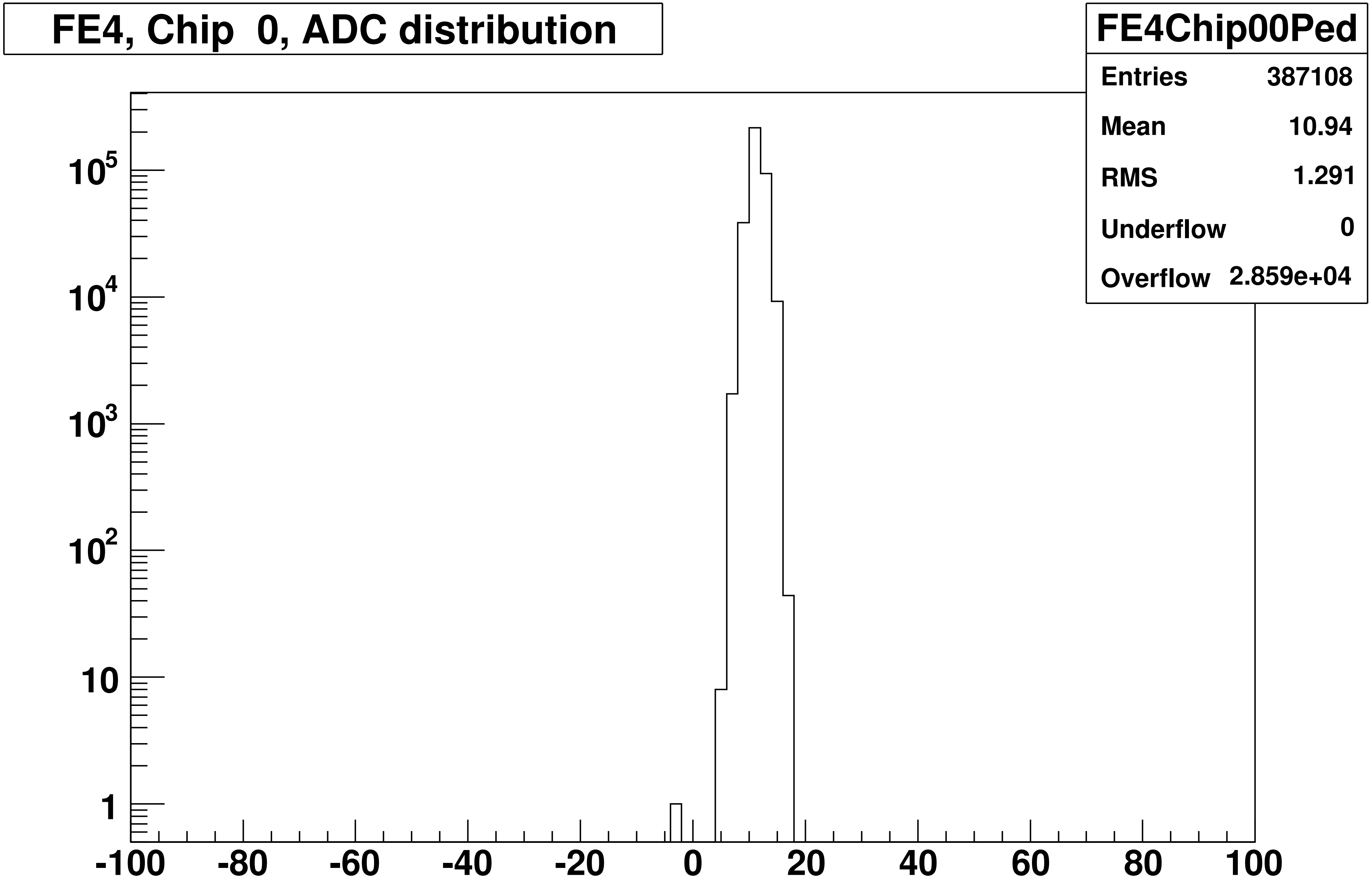


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 387108
Mean 3.484
RMS 1.33
Underflow 0
Overflow 2.885e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100

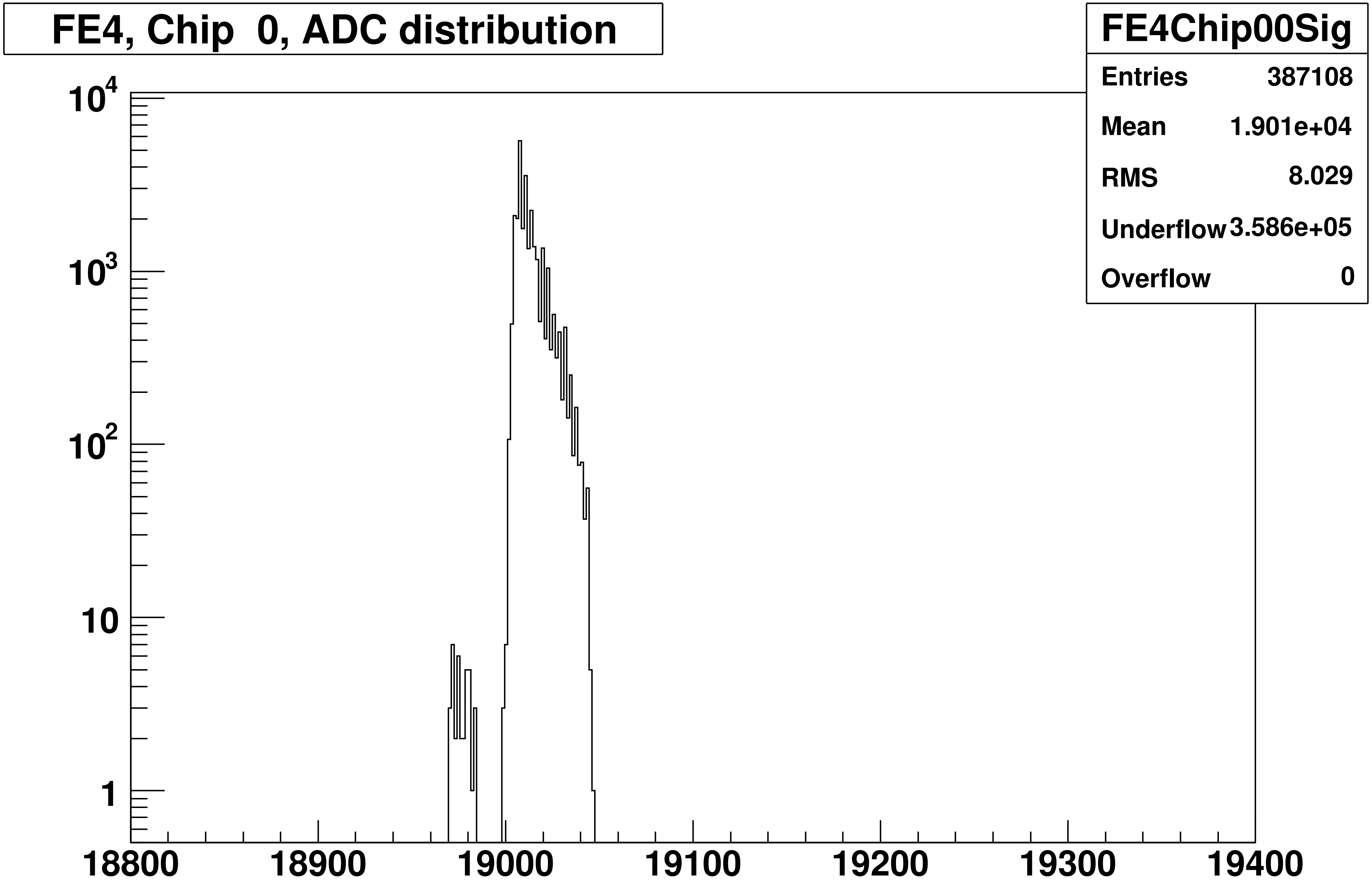


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 387108
Mean 1.915e+04
RMS 7.699
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

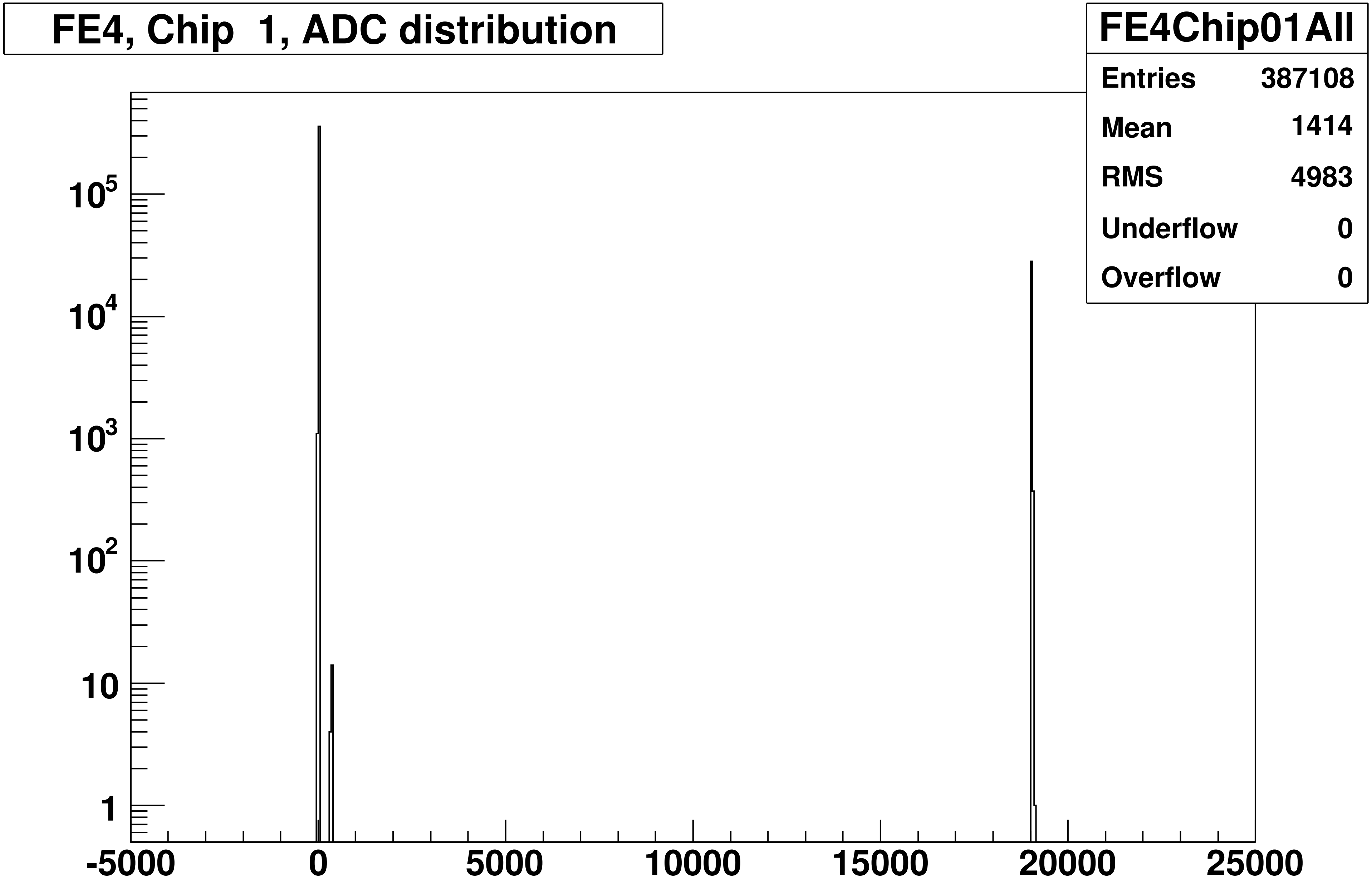

FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 387108
Mean 1411
RMS 4963
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

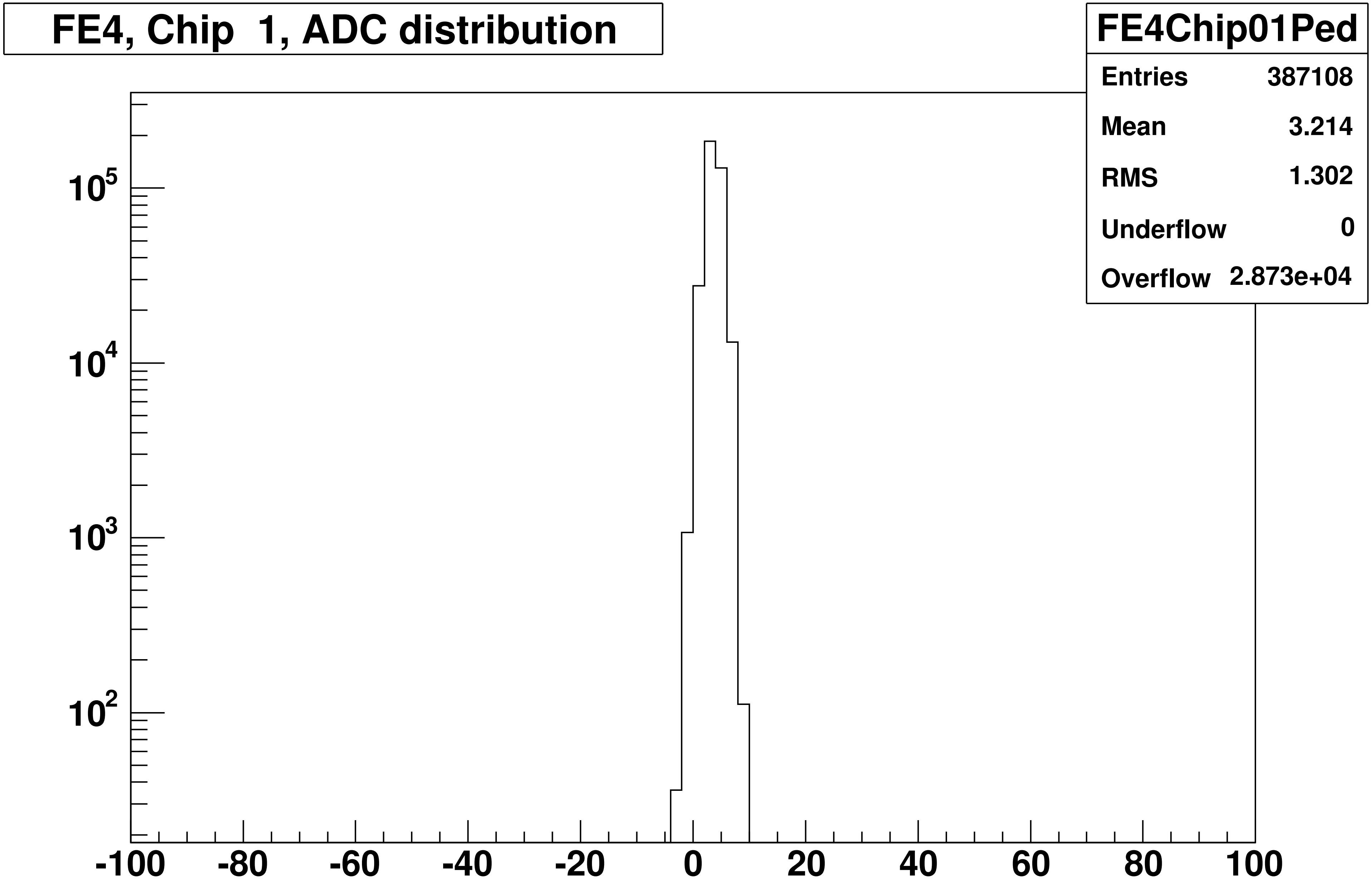


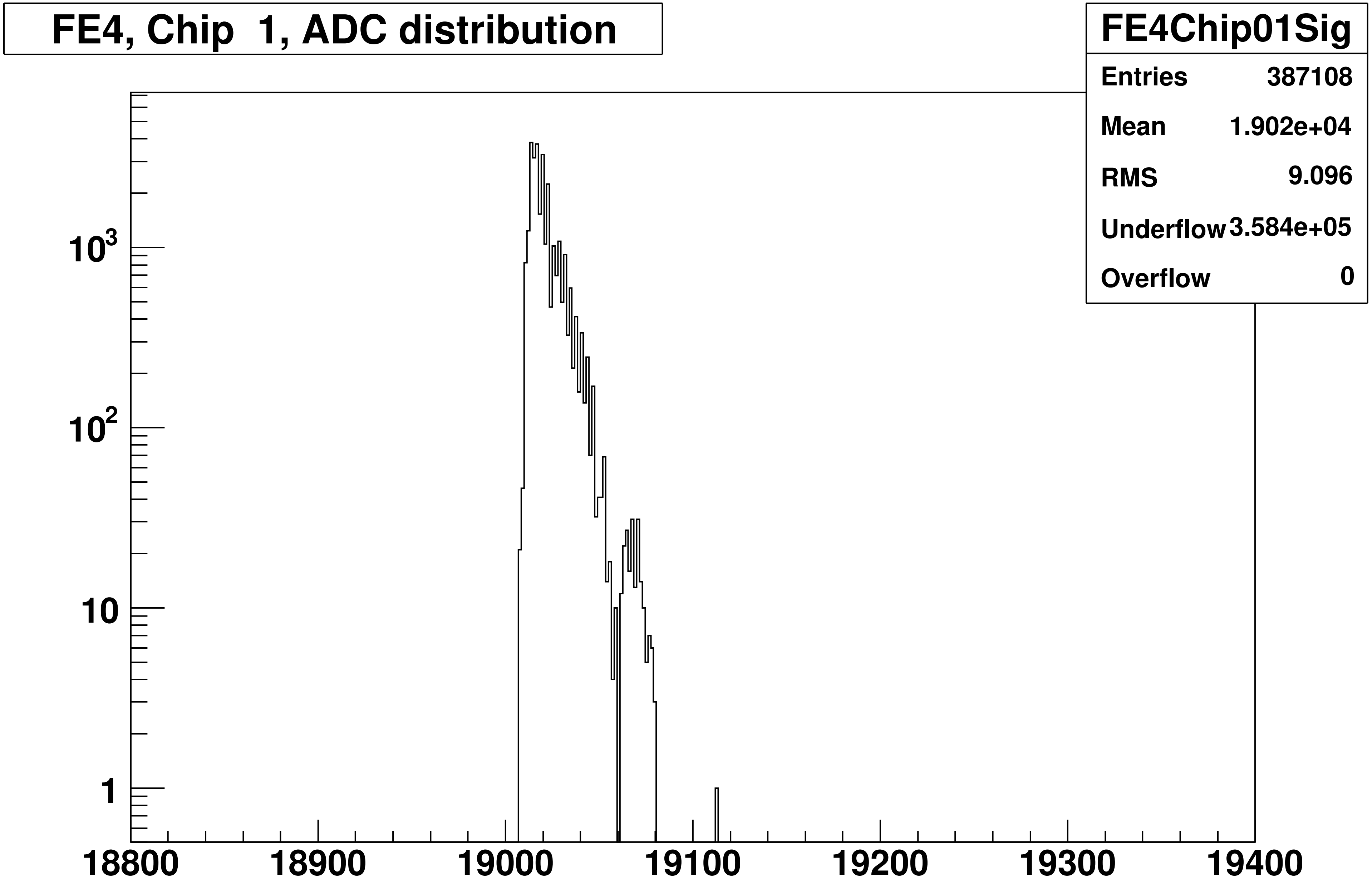
FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 387108
Mean 10.94
RMS 1.291
Underflow 0
Overflow 2.859e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

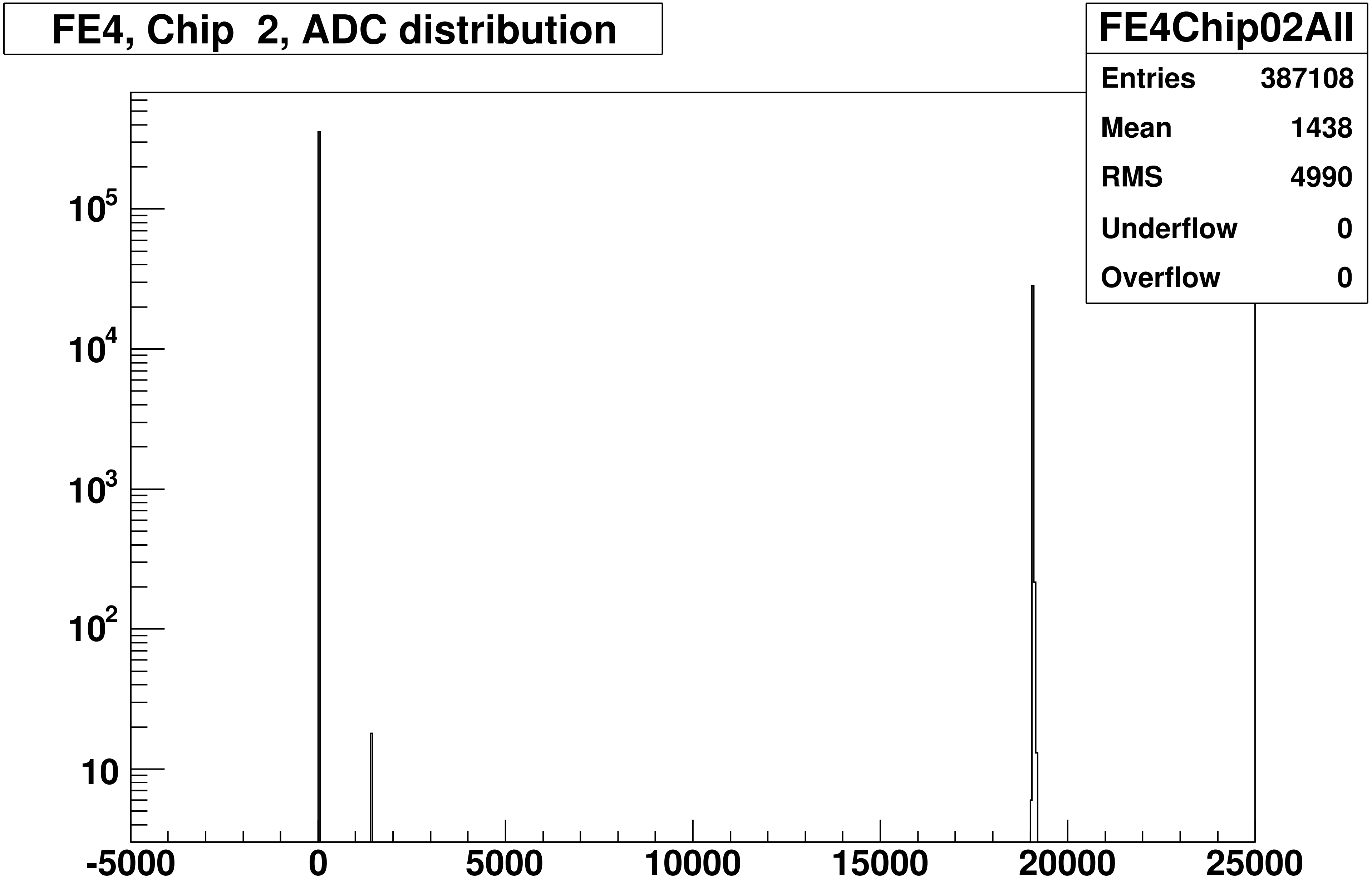


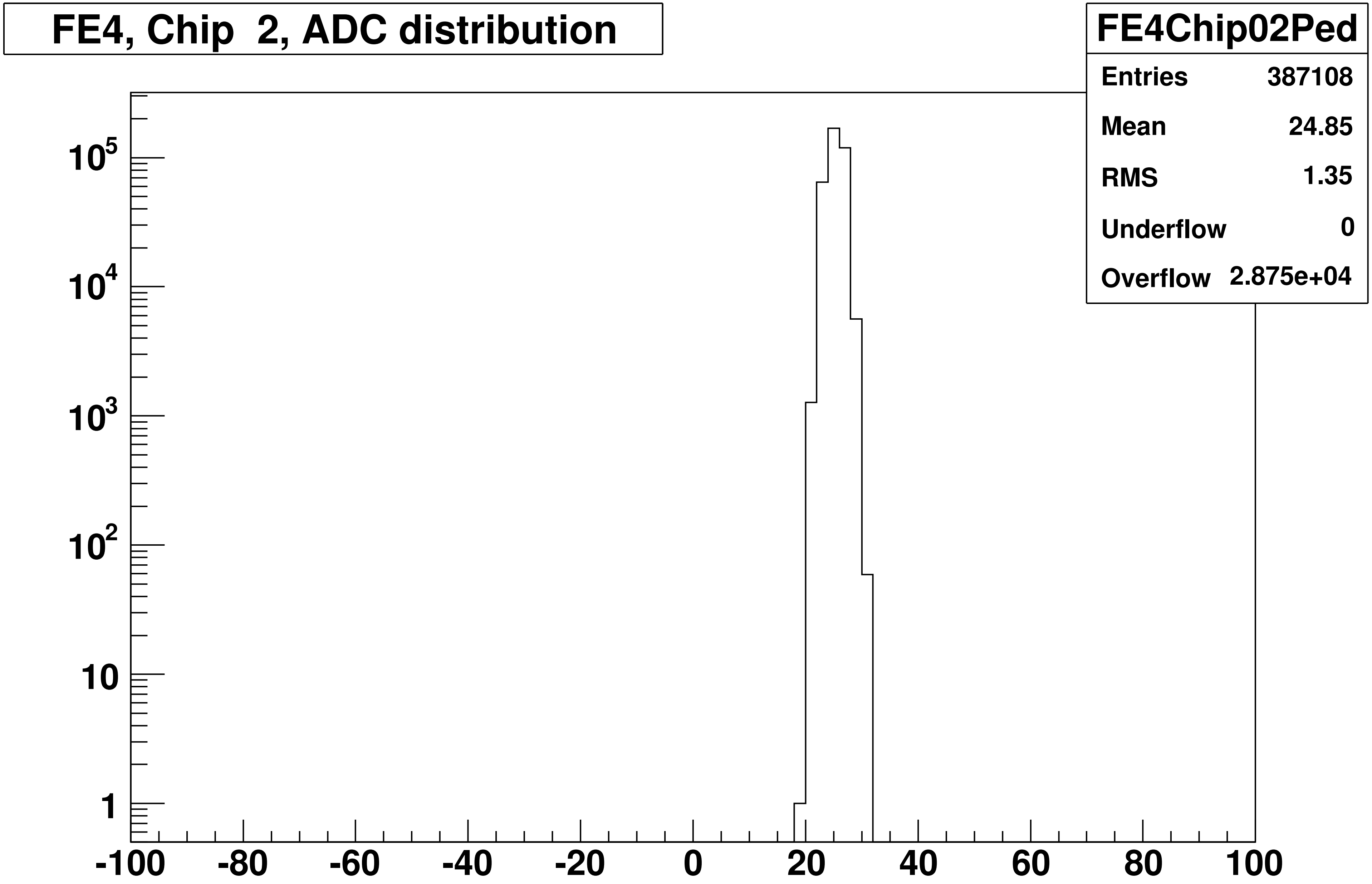
FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 387108
Mean 1.901e+04
RMS 8.029
Underflow 3.586e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 387108
Mean 1414
RMS 4983
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

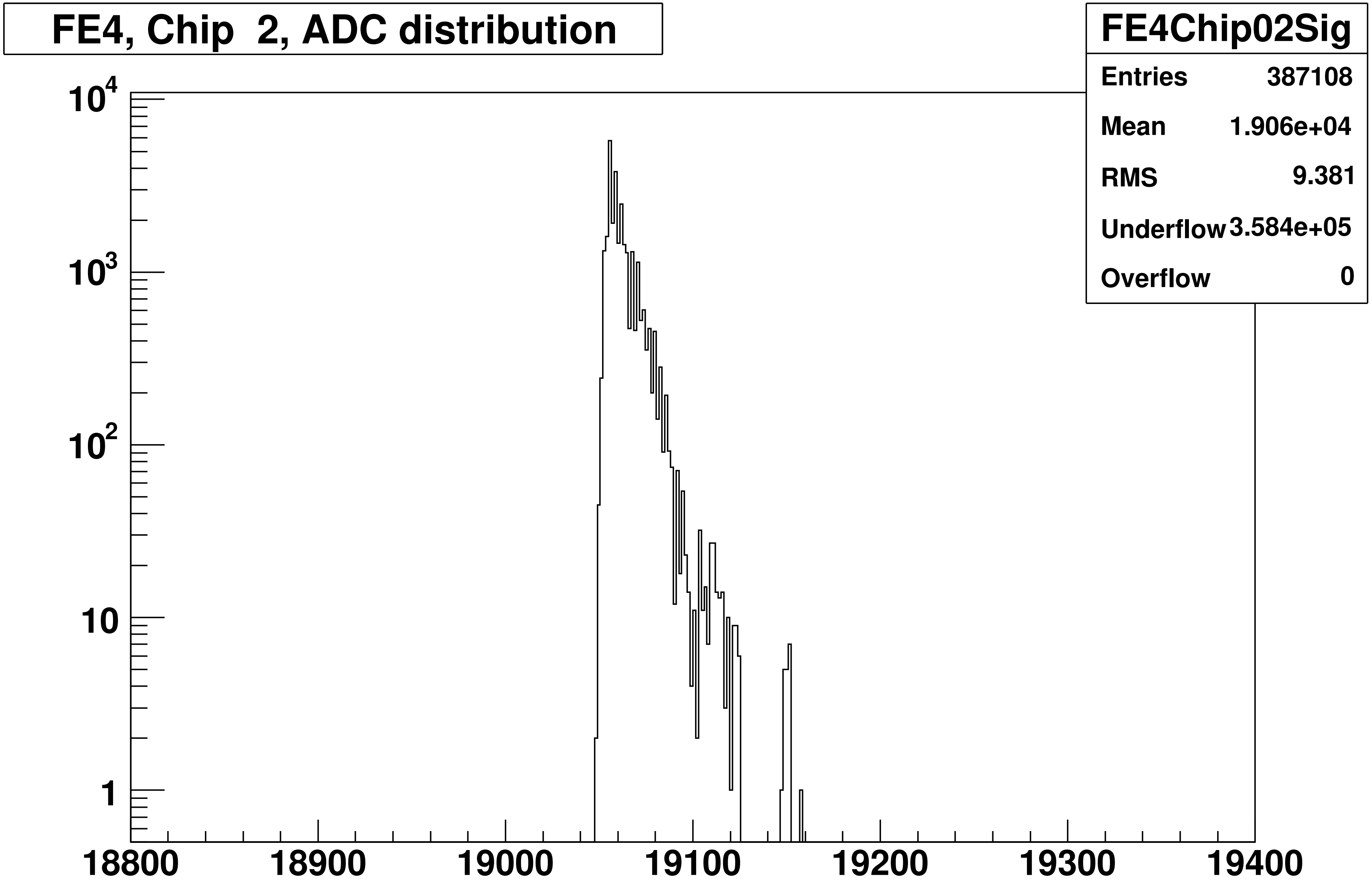

FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 387108
Mean 3.214
RMS 1.302
Underflow 0
Overflow 2.873e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

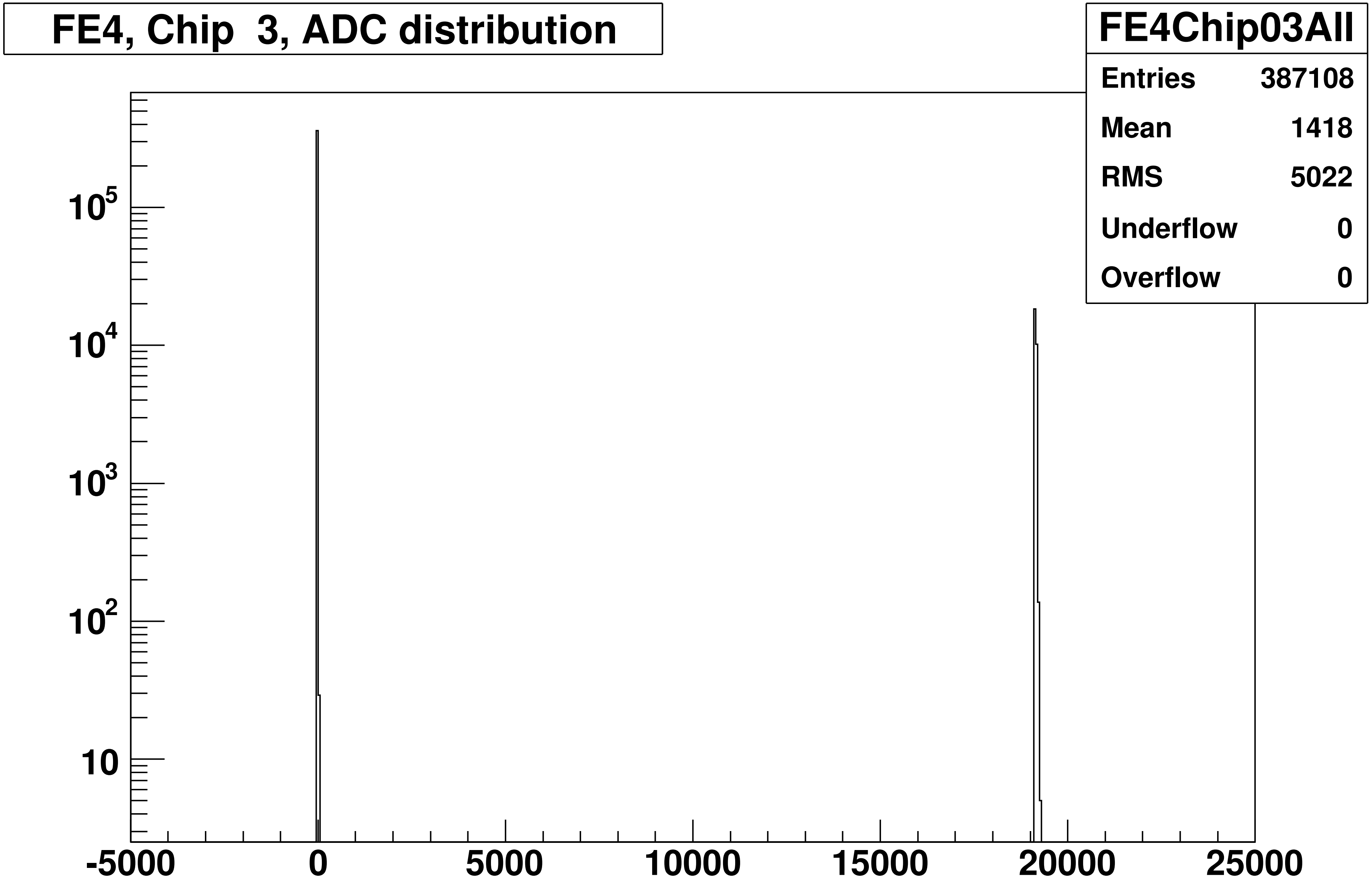

FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 387108
Mean 1.902e+04
RMS 9.096
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

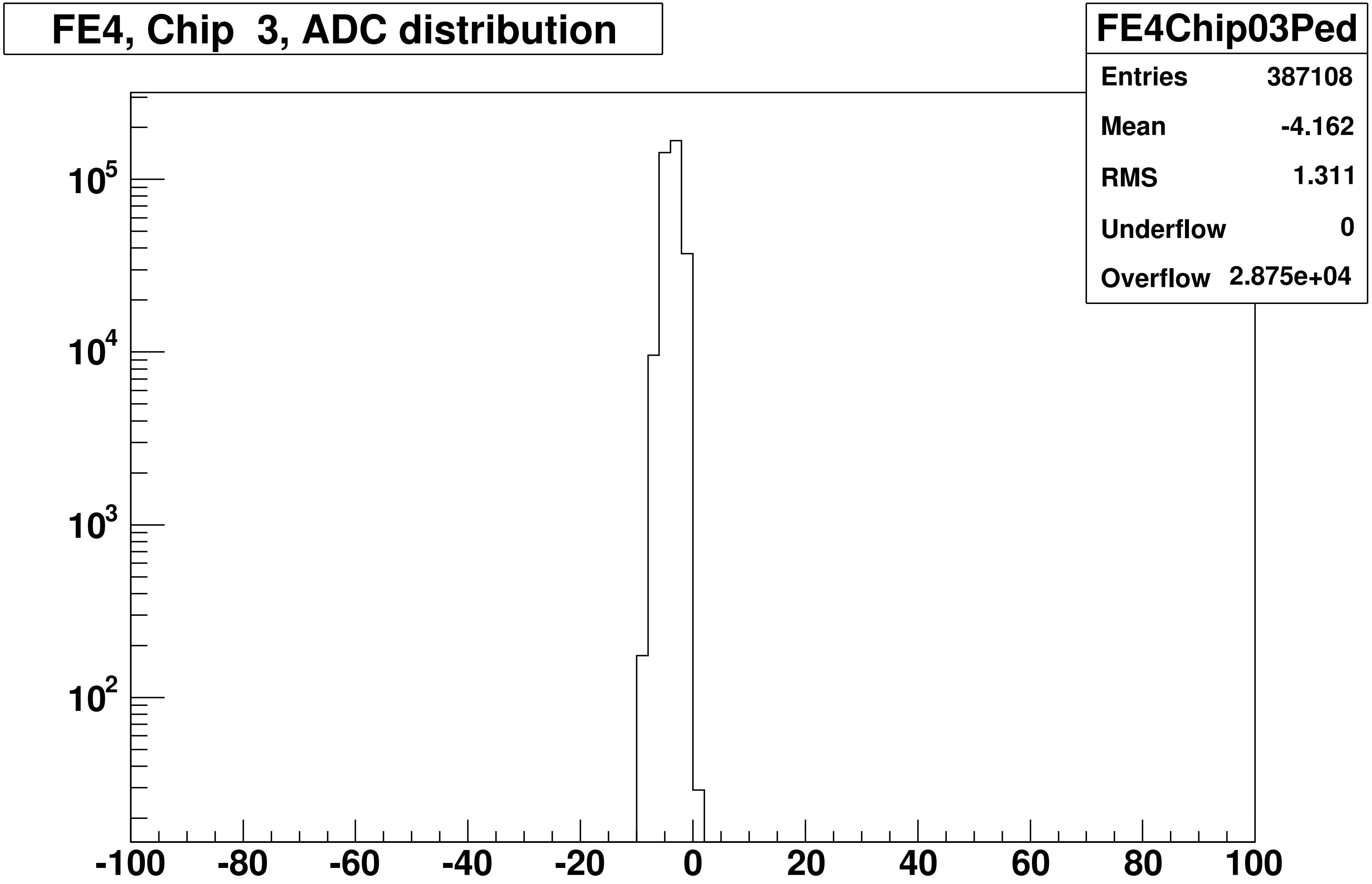

FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 387108
Mean 1438
RMS 4990
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



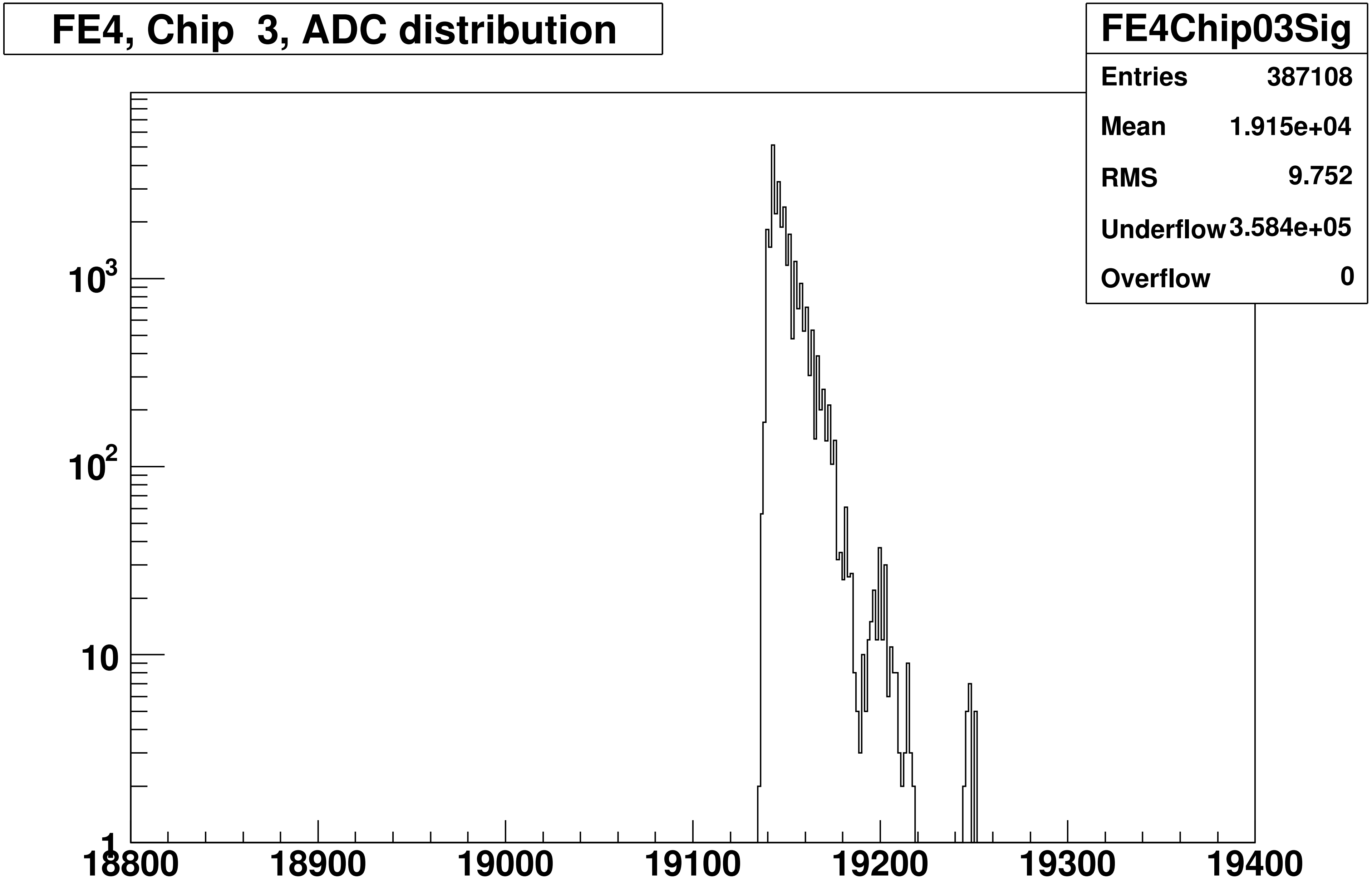
FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 387108
Mean 24.85
RMS 1.35
Underflow 0
Overflow 2.875e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

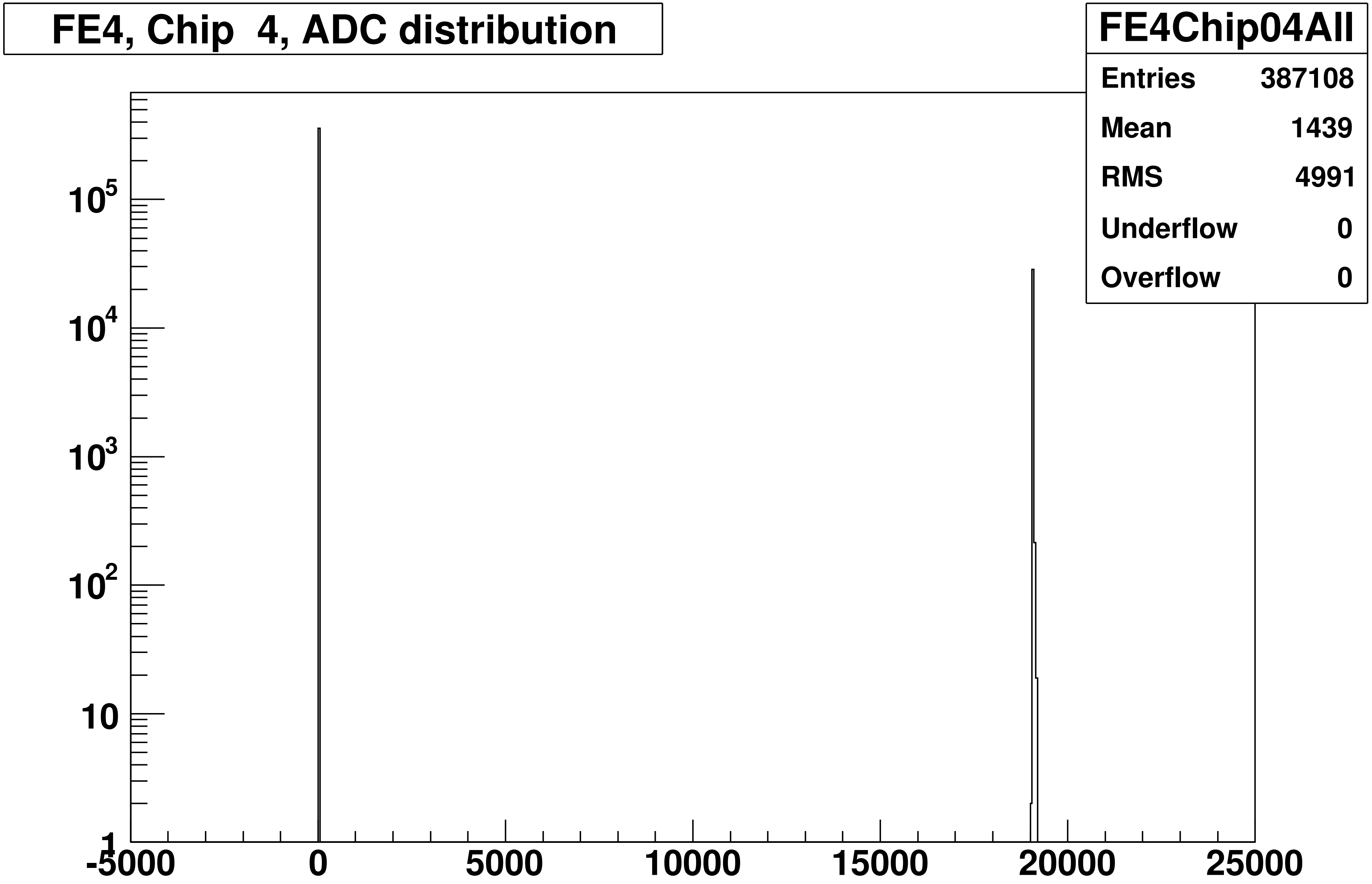

FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 387108
Mean 1.906e+04
RMS 9.381
Underflow 3.584e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 387108
Mean 1418
RMS 5022
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

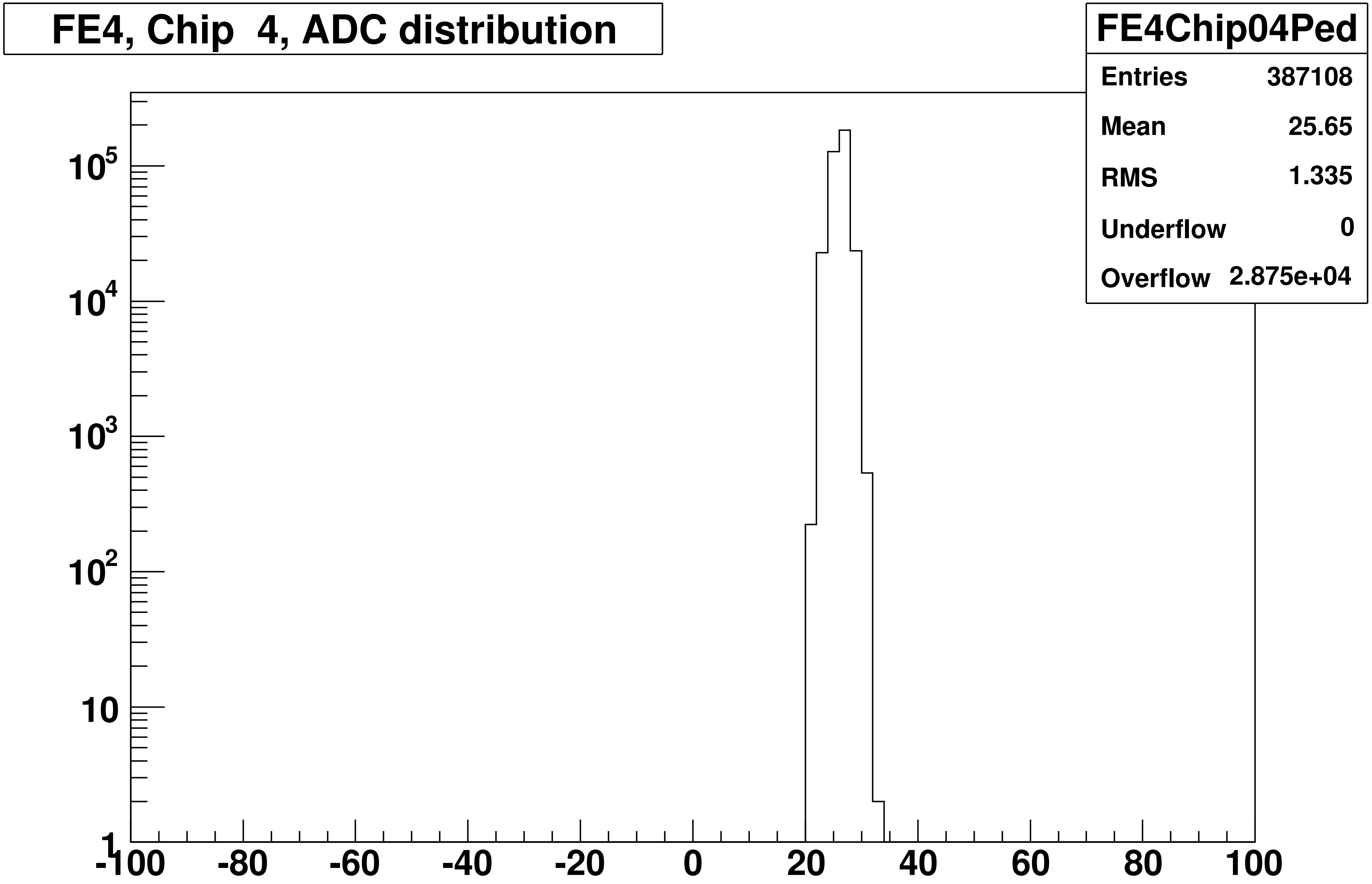


FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 387108
Mean -4.162
RMS 1.311
Underflow 0
Overflow 2.875e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

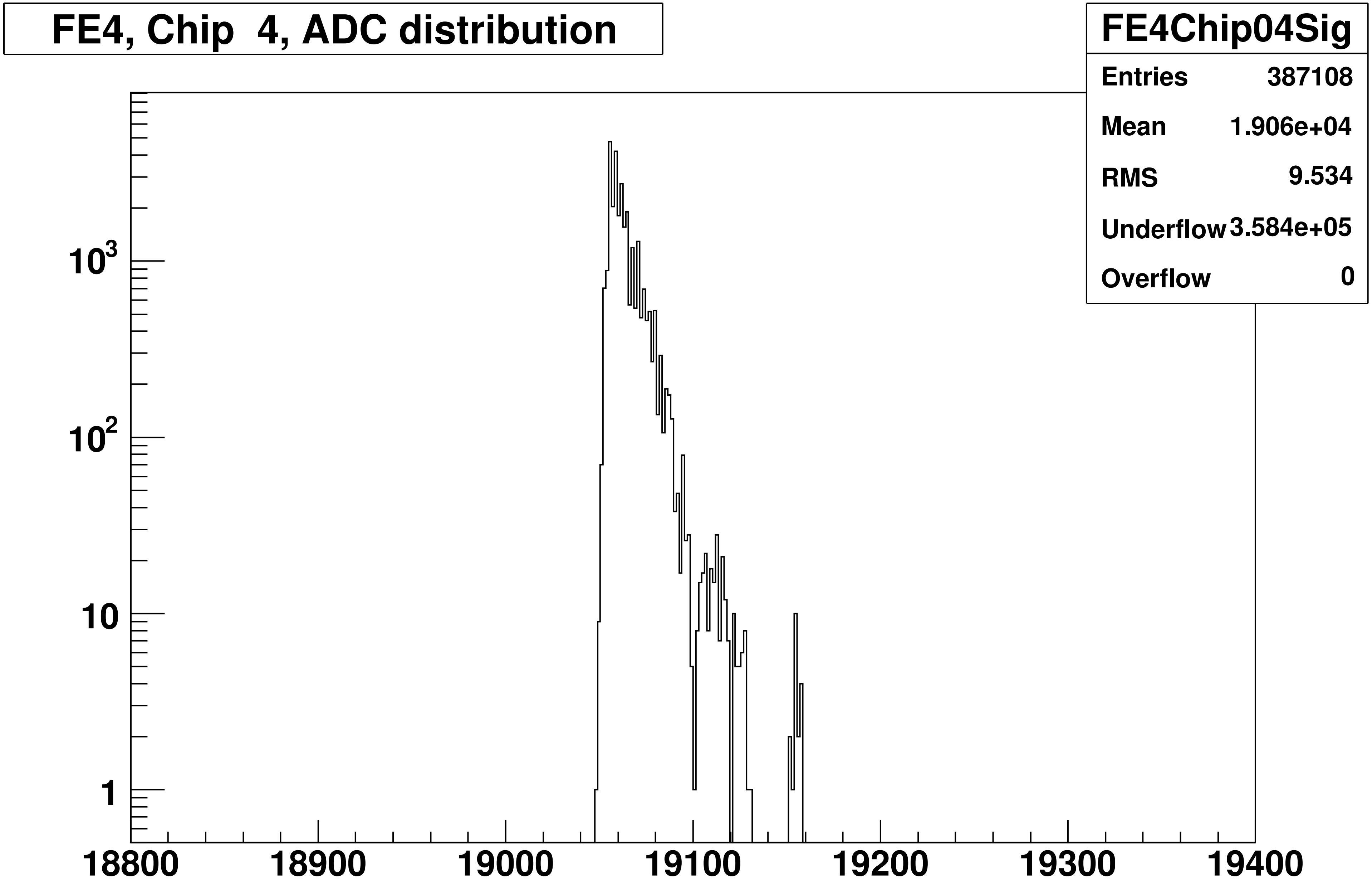

FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 387108
Mean 1.915e+04
RMS 9.752
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

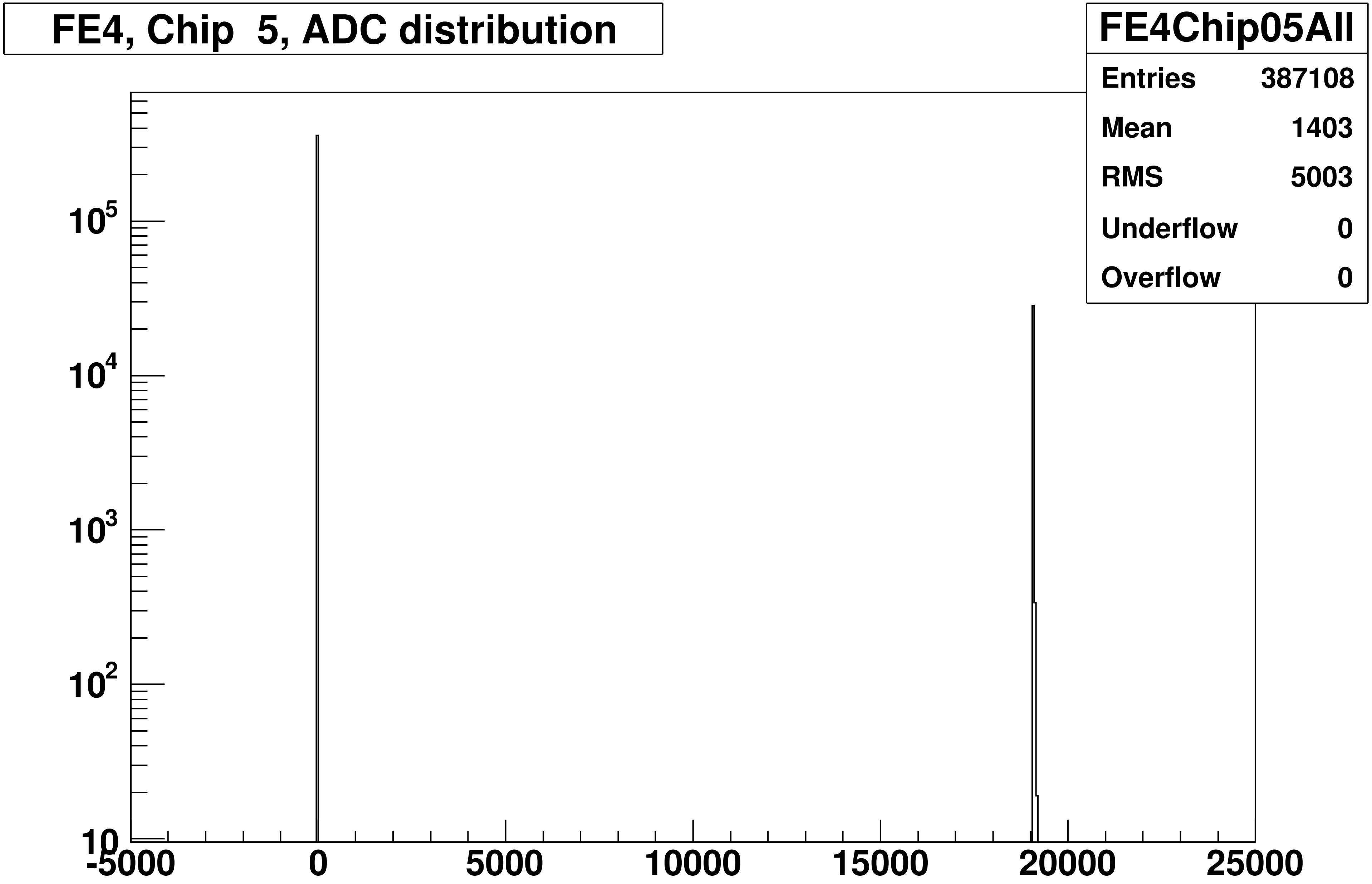


FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 387108
Mean 1439
RMS 4991
Underflow 0
Overflow 0
1
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

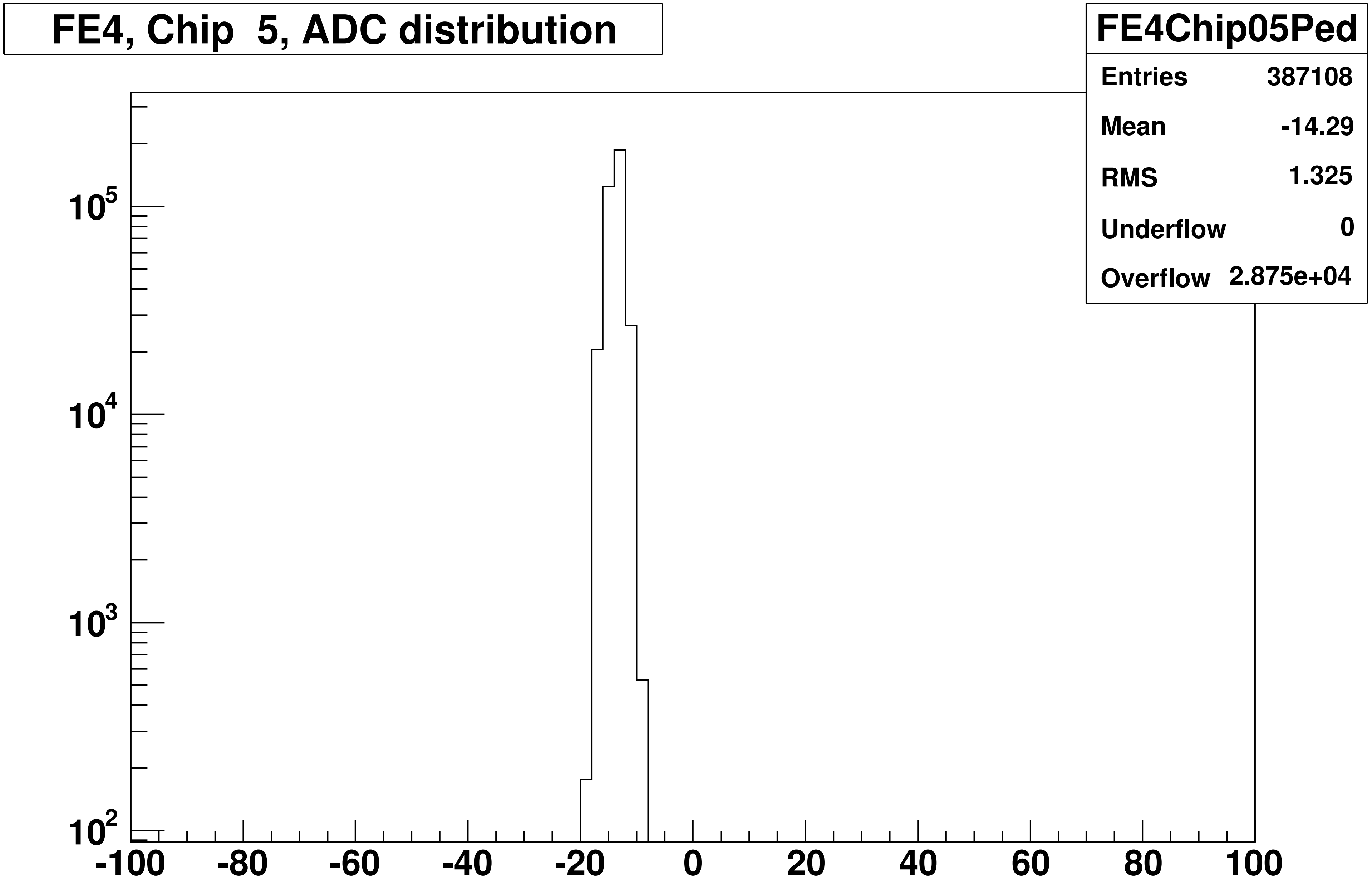


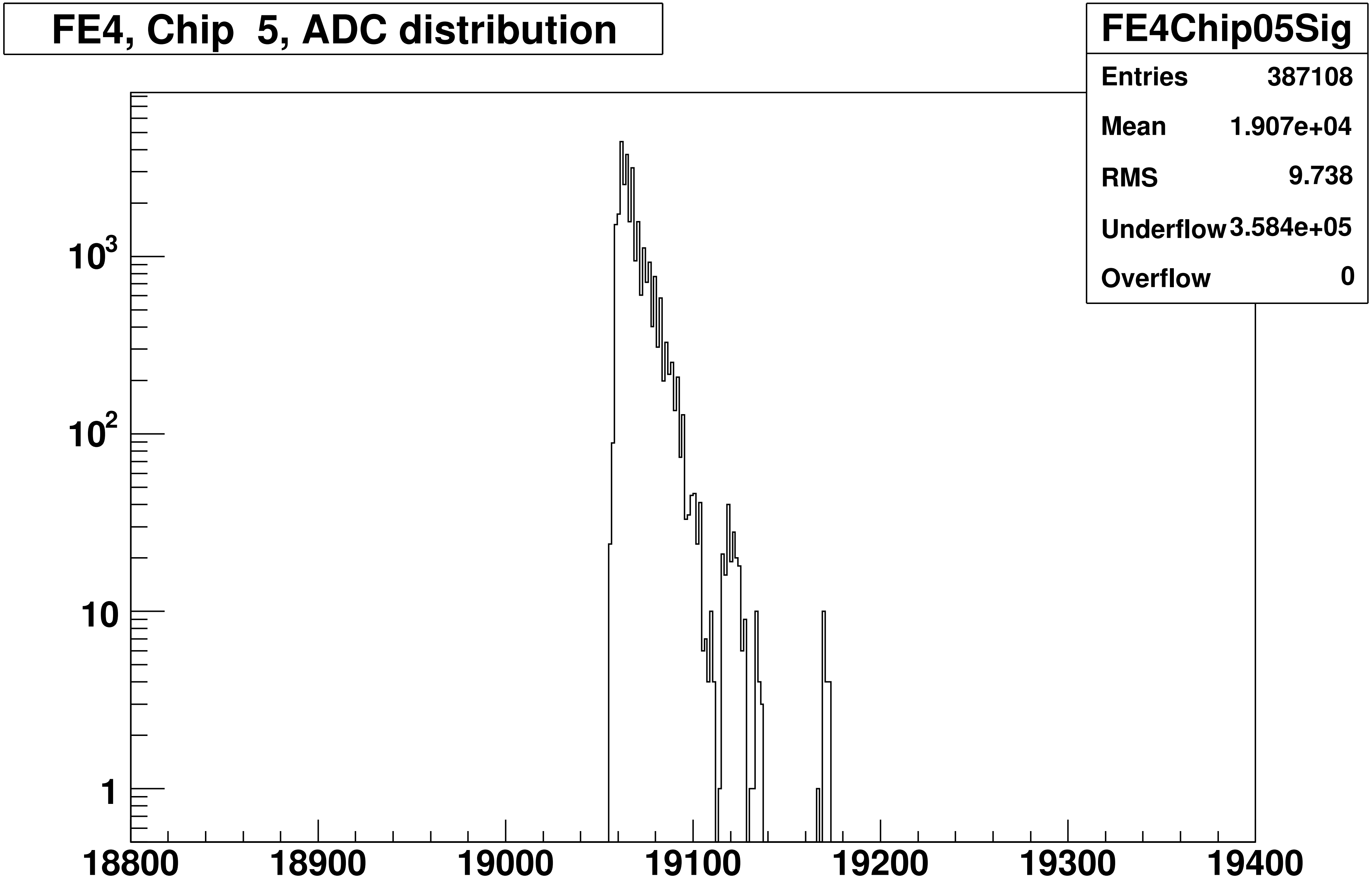
FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 387108
Mean 25.65
RMS 1.335
Underflow 0
Overflow 2.875e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

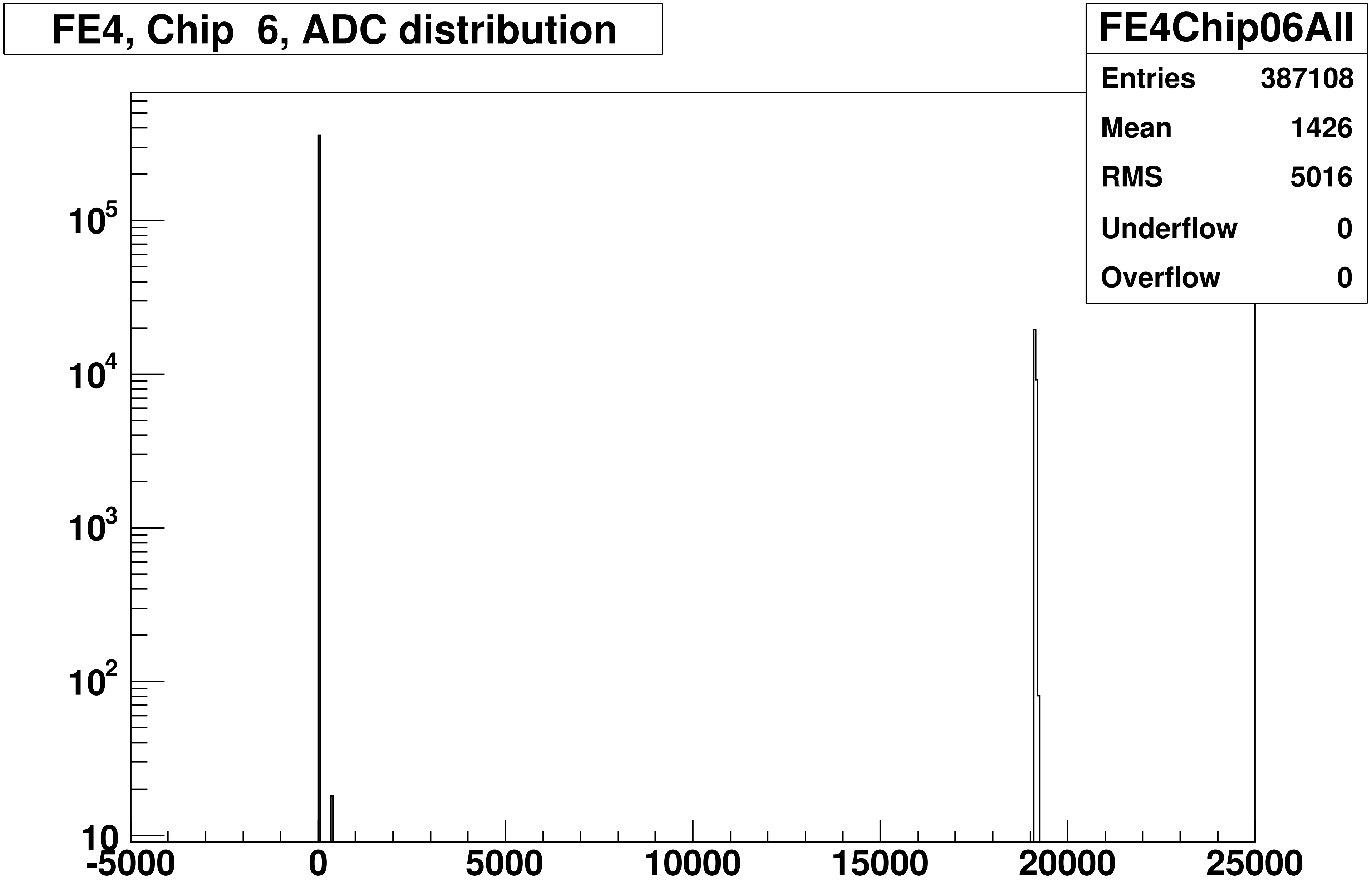

FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 387108
Mean 1.906e+04
RMS 9.534
Underflow 3.584e+05
Overflow 0
10
1
18800
18900
19000
19100
19200
19300
19400



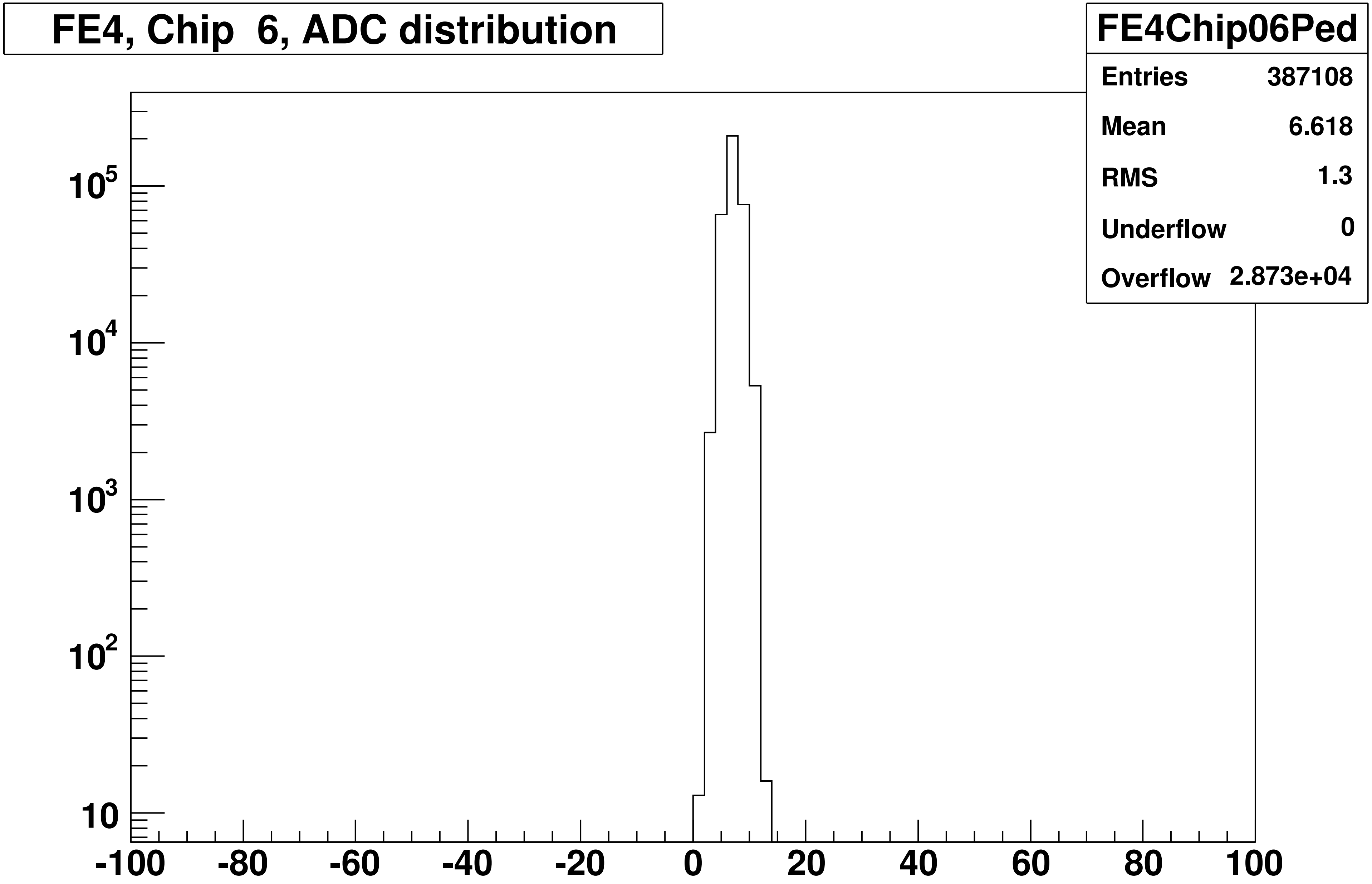
FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 387108
Mean 1403
RMS 5003
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 387108
Mean -14.29
RMS 1.325
Underflow 0
Overflow 2.875e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

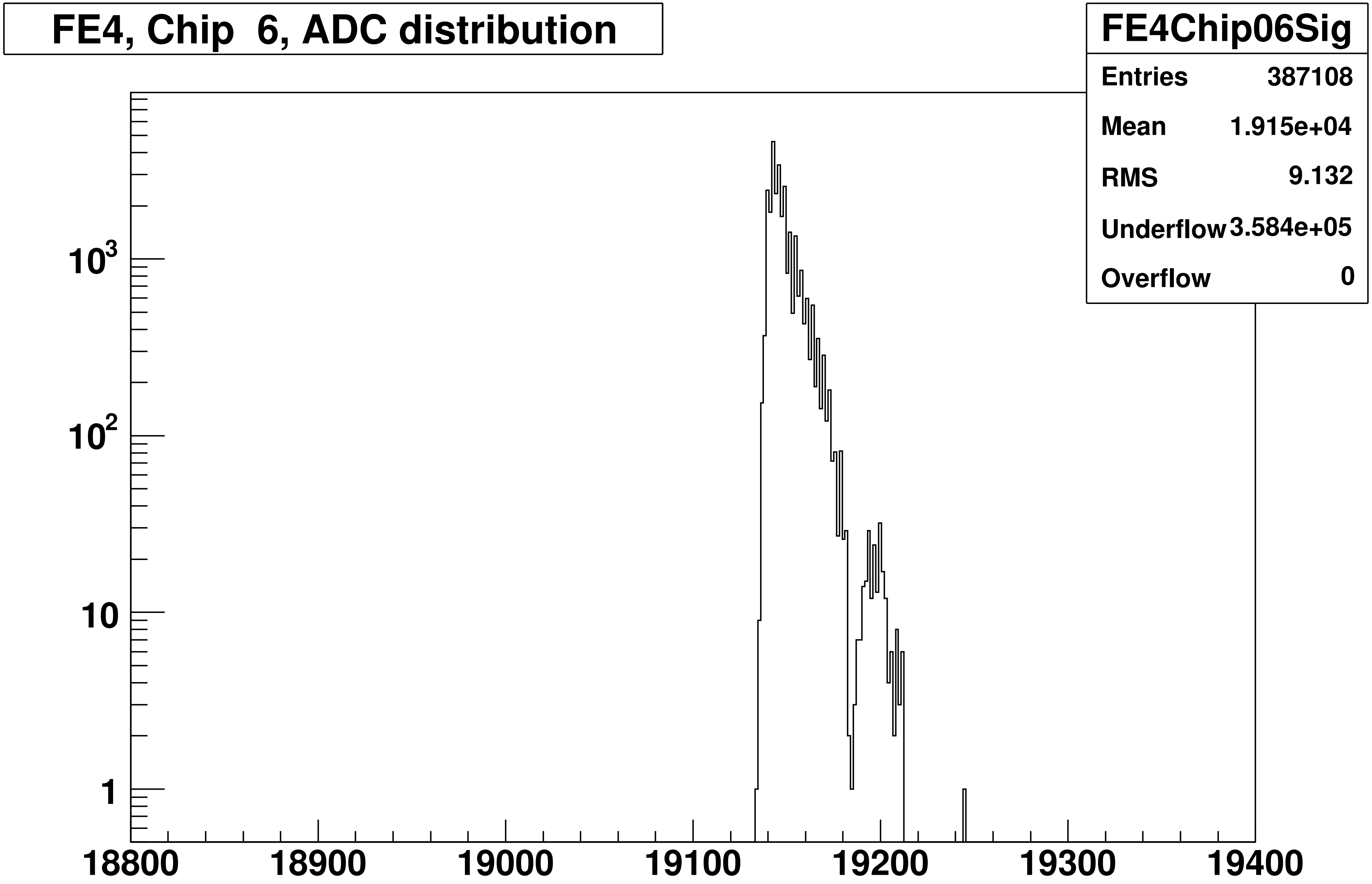

FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 387108
Mean 1.907e+04
RMS 9.738
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

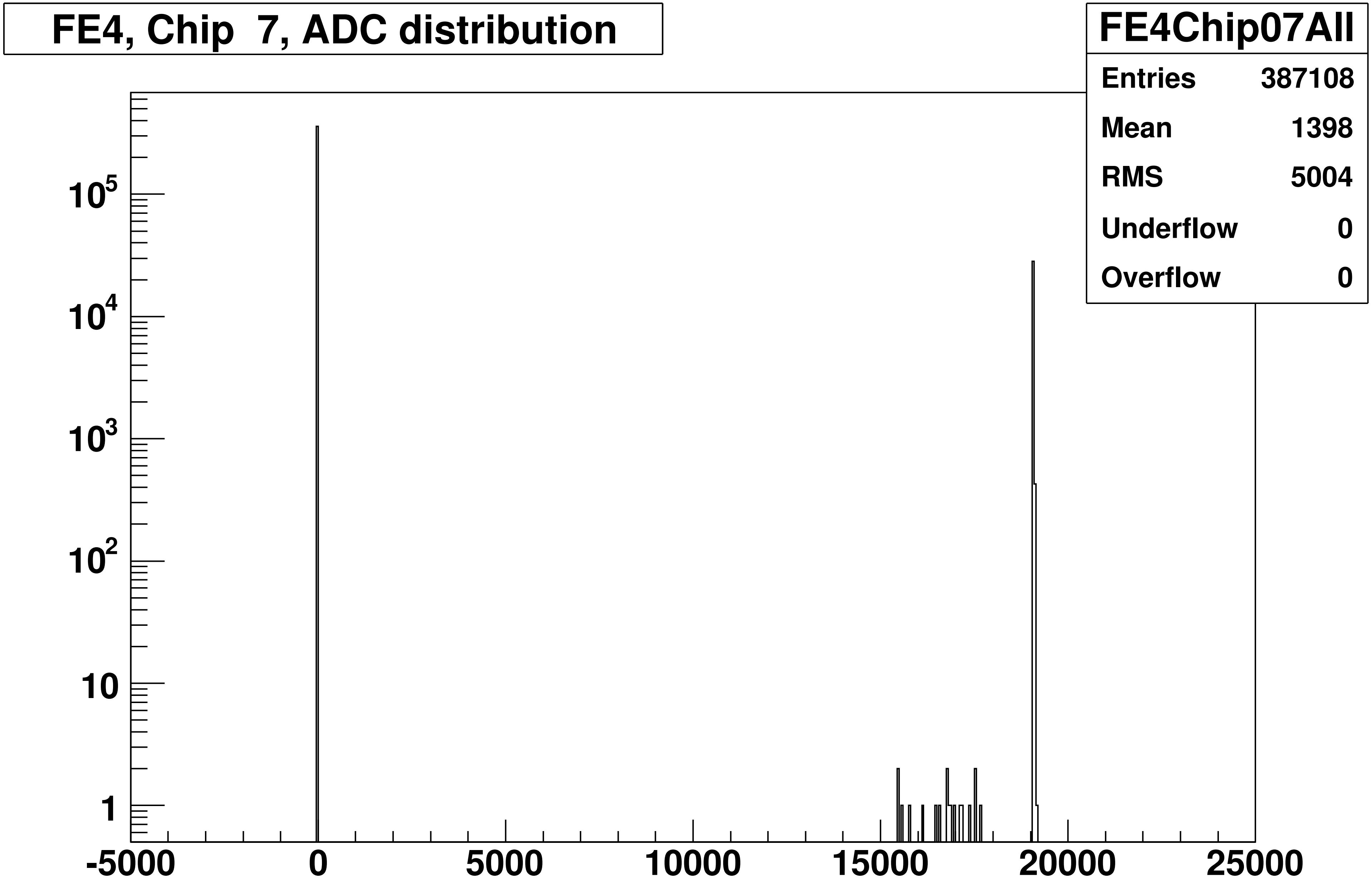


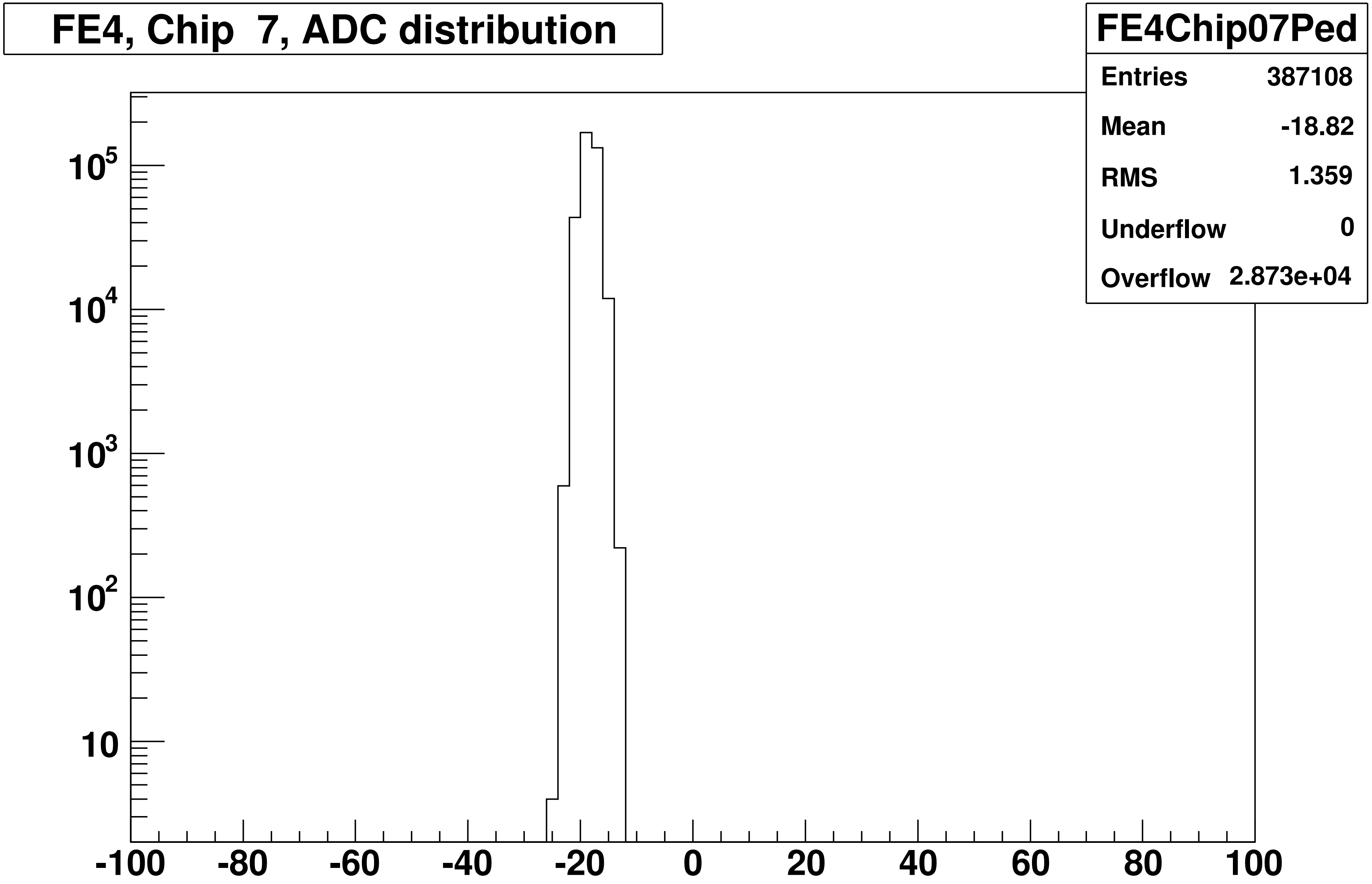
FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 387108
Mean 1426
RMS 5016
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

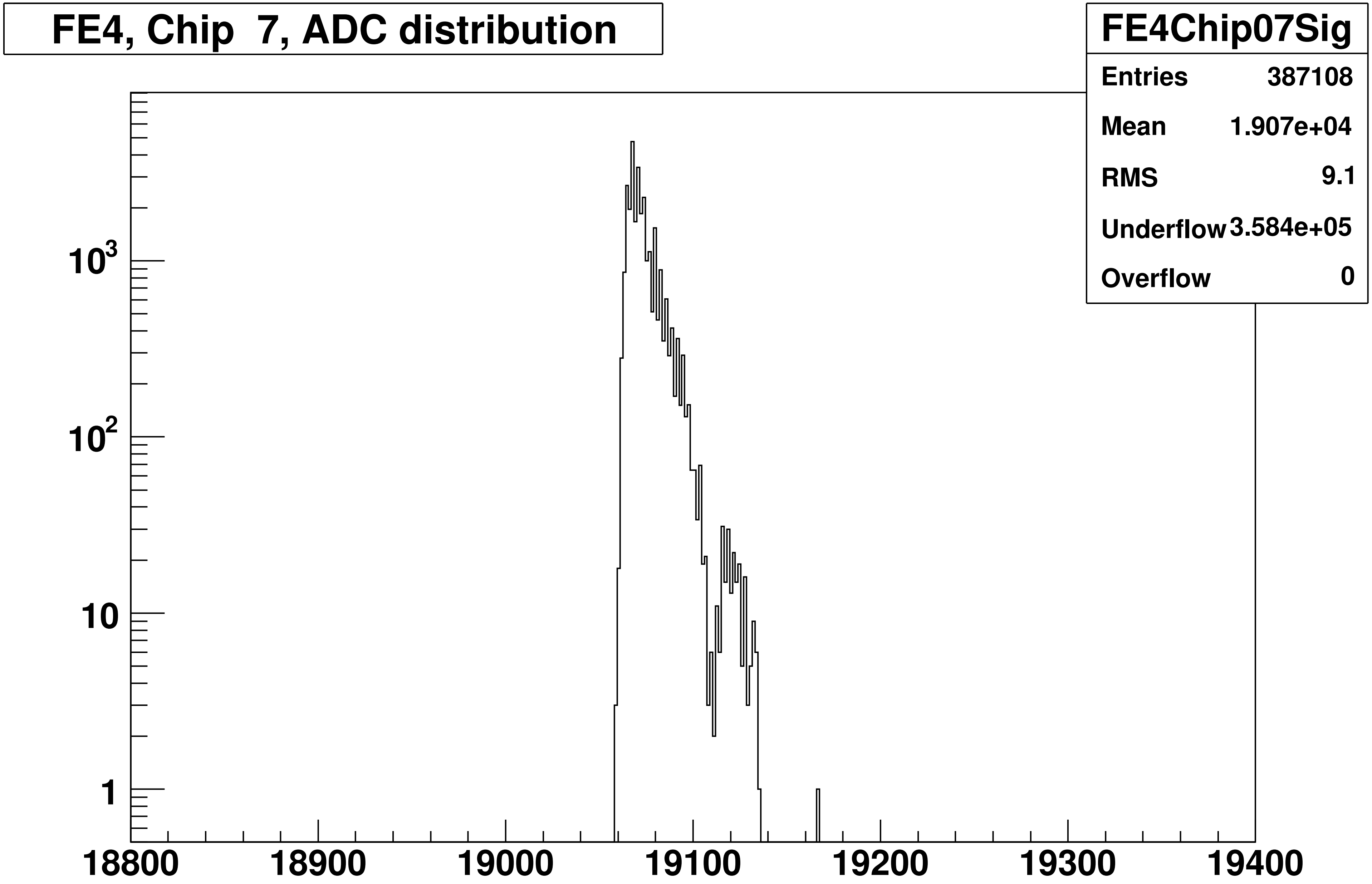


FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 387108
Mean 6.618
RMS 1.3
Underflow 0
Overflow 2.873e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

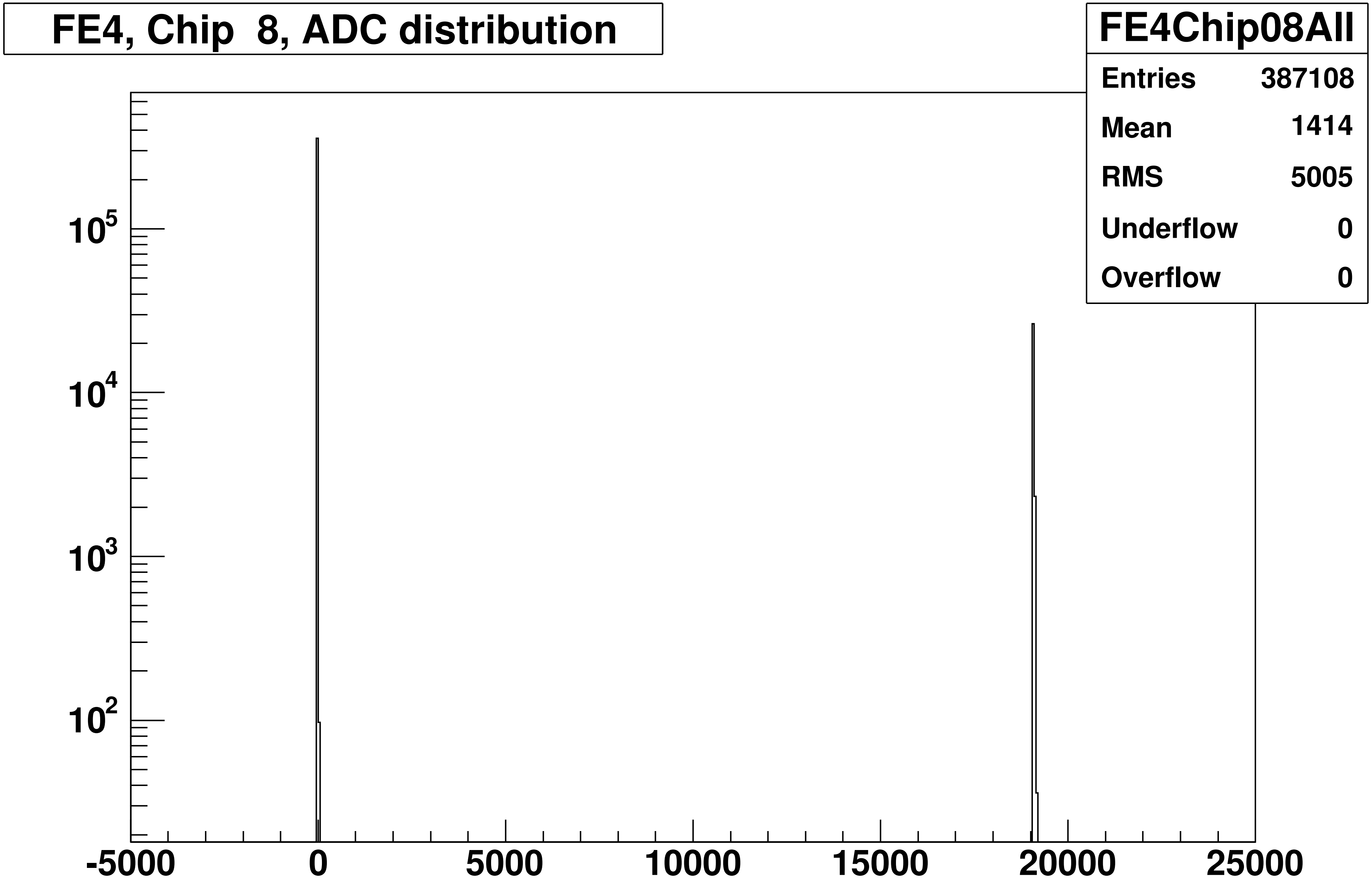

FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 387108
Mean 1.915e+04
RMS 9.132
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 387108
Mean 1398
RMS 5004
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

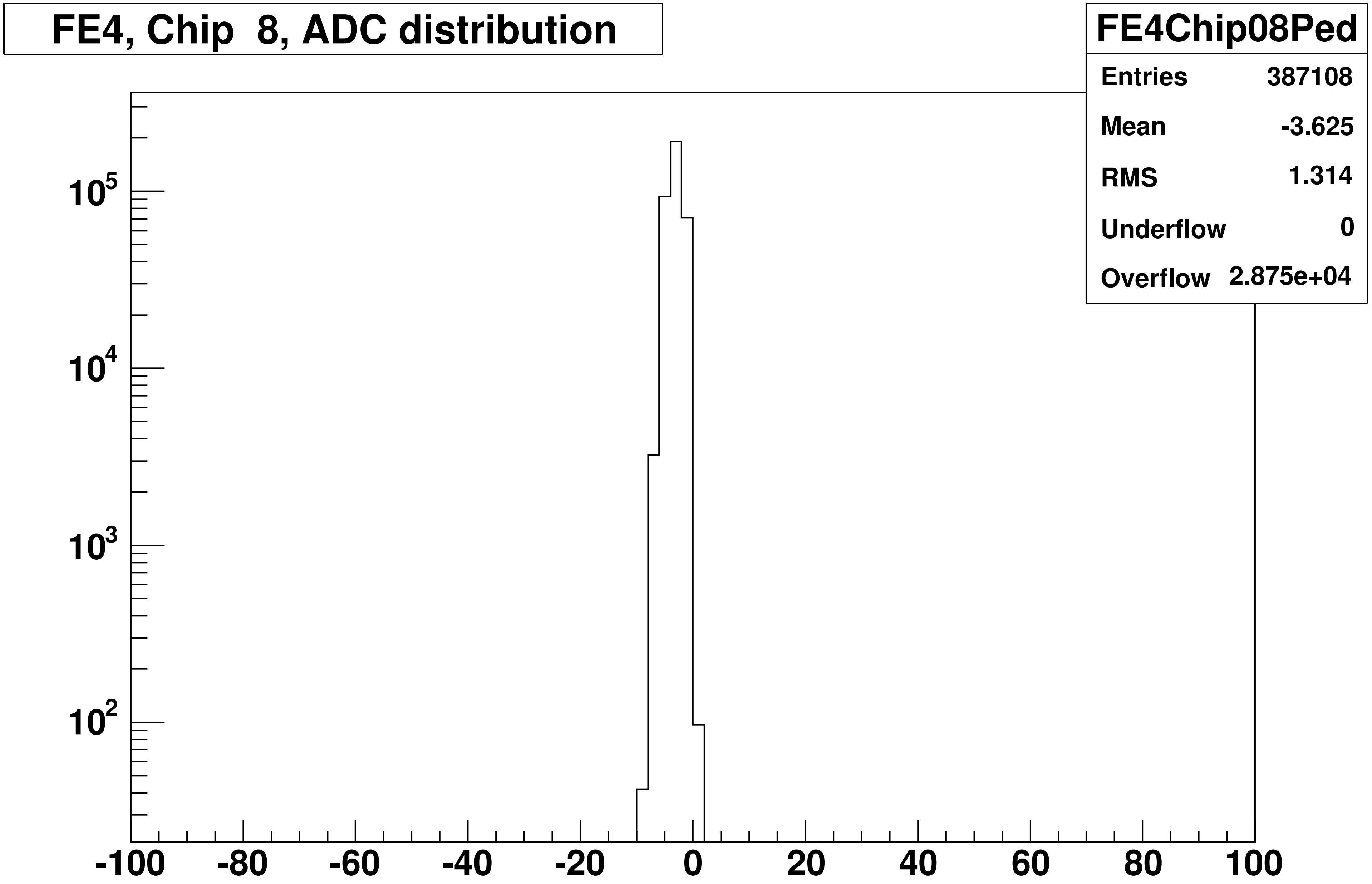

FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 387108
Mean -18.82
RMS 1.359
Underflow 0
Overflow 2.873e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

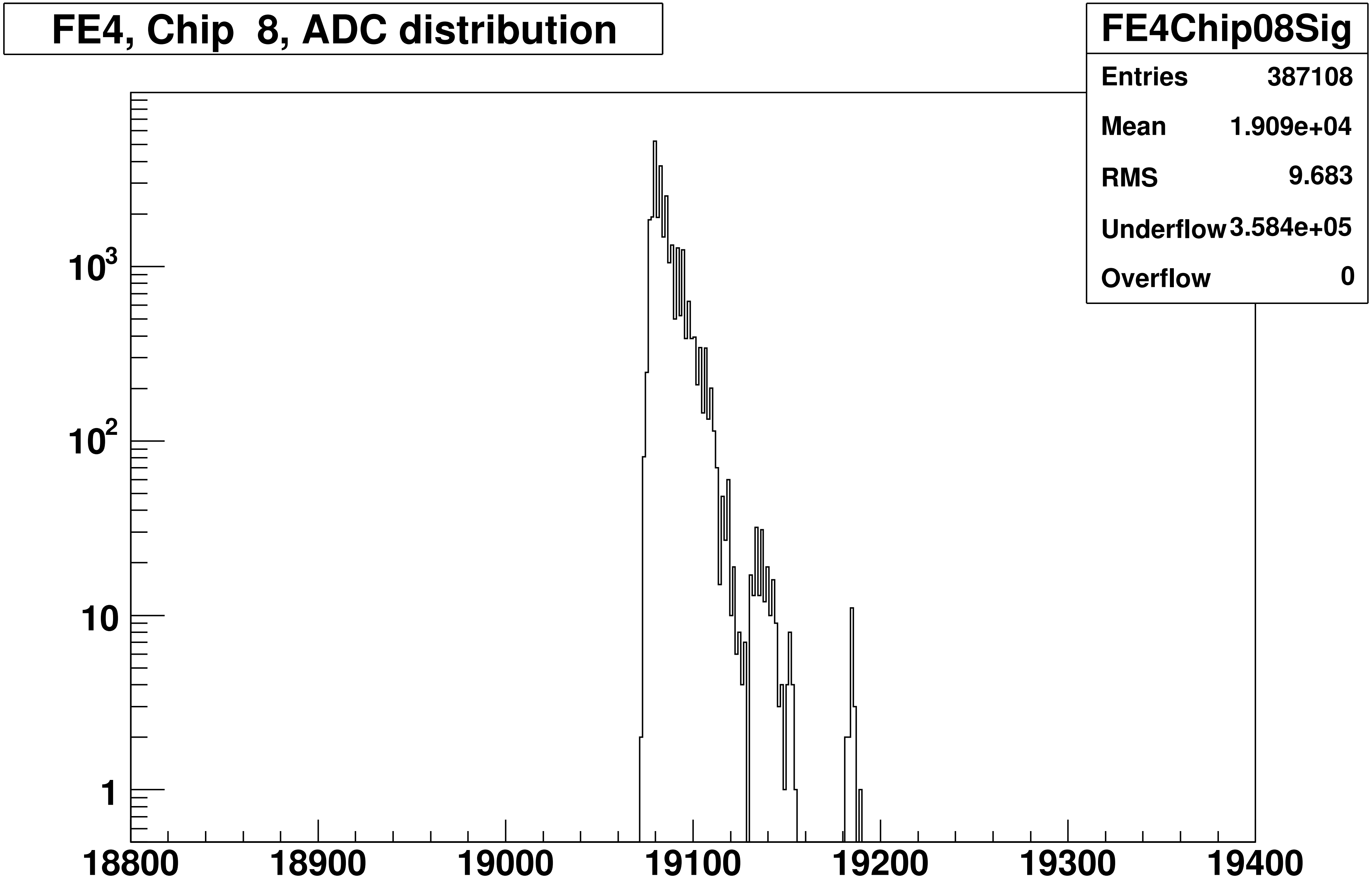


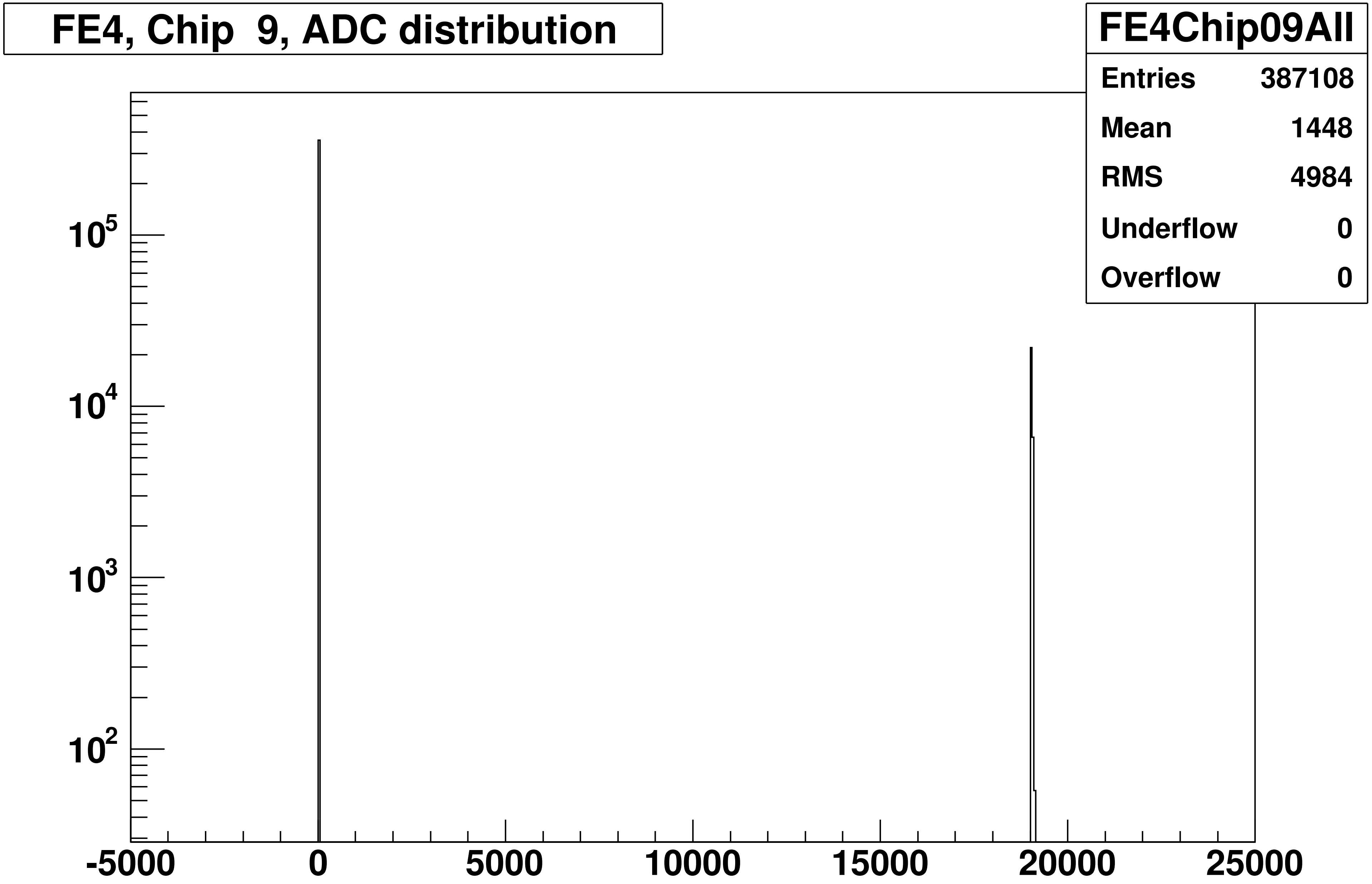
FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 387108
Mean 1.907e+04
RMS 9.1
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

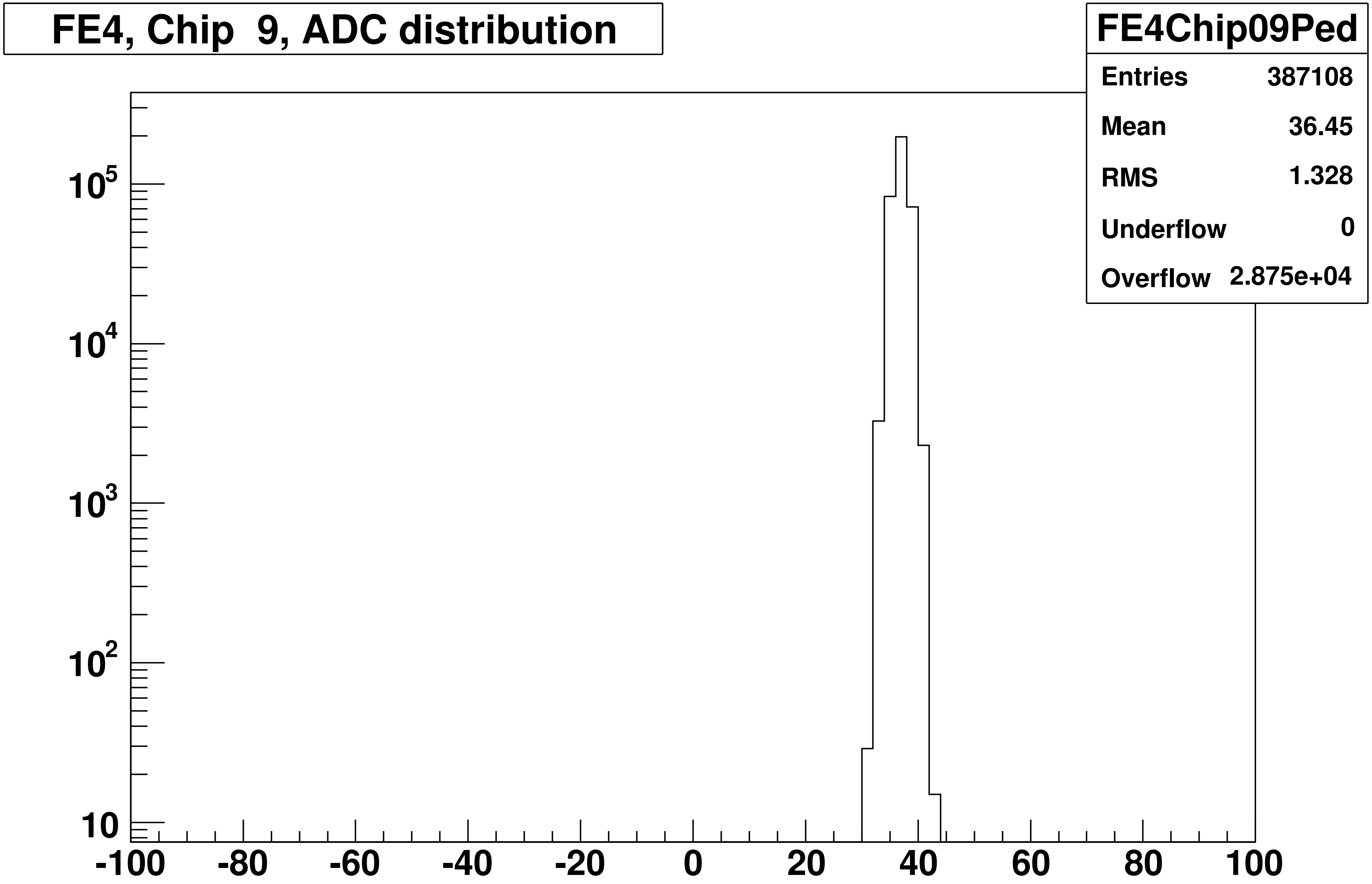


FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 387108
Mean 1414
RMS 5005
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

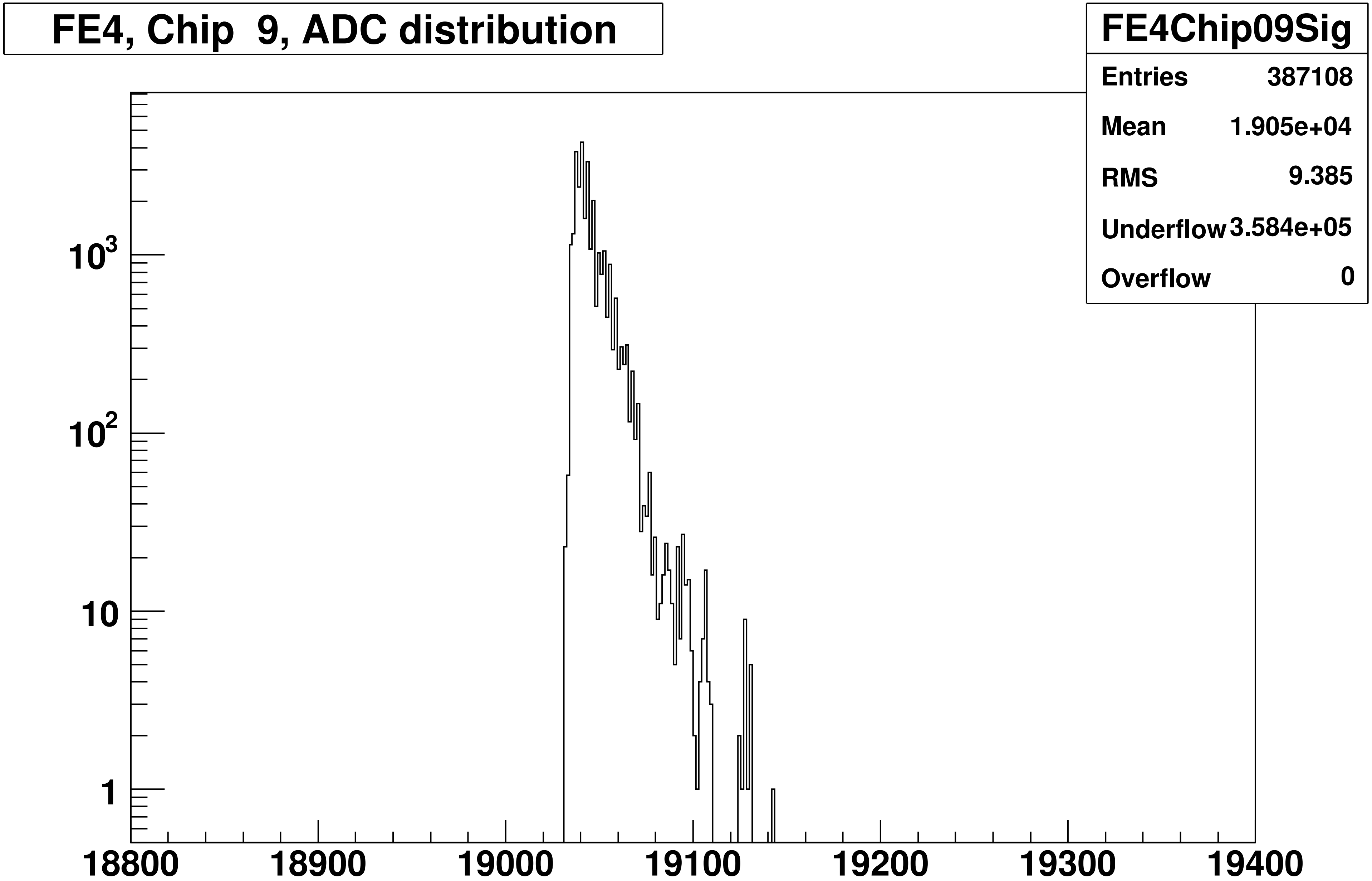

FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 387108
Mean -3.625
RMS 1.314
Underflow 0
Overflow 2.875e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 387108
Mean 1.909e+04
RMS 9.683
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

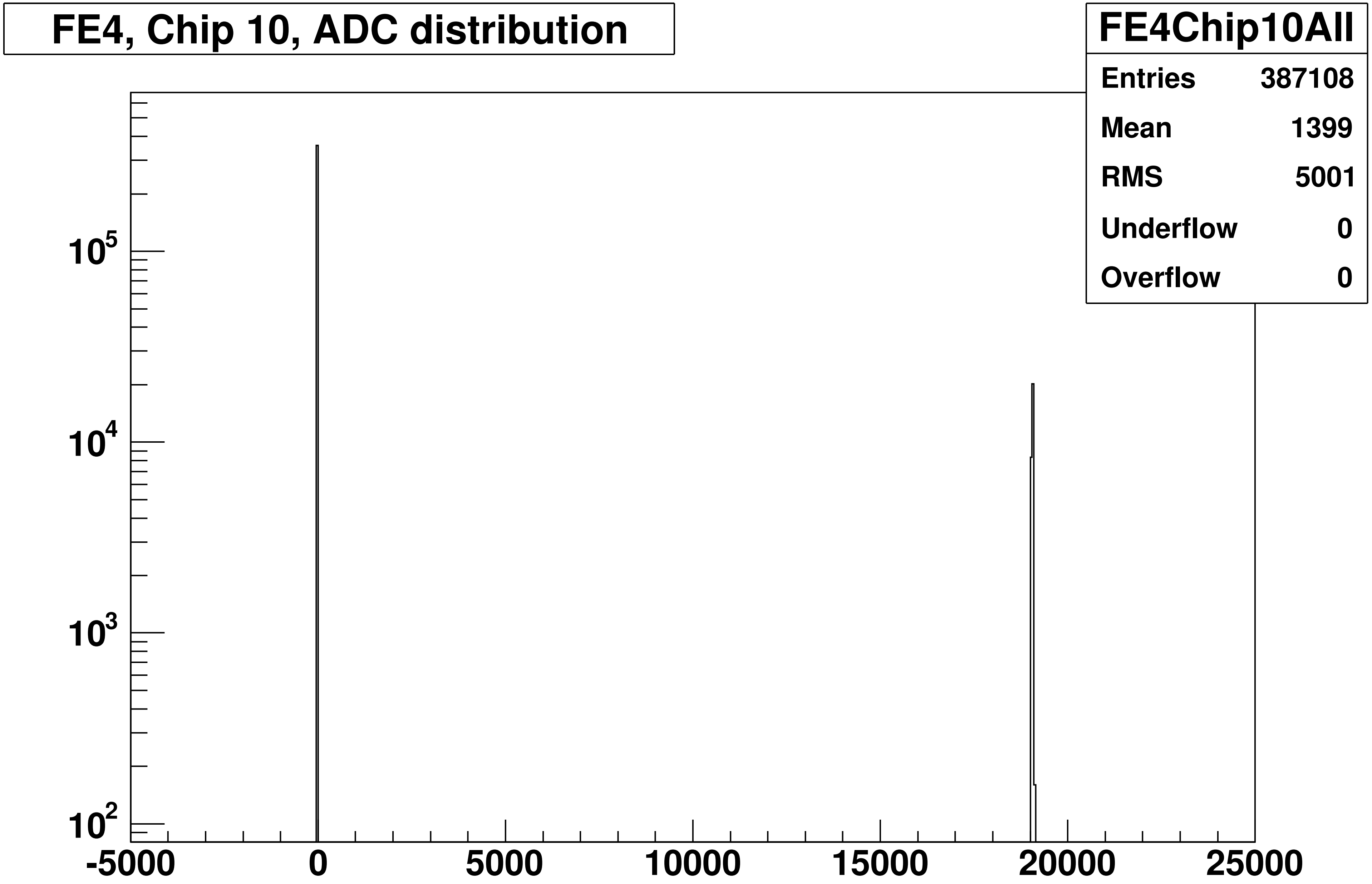

FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 387108
Mean 1448
RMS 4984
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



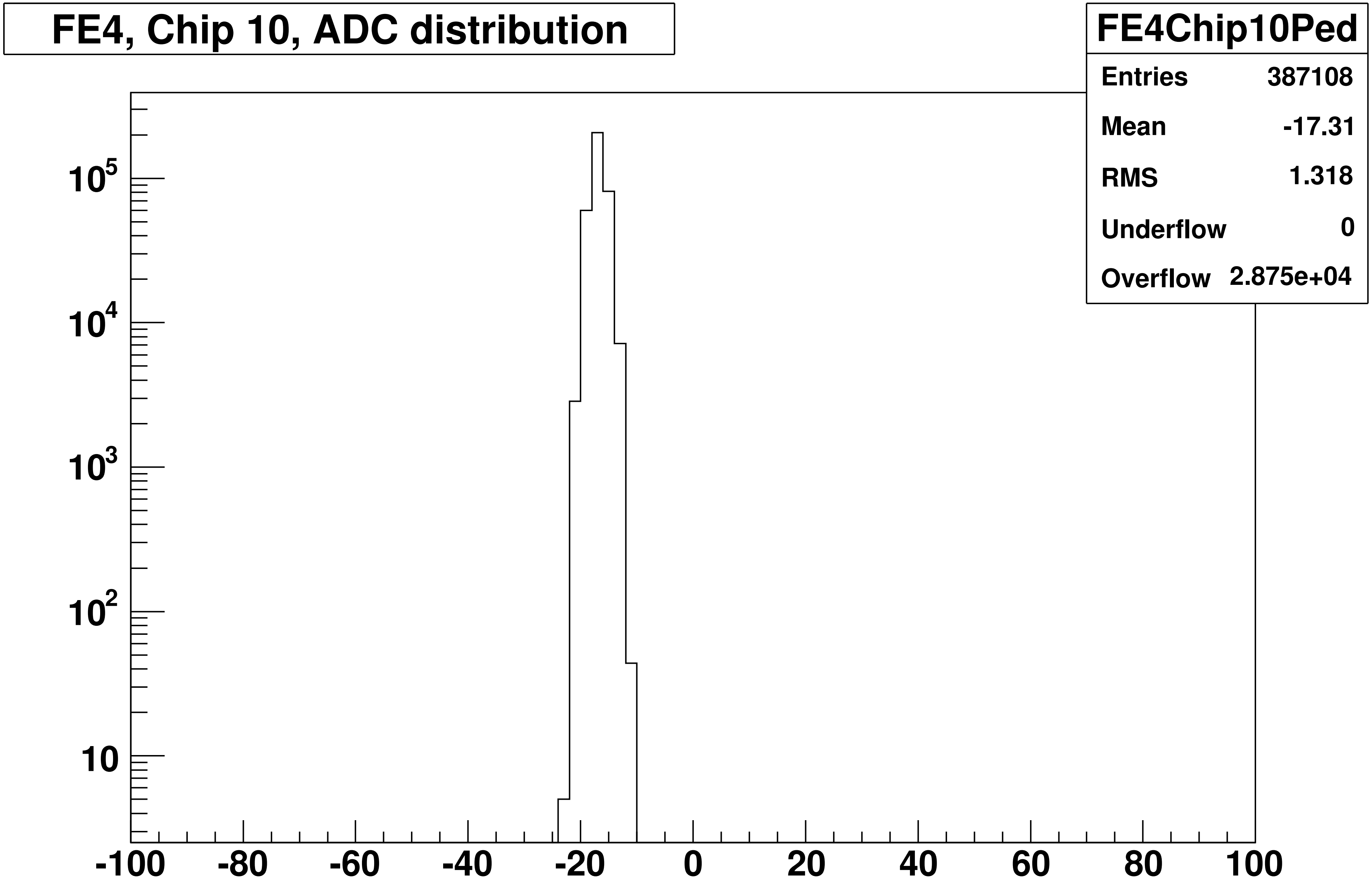
FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 387108
Mean 36.45
RMS 1.328
Underflow 0
Overflow 2.875e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

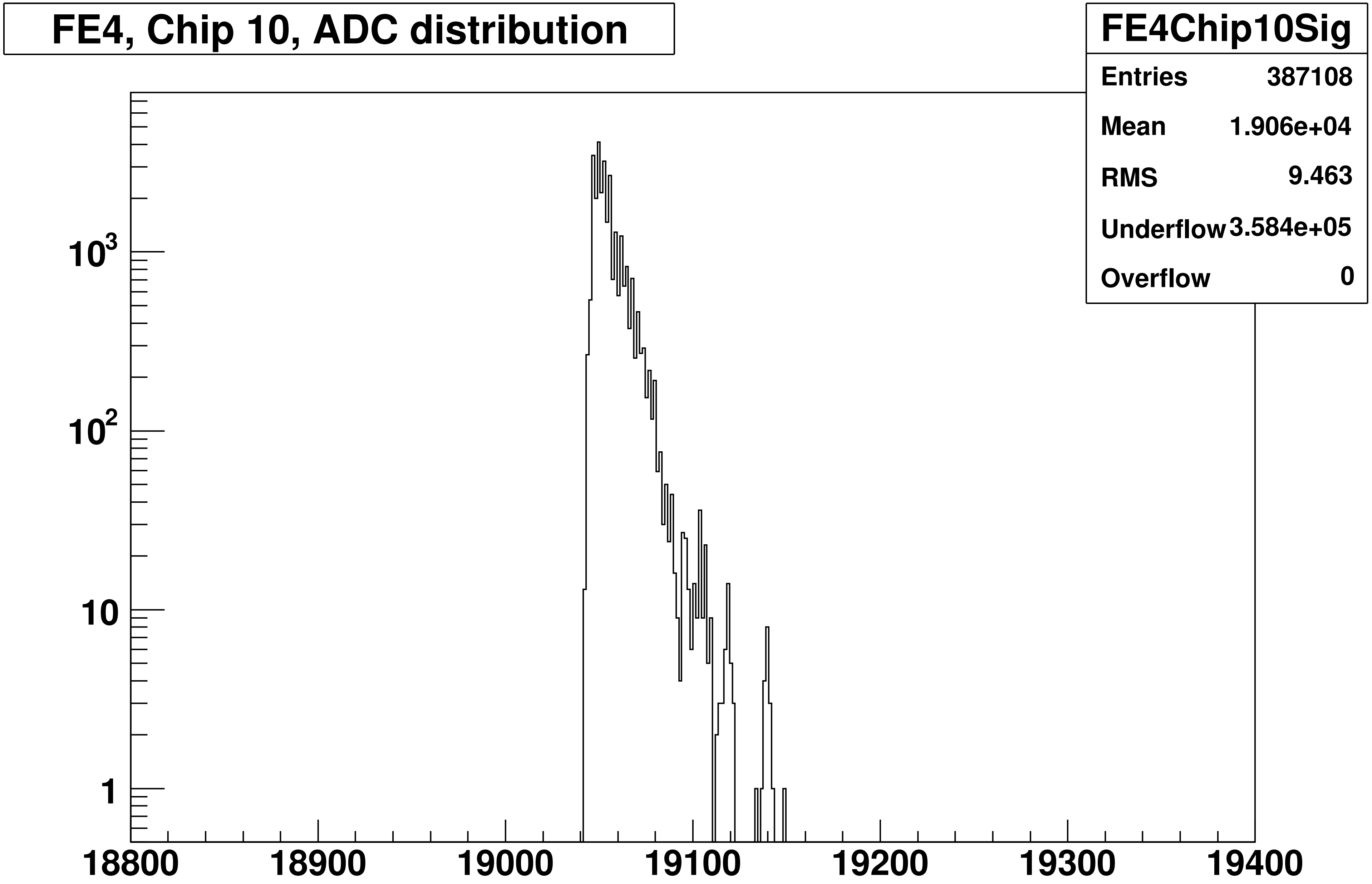


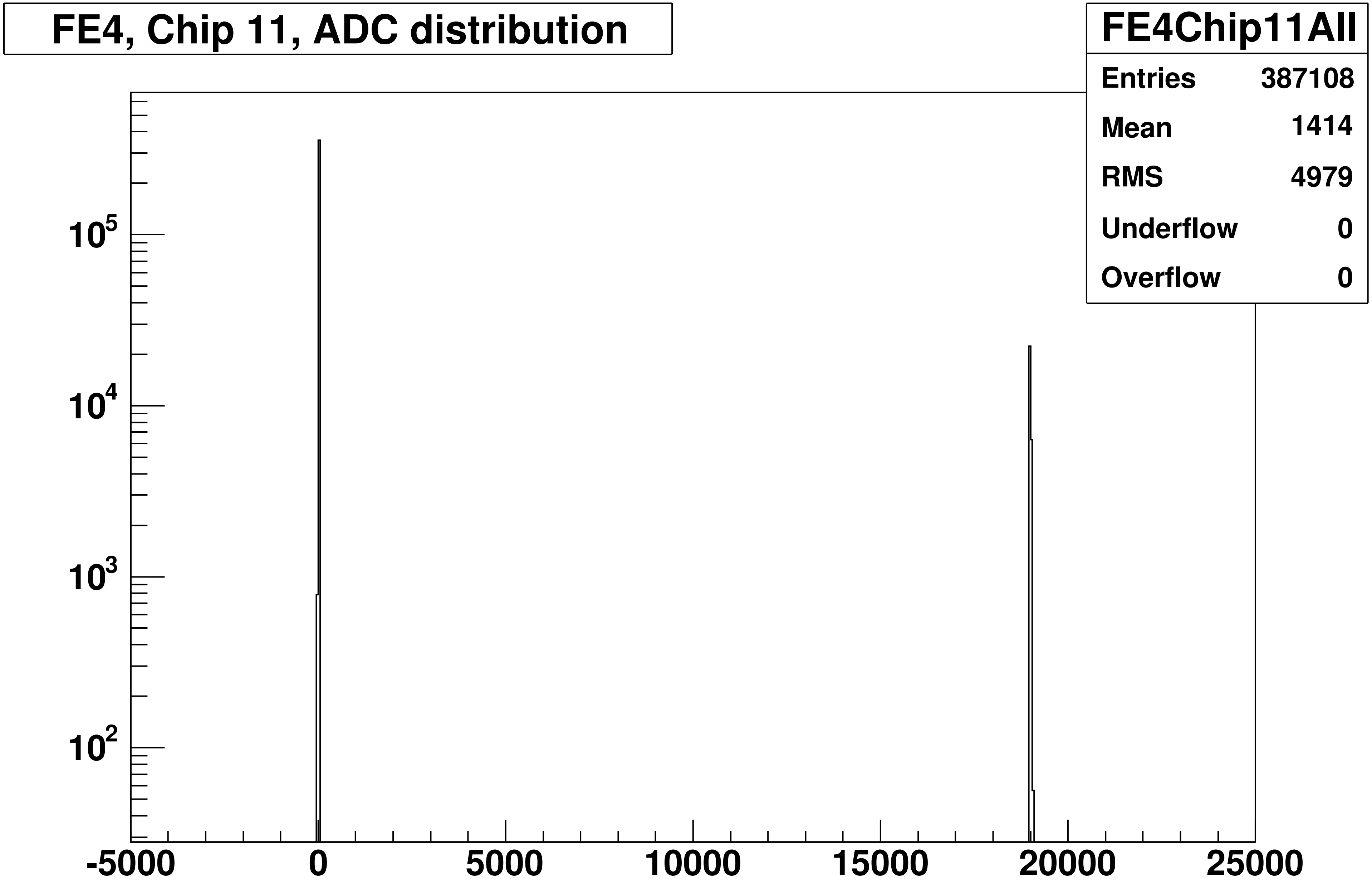
FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 387108
Mean 1.905e+04
RMS 9.385
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



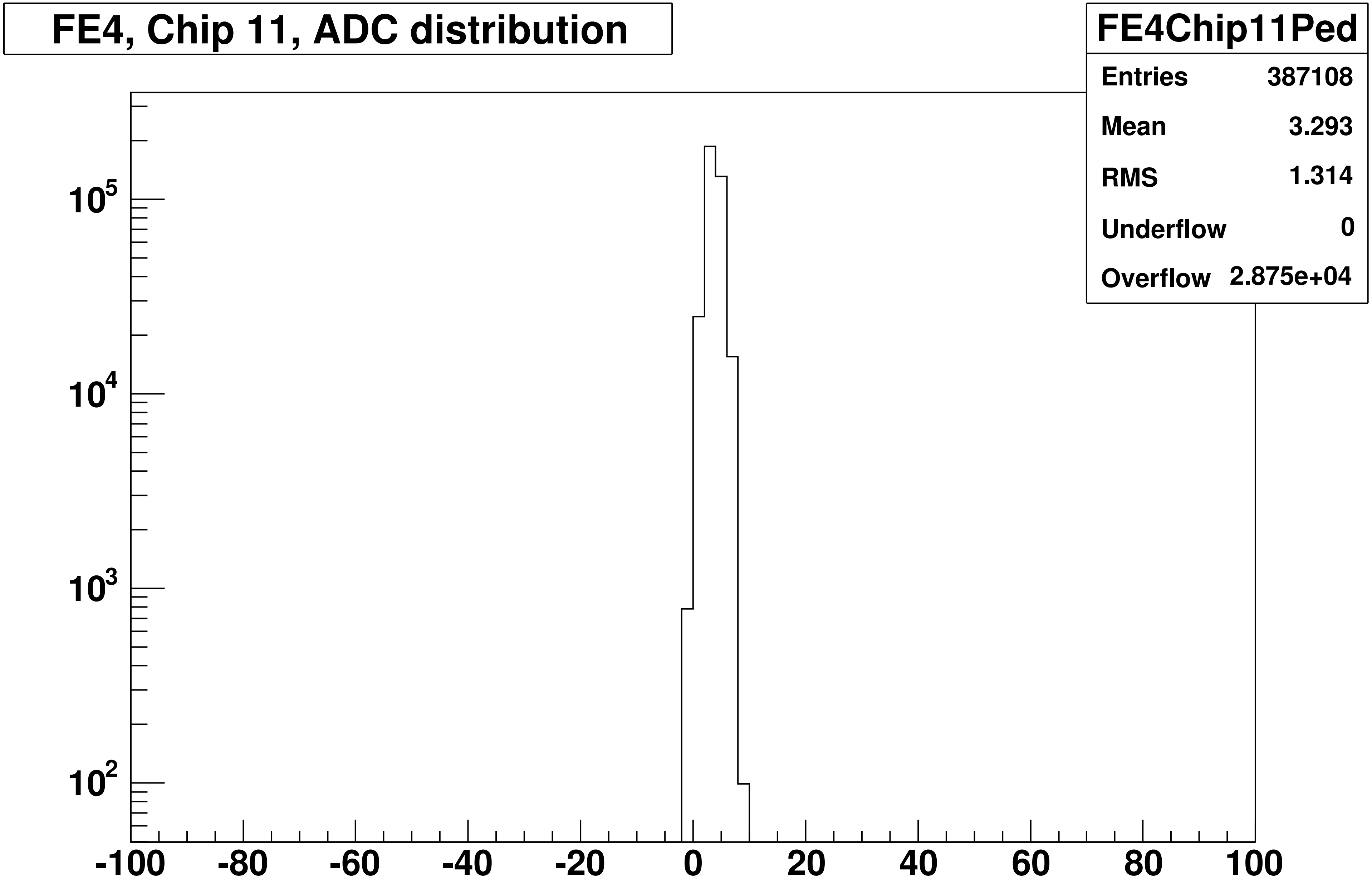
FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 387108
Mean 1399
RMS 5001
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 387108
Mean -17.31
RMS 1.318
Underflow 0
Overflow 2.875e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

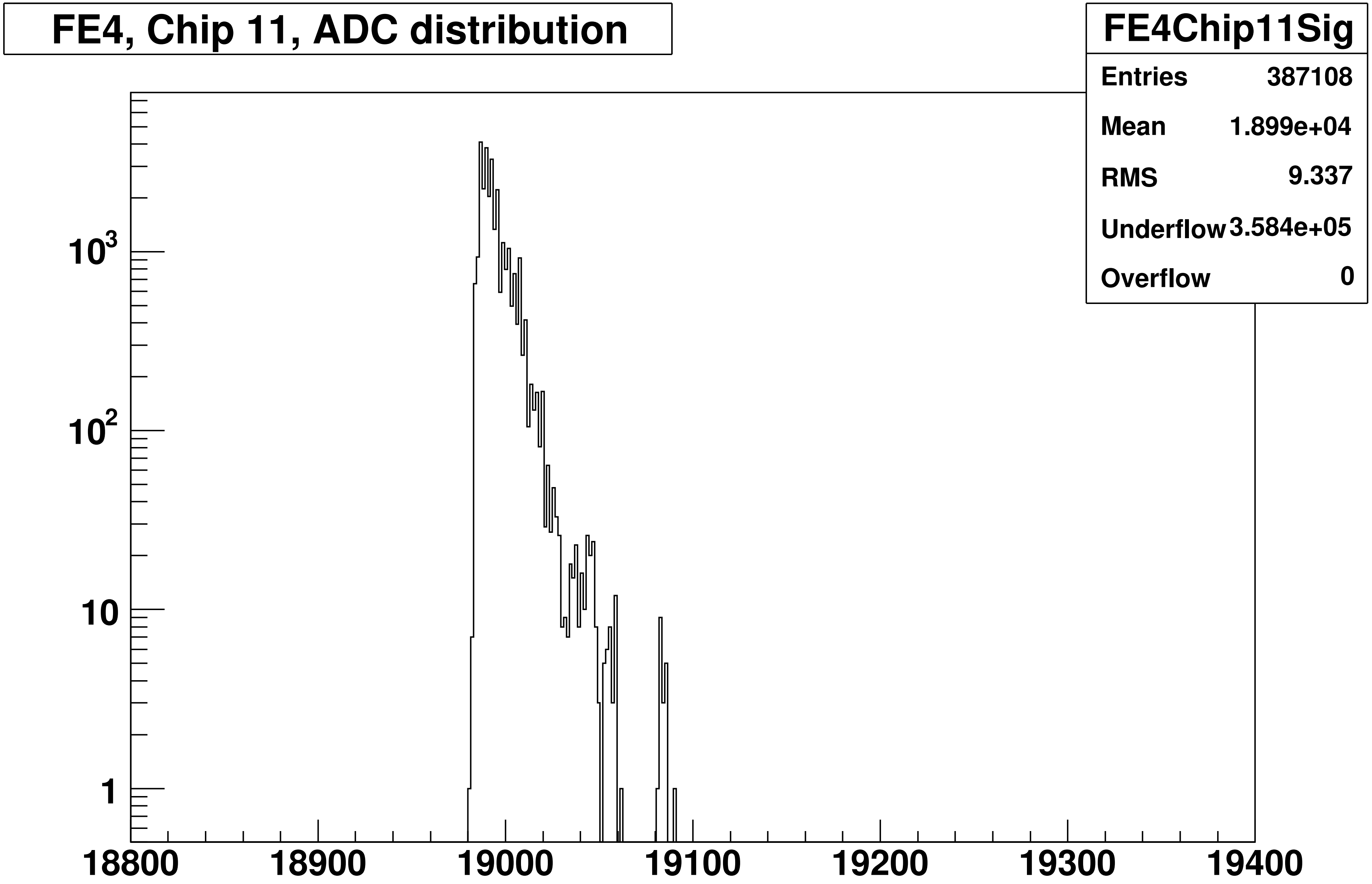

FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 387108
Mean 1.906e+04
RMS 9.463
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



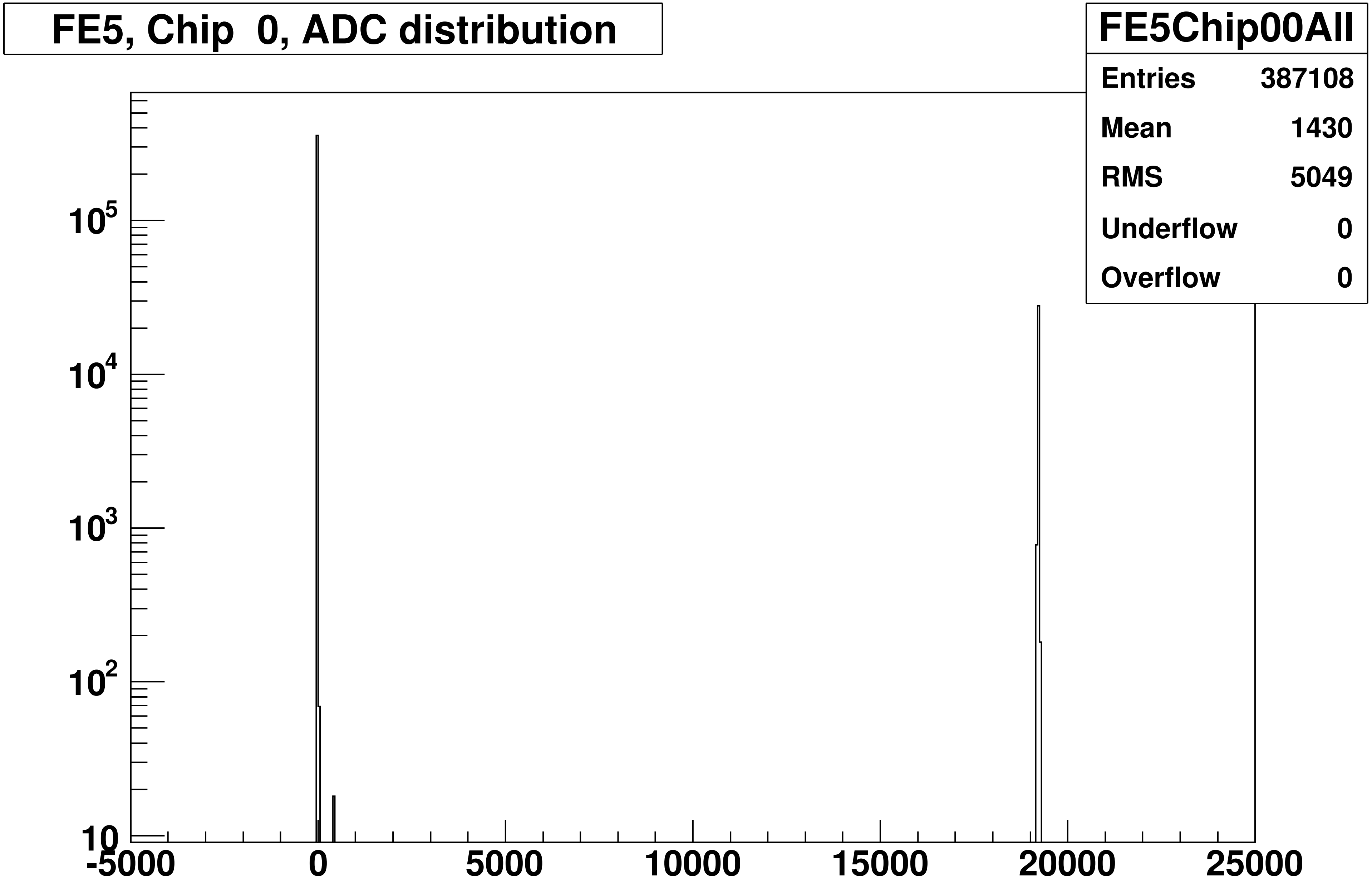
FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 387108
Mean 1414
RMS 4979
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



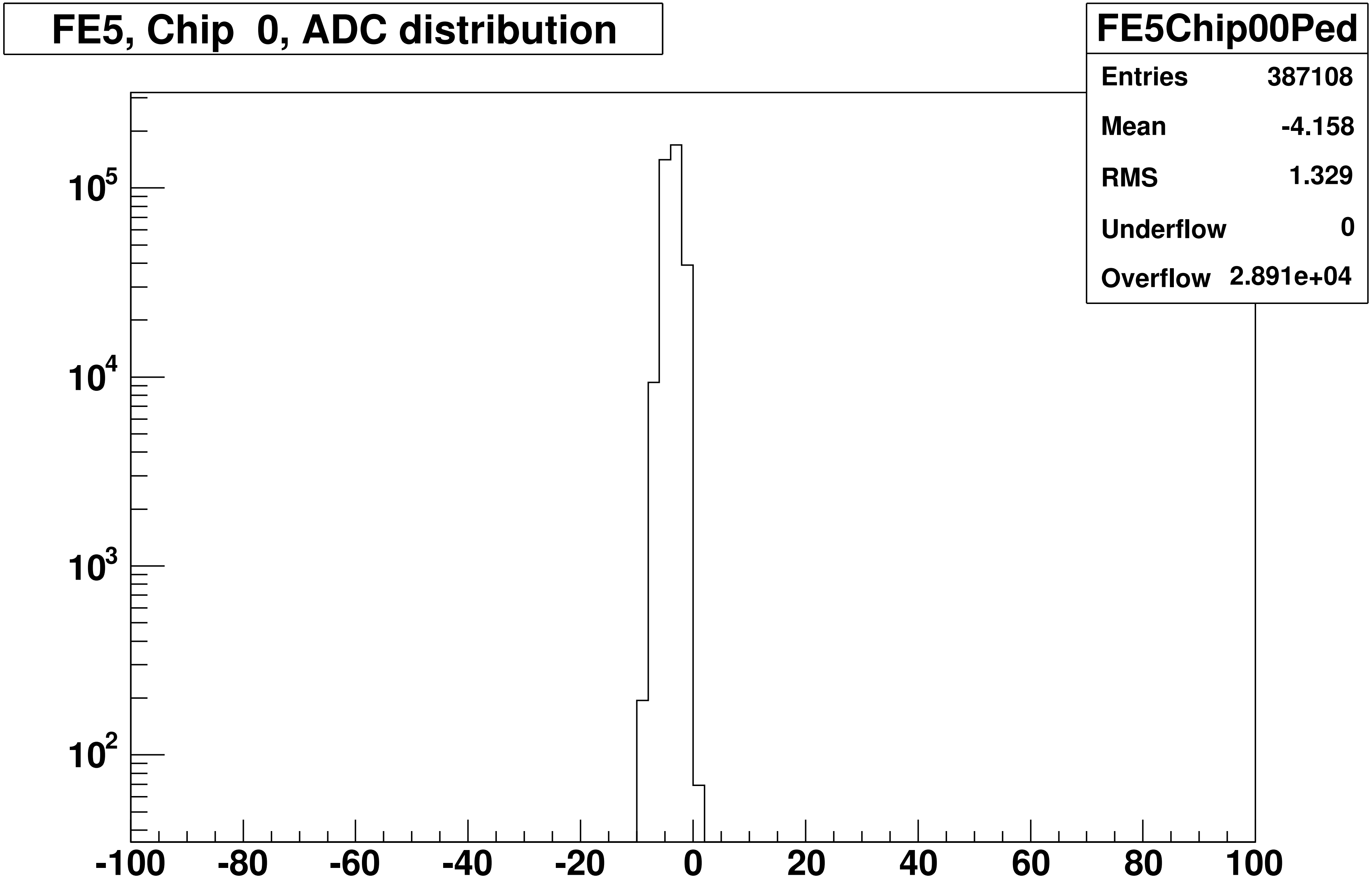
FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 387108
Mean 3.293
RMS 1.314
Underflow 0
Overflow 2.875e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

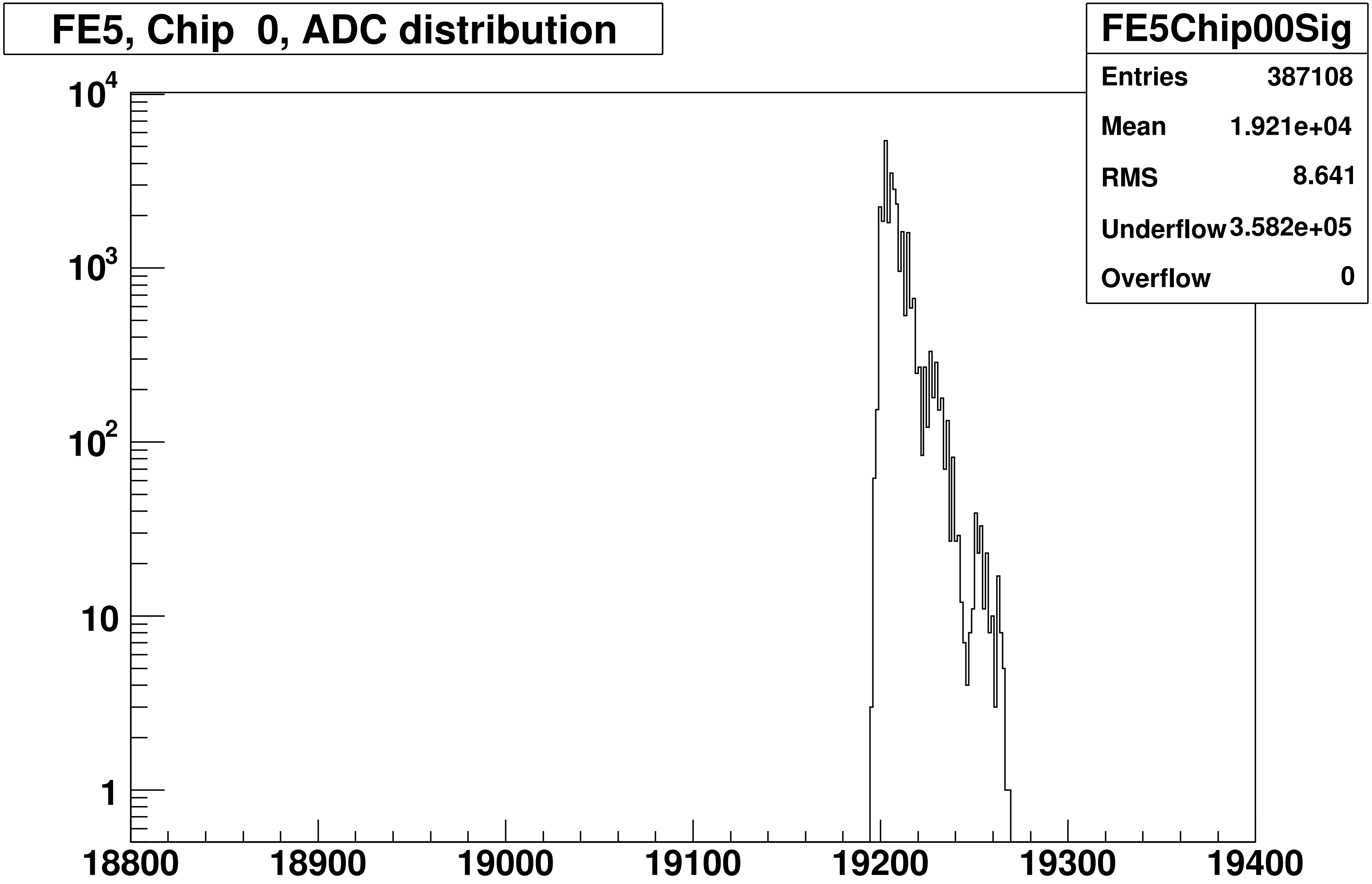


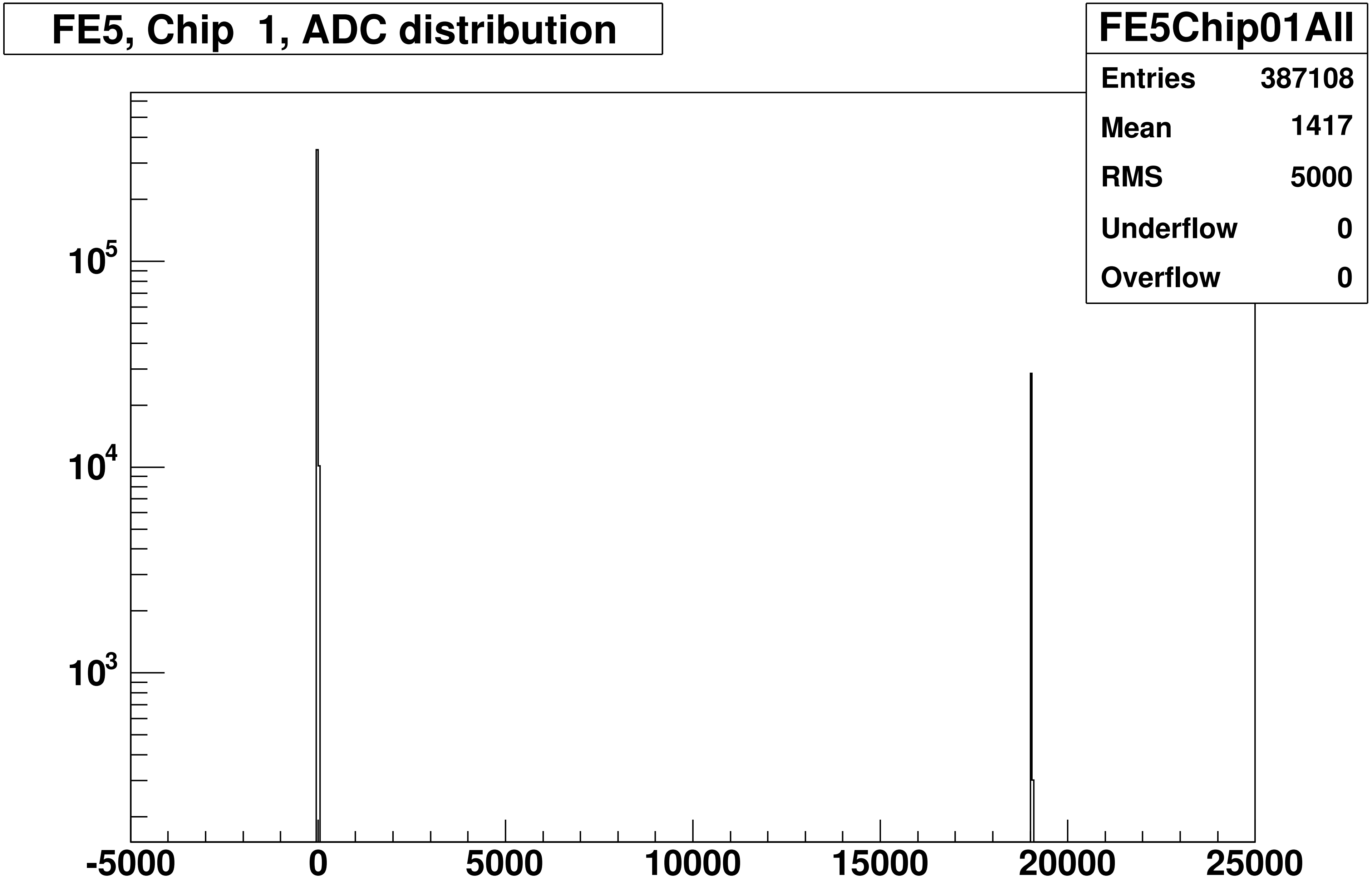
FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 387108
Mean 1.899e+04
RMS 9.337
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



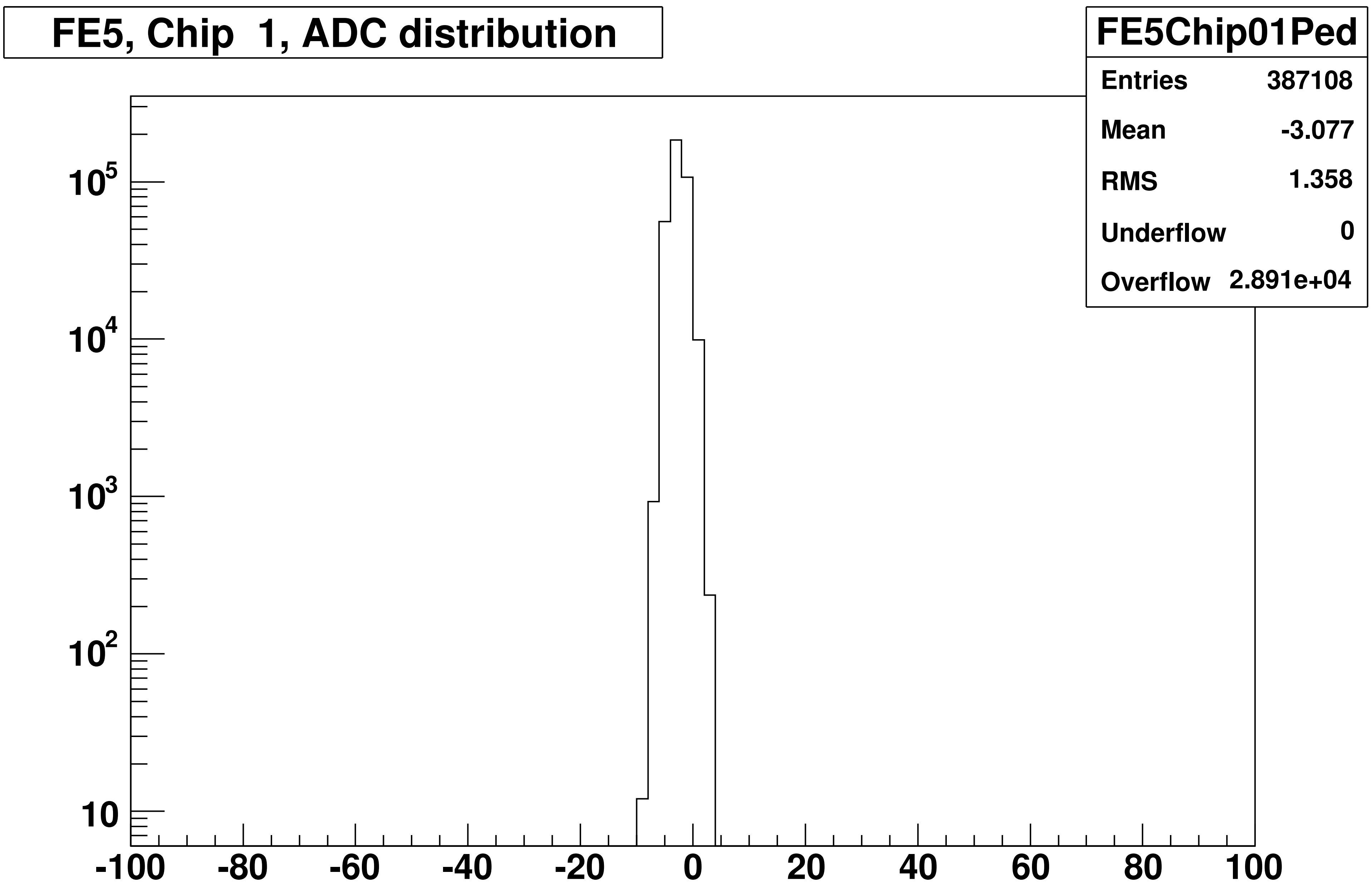
FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 387108
Mean 1430
RMS 5049
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

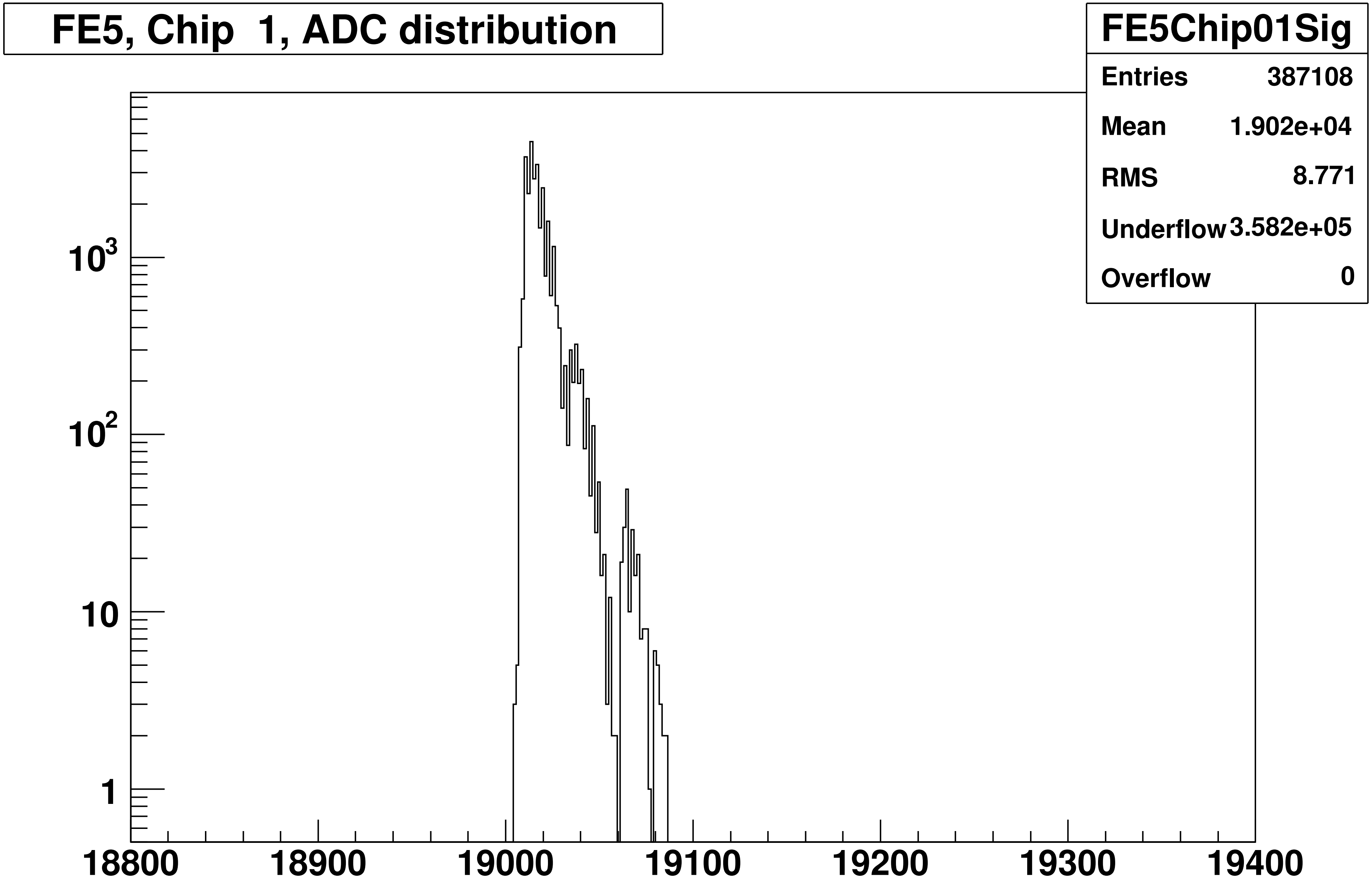

FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 387108
Mean -4.158
RMS 1.329
Underflow 0
Overflow 2.891e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

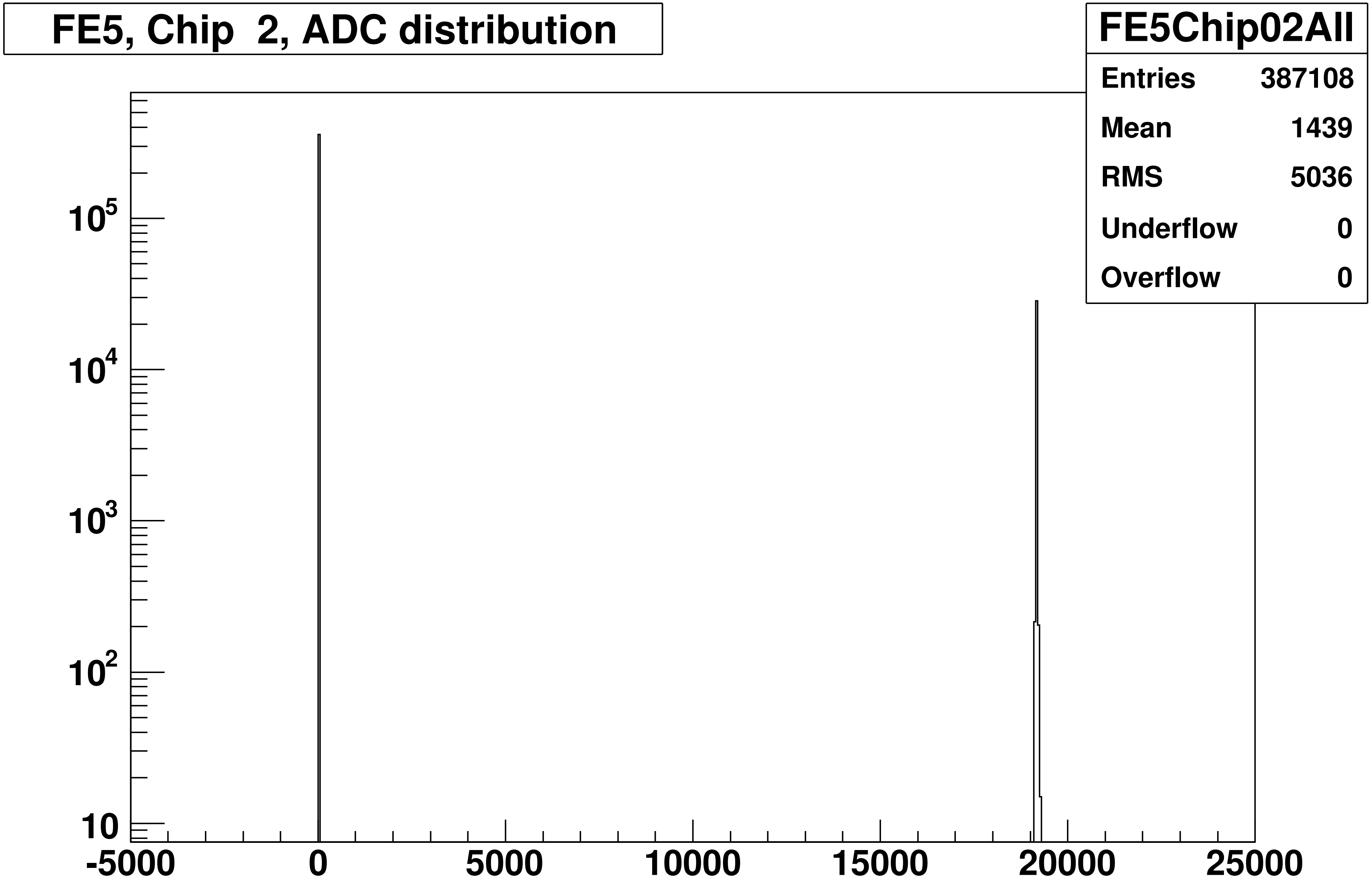

FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 387108
Mean 1.921e+04
RMS 8.641
Underflow 3.582e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

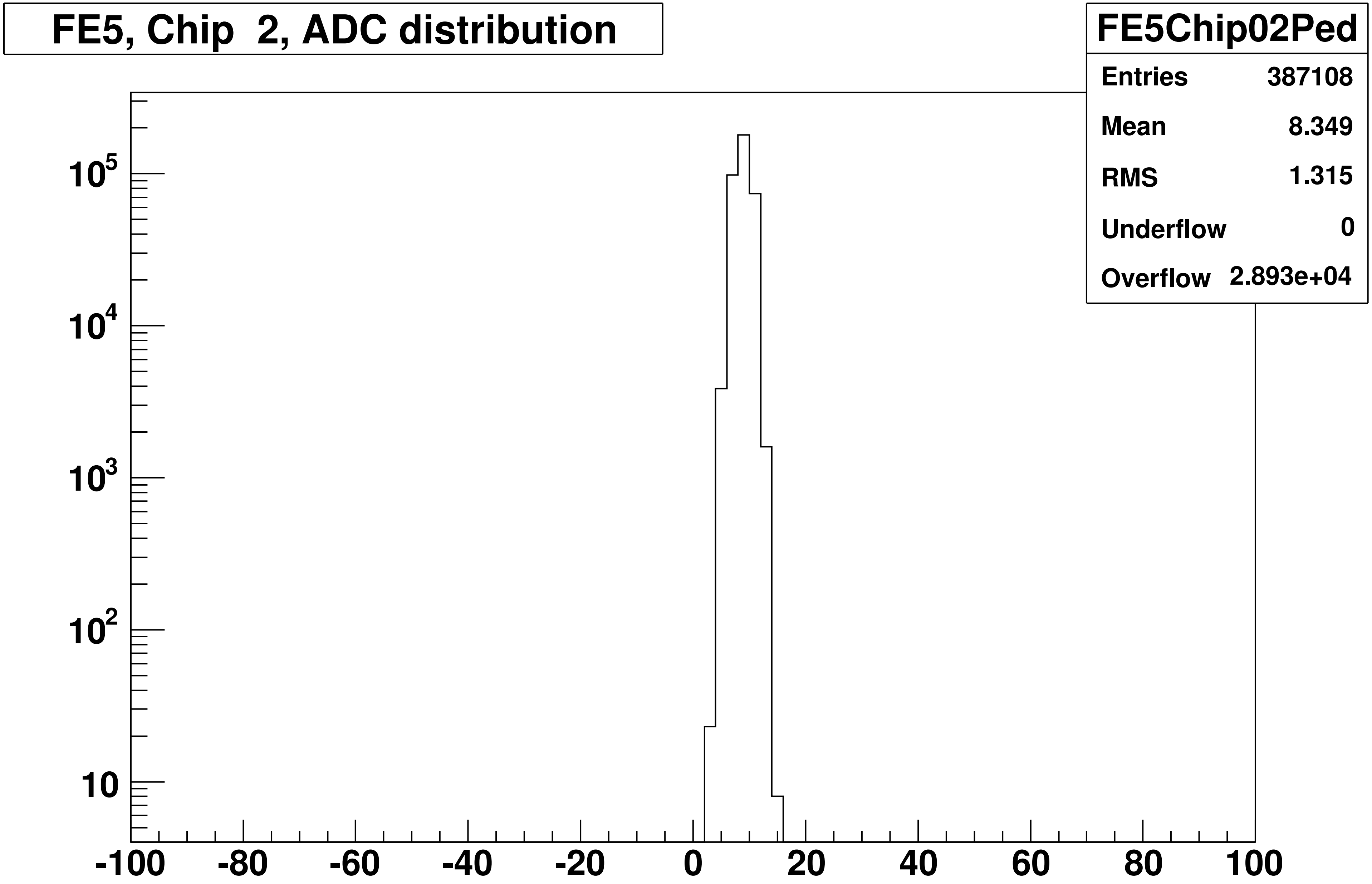


FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 387108
Mean 1417
RMS 5000
Underflow 0
Overflow 0
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

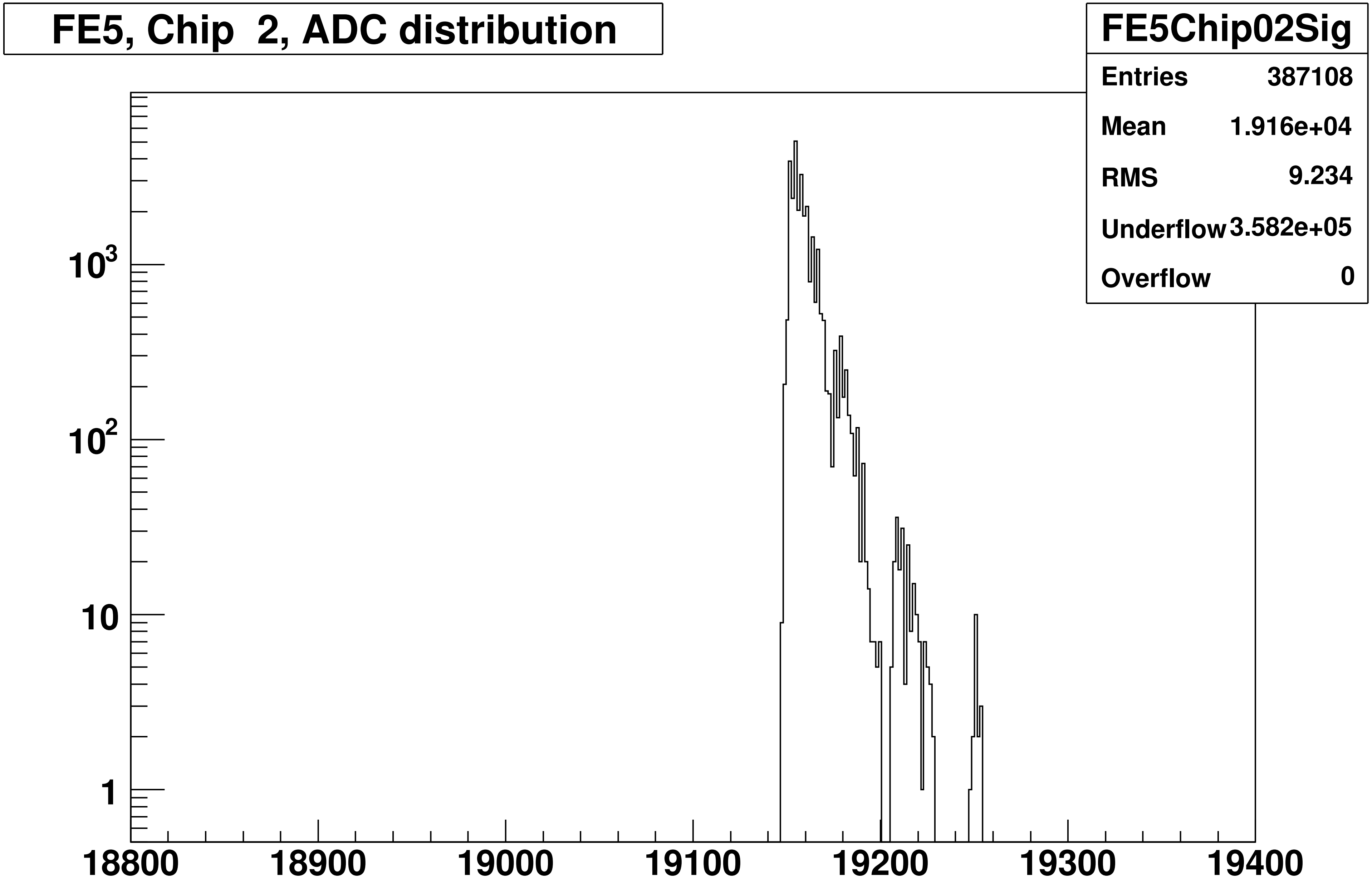

FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 387108
Mean -3.077
RMS 1.358
Underflow 0
Overflow 2.891e+04
10
10^2
10^3
10^4
10^5
-100 -80 -60 -40 -20 0 20 40 60 80 100

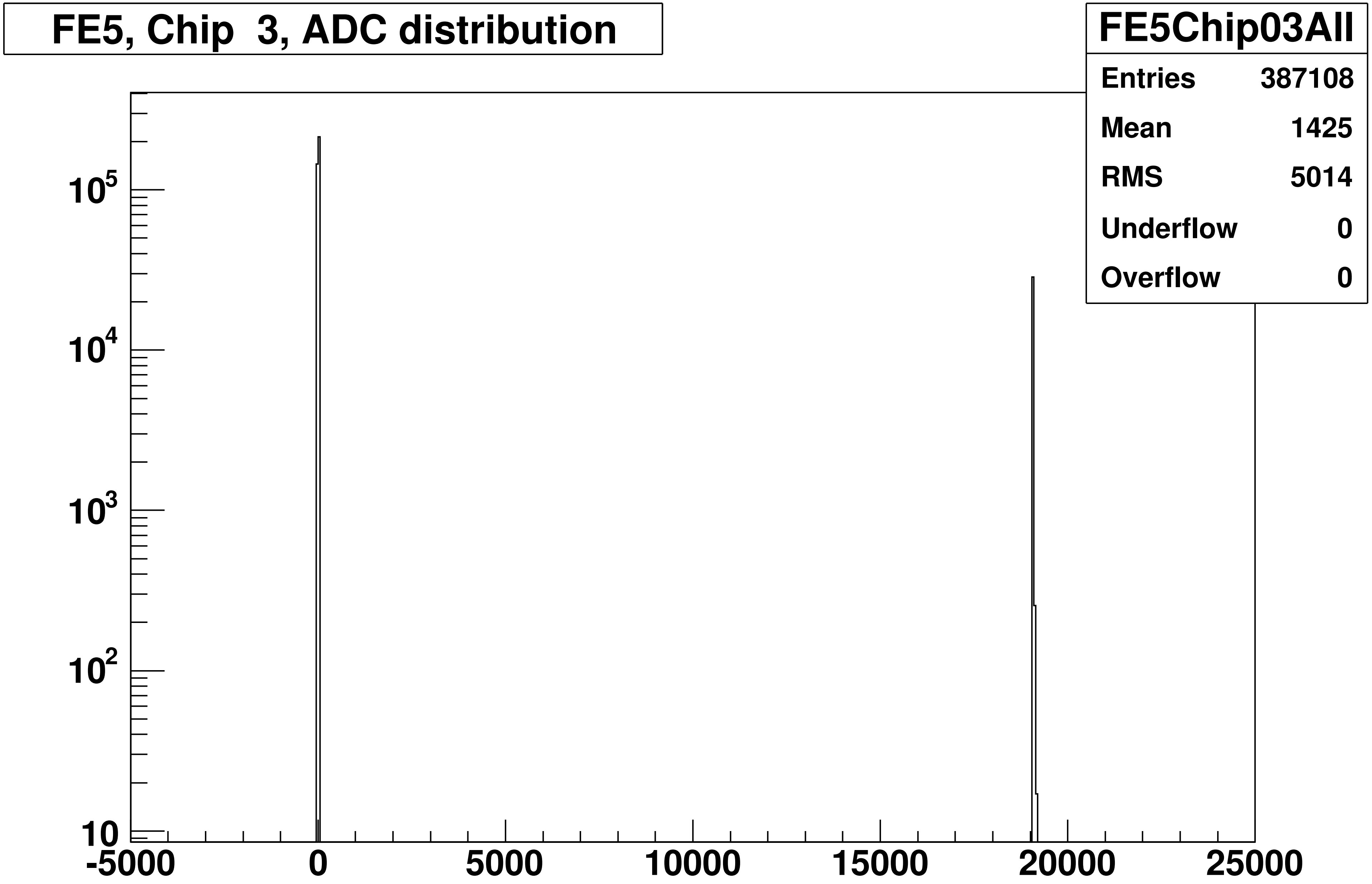

FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 387108
Mean 1.902e+04
RMS 8.771
Underflow 3.582e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

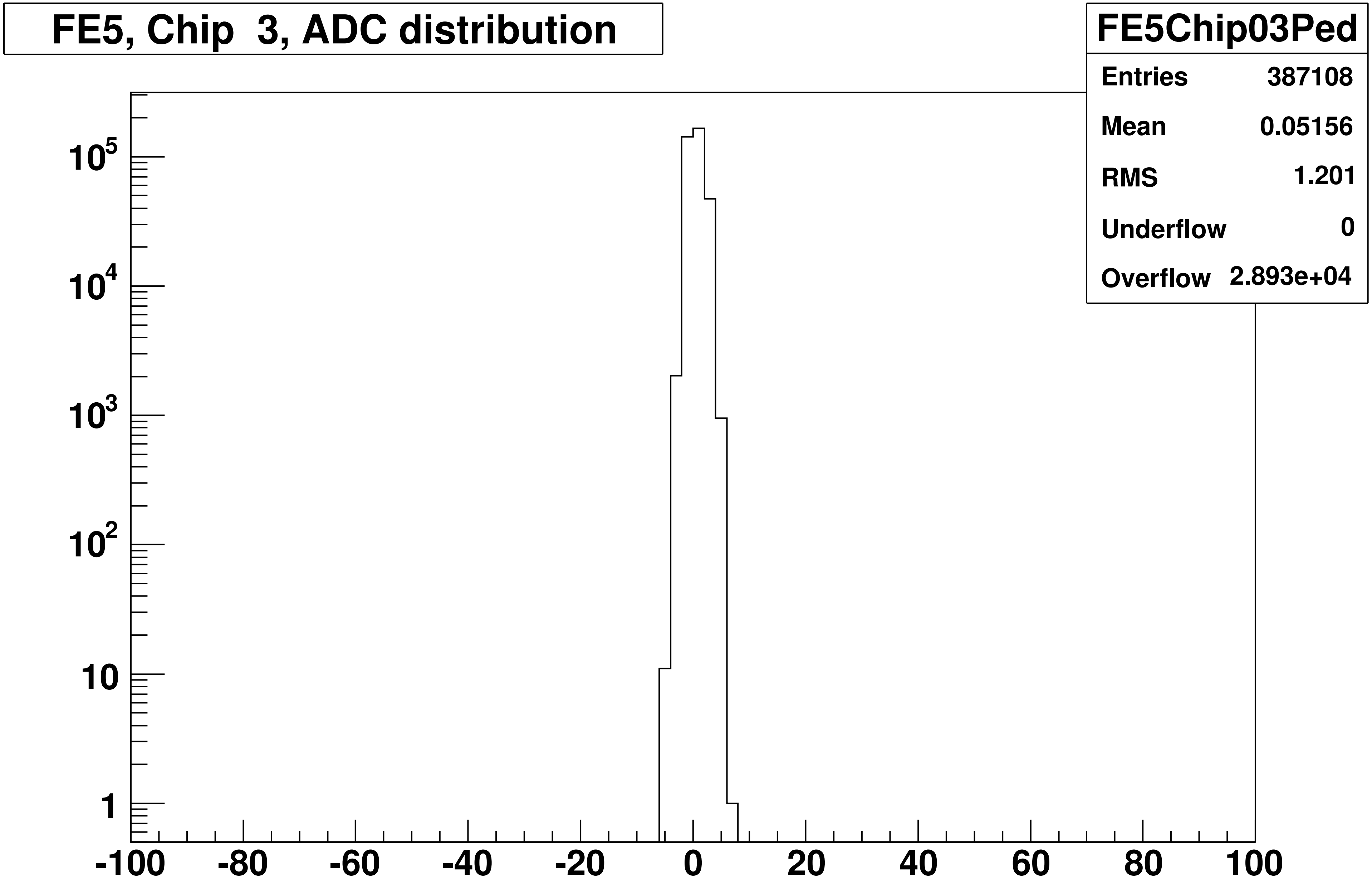

FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 387108
Mean 1439
RMS 5036
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

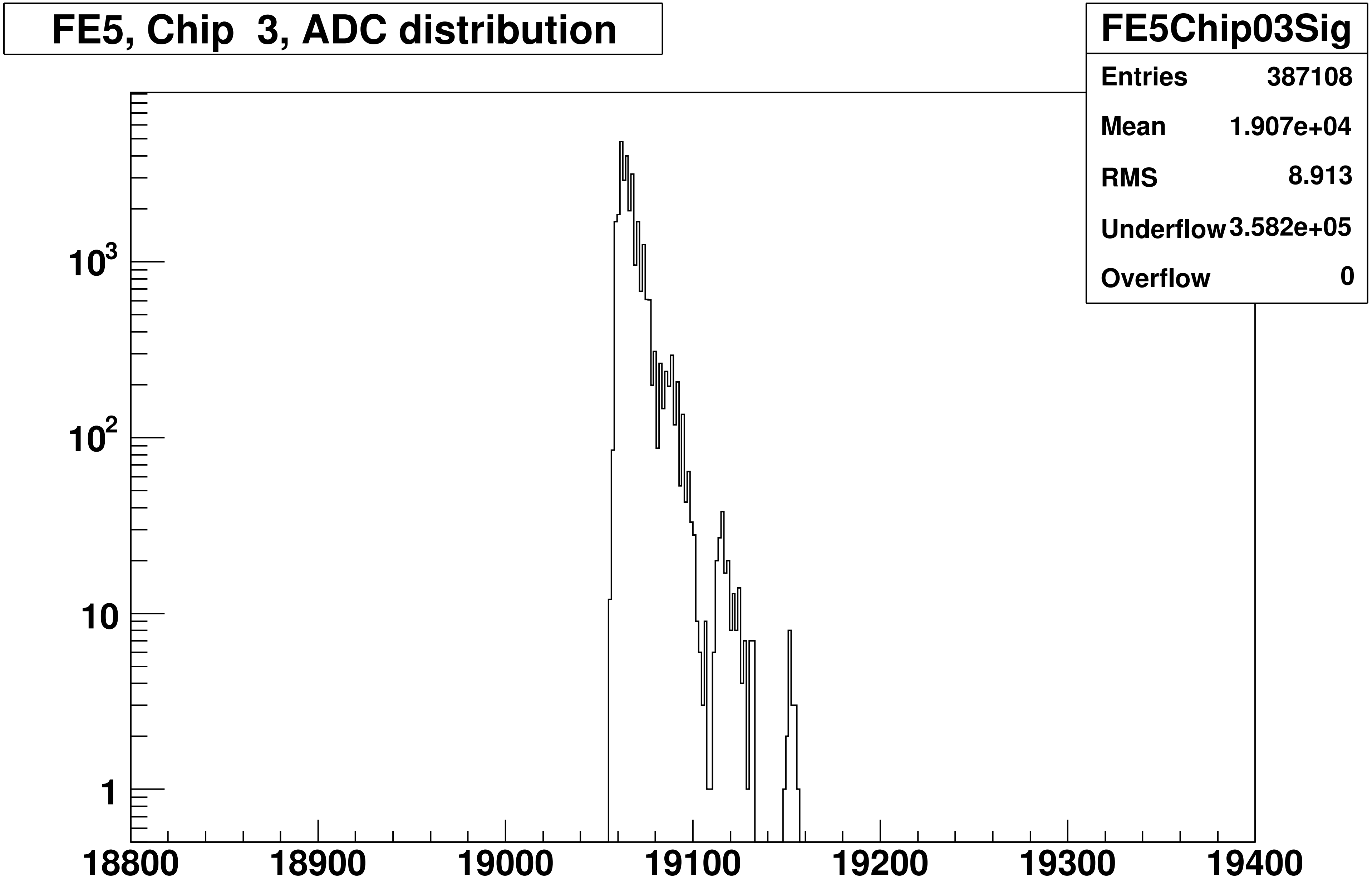


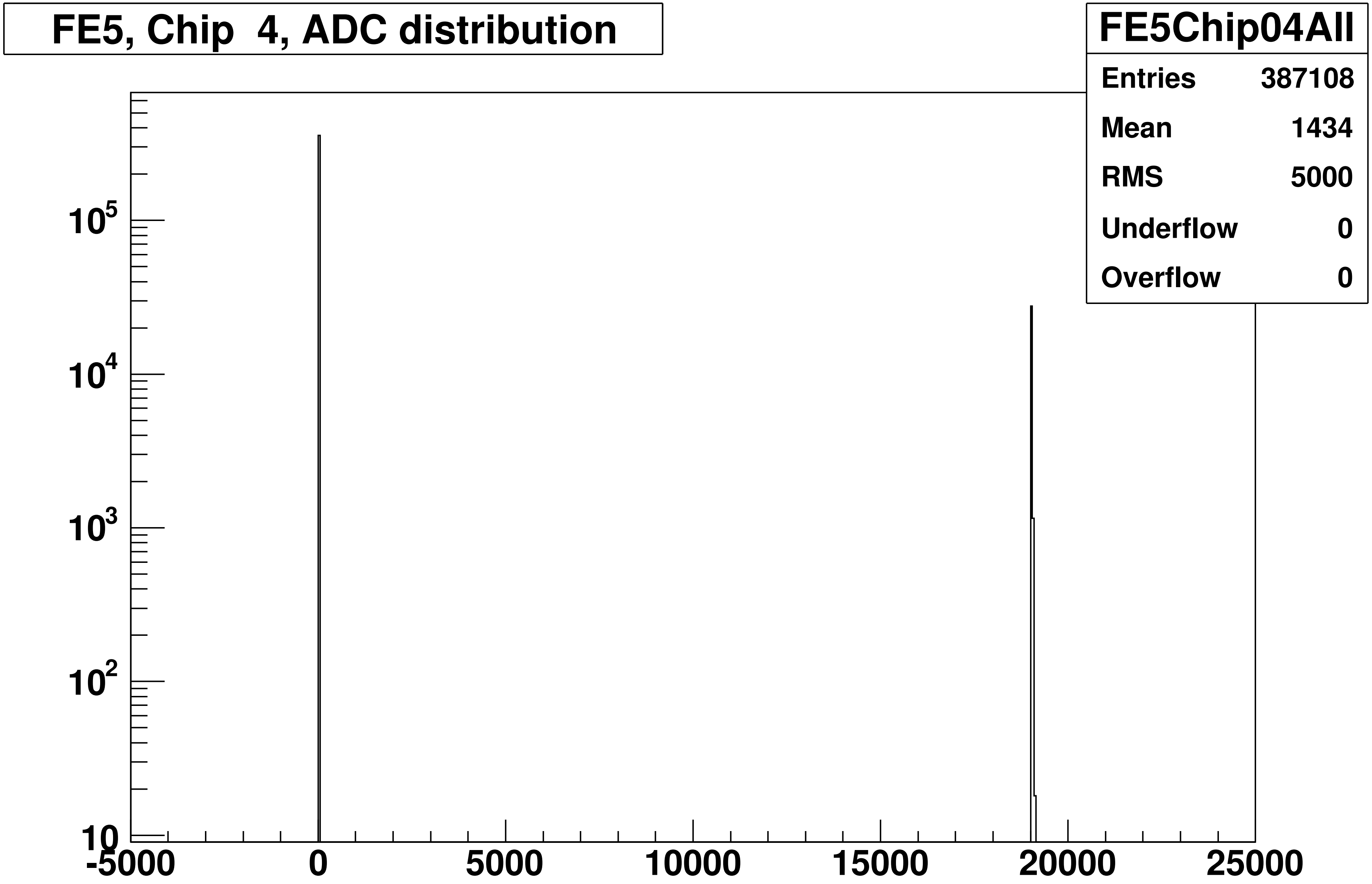
FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 387108
Mean 8.349
RMS 1.315
Underflow 0
Overflow 2.893e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 387108
Mean 1.916e+04
RMS 9.234
Underflow 3.582e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

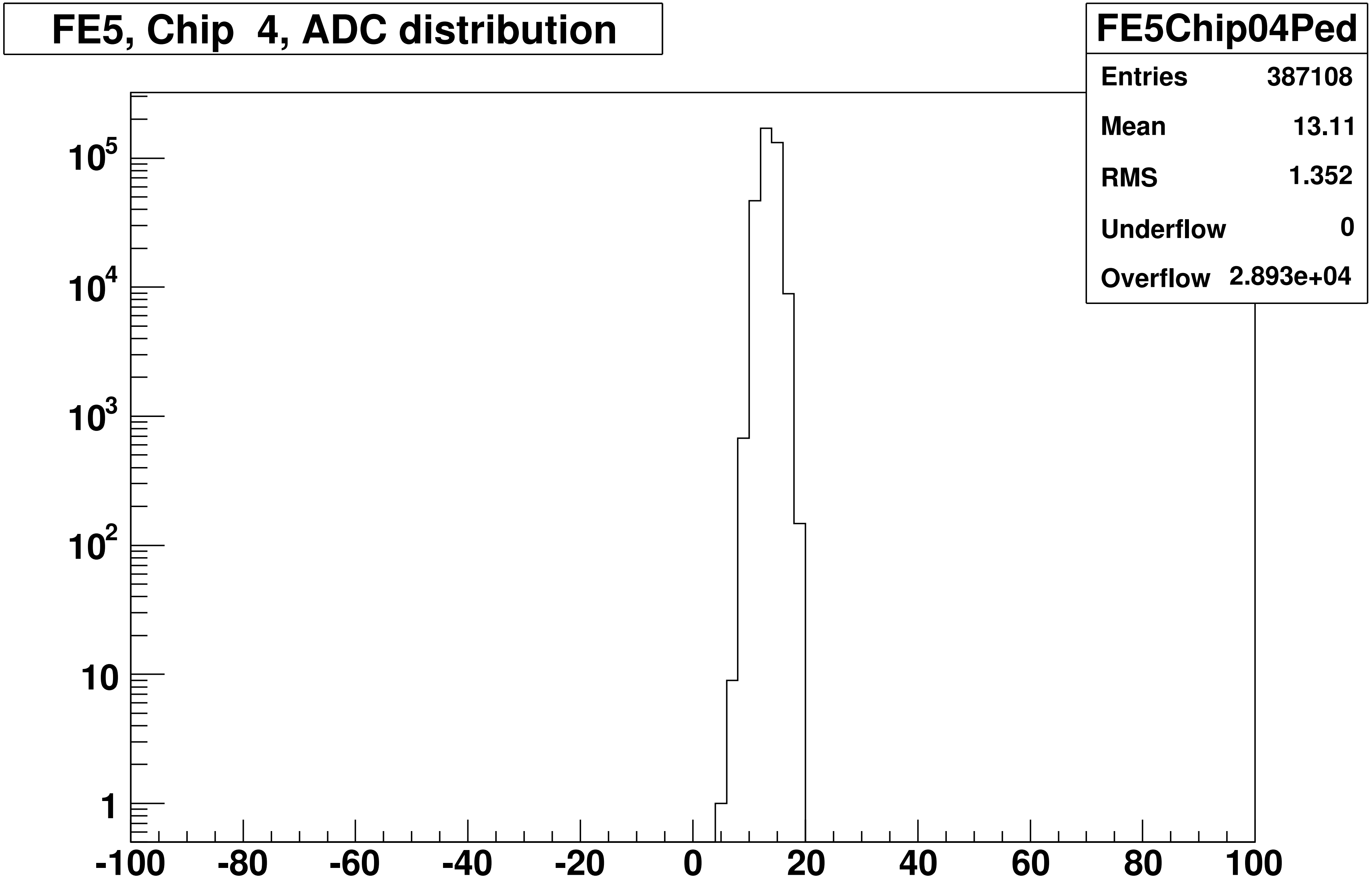

FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 387108
Mean 1425
RMS 5014
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 387108
Mean 0.05156
RMS 1.201
Underflow 0
Overflow 2.893e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

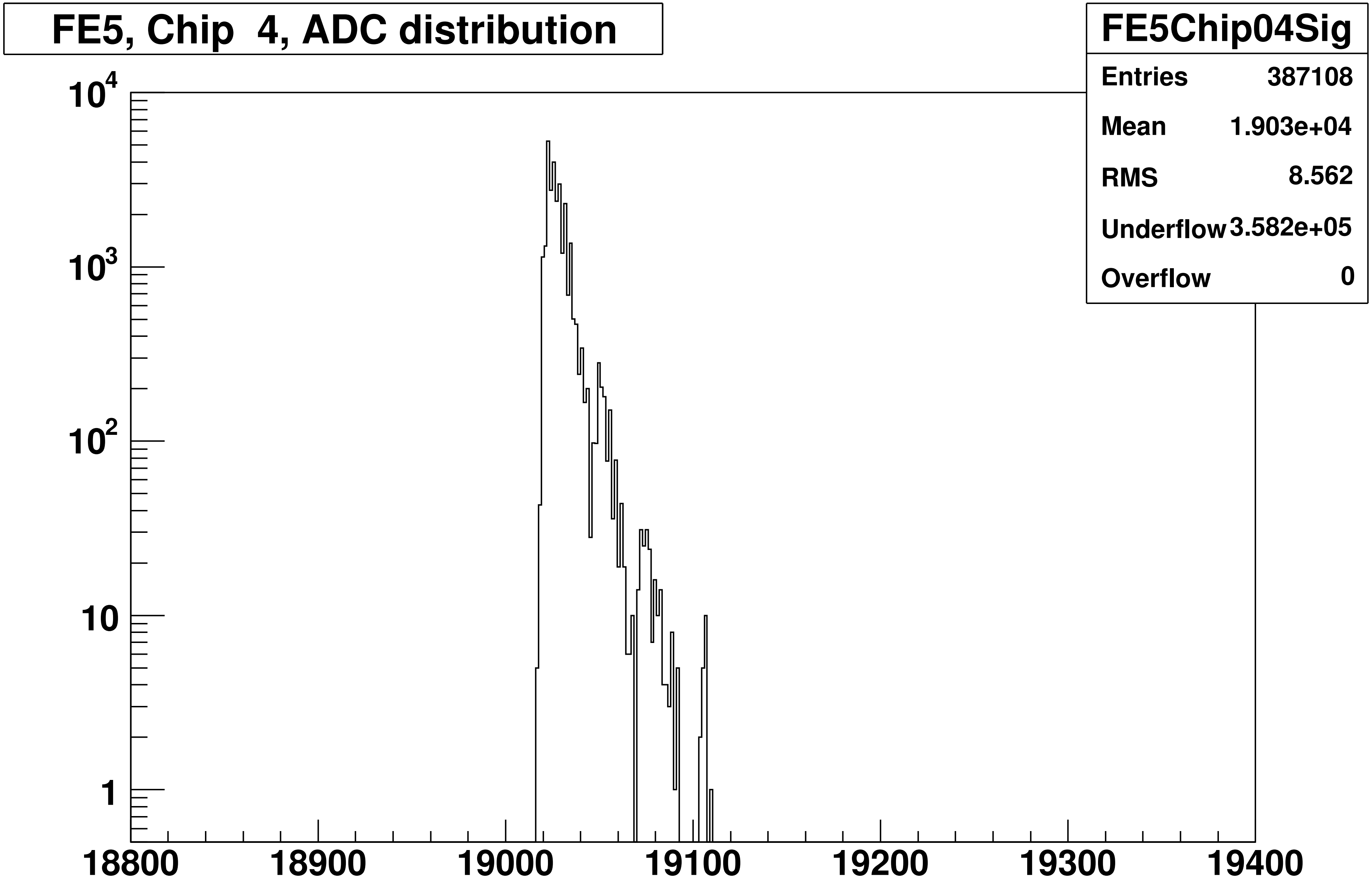

FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 387108
Mean 1.907e+04
RMS 8.913
Underflow 3.582e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



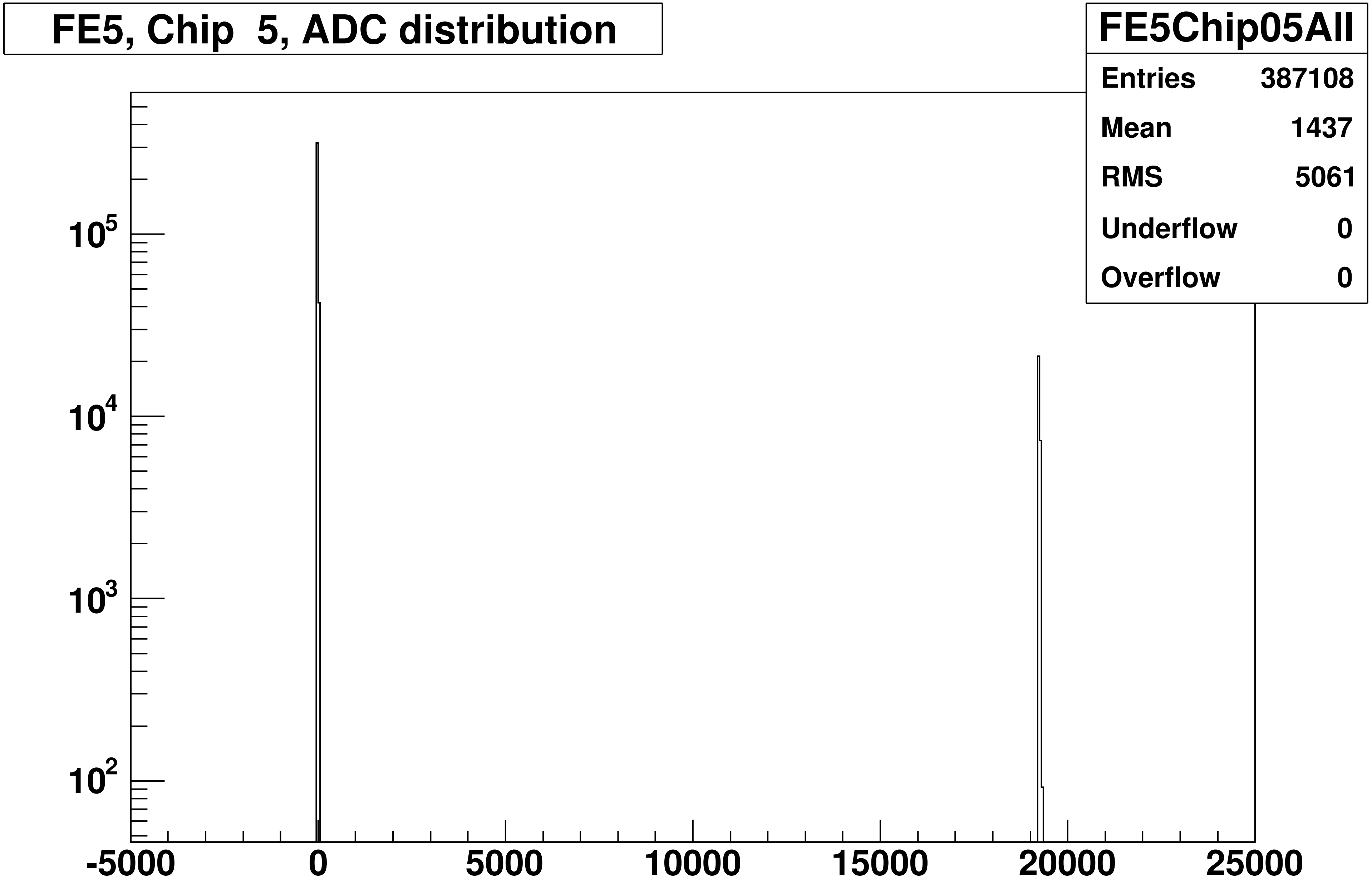
FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 387108
Mean 1434
RMS 5000
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

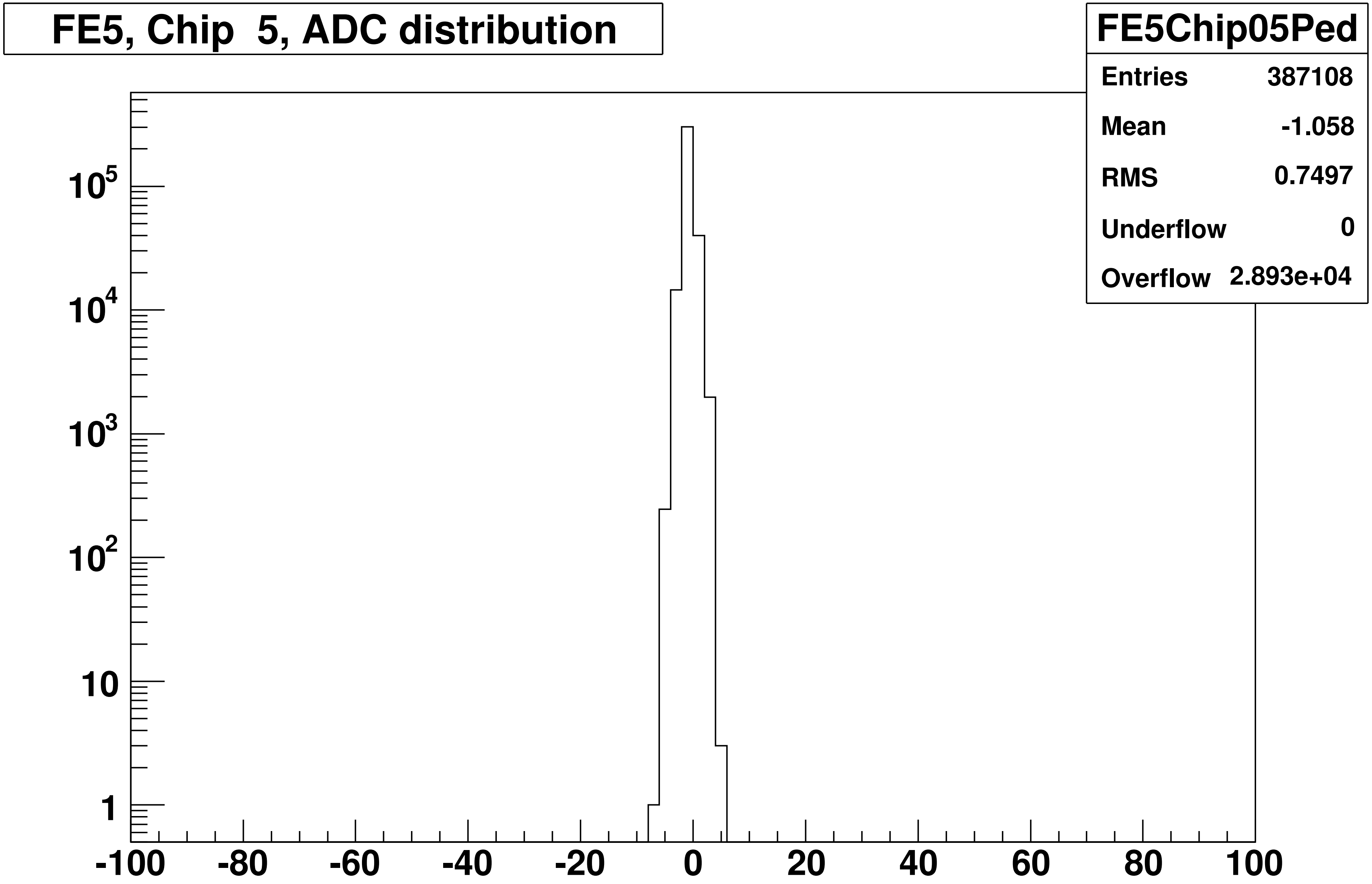


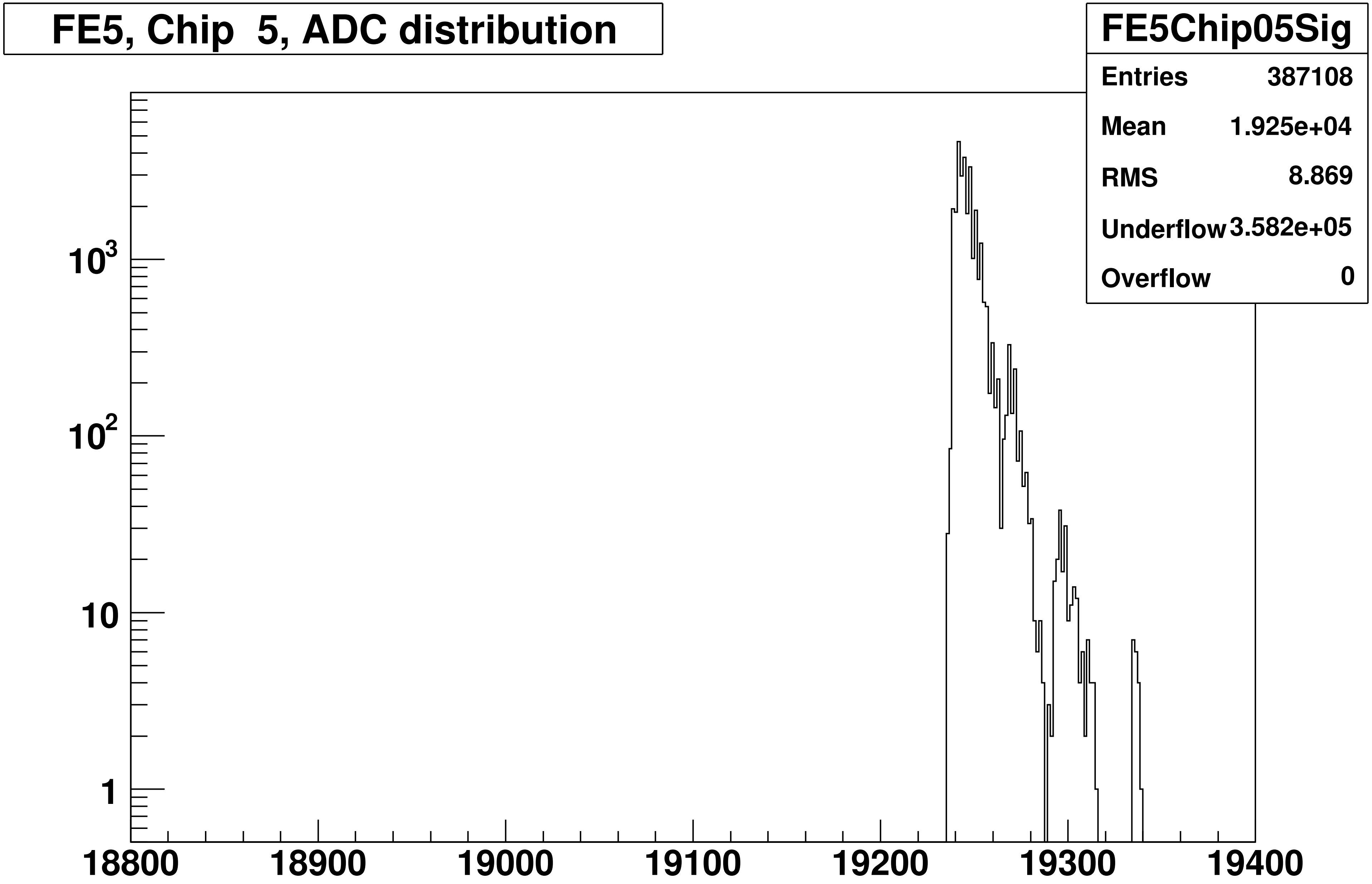
FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 387108
Mean 13.11
RMS 1.352
Underflow 0
Overflow 2.893e+04
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



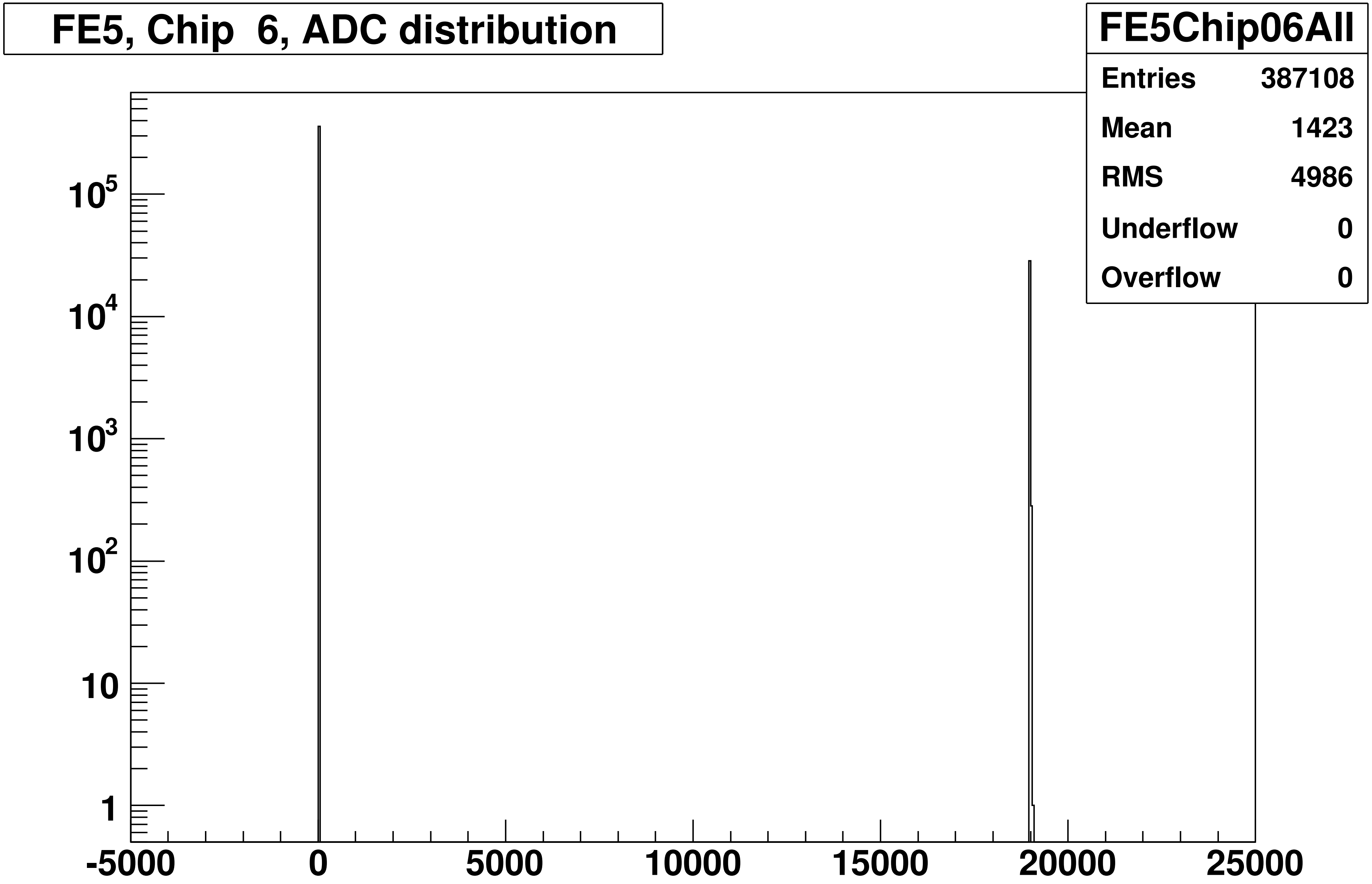
FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 387108
Mean 1.903e+04
RMS 8.562
Underflow 3.582e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

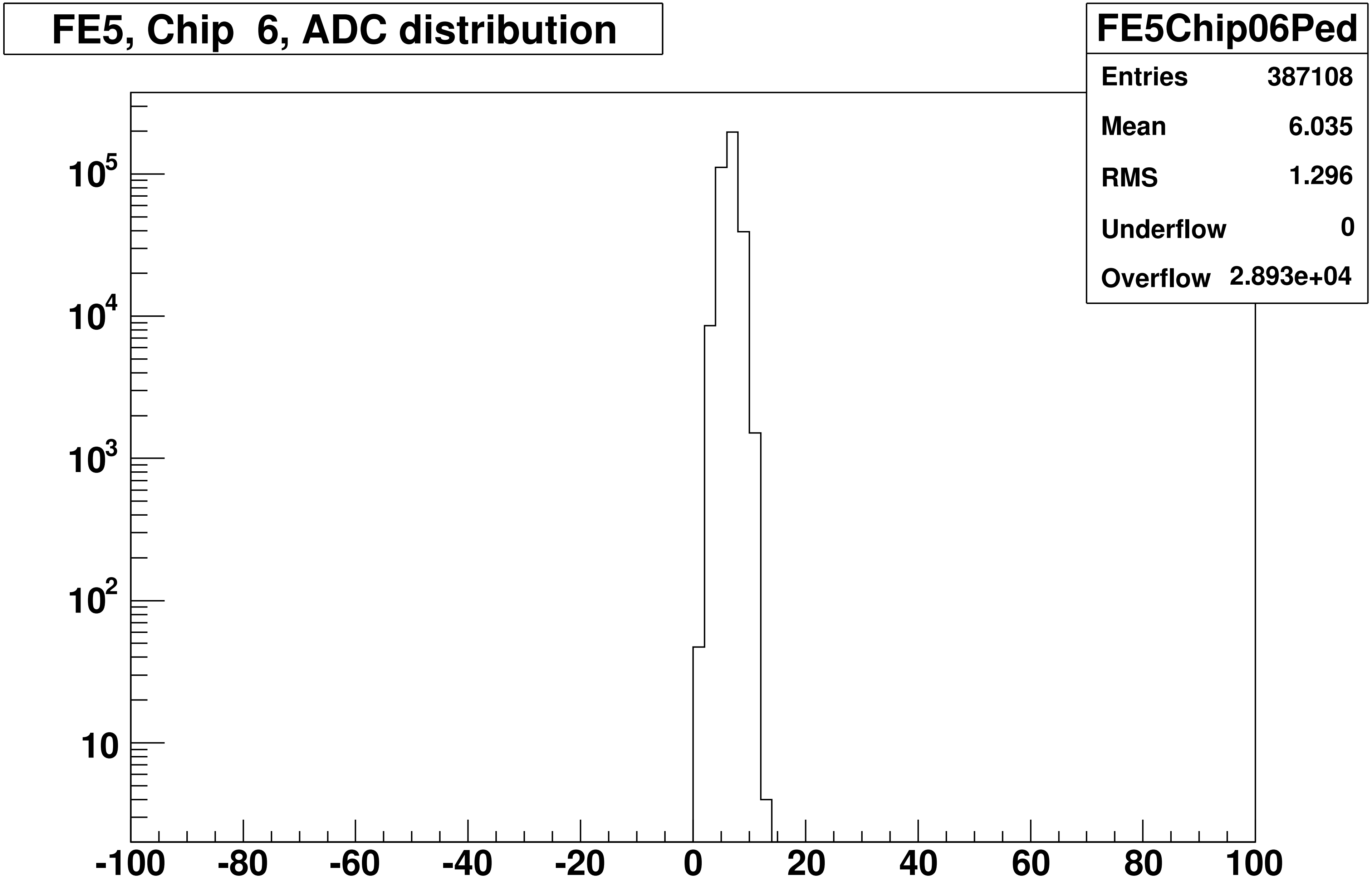

FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 387108
Mean 1437
RMS 5061
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

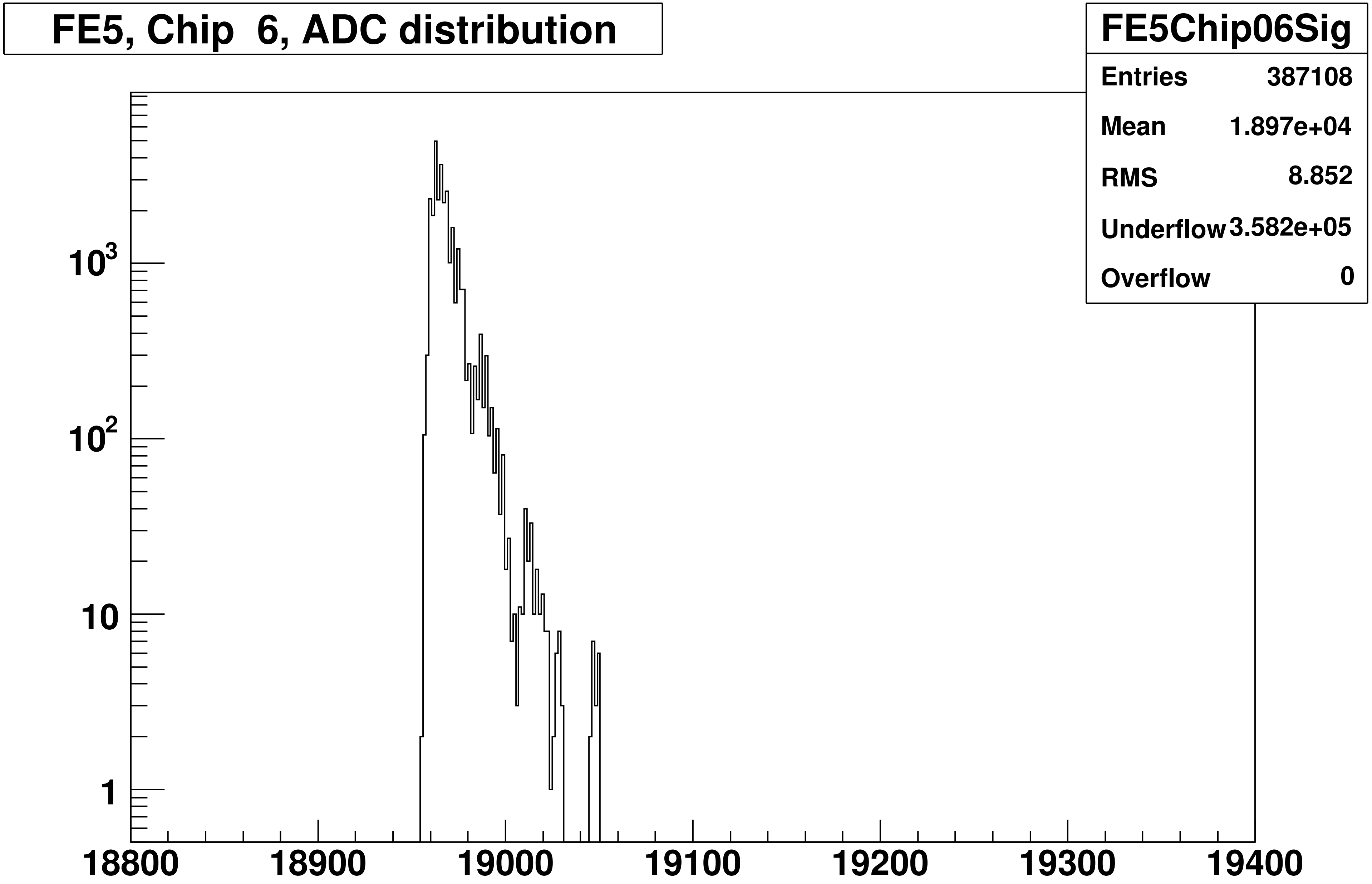

FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 387108
Mean -1.058
RMS 0.7497
Underflow 0
Overflow 2.893e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



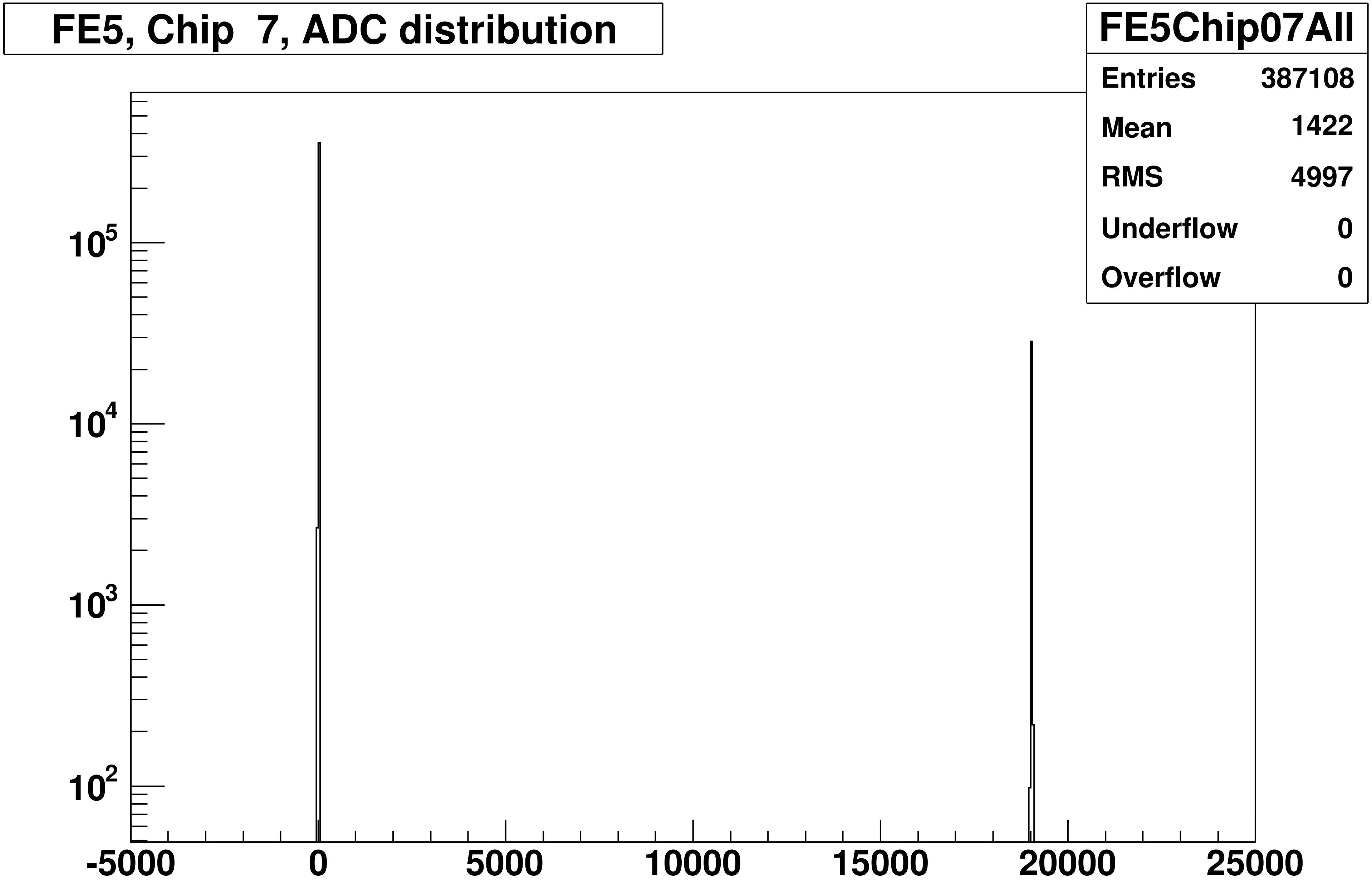
FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 387108
Mean 1.925e+04
RMS 8.869
Underflow 3.582e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 387108
Mean 1423
RMS 4986
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

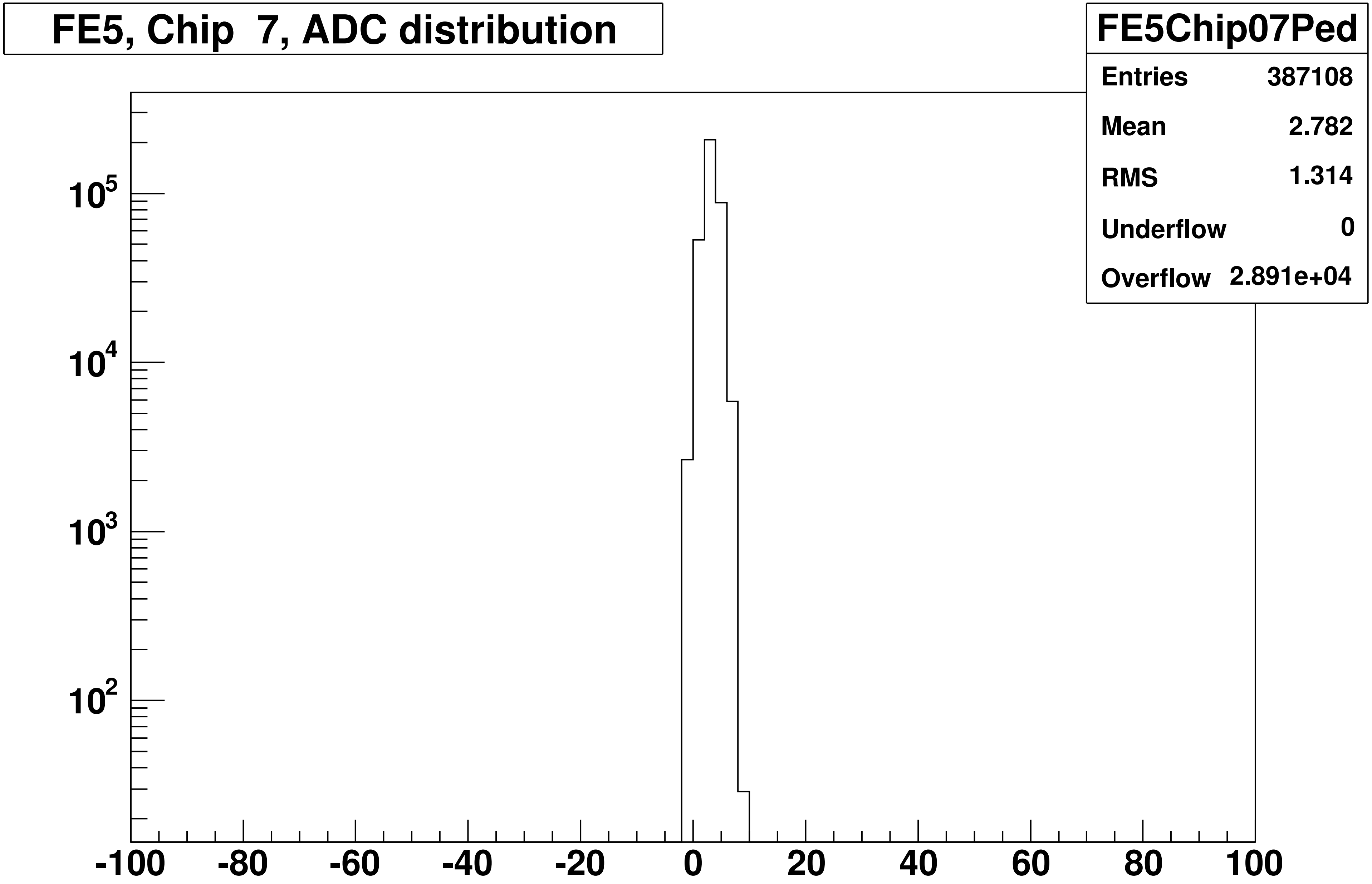

FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 387108
Mean 6.035
RMS 1.296
Underflow 0
Overflow 2.893e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



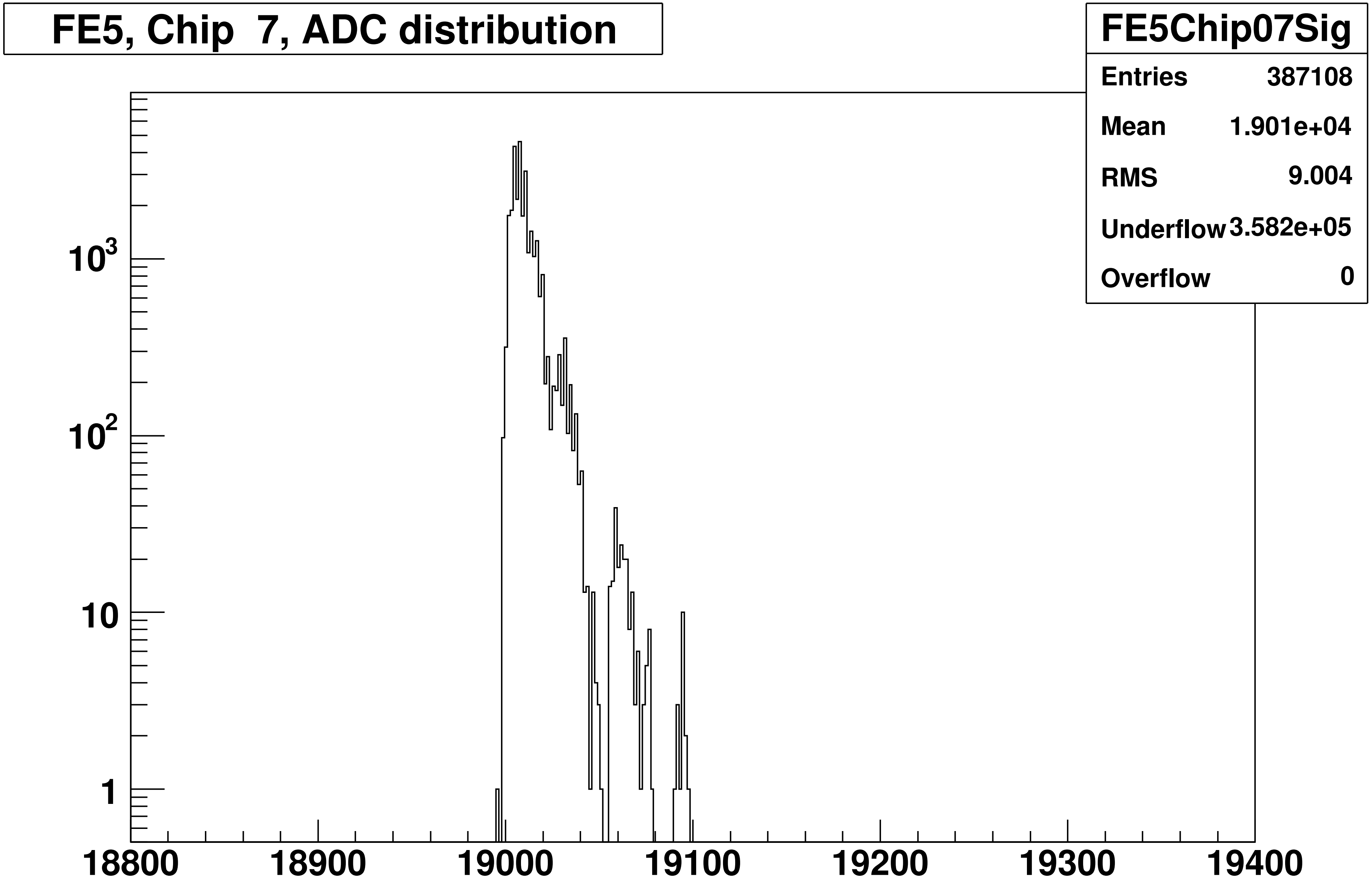
FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 387108
Mean 1.897e+04
RMS 8.852
Underflow 3.582e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



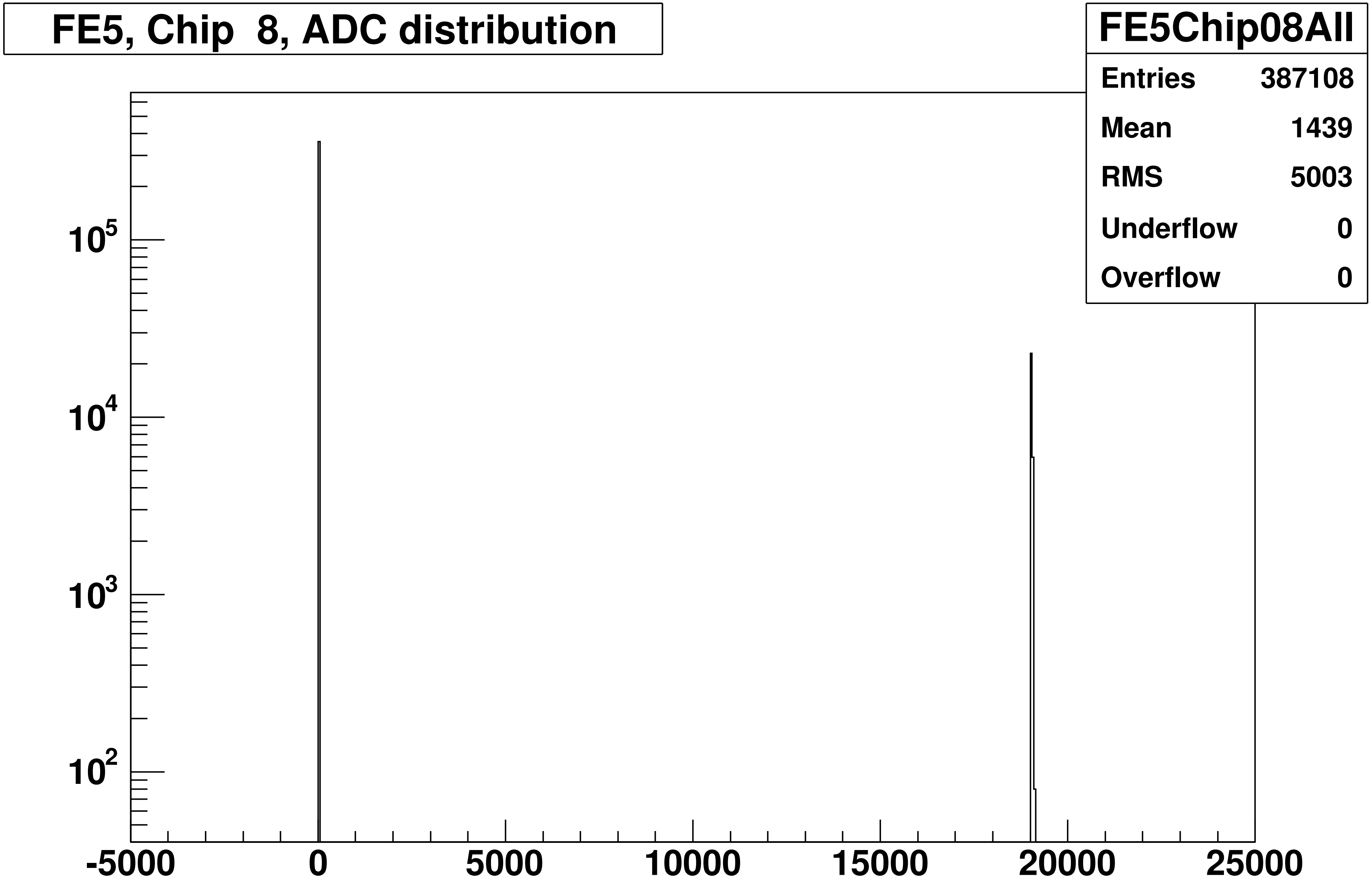
FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 387108
Mean 1422
RMS 4997
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



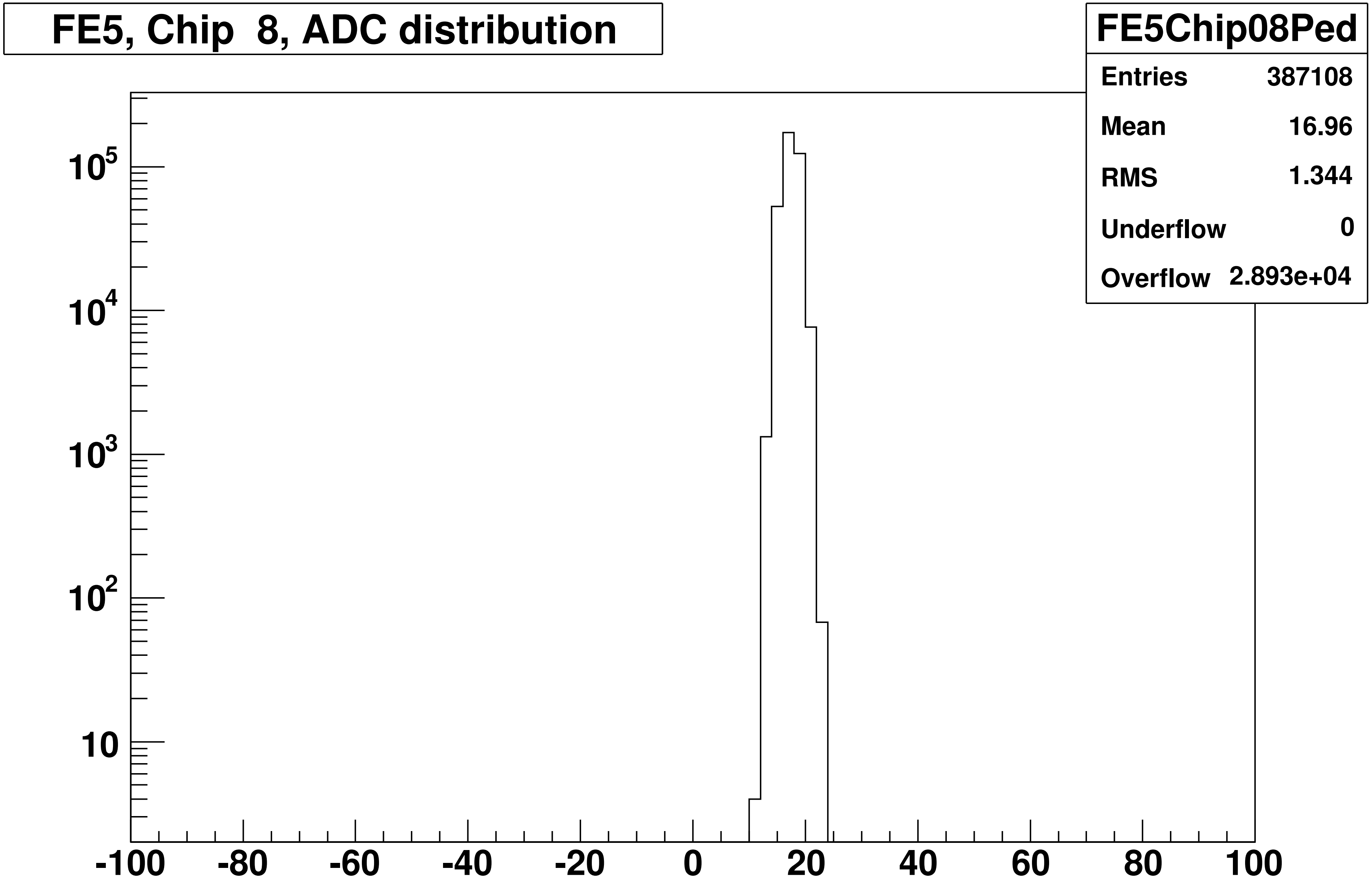
FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 387108
Mean 2.782
RMS 1.314
Underflow 0
Overflow 2.891e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

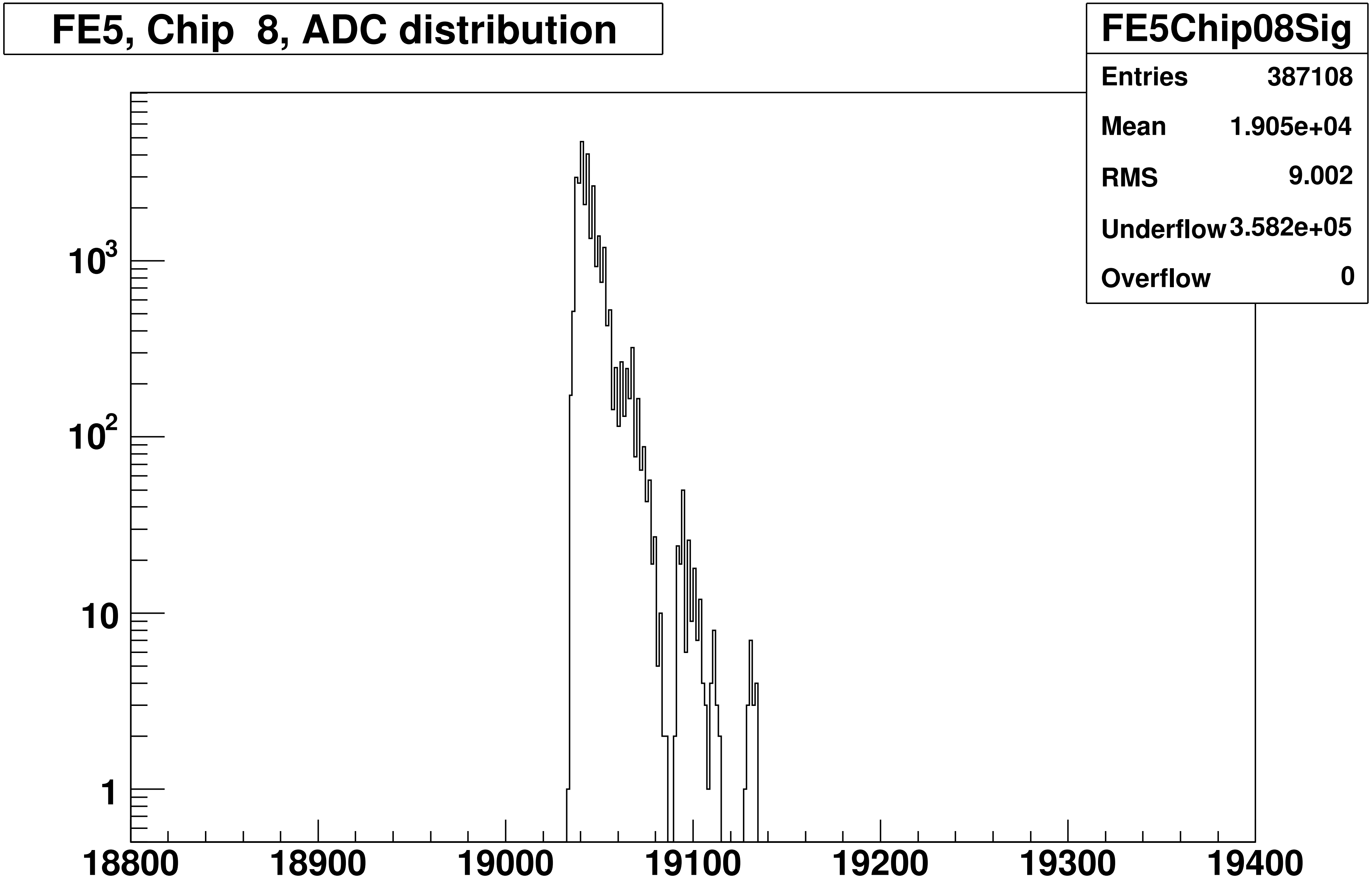


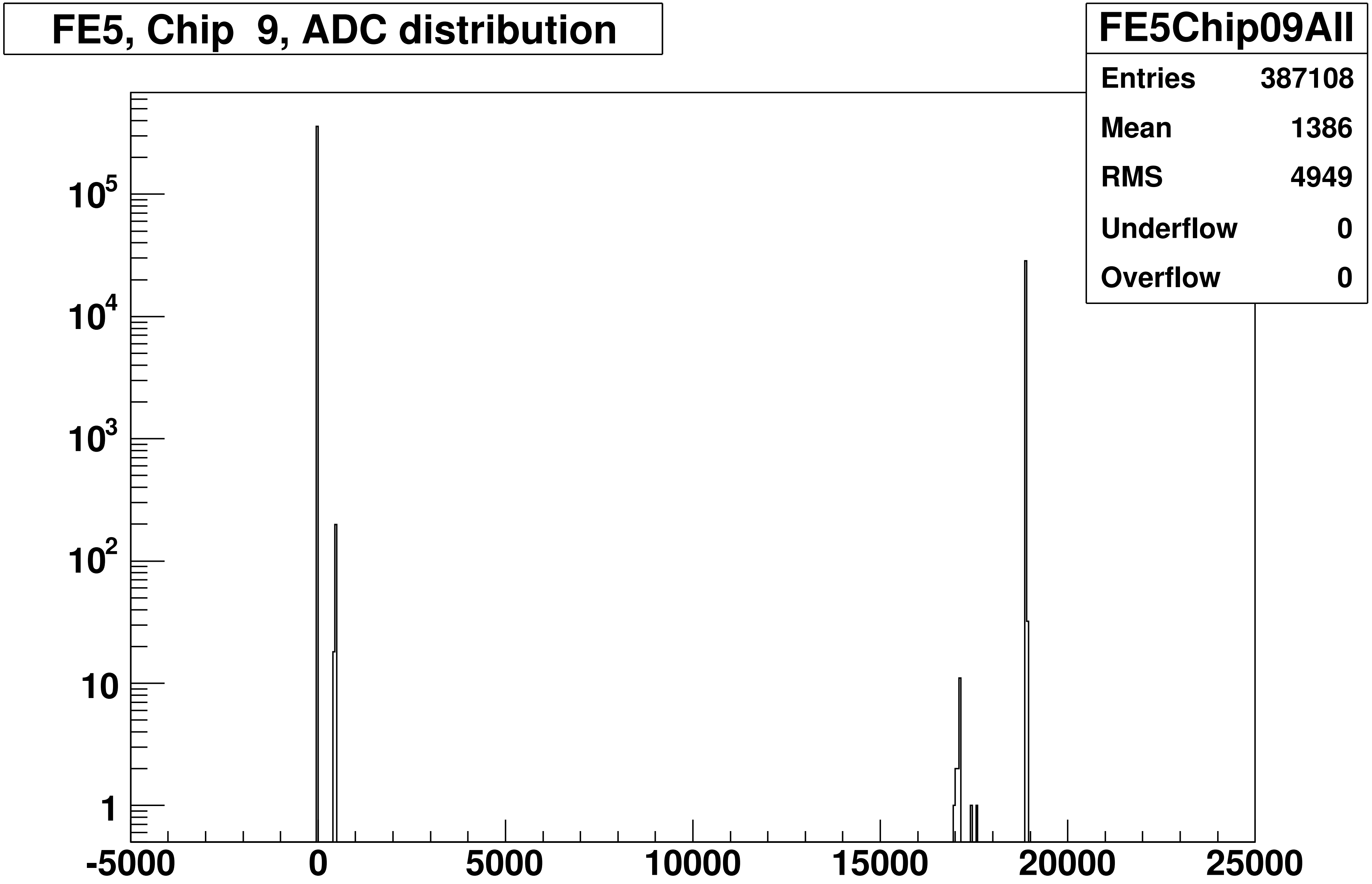
FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 387108
Mean 1.901e+04
RMS 9.004
Underflow 3.582e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



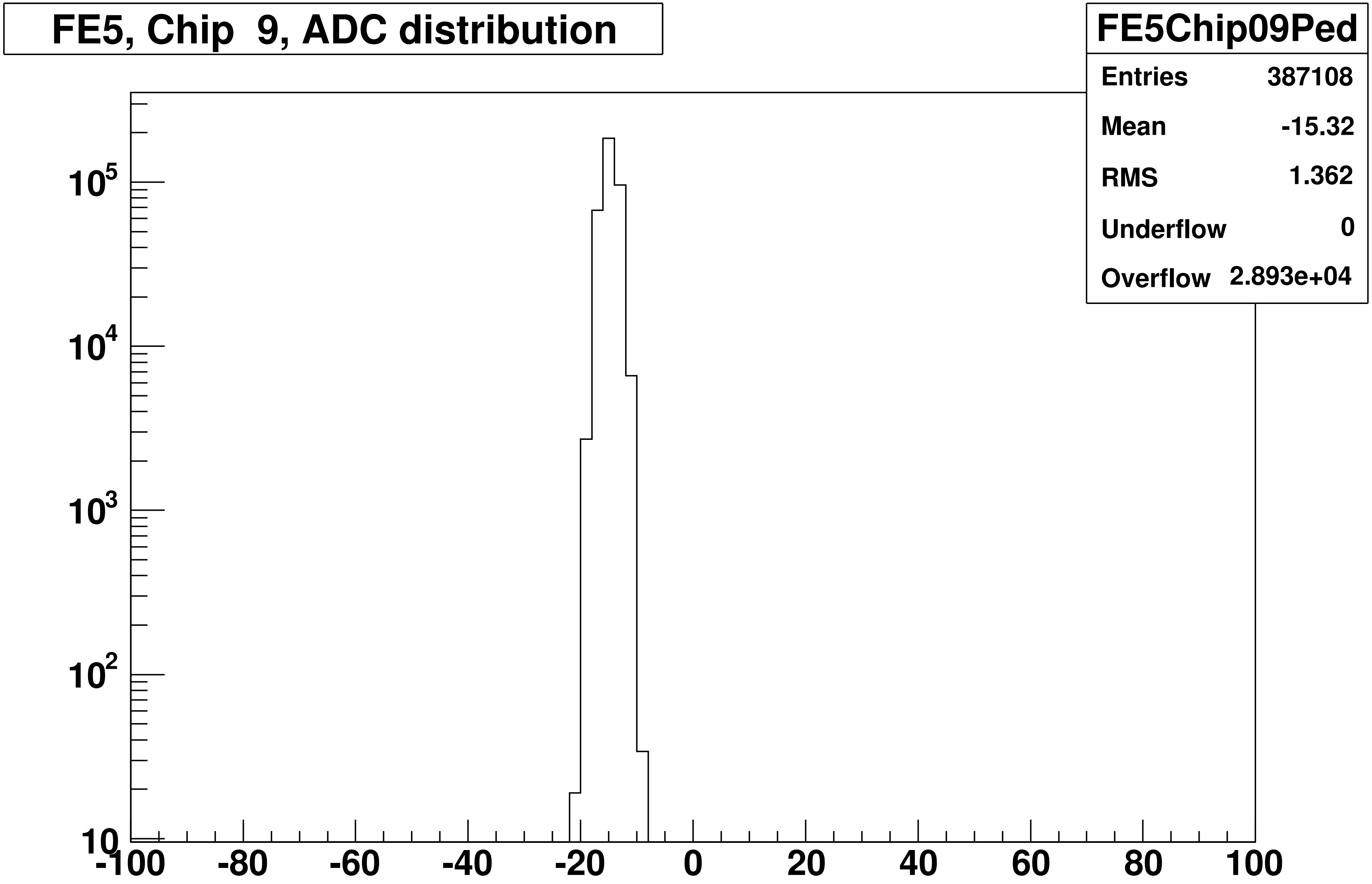
FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 387108
Mean 1439
RMS 5003
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

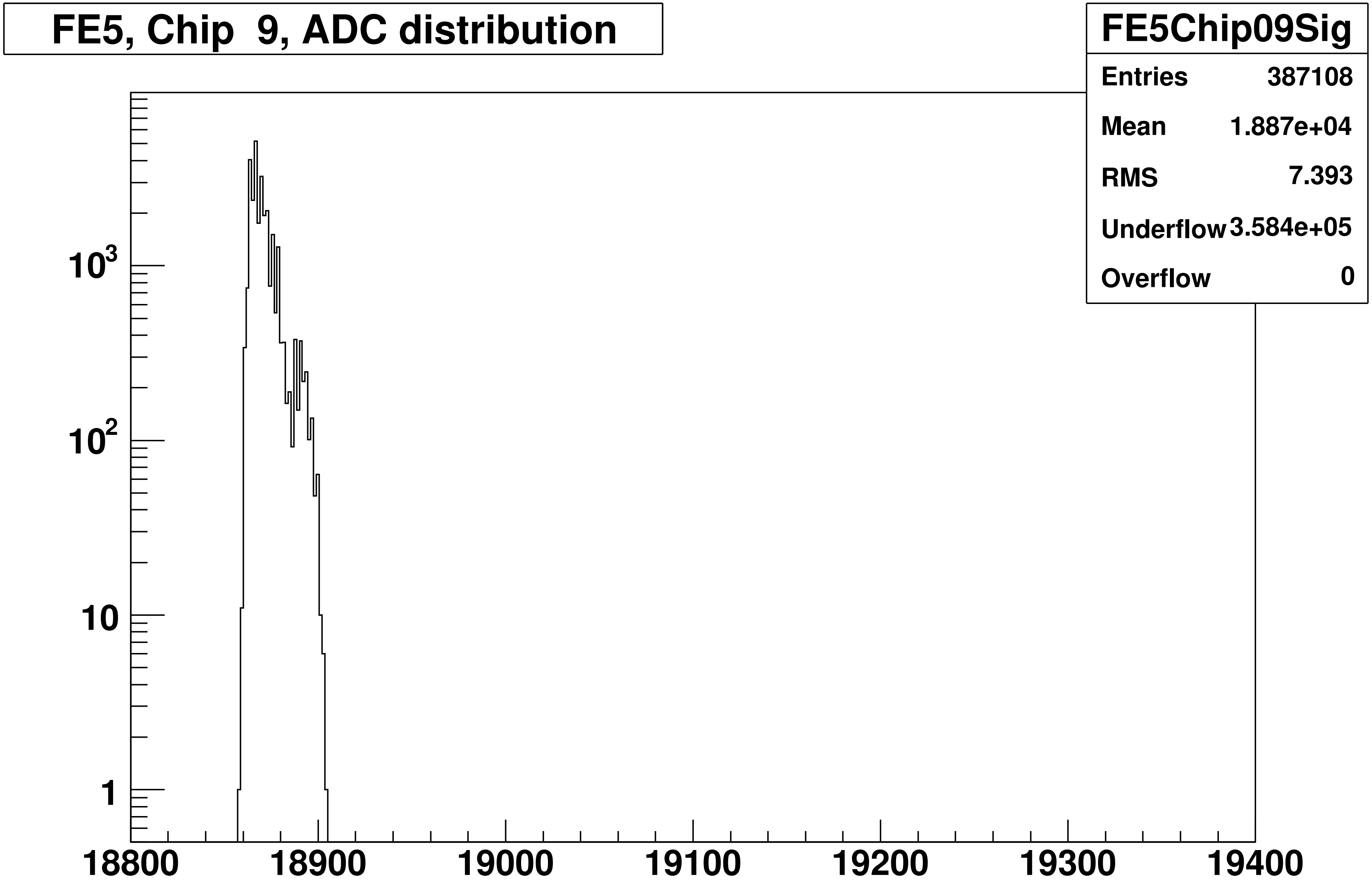

FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 387108
Mean 16.96
RMS 1.344
Underflow 0
Overflow 2.893e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 387108
Mean 1.905e+04
RMS 9.002
Underflow 3.582e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

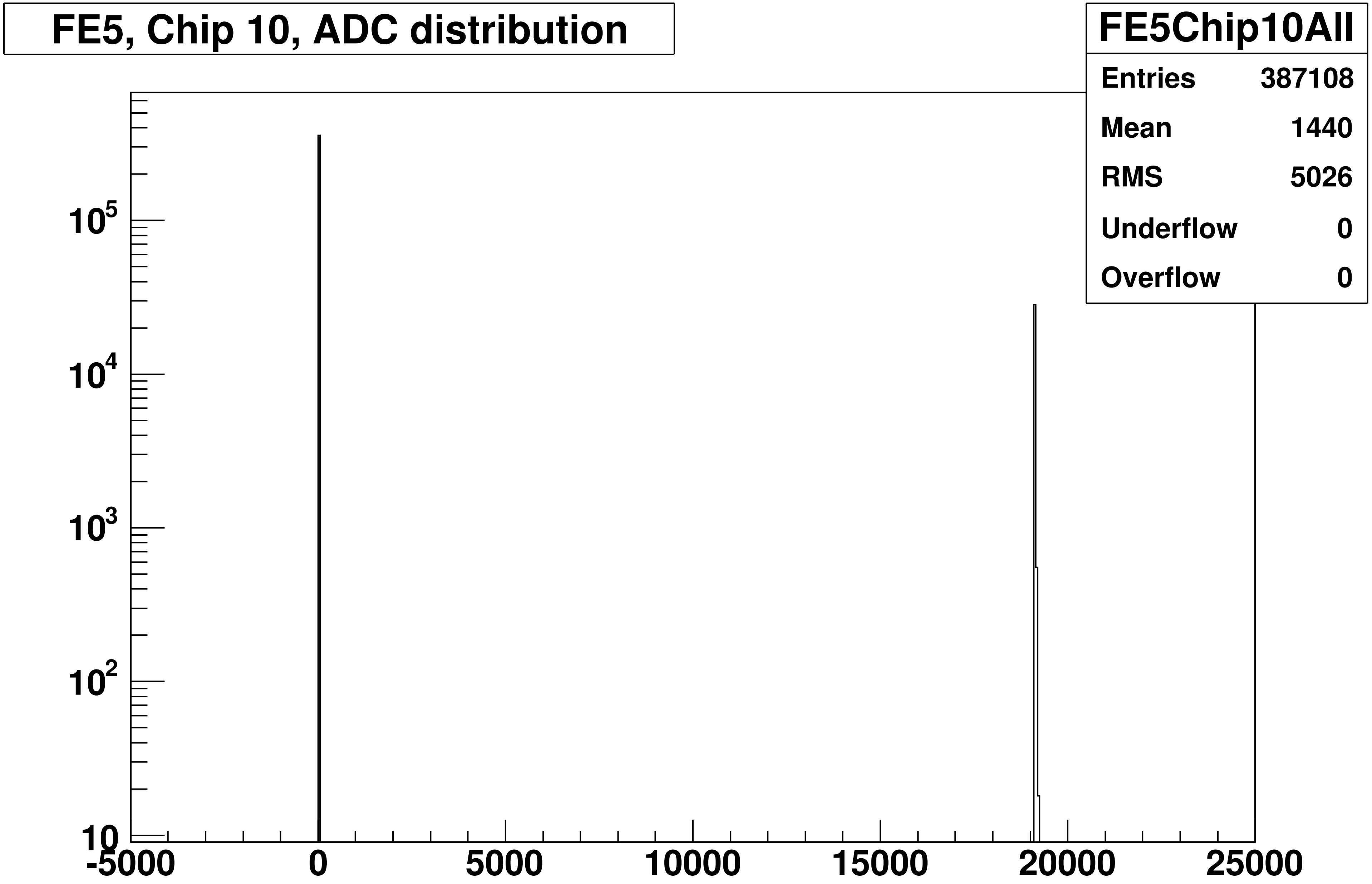


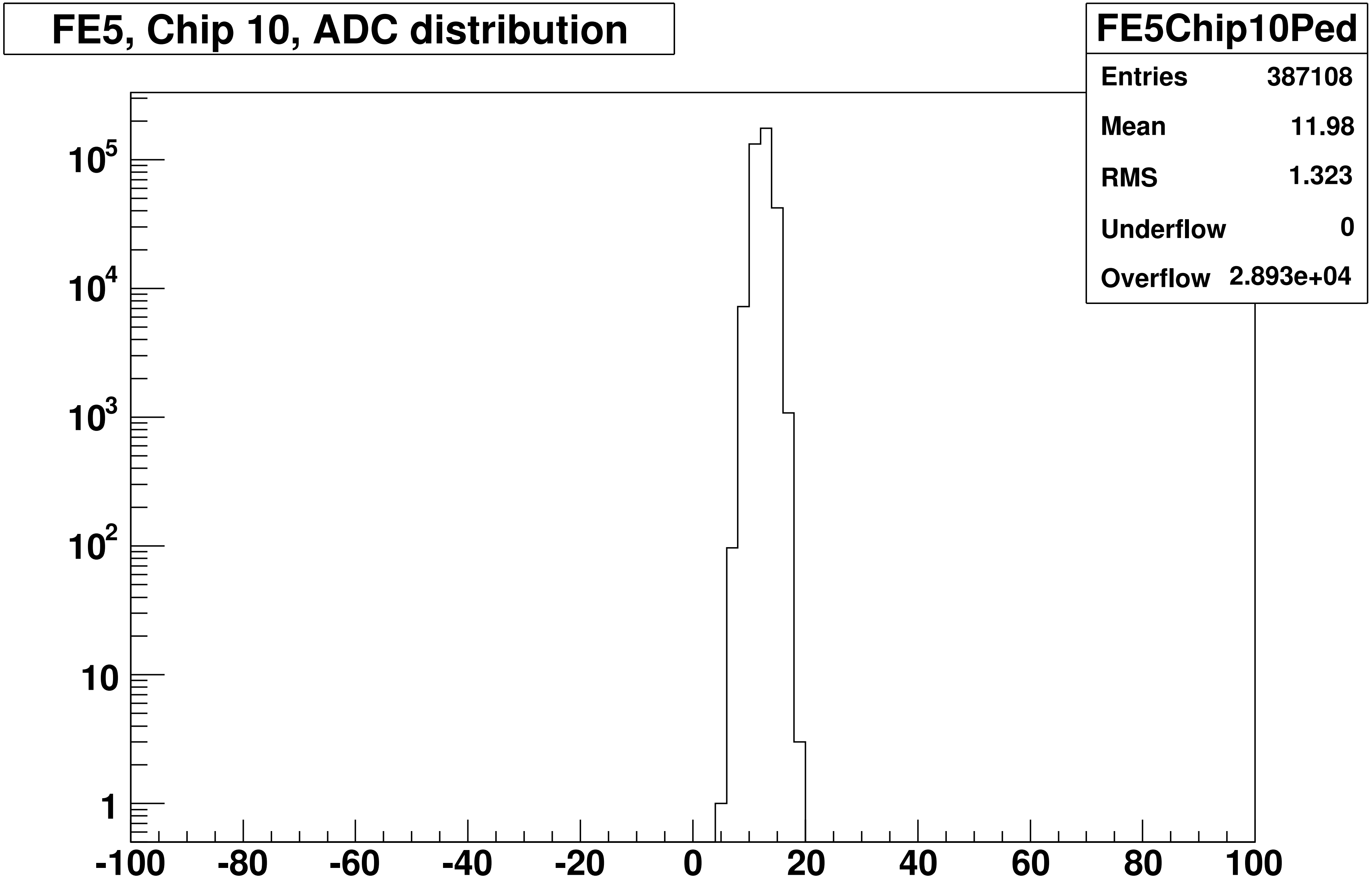
FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 387108
Mean 1386
RMS 4949
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

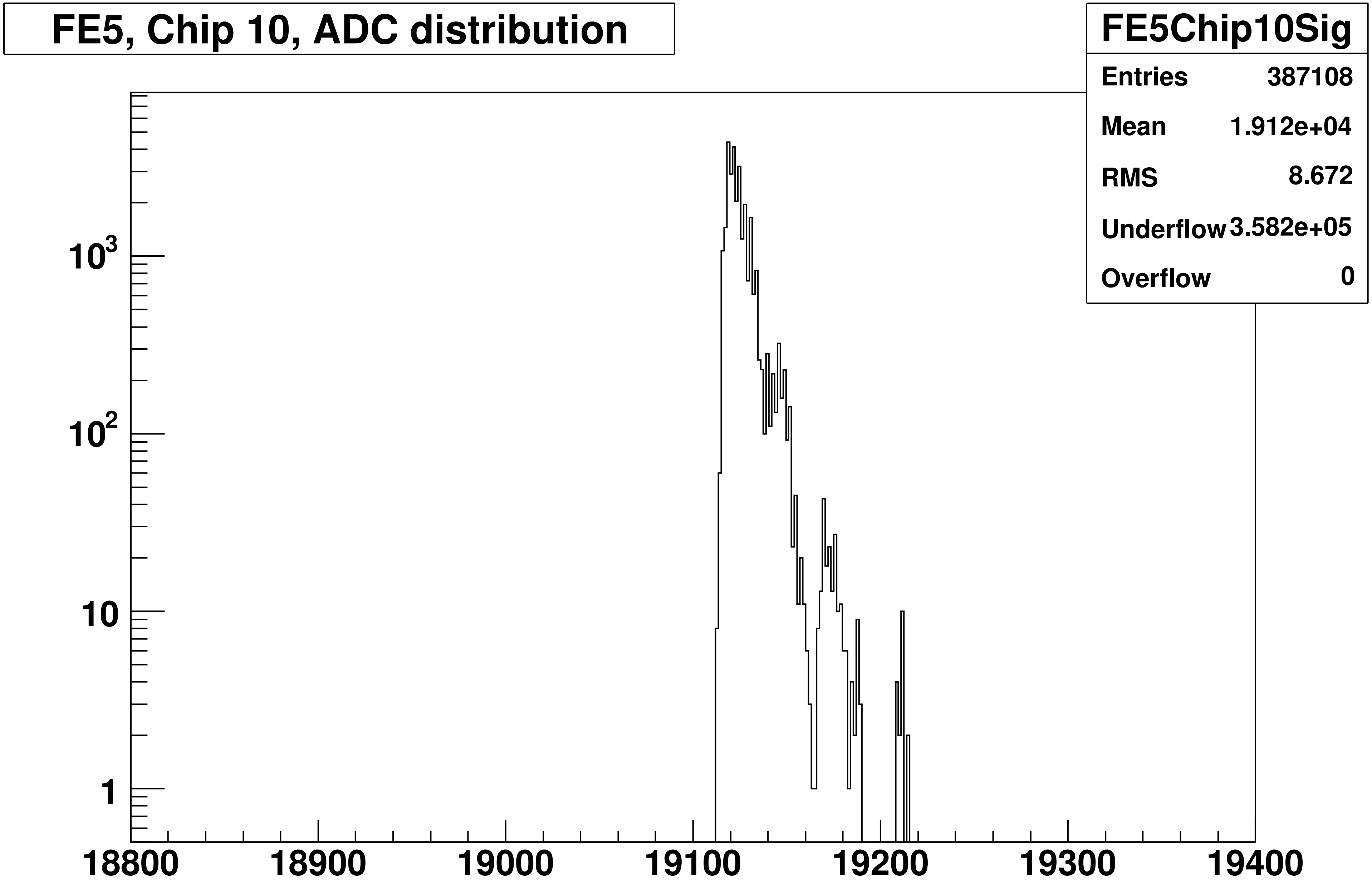

FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 387108
Mean -15.32
RMS 1.362
Underflow 0
Overflow 2.893e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



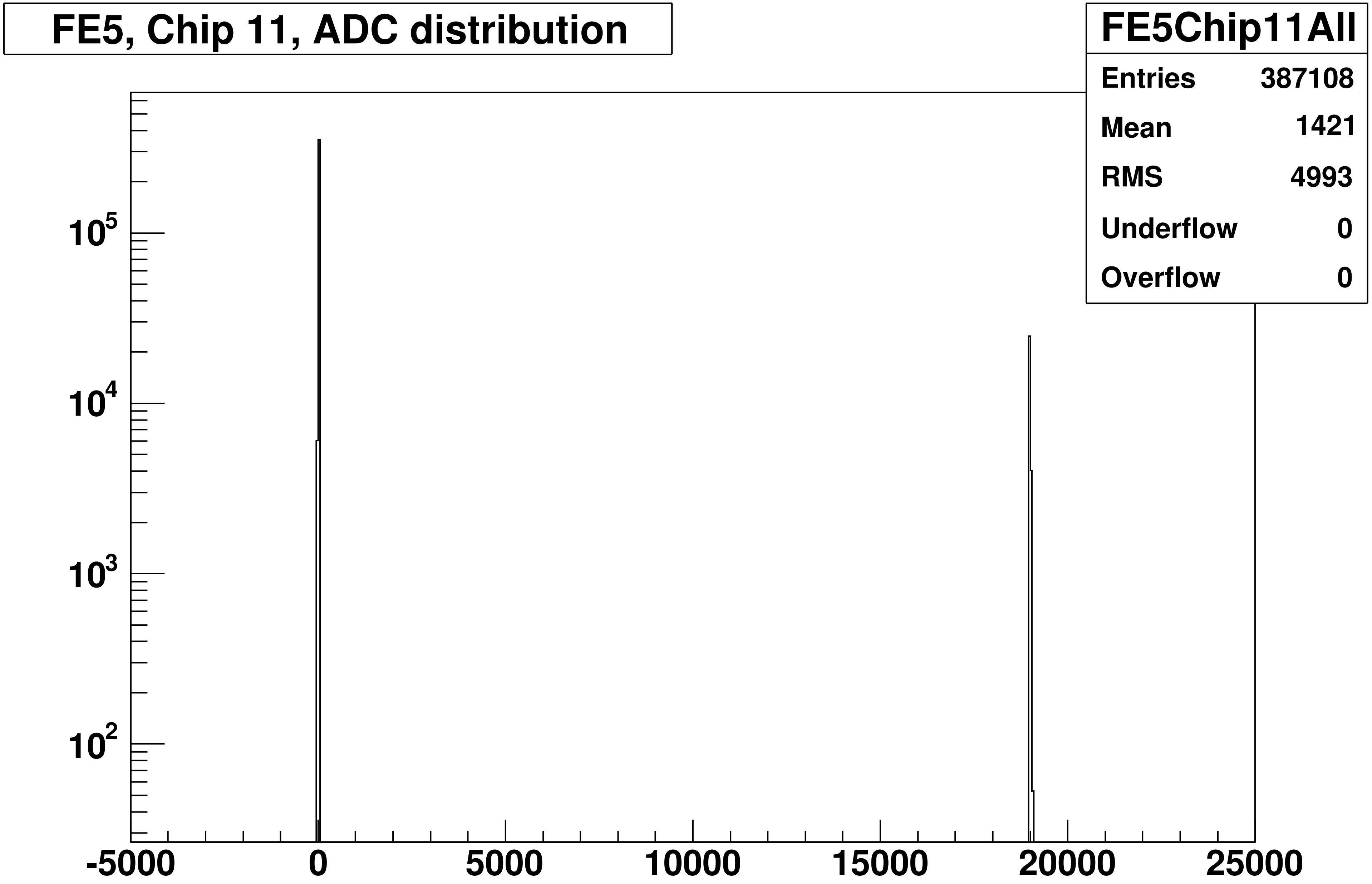
FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 387108
Mean 1.887e+04
RMS 7.393
Underflow 3.584e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 387108
Mean 1440
RMS 5026
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

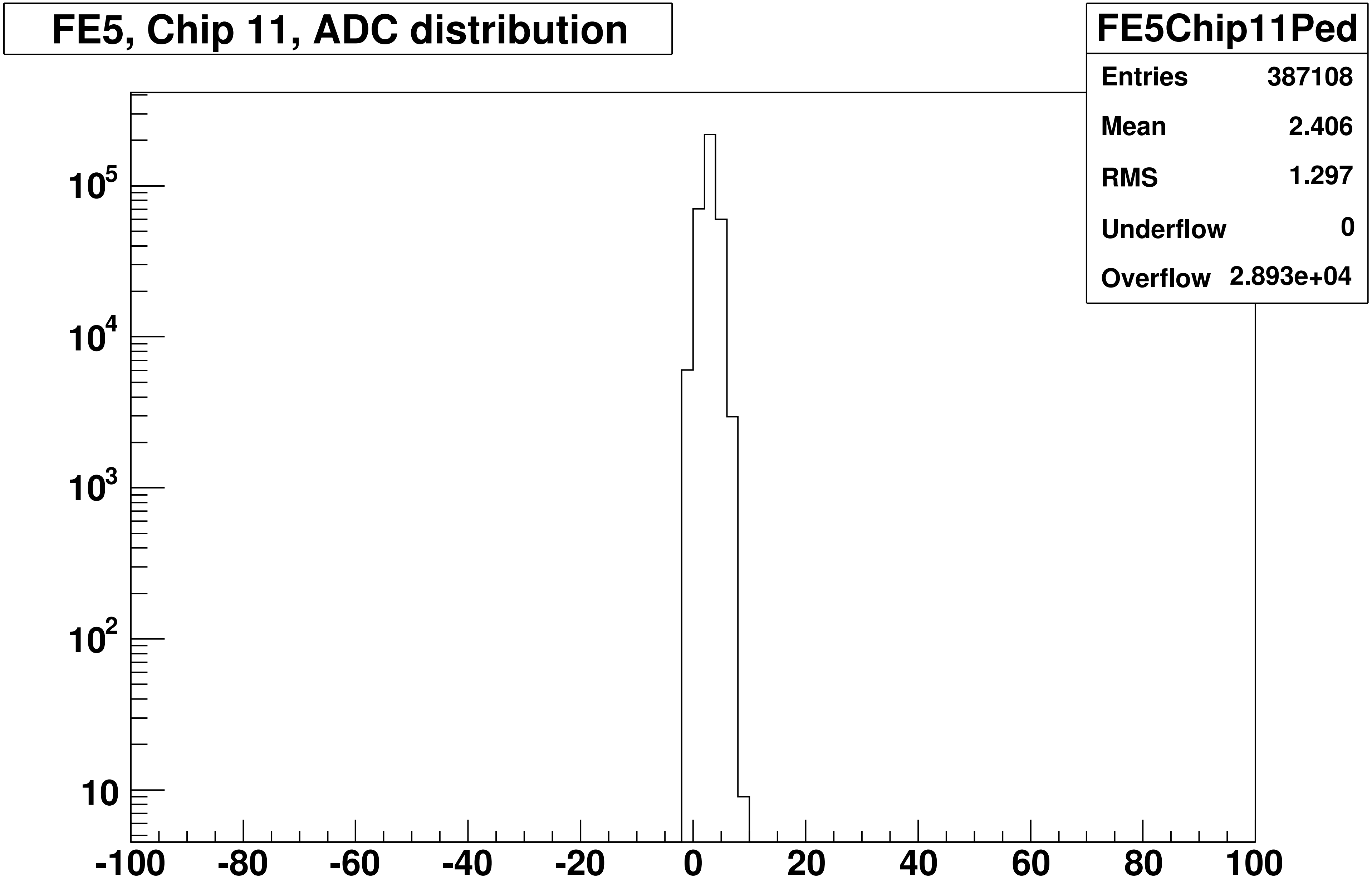

FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 387108
Mean 11.98
RMS 1.323
Underflow 0
Overflow 2.893e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



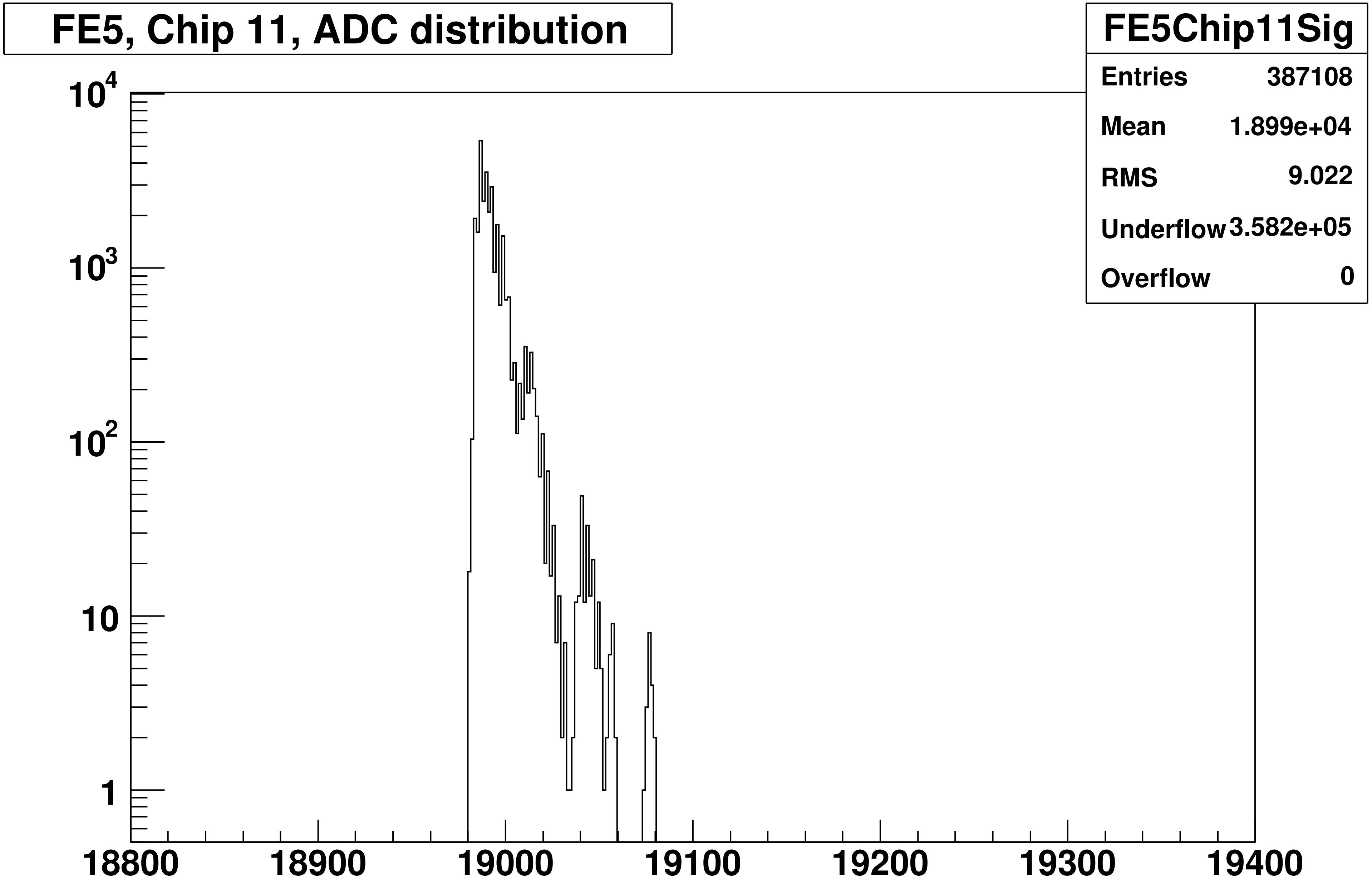
FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 387108
Mean 1.912e+04
RMS 8.672
Underflow 3.582e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



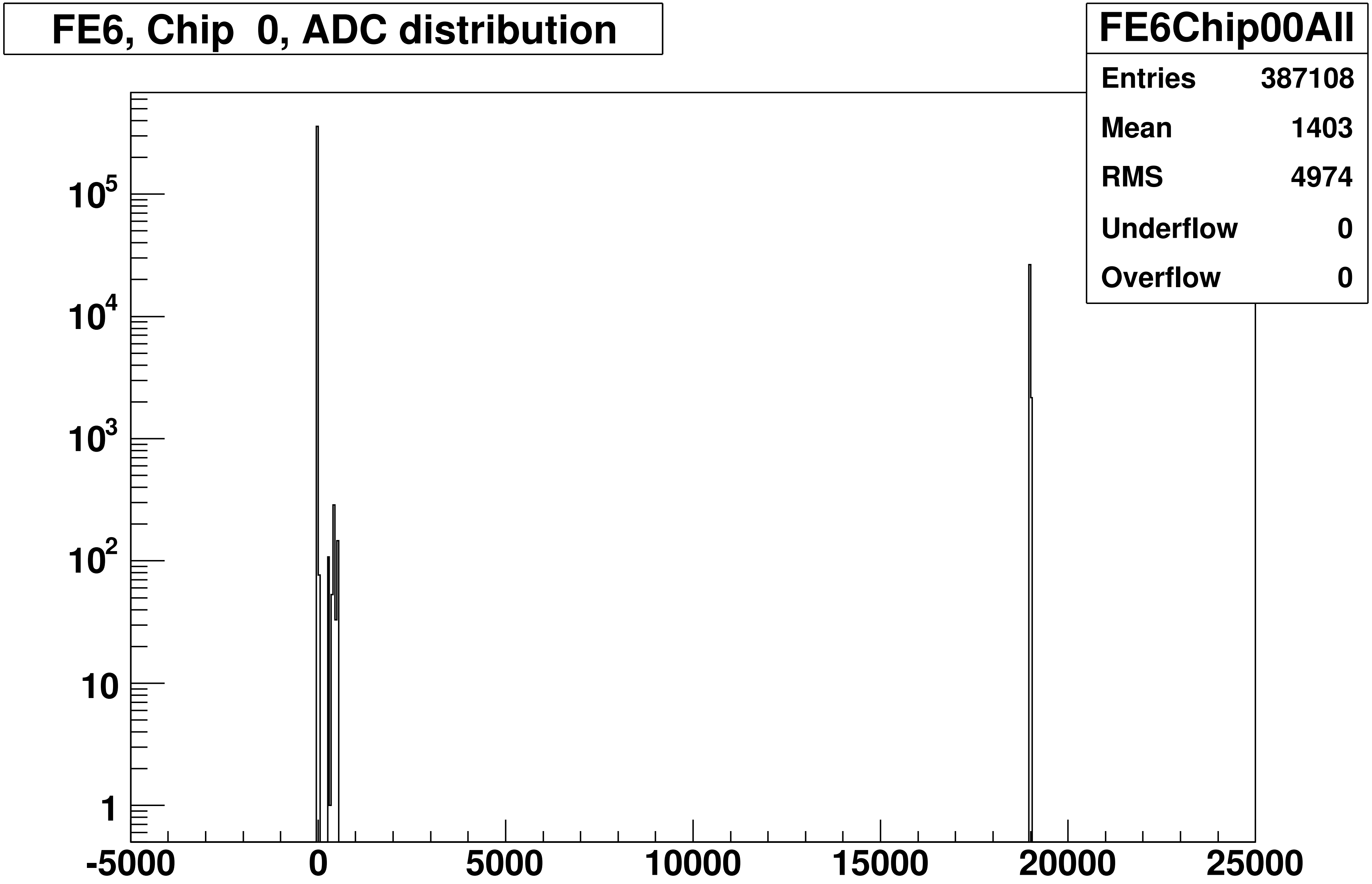
FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 387108
Mean 1421
RMS 4993
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

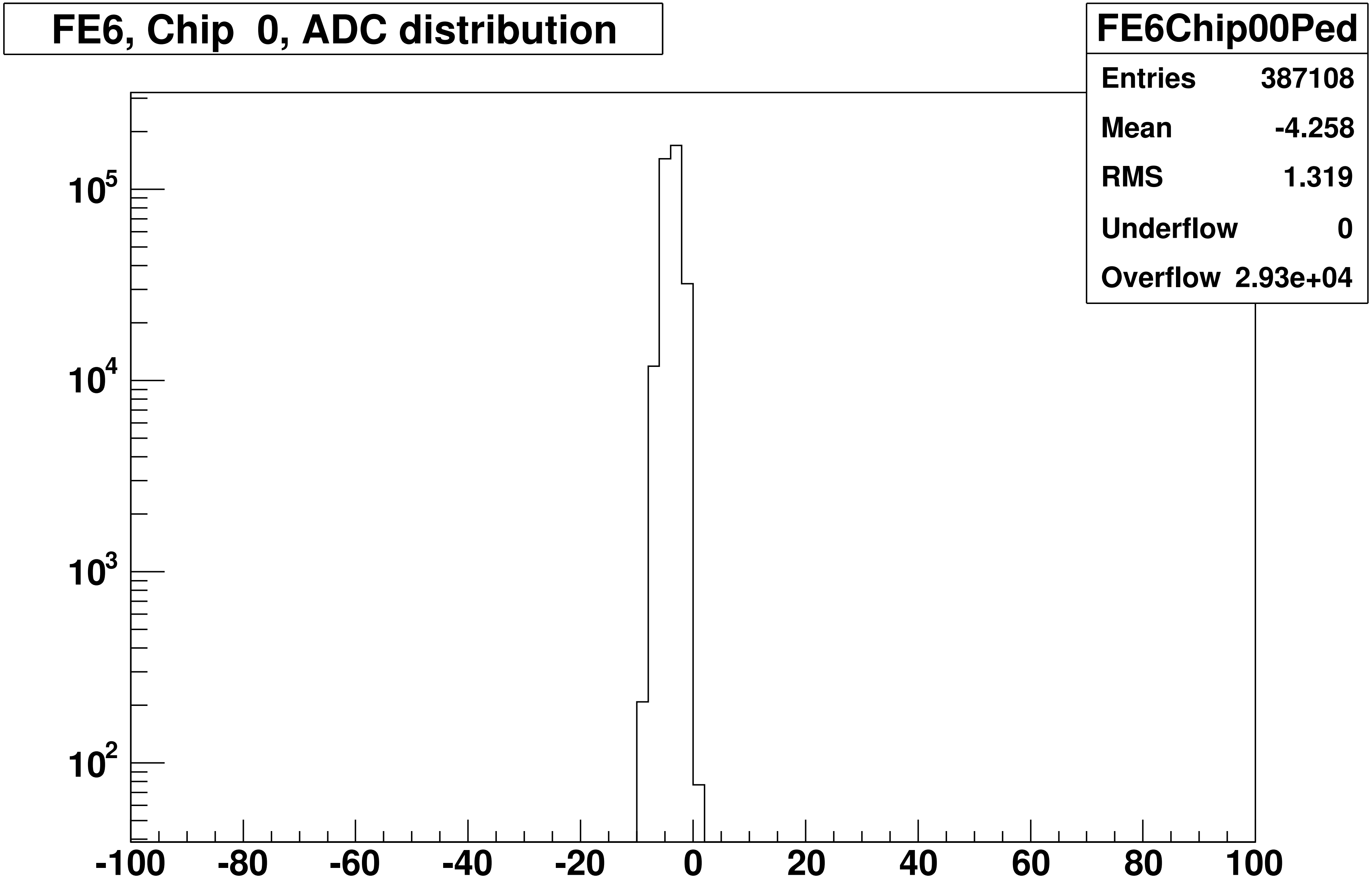


FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 387108
Mean 2.406
RMS 1.297
Underflow 0
Overflow 2.893e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

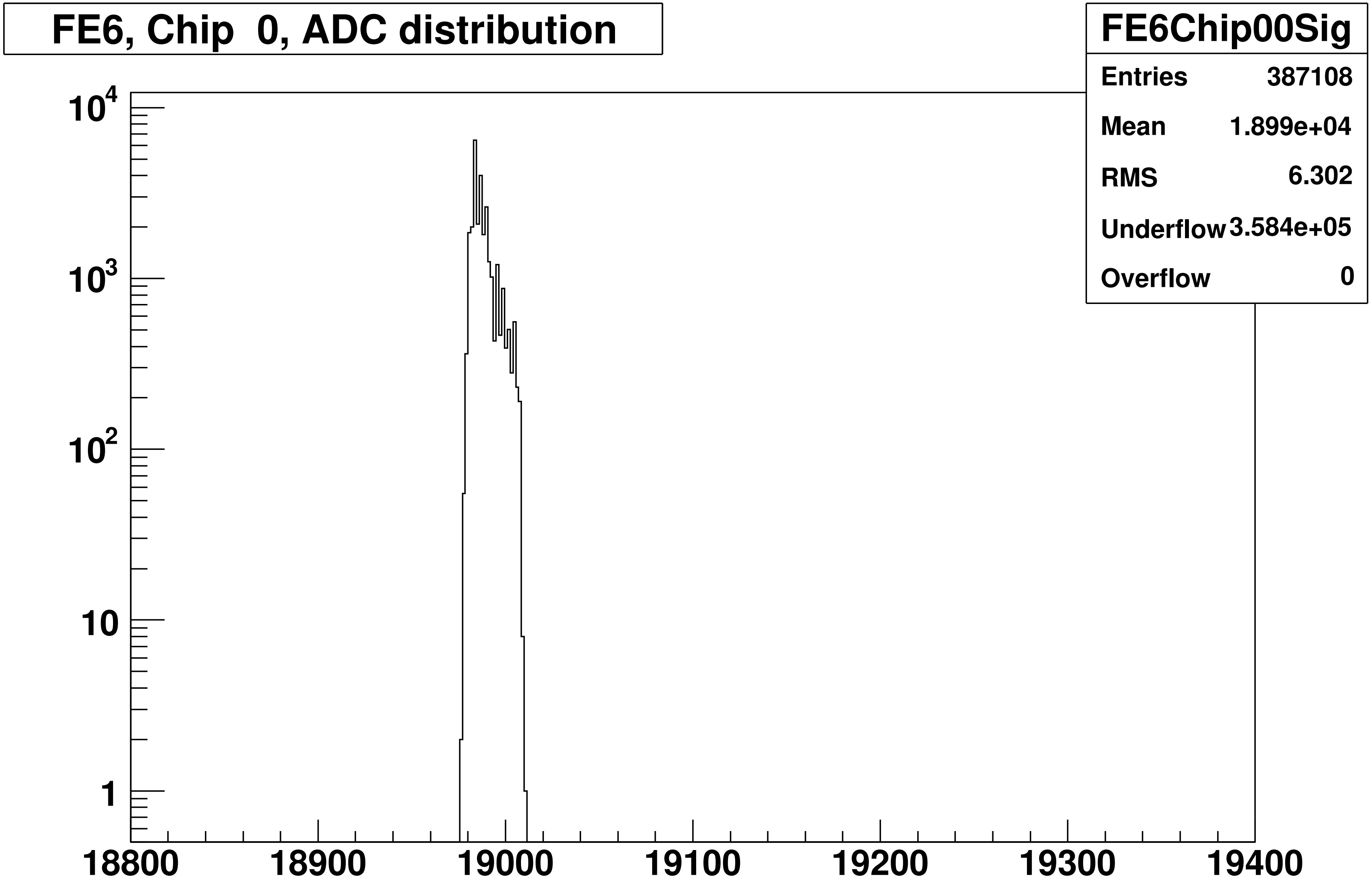


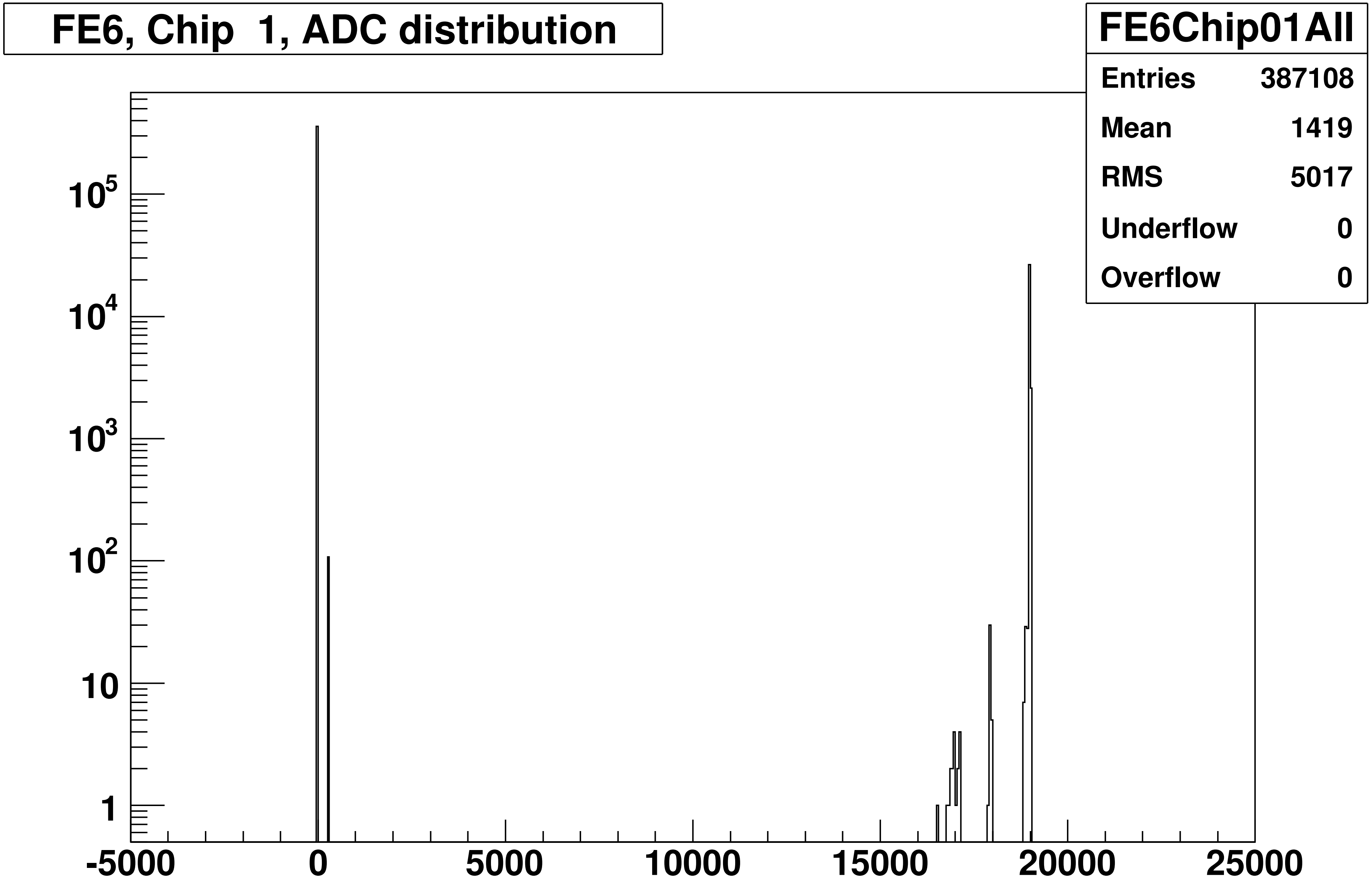
FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 387108
Mean 1.899e+04
RMS 9.022
Underflow 3.582e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 387108
Mean 1403
RMS 4974
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

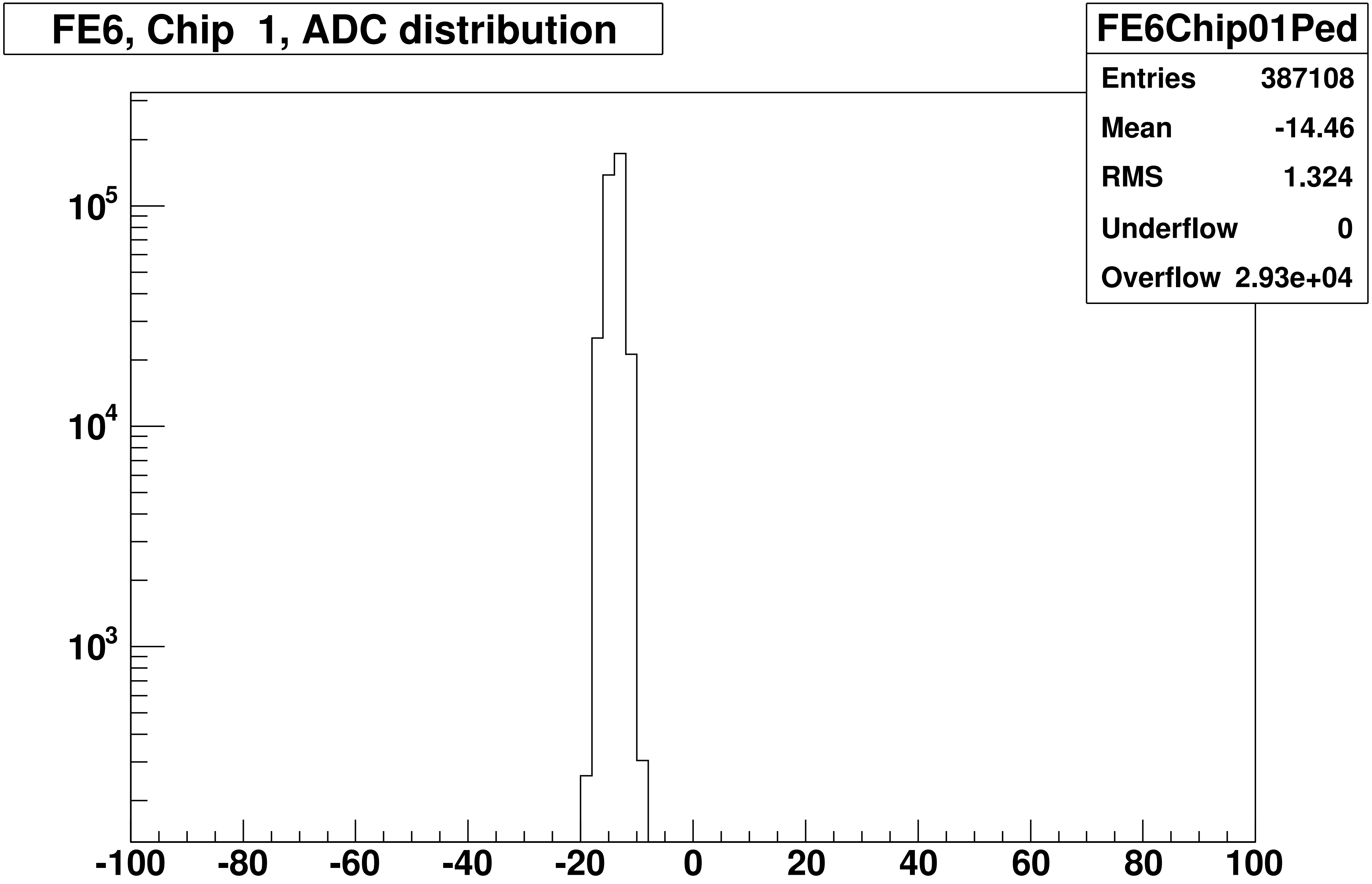


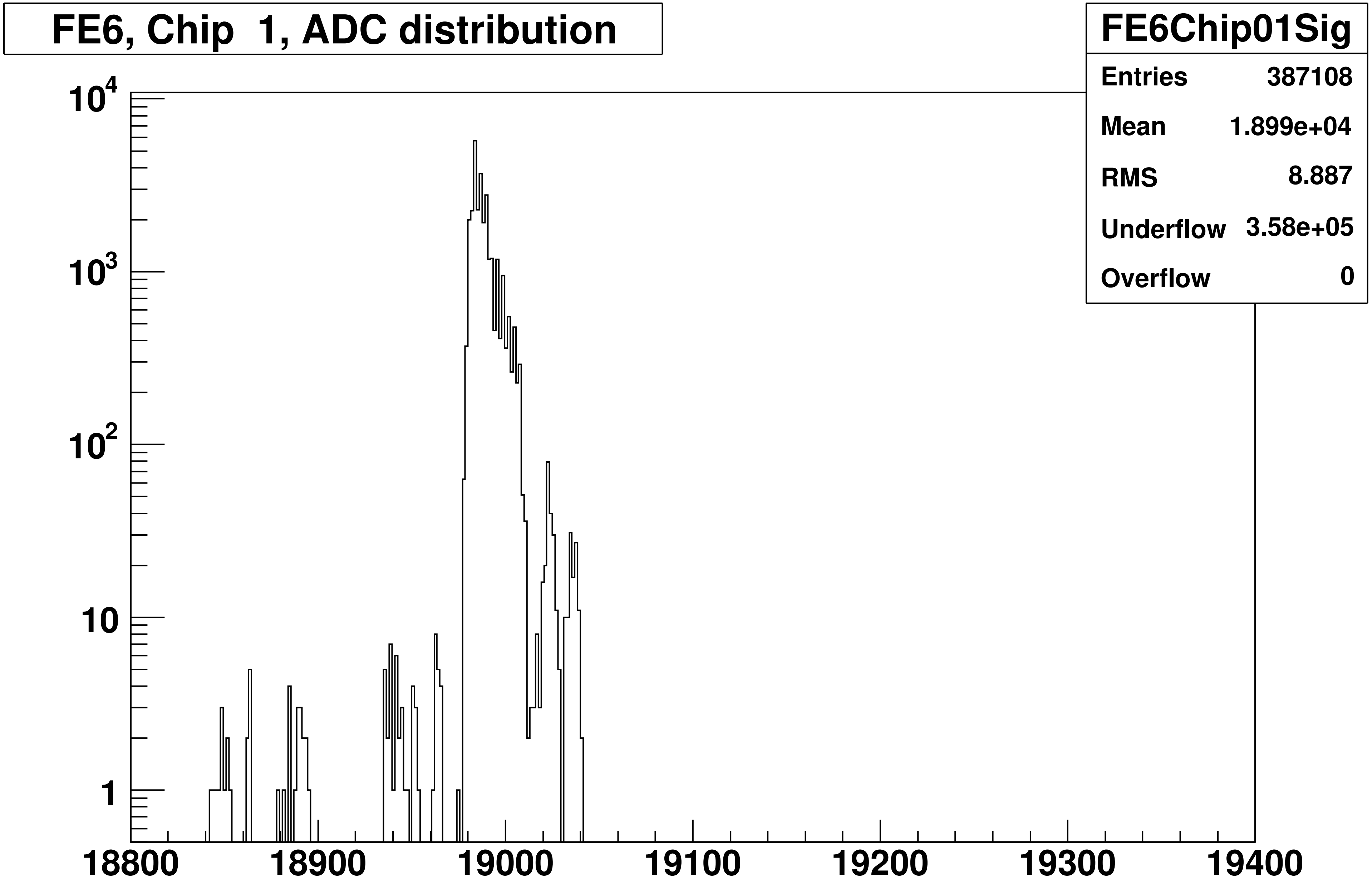
FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 387108
Mean -4.258
RMS 1.319
Underflow 0
Overflow 2.93e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

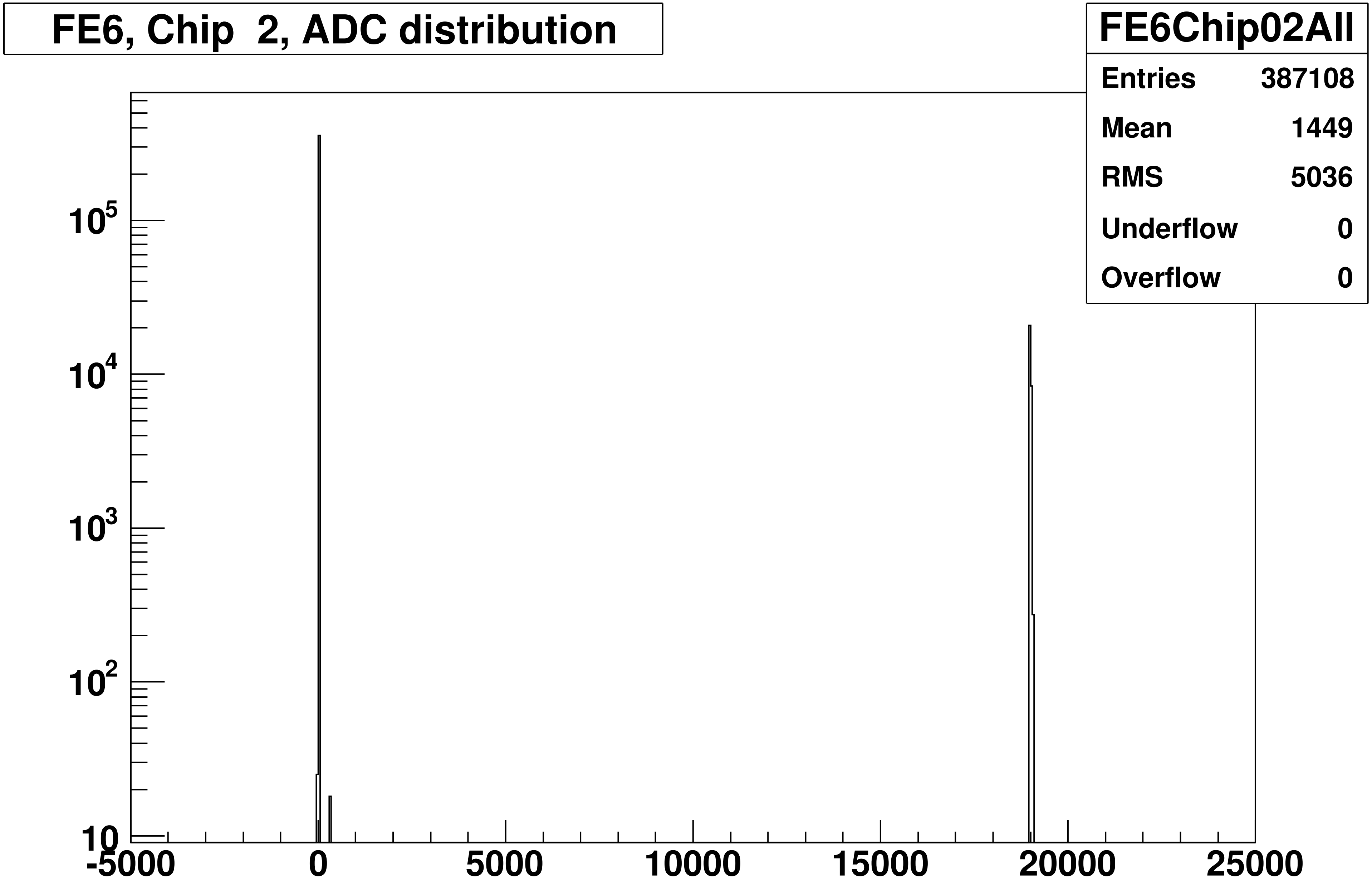

FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 387108
Mean 1.899e+04
RMS 6.302
Underflow 3.584e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

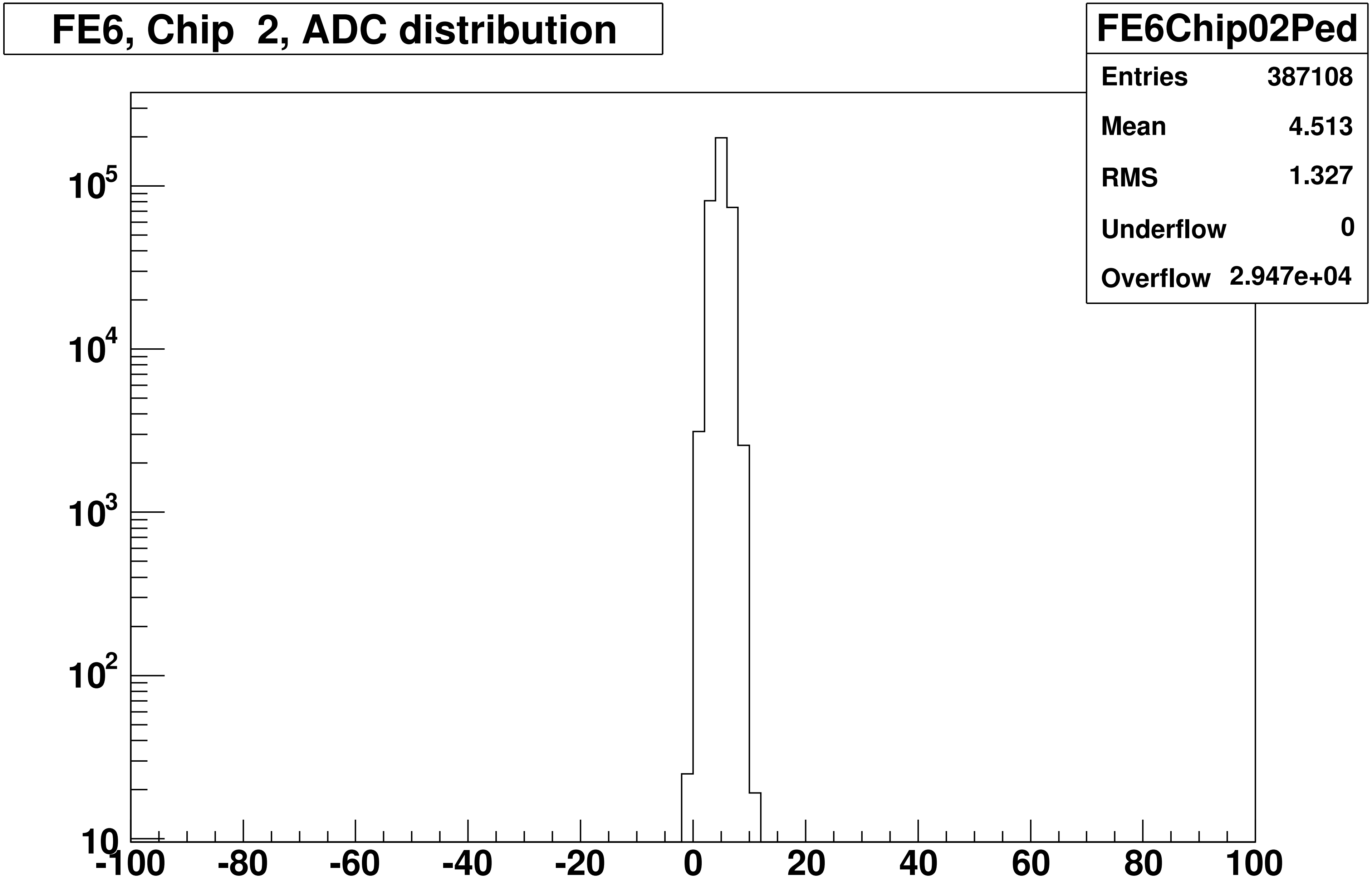


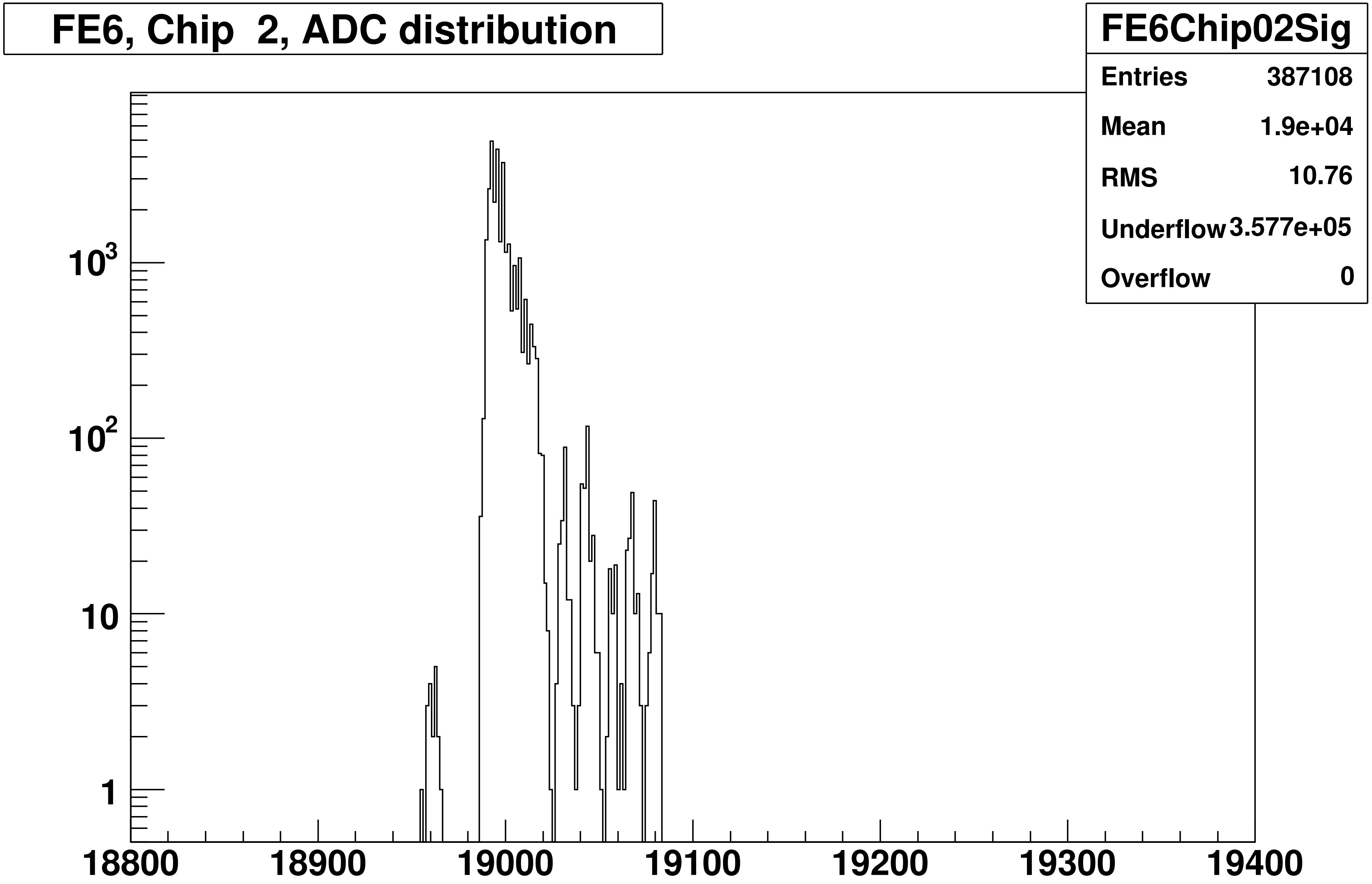
FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 387108
Mean 1419
RMS 5017
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 387108
Mean -14.46
RMS 1.324
Underflow 0
Overflow 2.93e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100

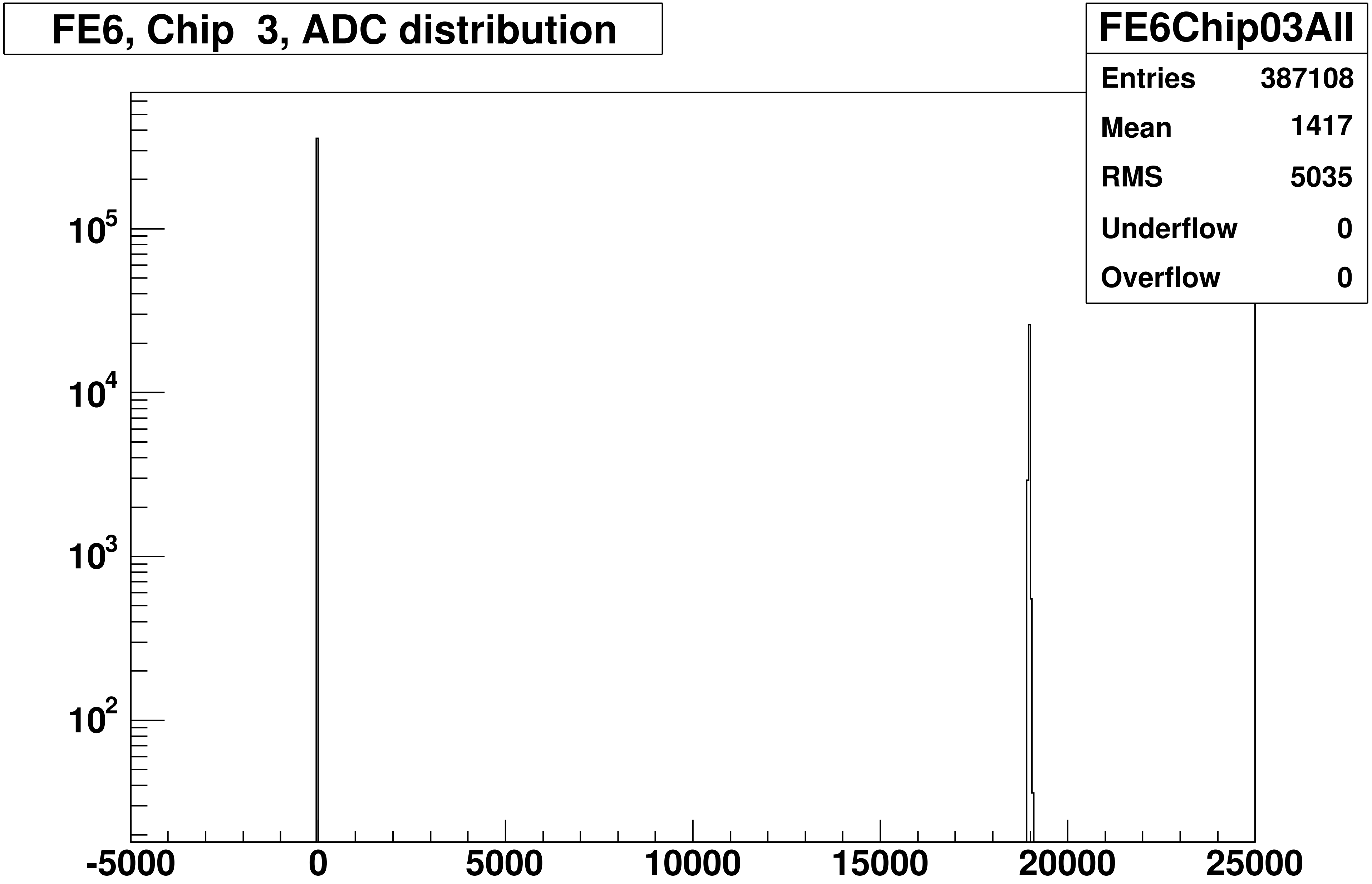

FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 387108
Mean 1.899e+04
RMS 8.887
Underflow 3.58e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

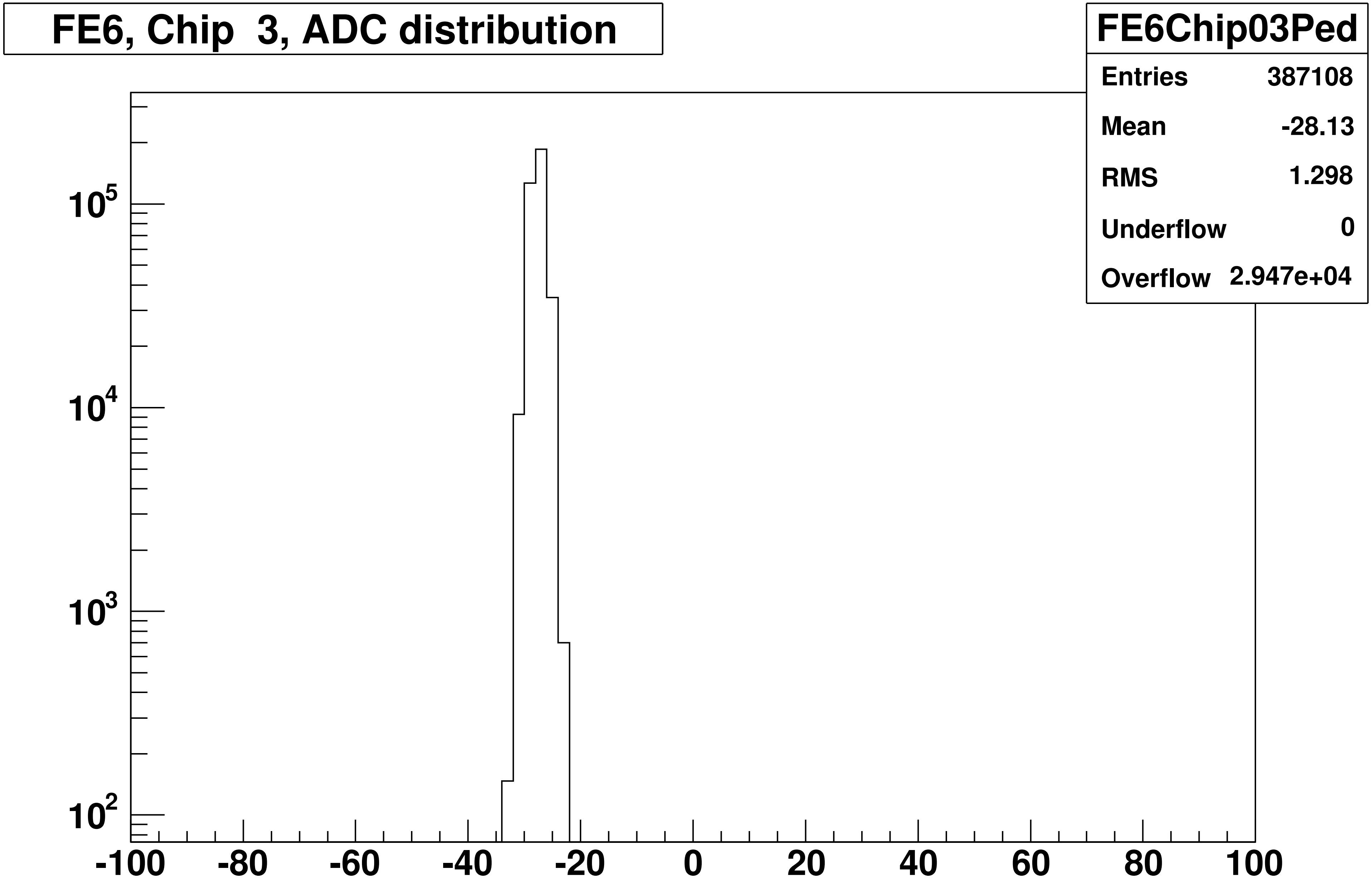

FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 387108
Mean 1449
RMS 5036
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

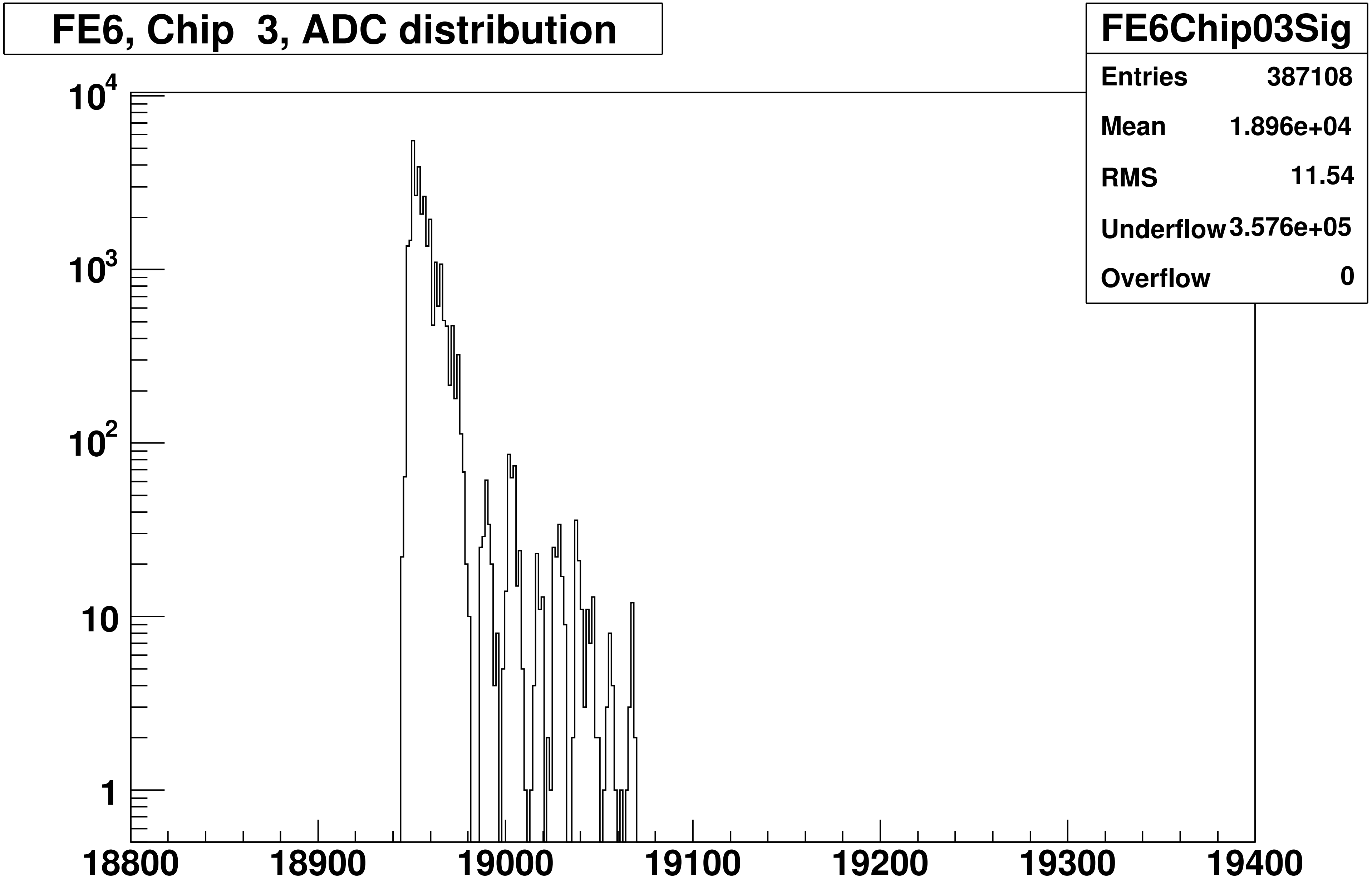

FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 387108
Mean 4.513
RMS 1.327
Underflow 0
Overflow 2.947e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

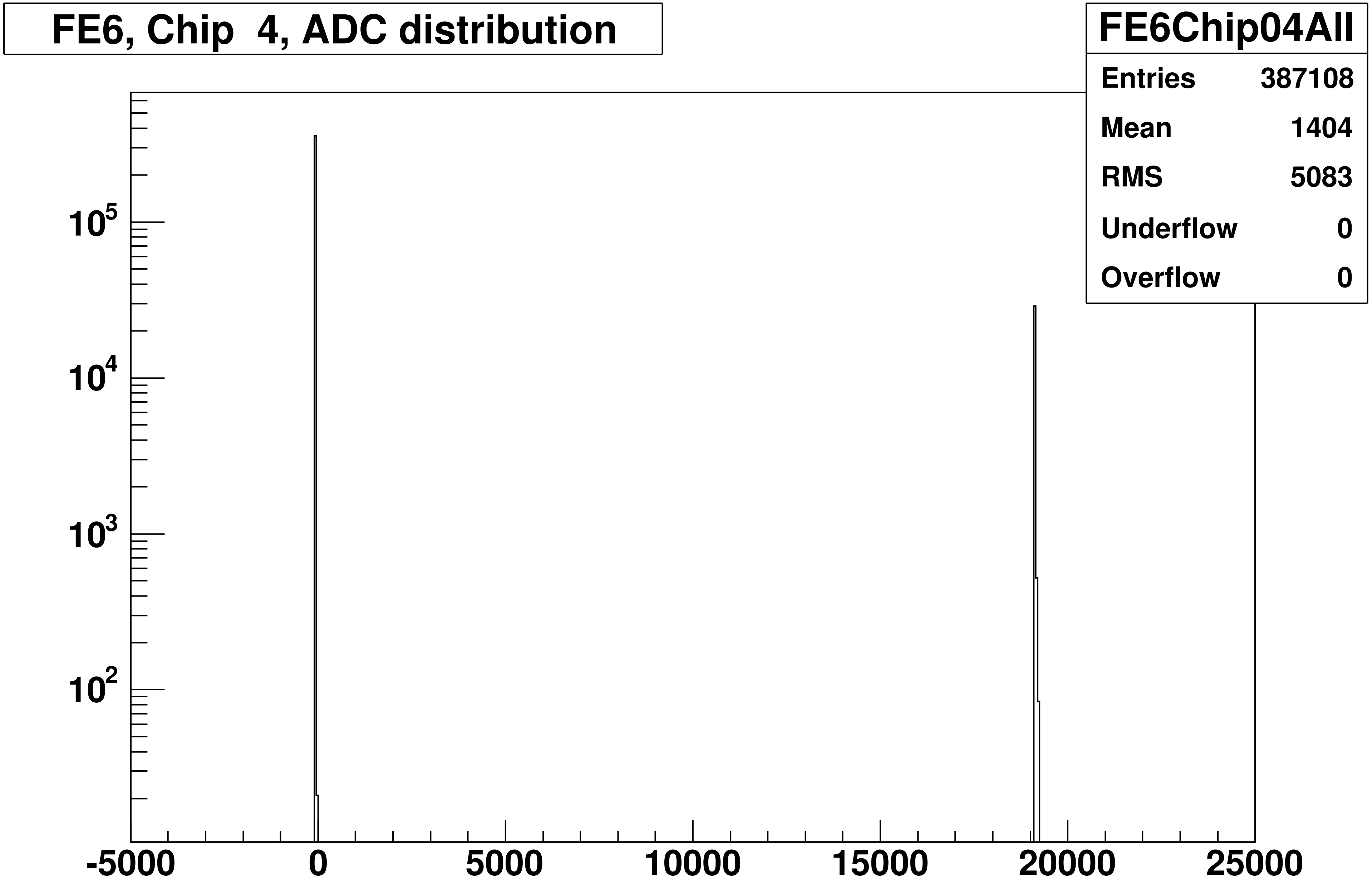


FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 387108
Mean 1.9e+04
RMS 10.76
Underflow 3.577e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

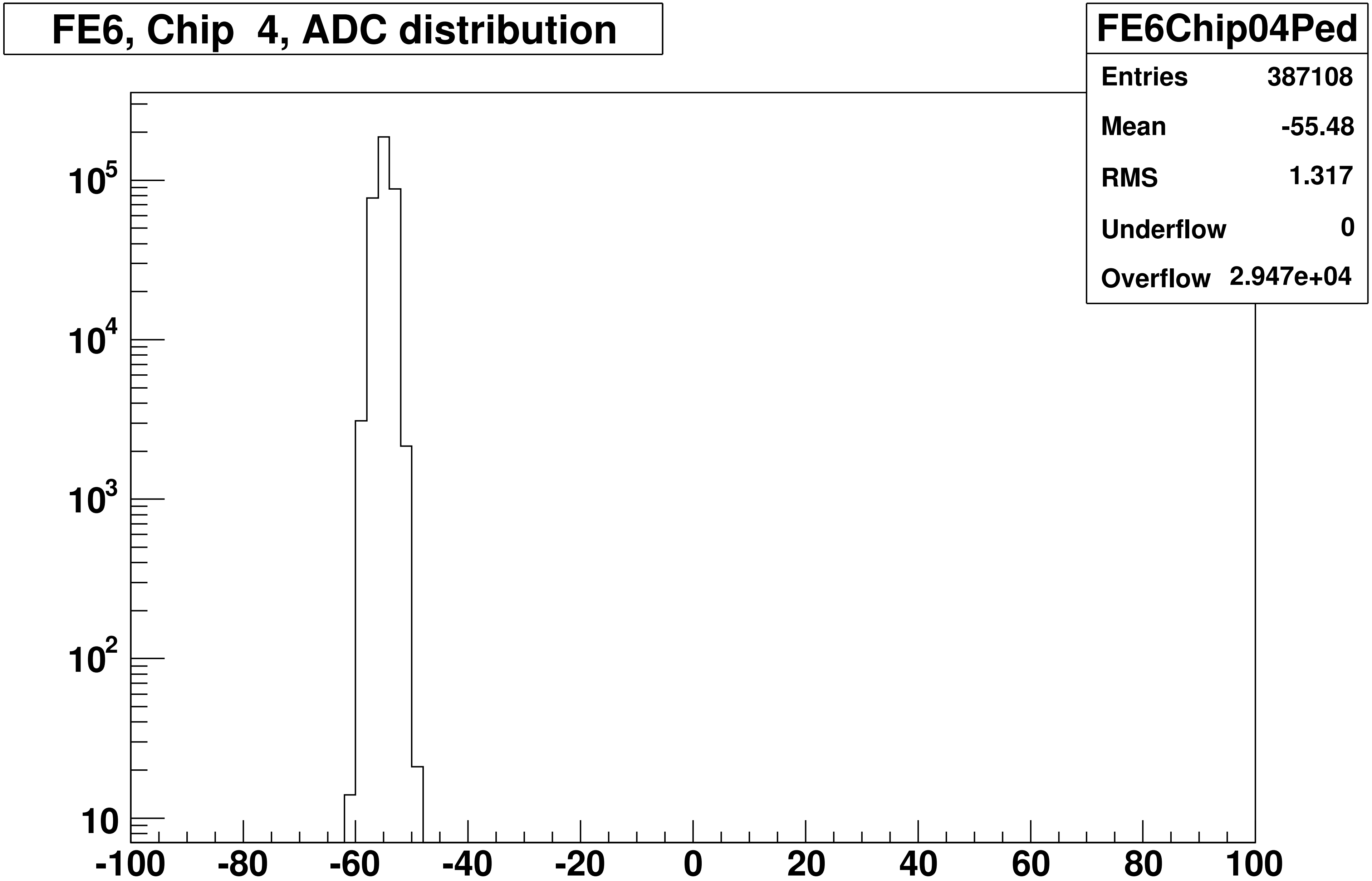

FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 387108
Mean 1417
RMS 5035
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 387108
Mean -28.13
RMS 1.298
Underflow 0
Overflow 2.947e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

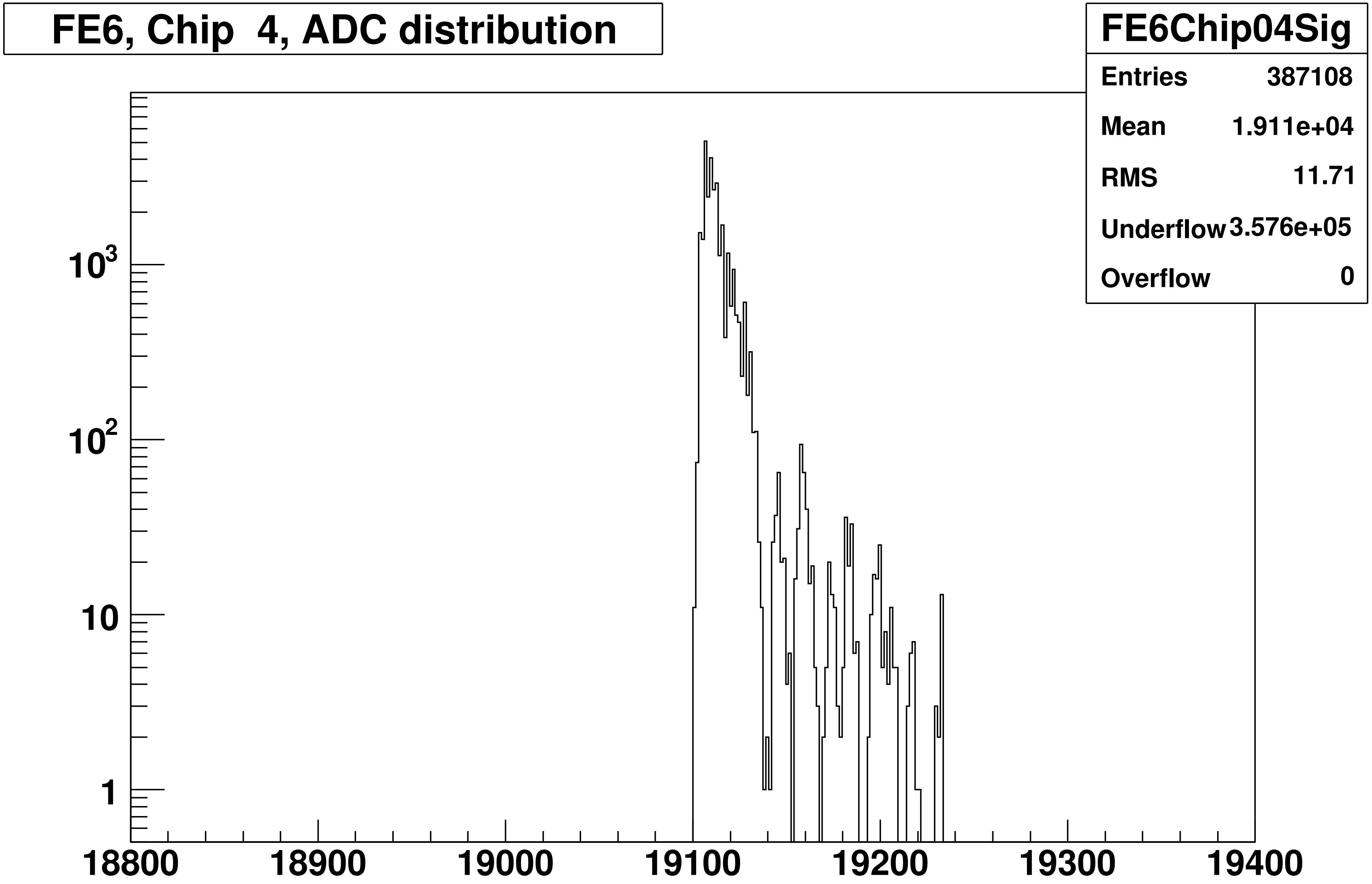

FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 387108
Mean 1.896e+04
RMS 11.54
Underflow 3.576e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

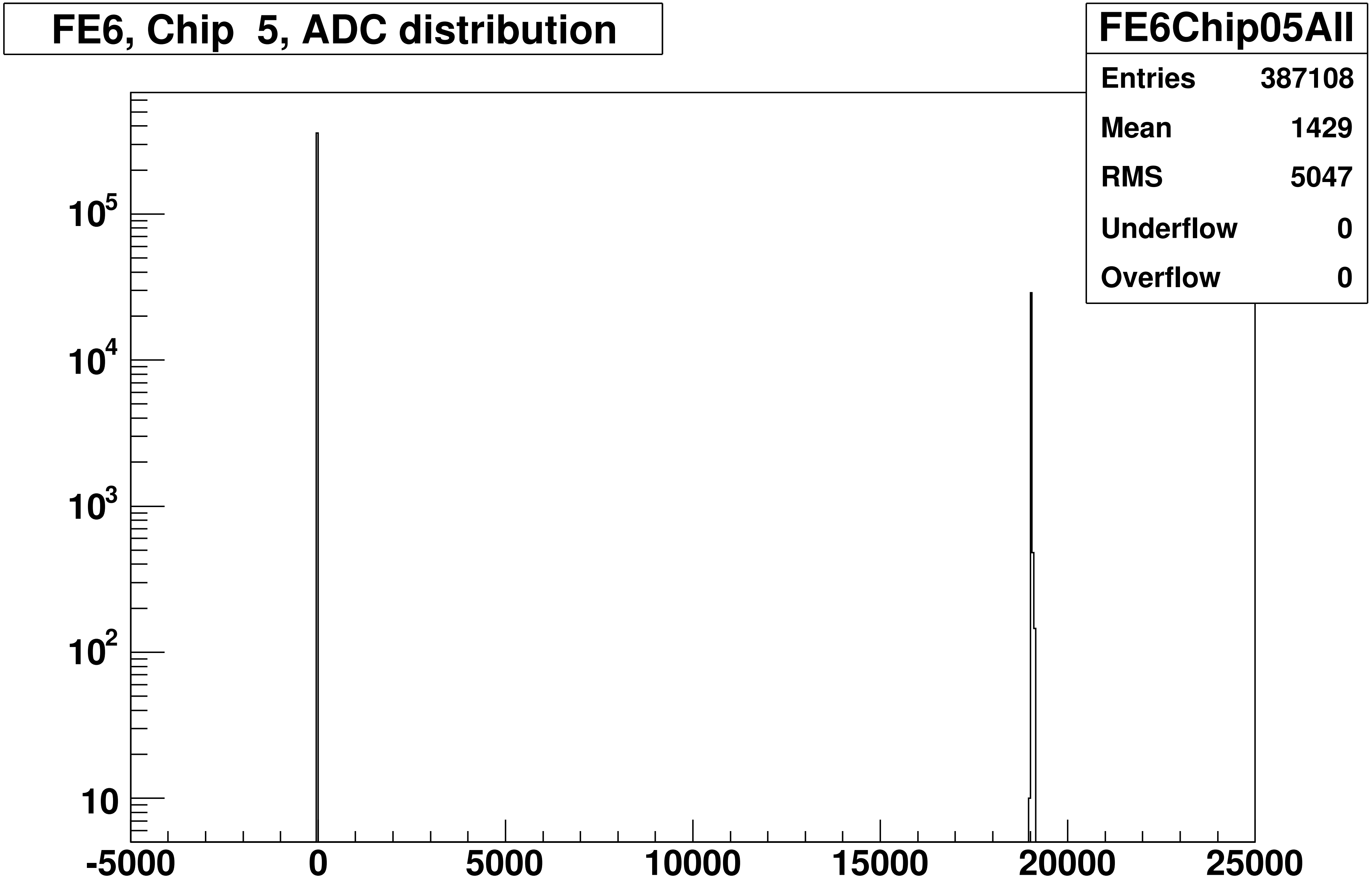


FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 387108
Mean 1404
RMS 5083
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

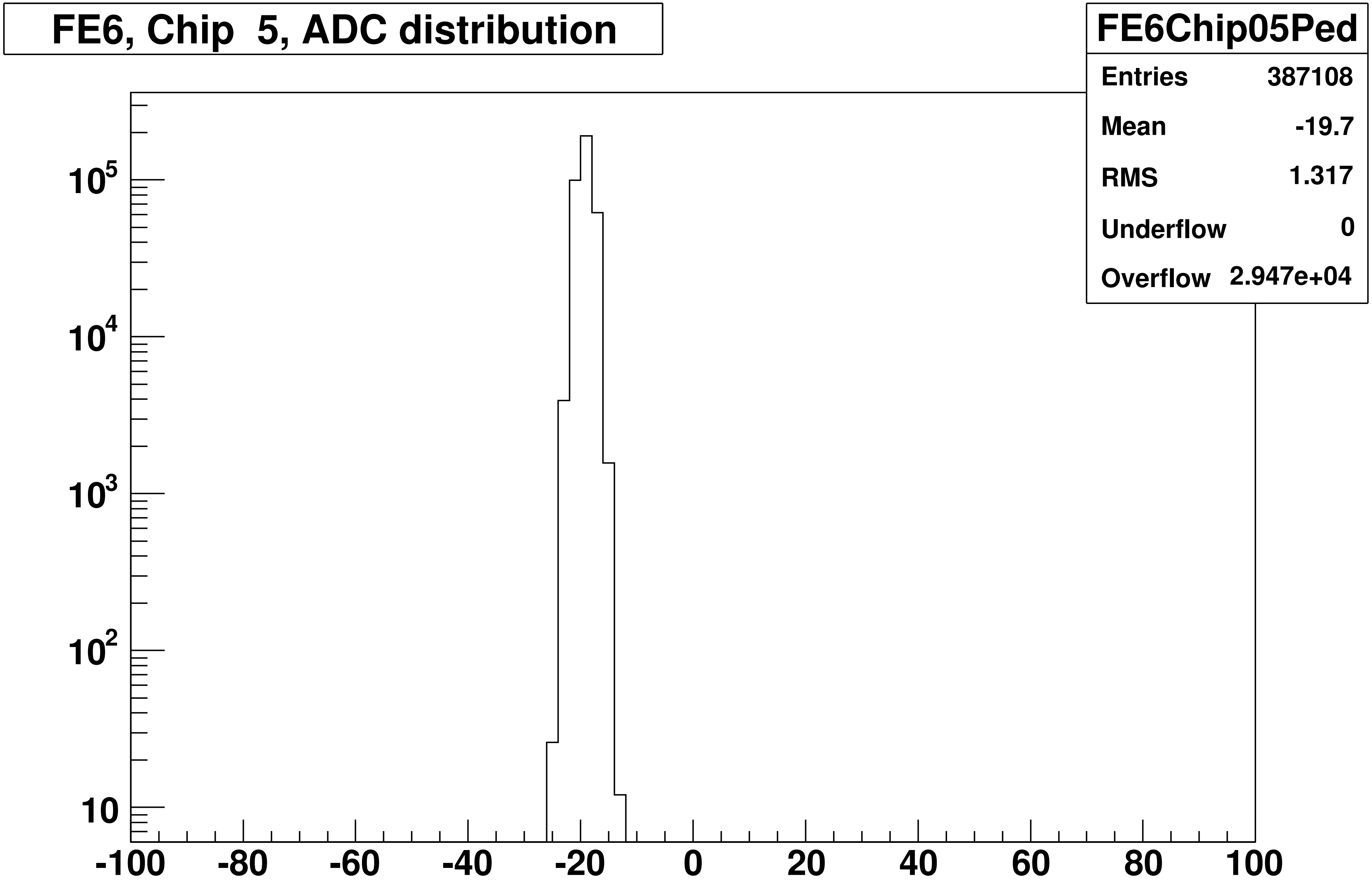


FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 387108
Mean -55.48
RMS 1.317
Underflow 0
Overflow 2.947e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

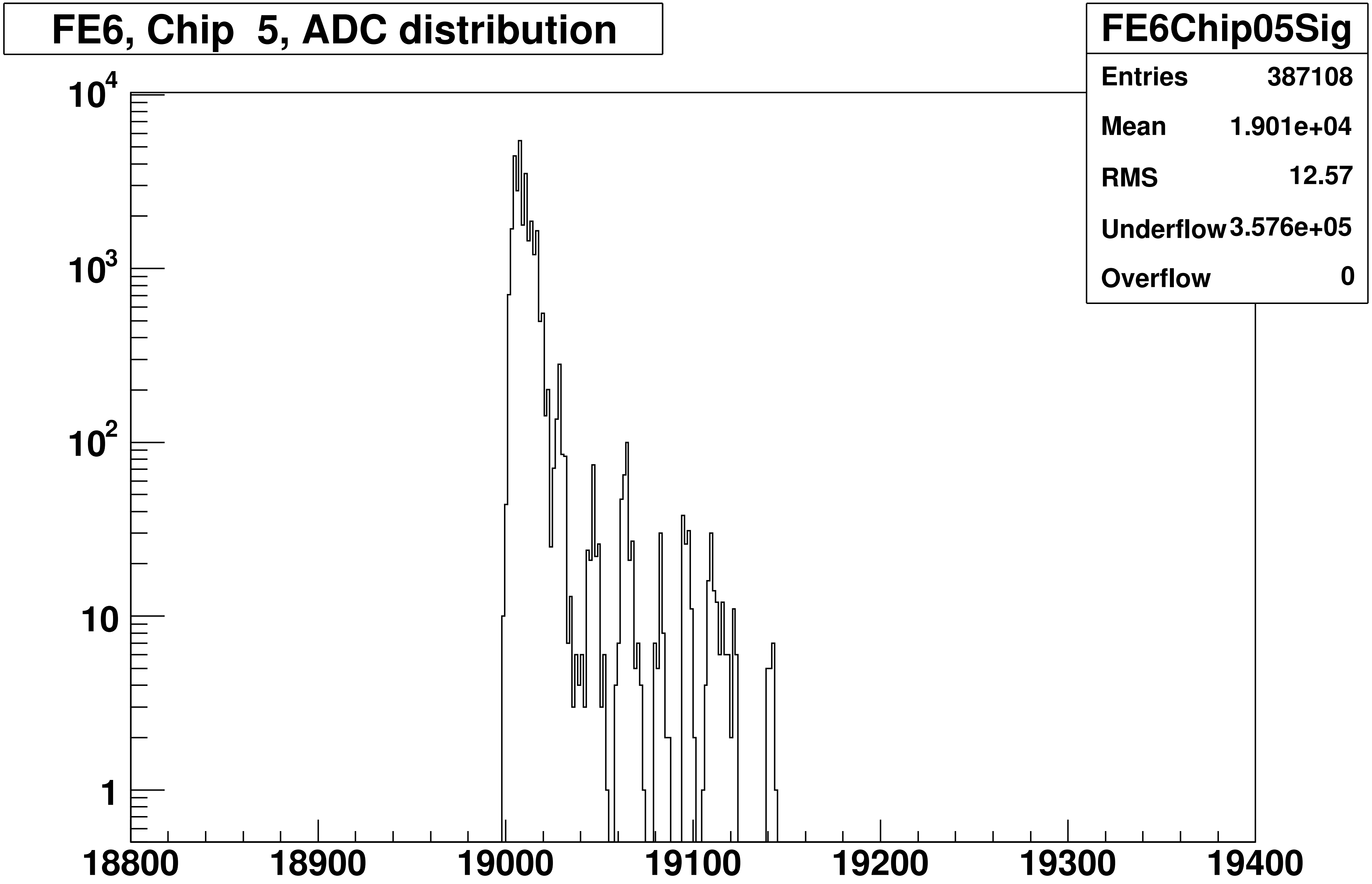

FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 387108
Mean 1.911e+04
RMS 11.71
Underflow 3.576e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

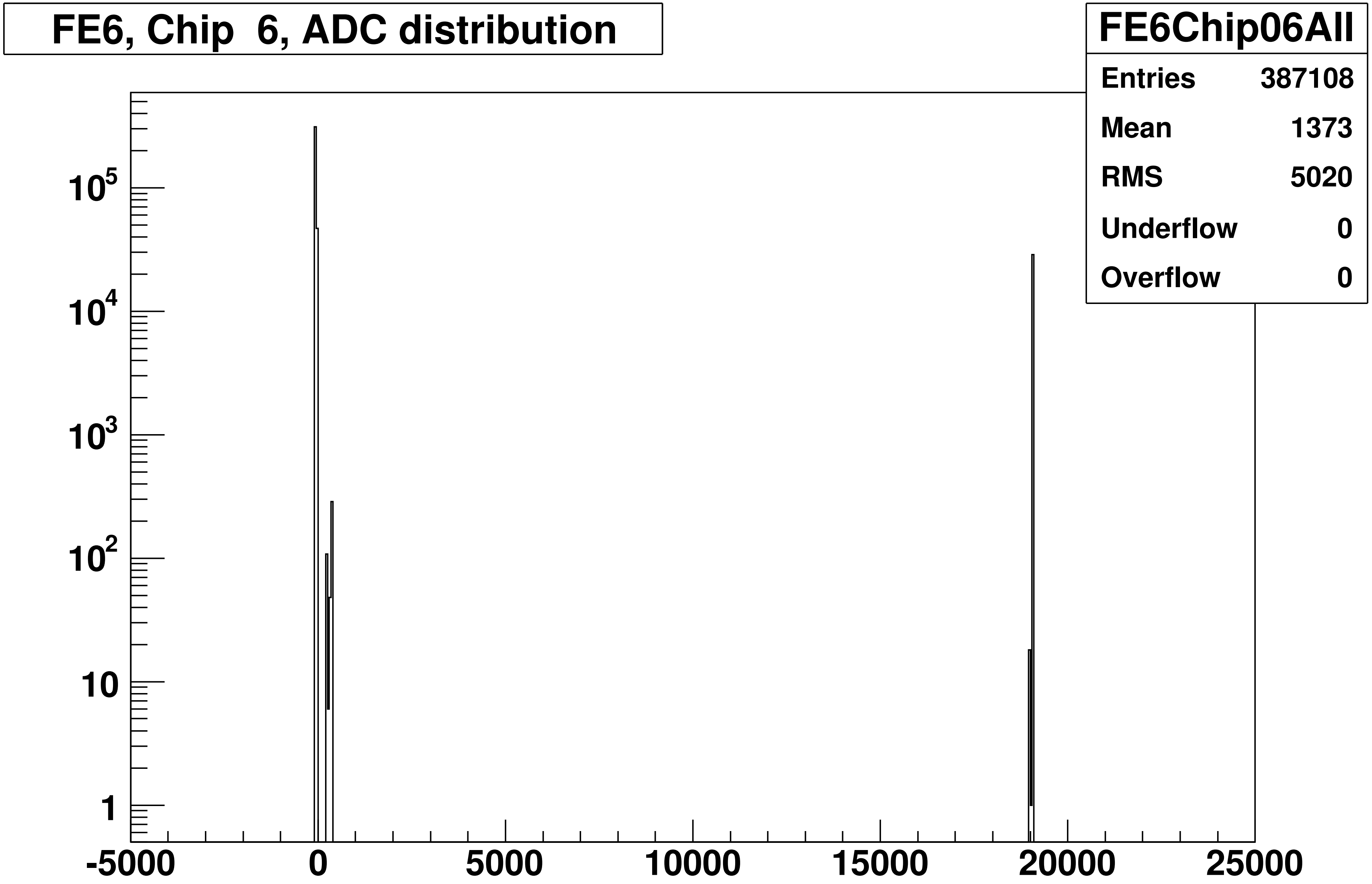


FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 387108
Mean 1429
RMS 5047
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
5000
10000
15000
20000
25000

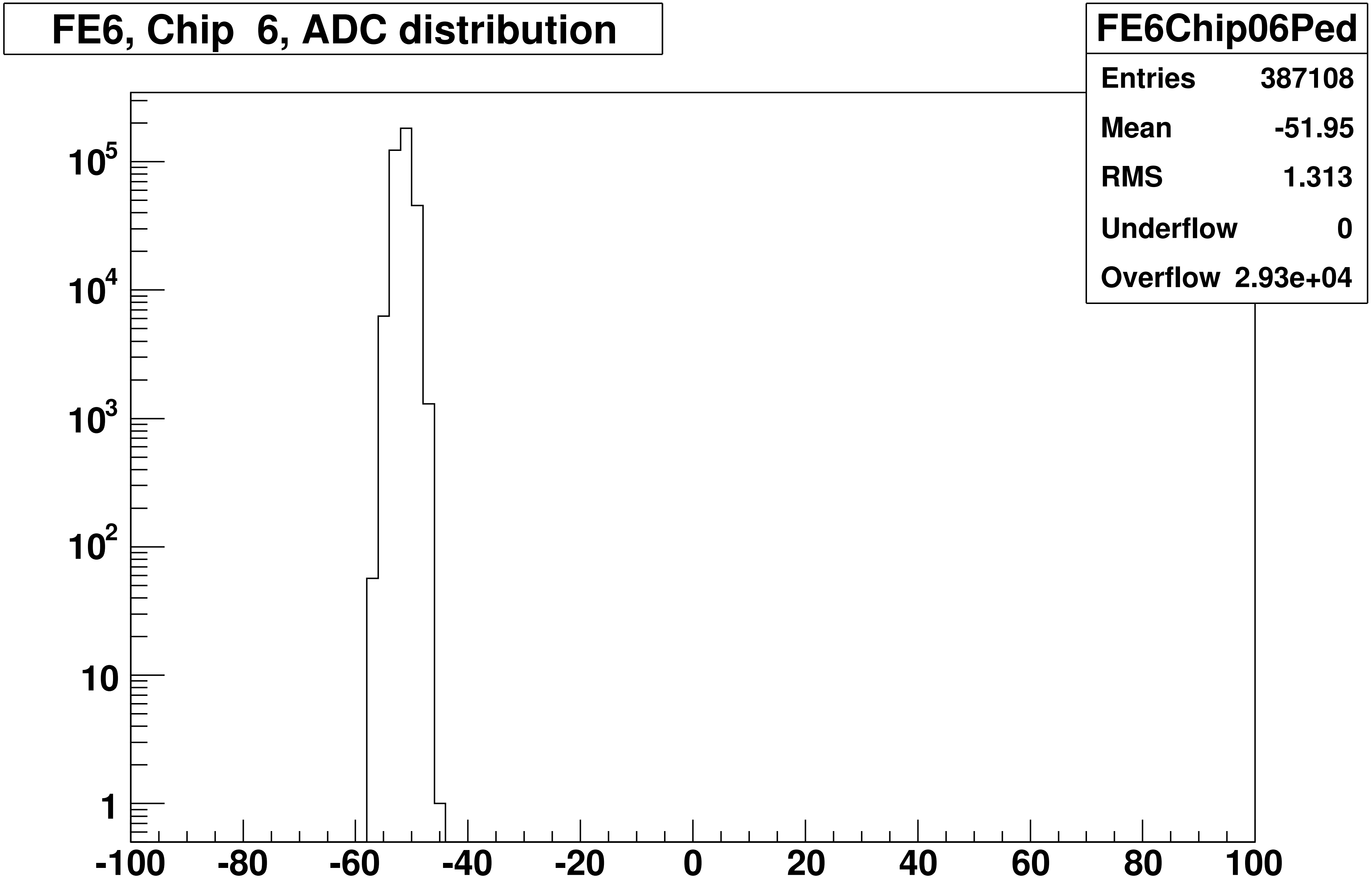


FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 387108
Mean -19.7
RMS 1.317
Underflow 0
Overflow 2.947e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

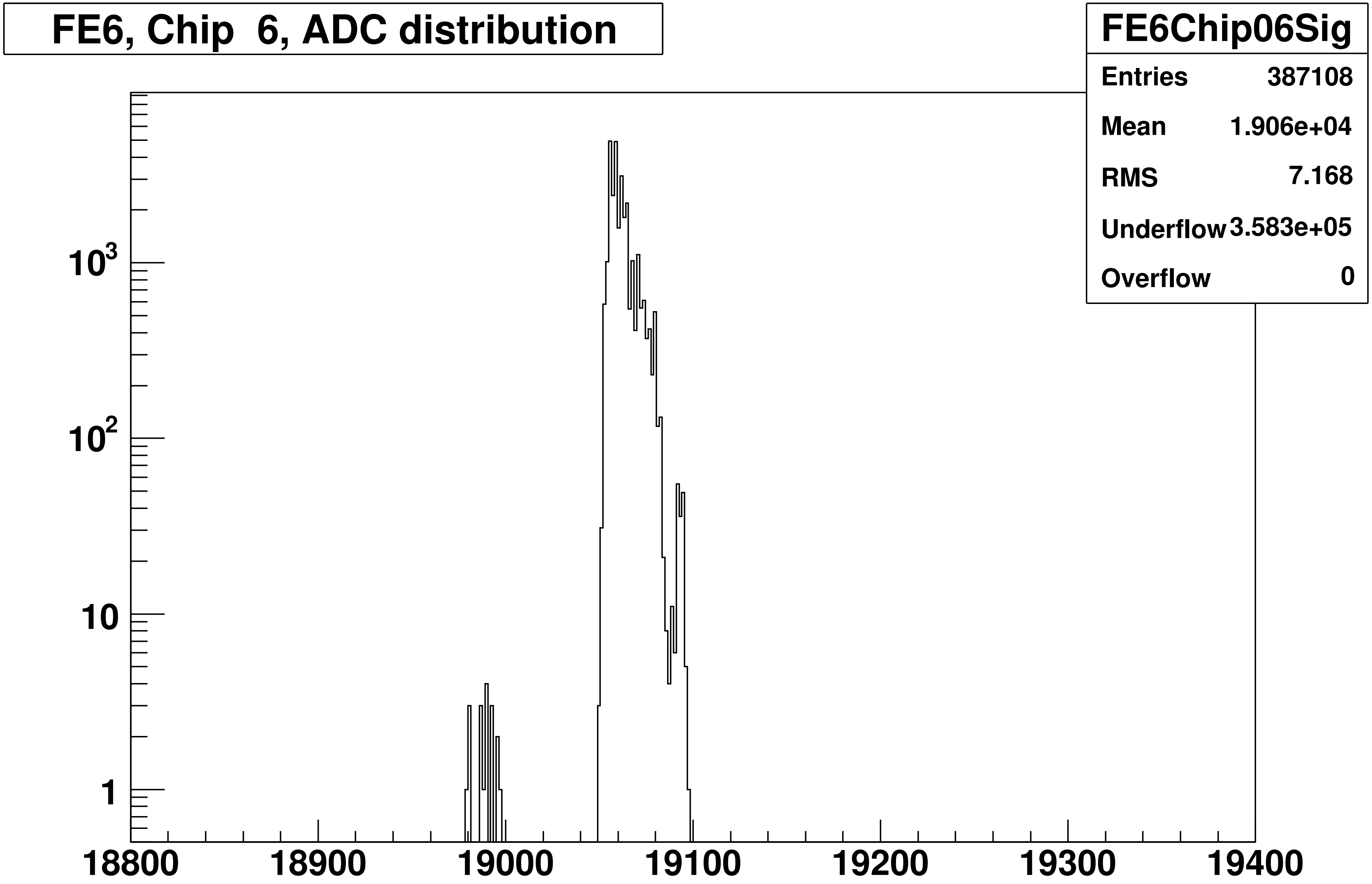

FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 387108
Mean 1.901e+04
RMS 12.57
Underflow 3.576e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



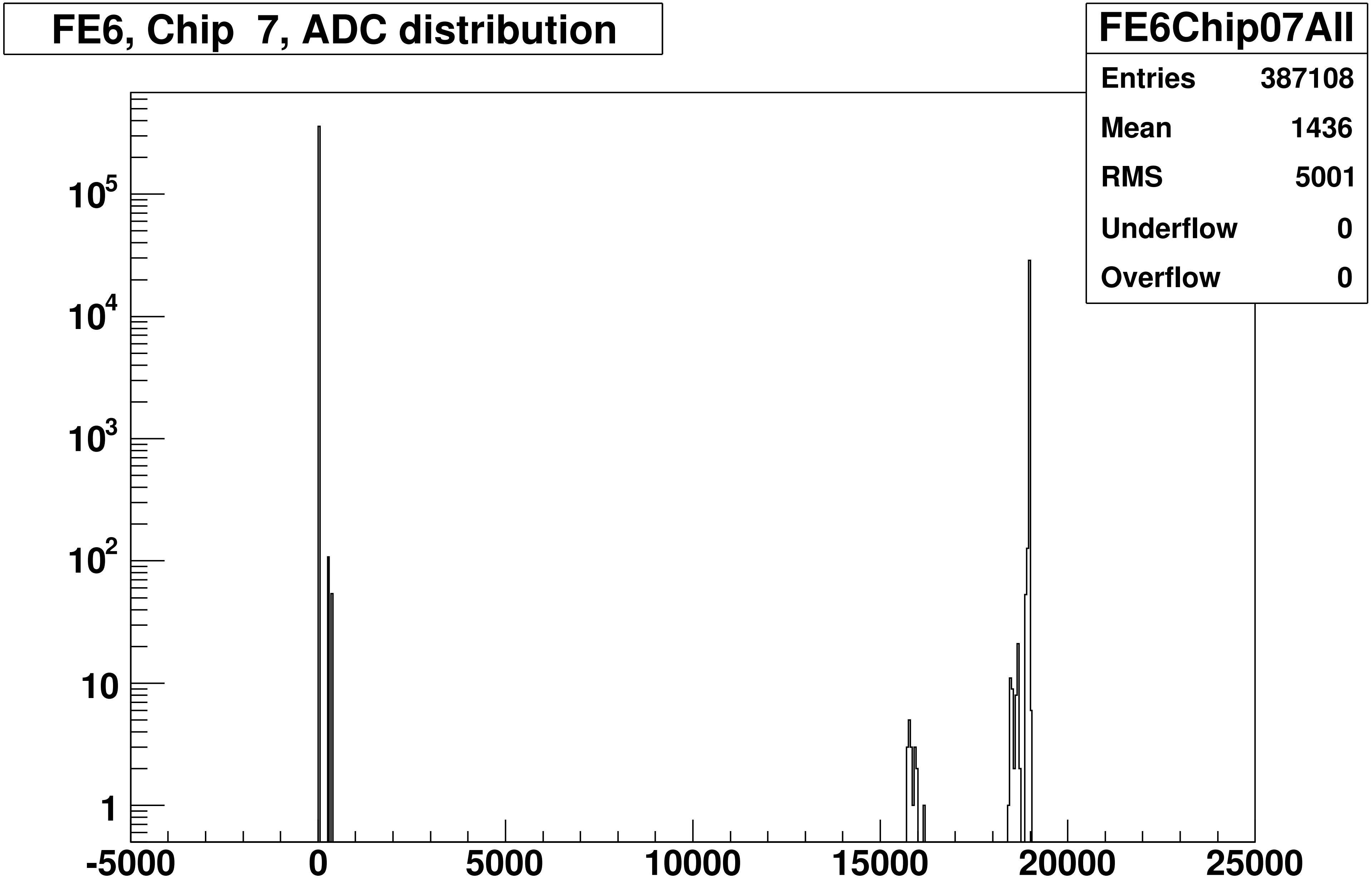
FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 387108
Mean 1373
RMS 5020
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



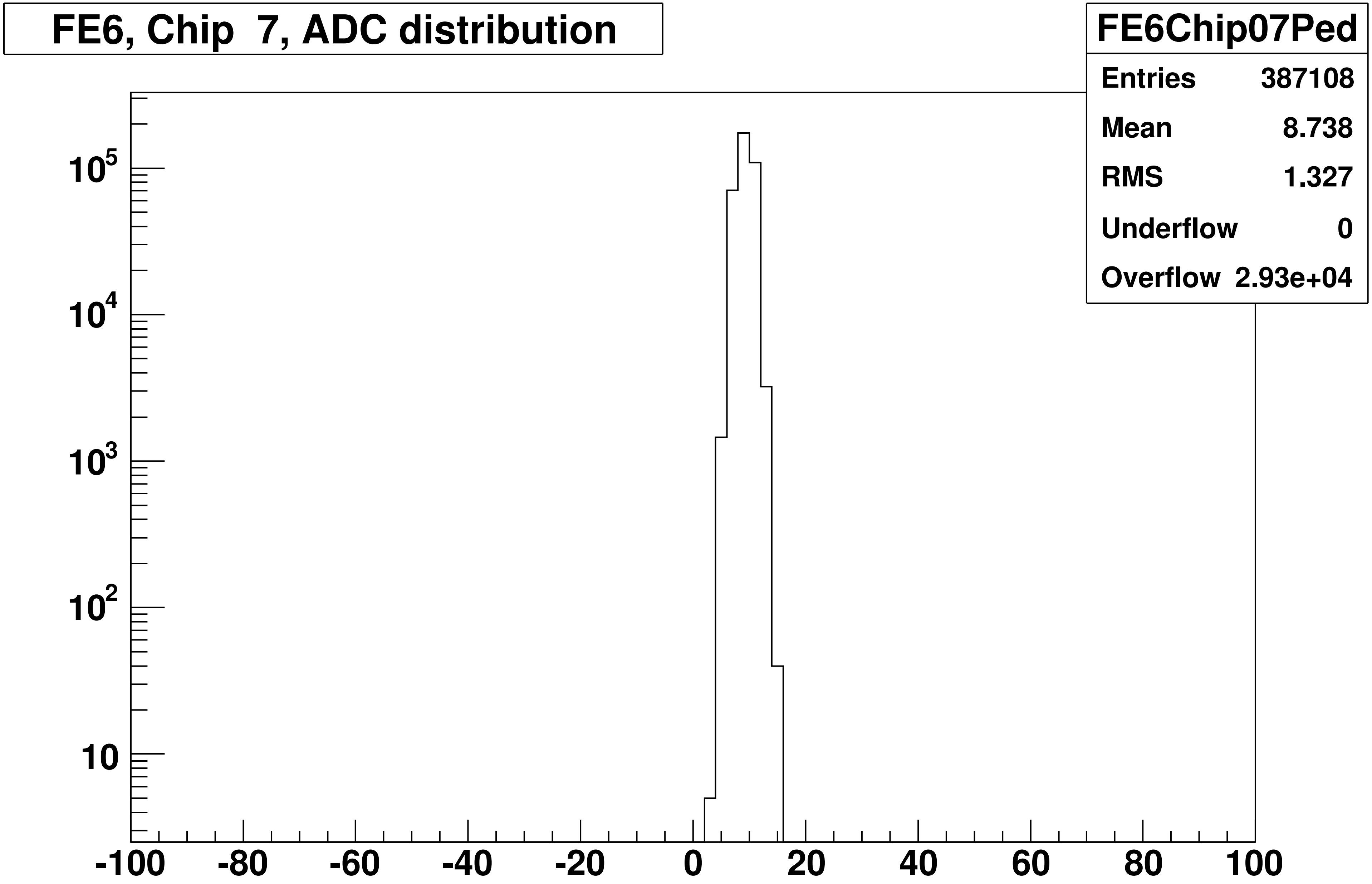
FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 387108
Mean -51.95
RMS 1.313
Underflow 0
Overflow 2.93e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

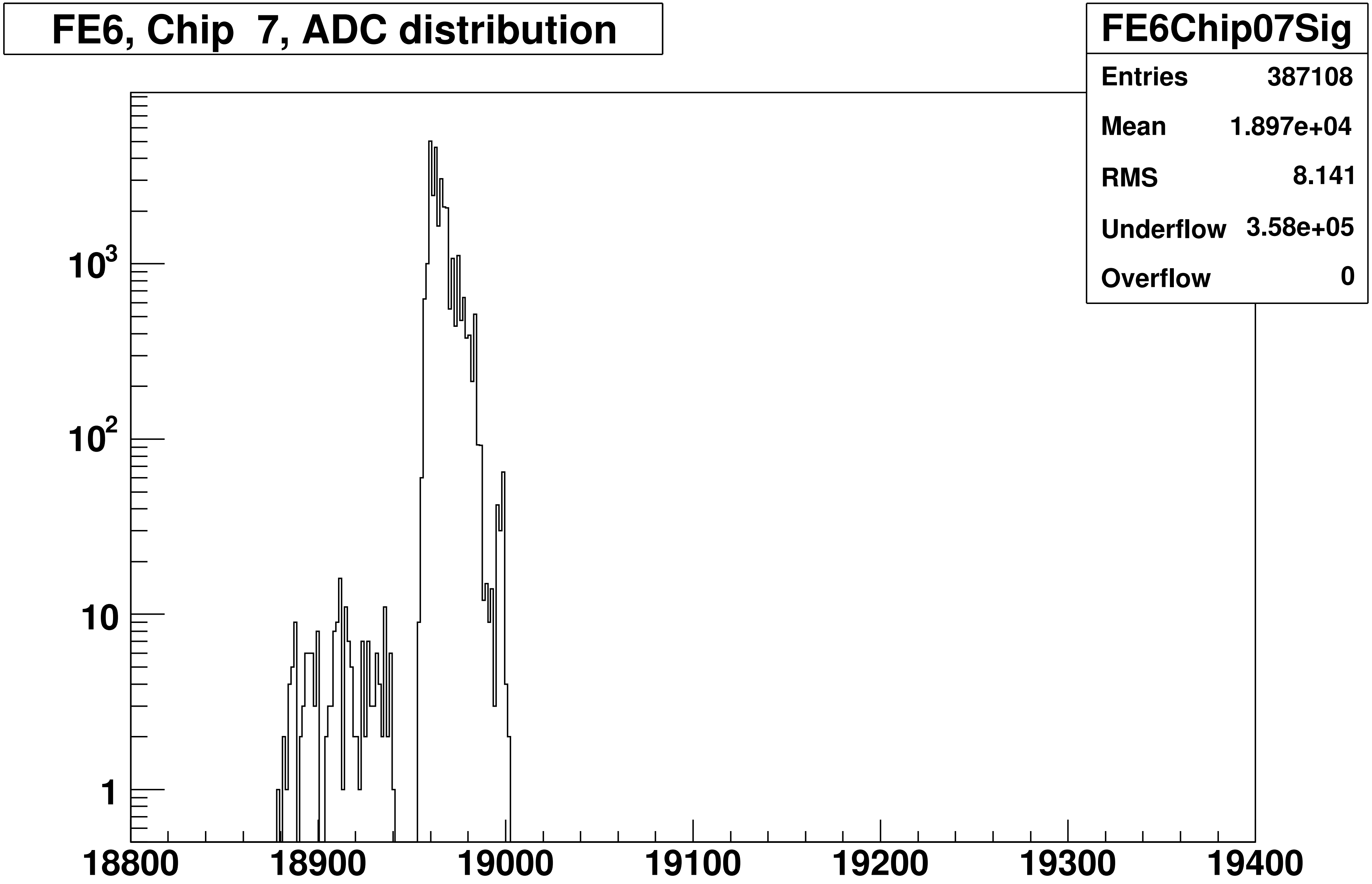


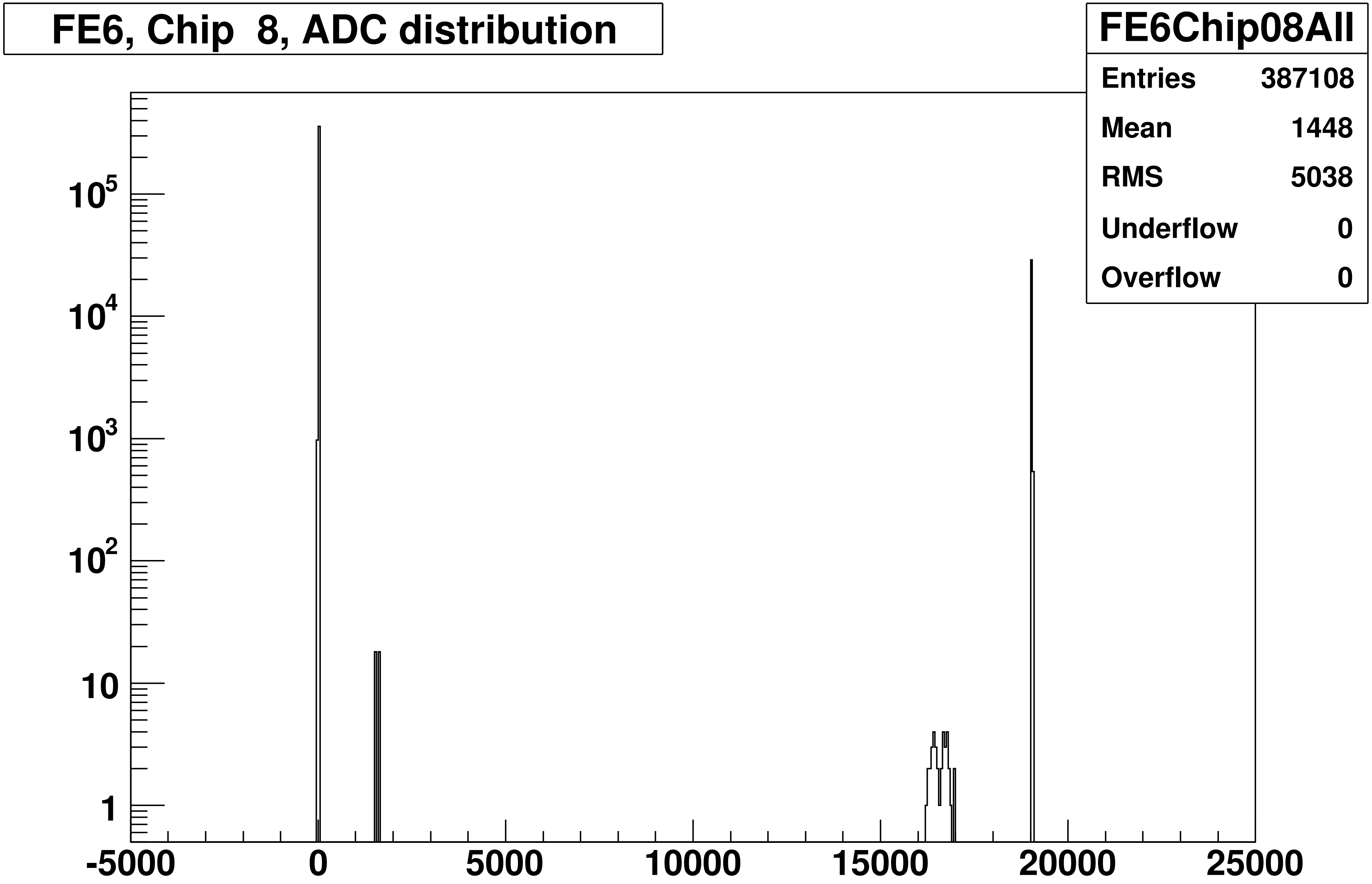
FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 387108
Mean 1.906e+04
RMS 7.168
Underflow 3.583e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



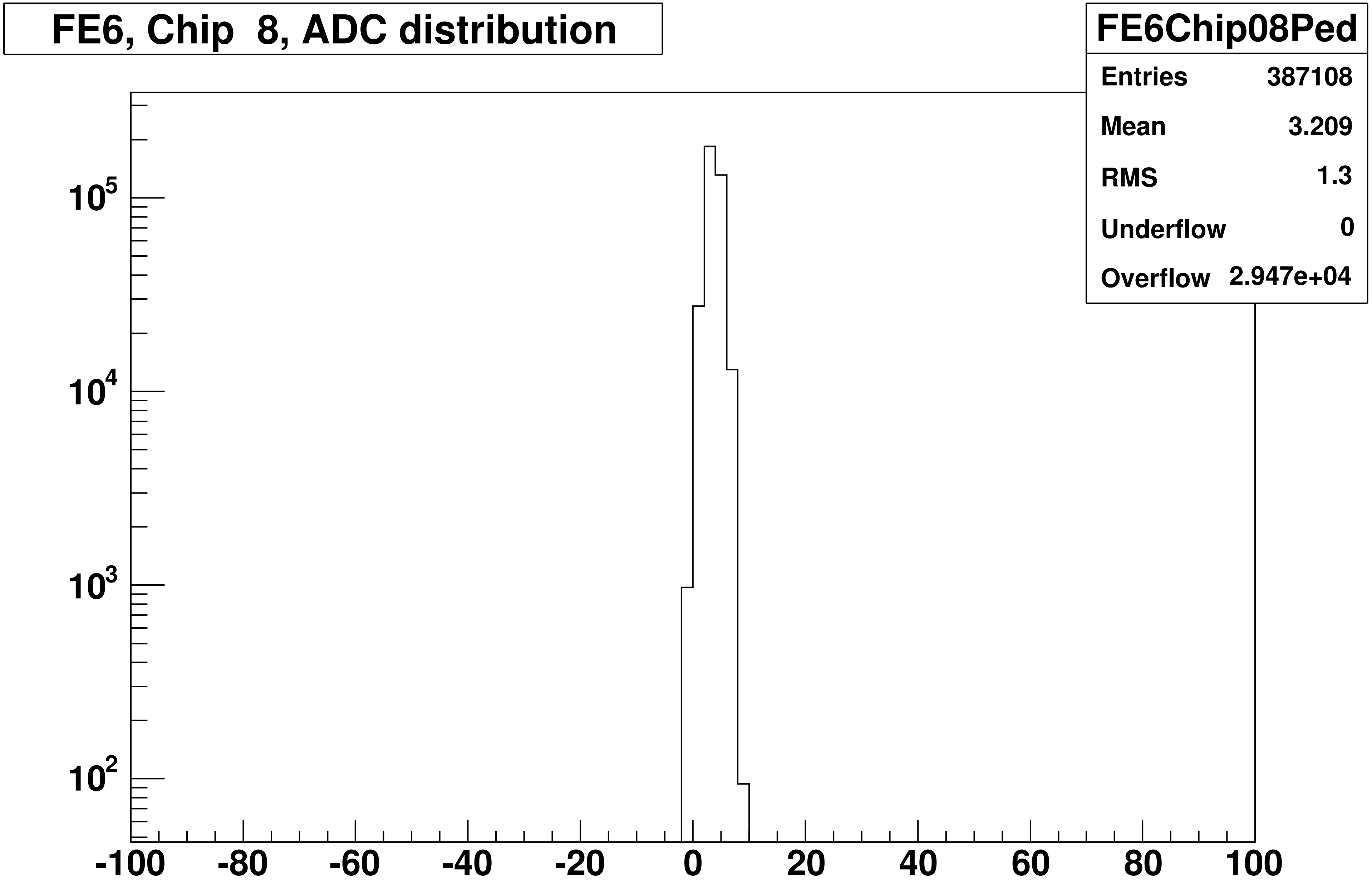
FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 387108
Mean 1436
RMS 5001
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

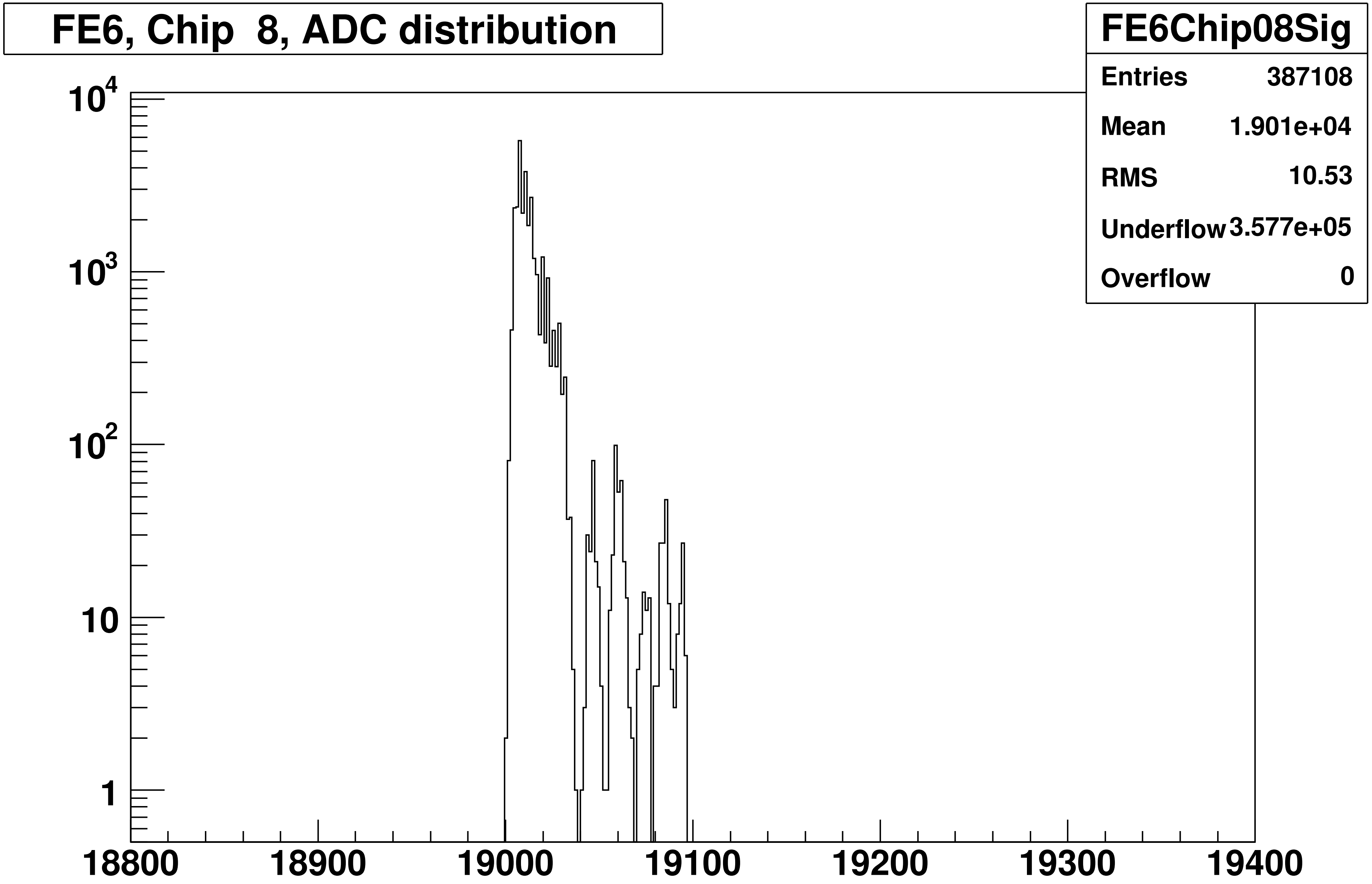

FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 387108
Mean 8.738
RMS 1.327
Underflow 0
Overflow 2.93e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

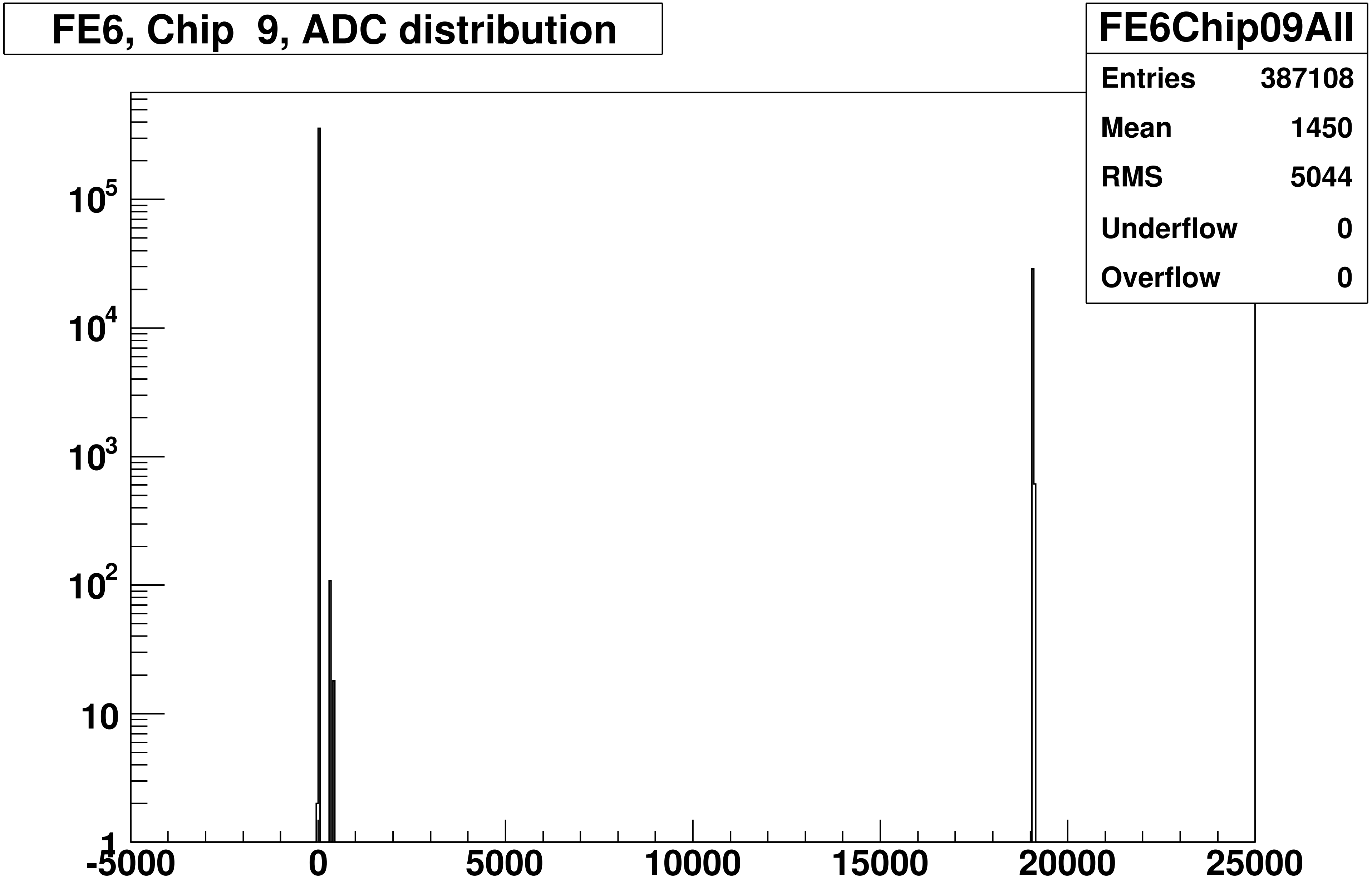

FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 387108
Mean 1.897e+04
RMS 8.141
Underflow 3.58e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

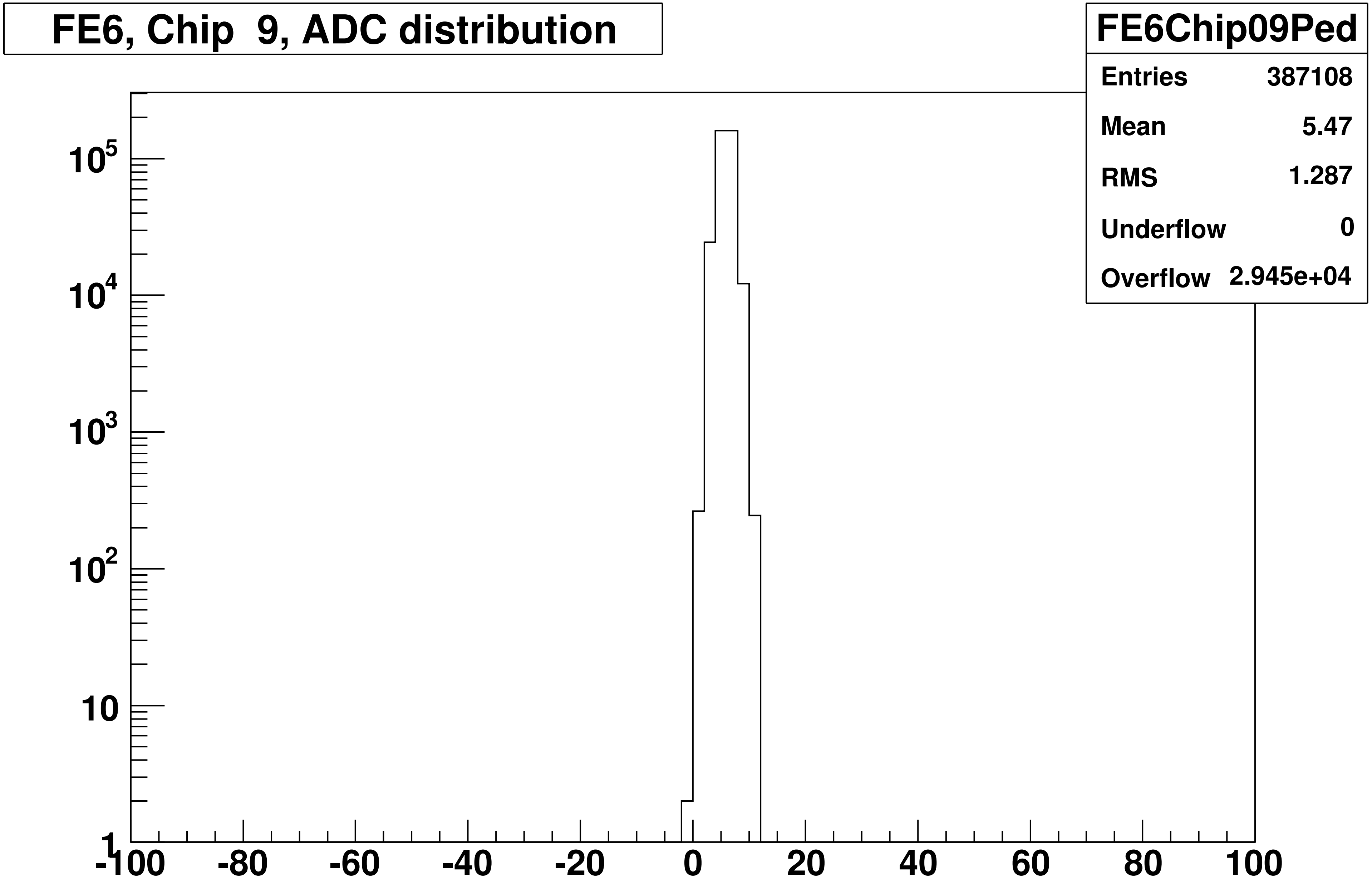


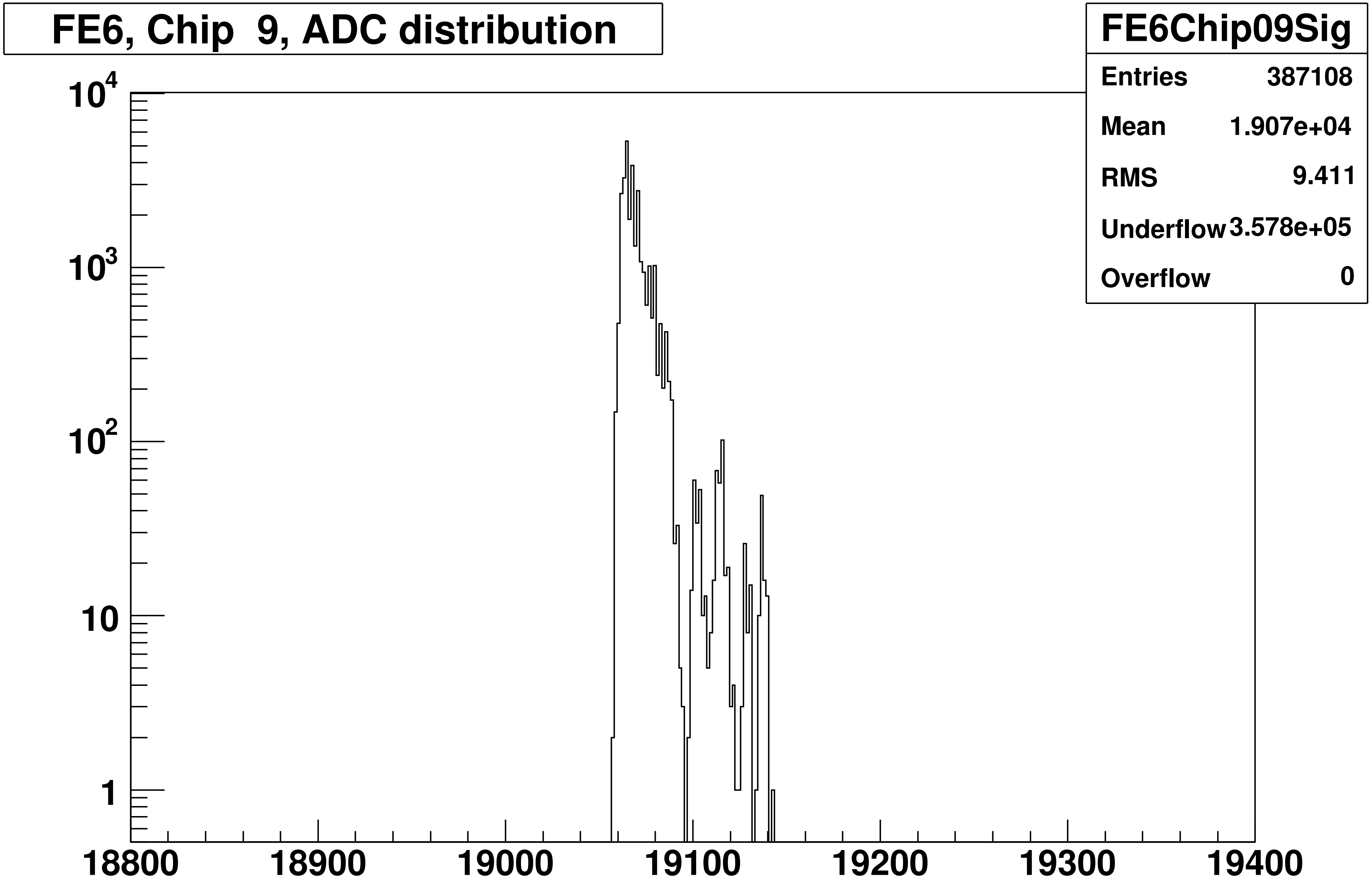
FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 387108
Mean 1448
RMS 5038
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 387108
Mean 3.209
RMS 1.3
Underflow 0
Overflow 2.947e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

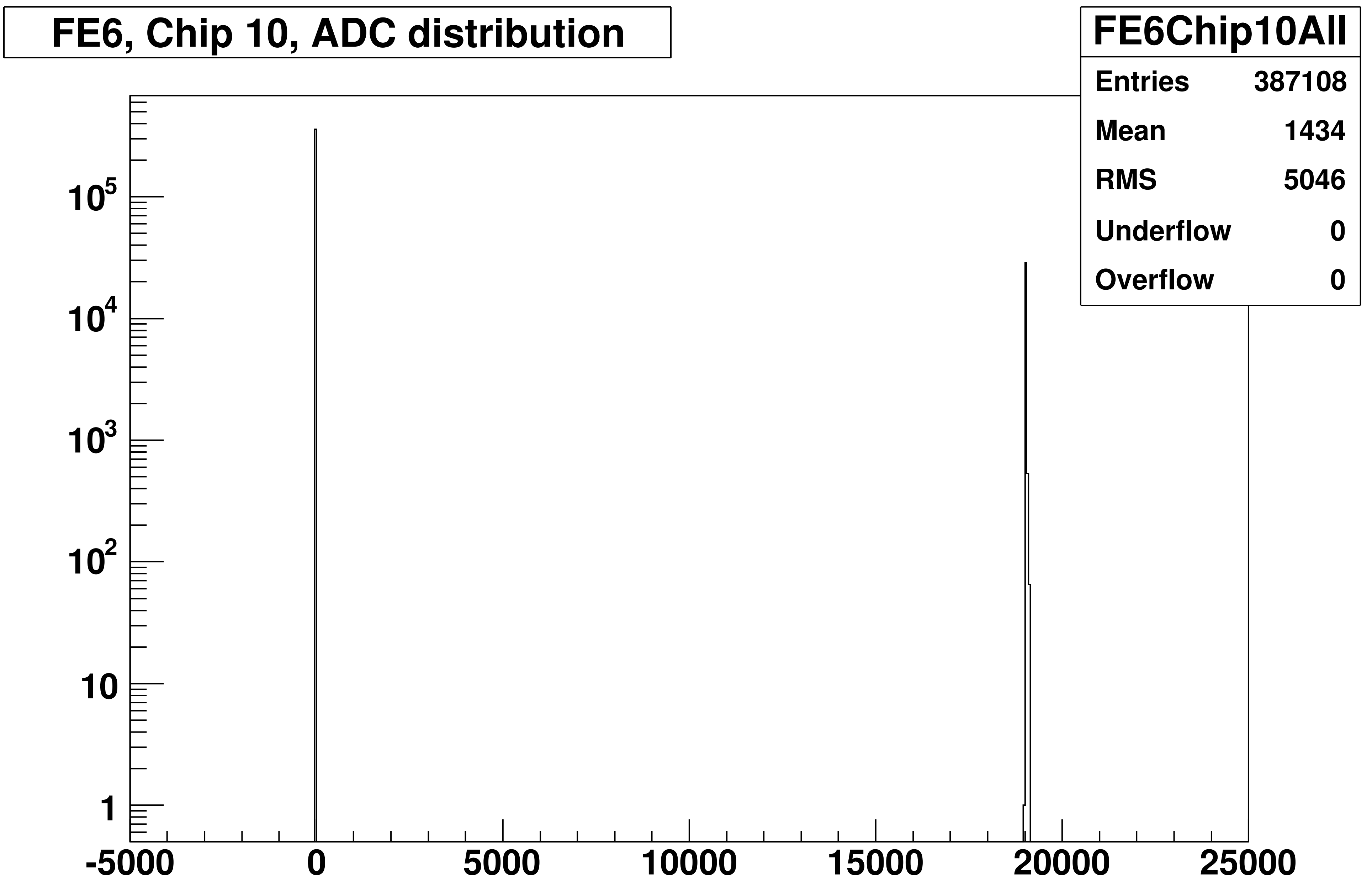

FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 387108
Mean 1.901e+04
RMS 10.53
Underflow 3.577e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

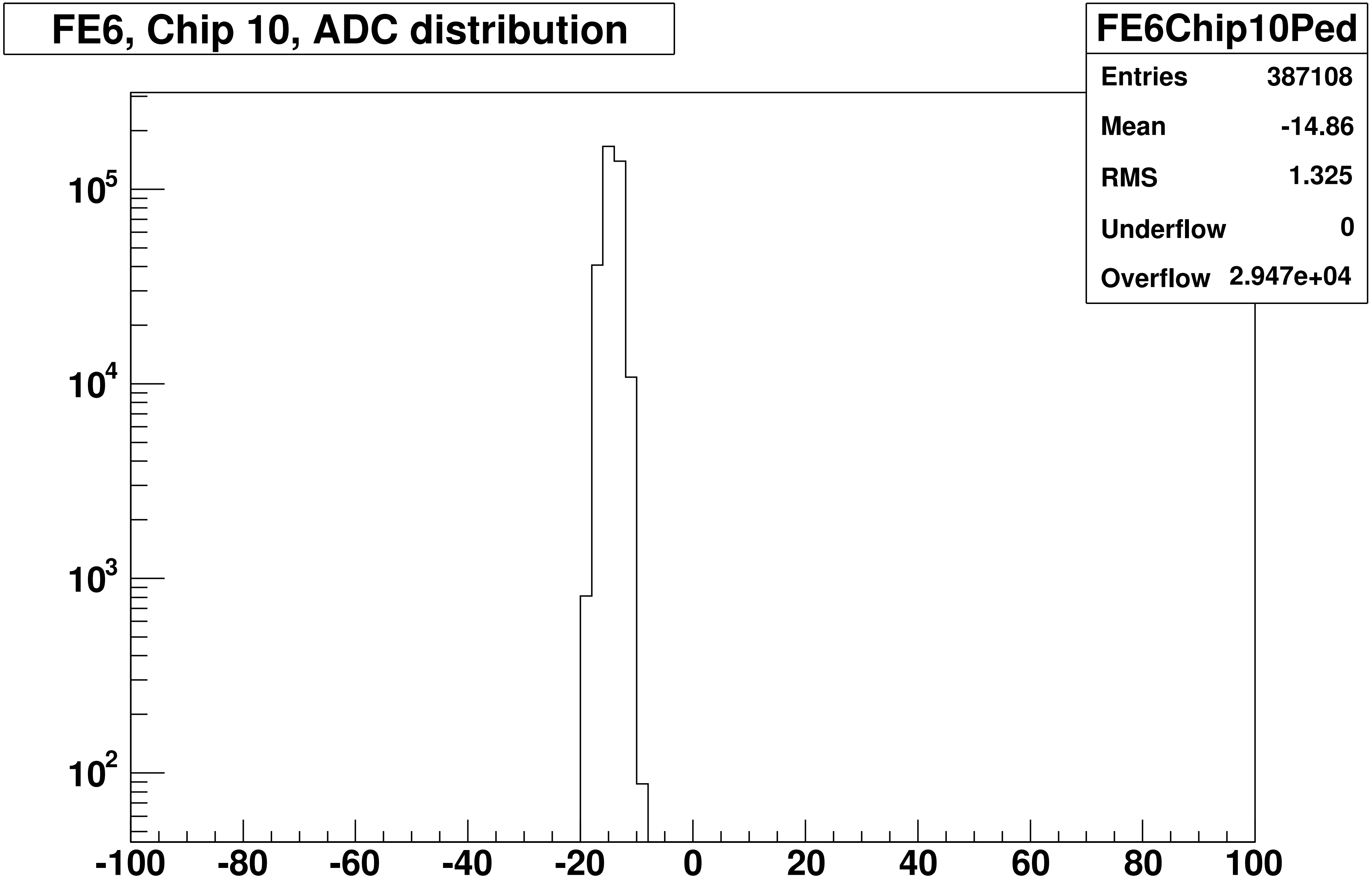

FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 387108
Mean 1450
RMS 5044
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 387108
Mean 5.47
RMS 1.287
Underflow 0
Overflow 2.945e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

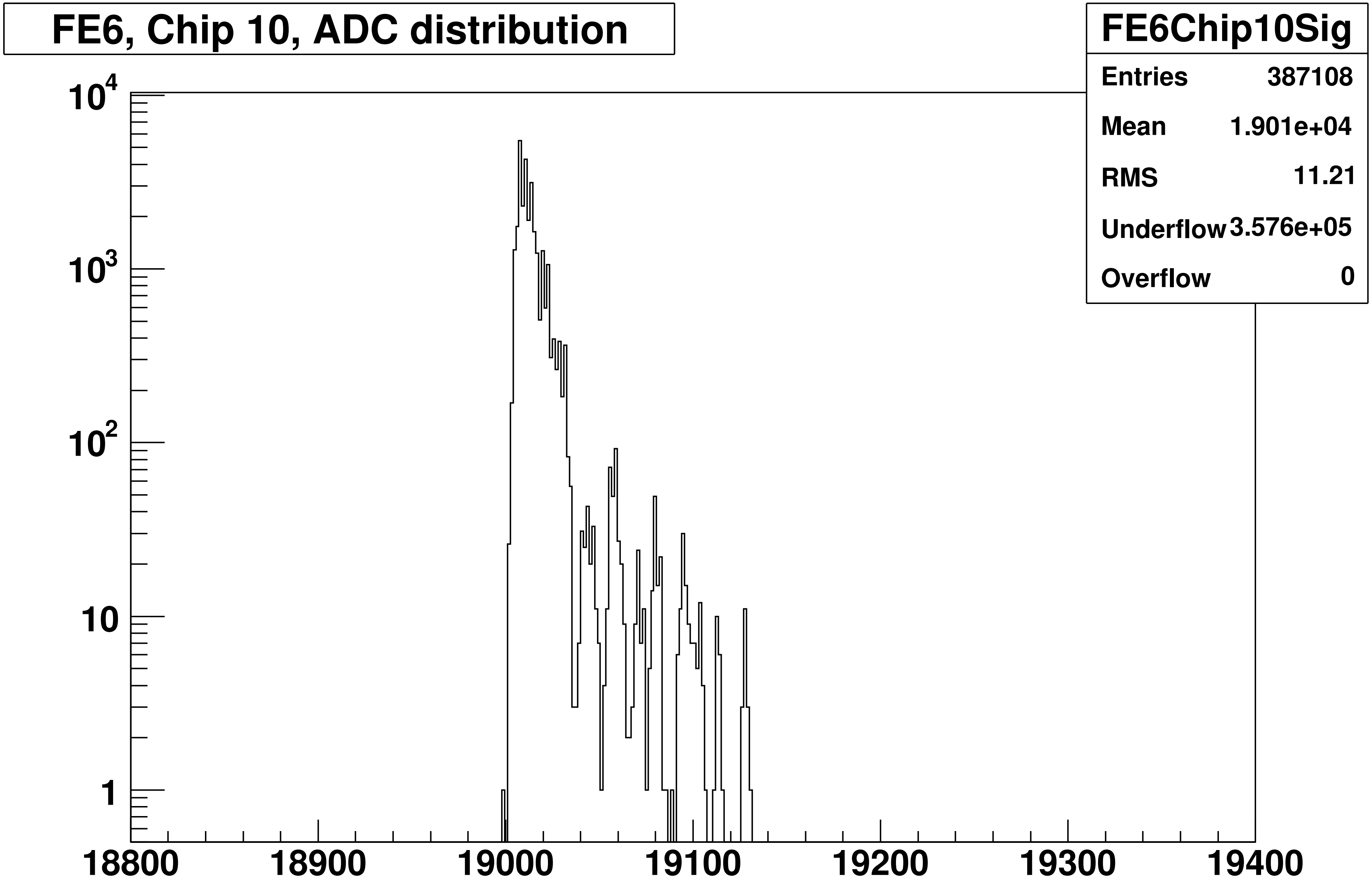


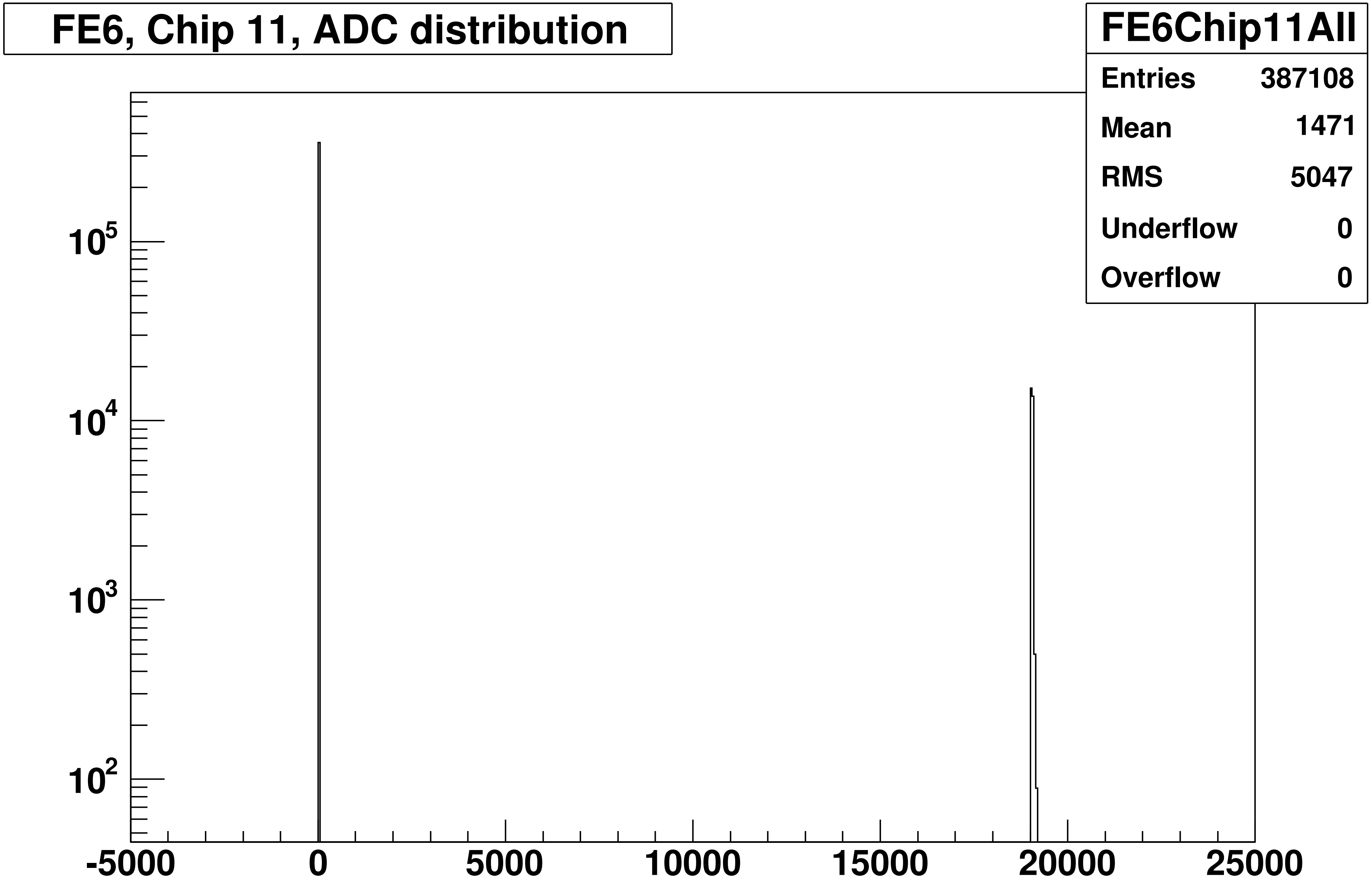
FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 387108
Mean 1.907e+04
RMS 9.411
Underflow 3.578e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 387108
Mean 1434
RMS 5046
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

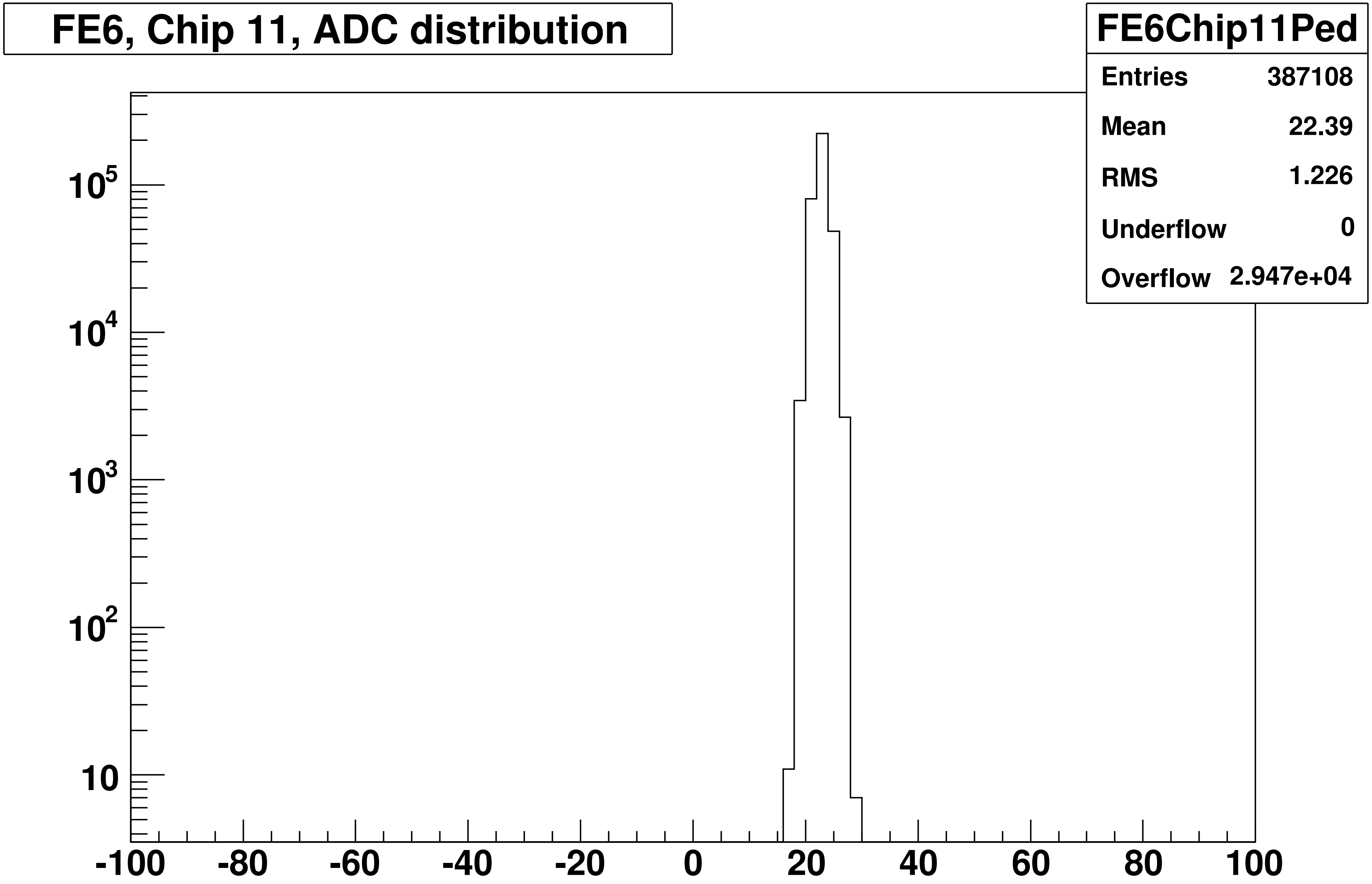


FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 387108
Mean -14.86
RMS 1.325
Underflow 0
Overflow 2.947e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

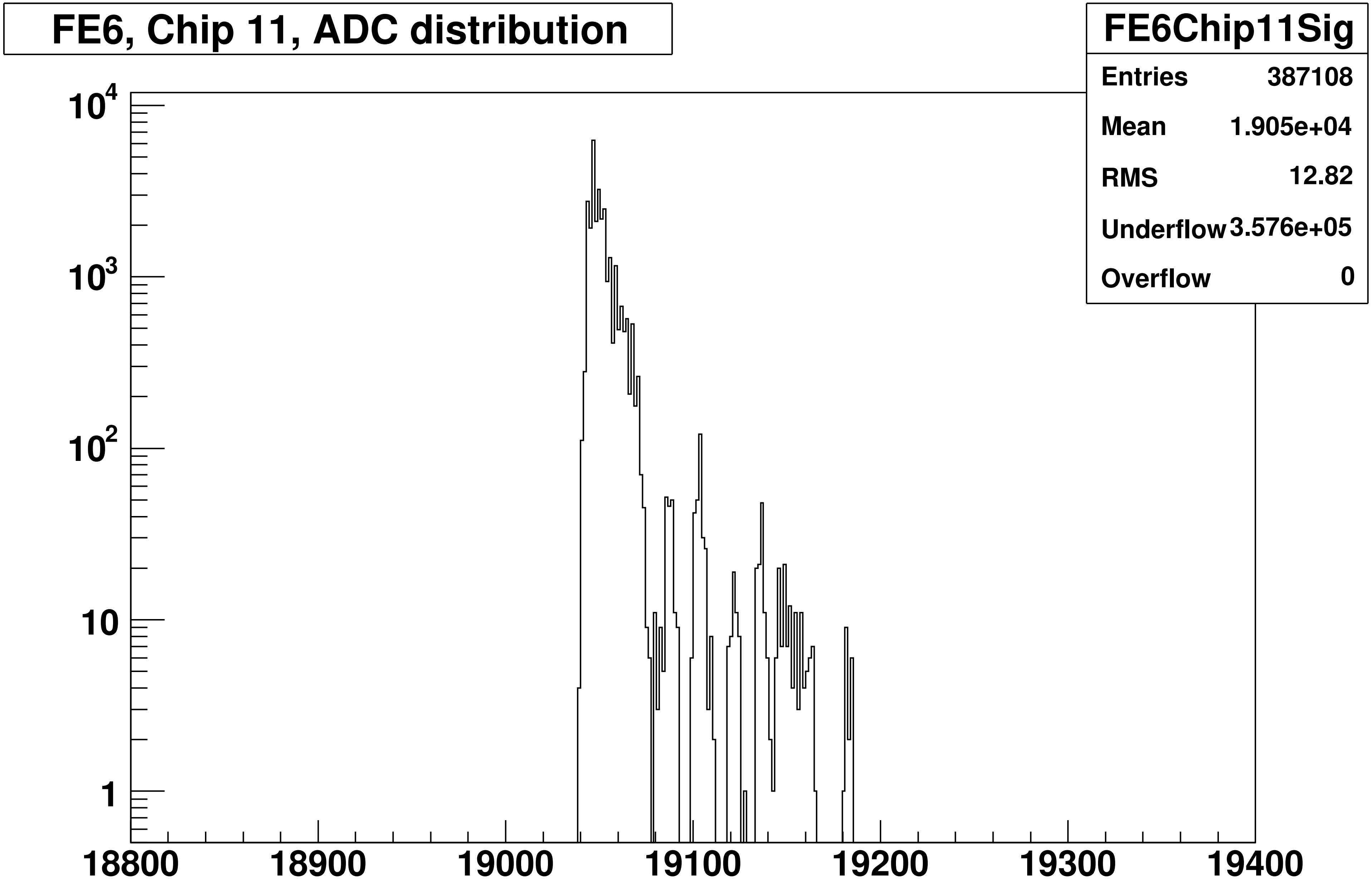

FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 387108
Mean 1.901e+04
RMS 11.21
Underflow 3.576e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

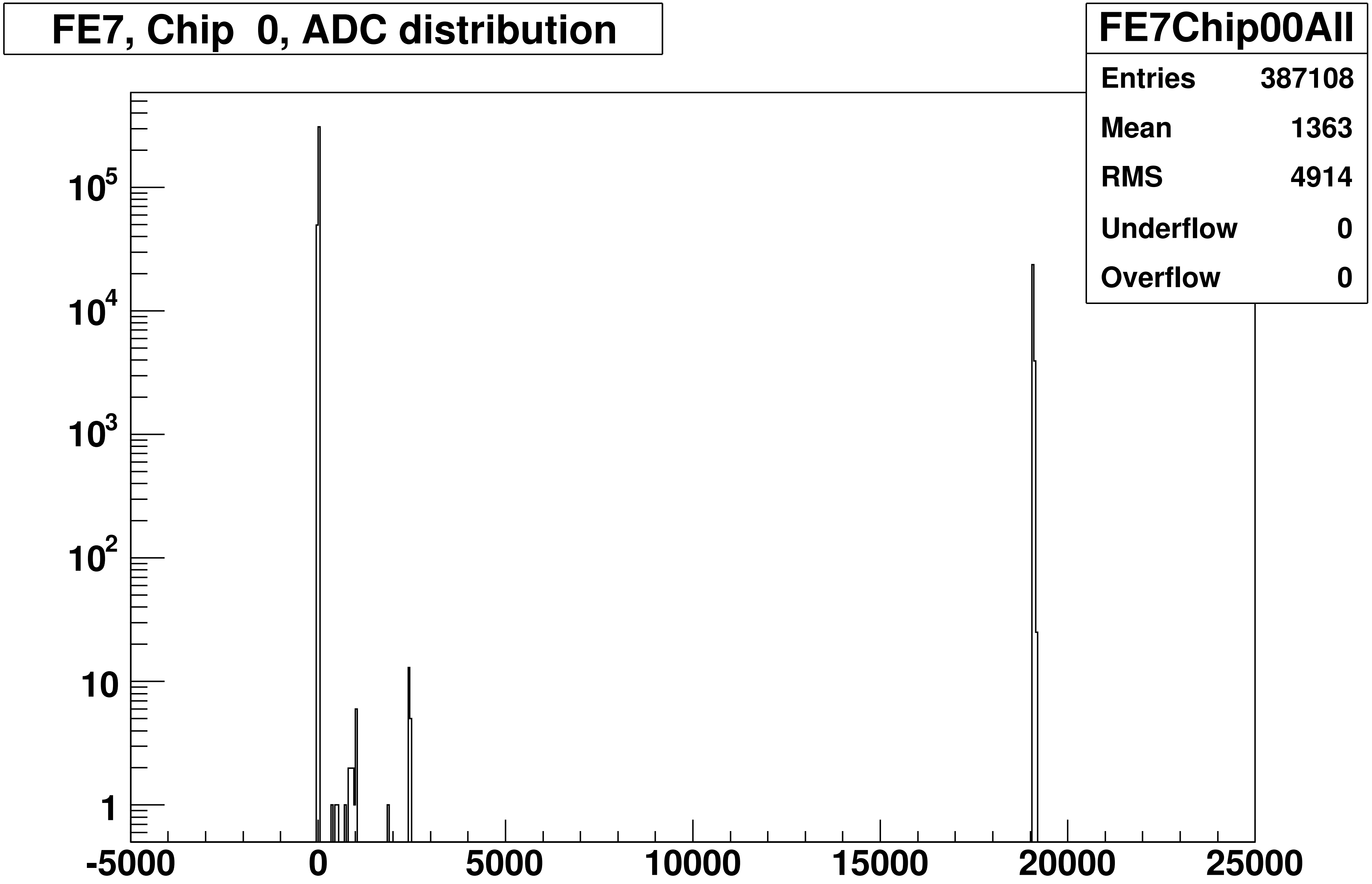


FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 387108
Mean 1471
RMS 5047
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000

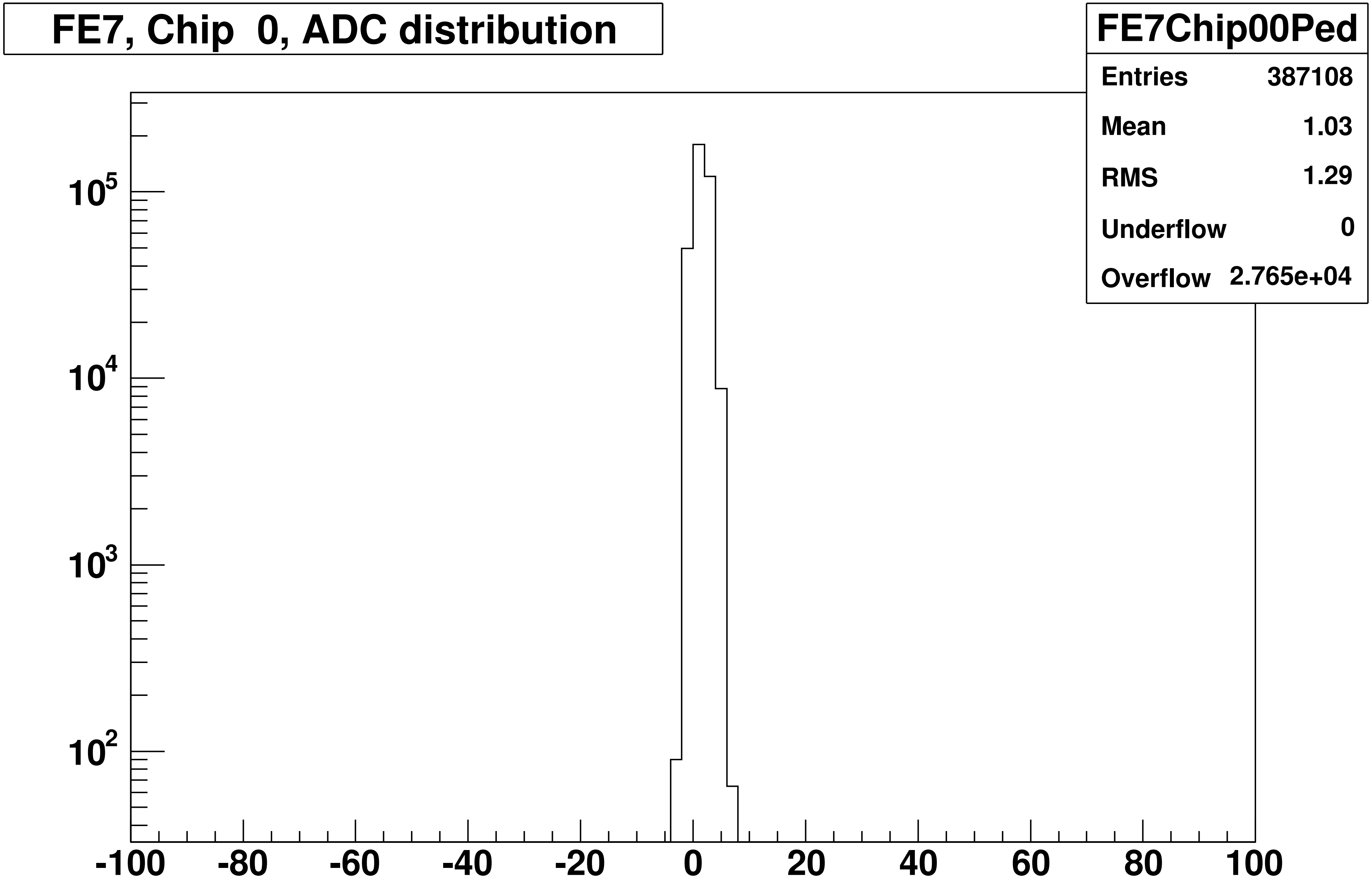


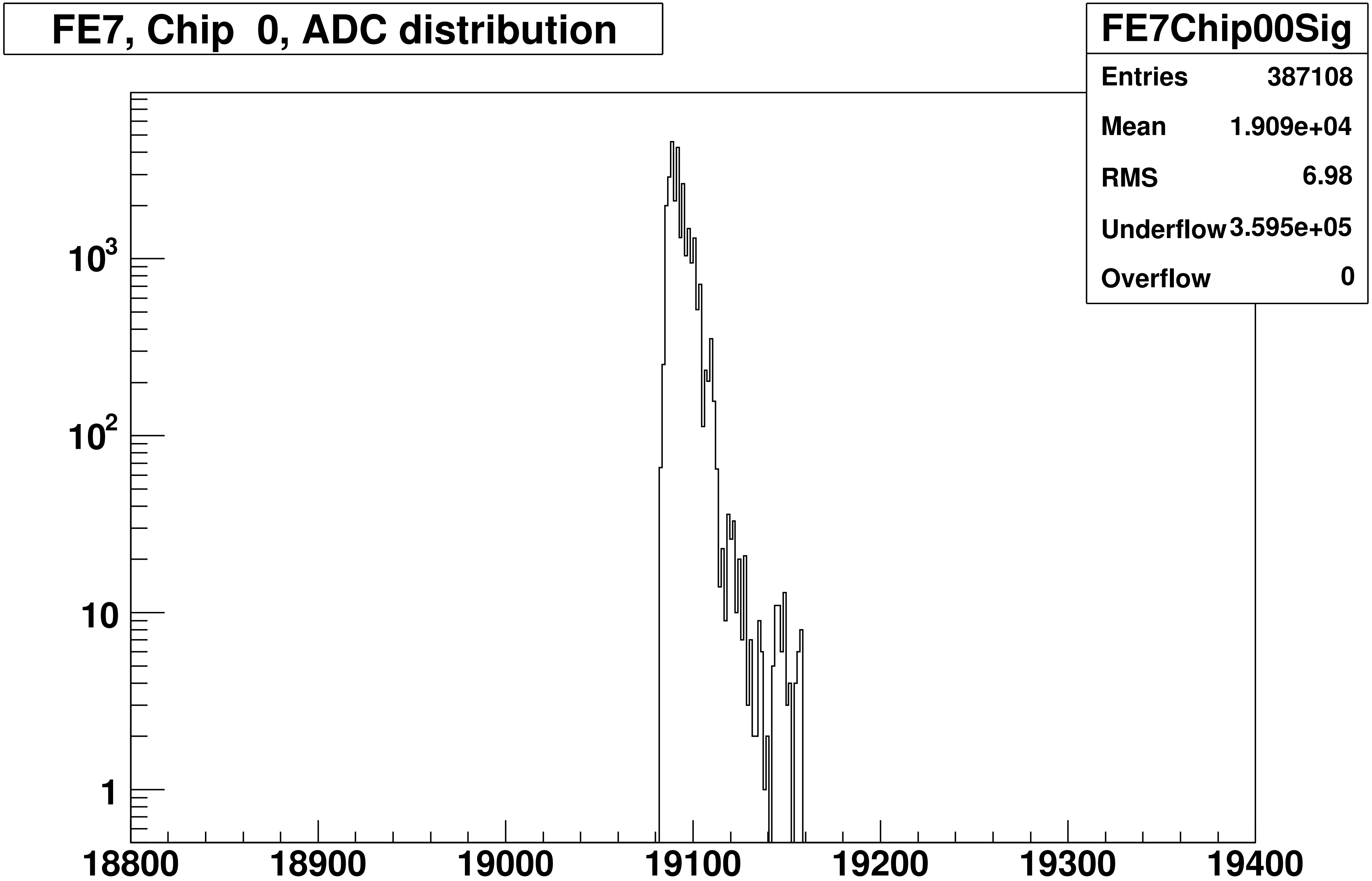
FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 387108
Mean 22.39
RMS 1.226
Underflow 0
Overflow 2.947e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 387108
Mean 1.905e+04
RMS 12.82
Underflow 3.576e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

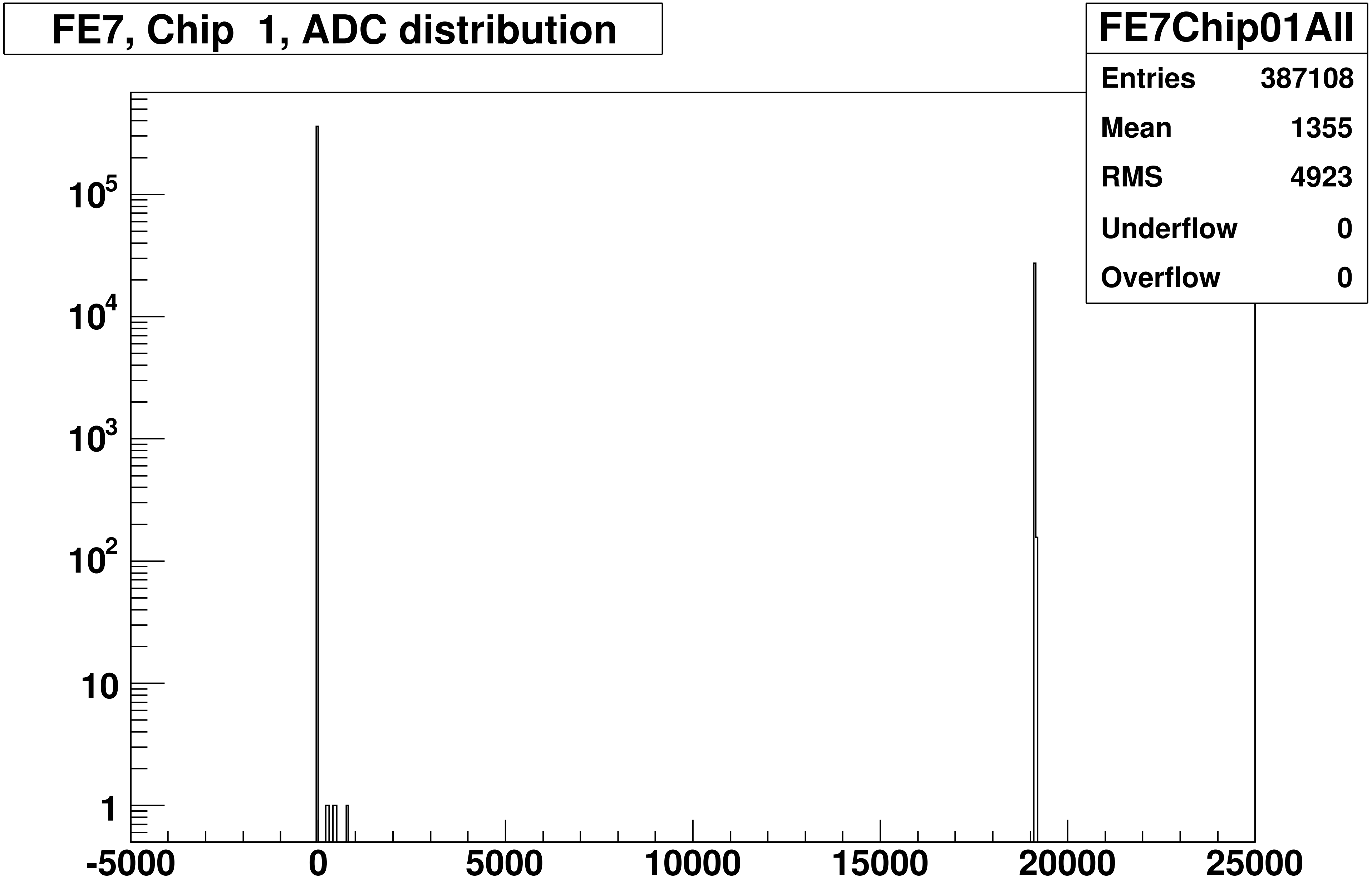


FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 387108
Mean 1363
RMS 4914
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

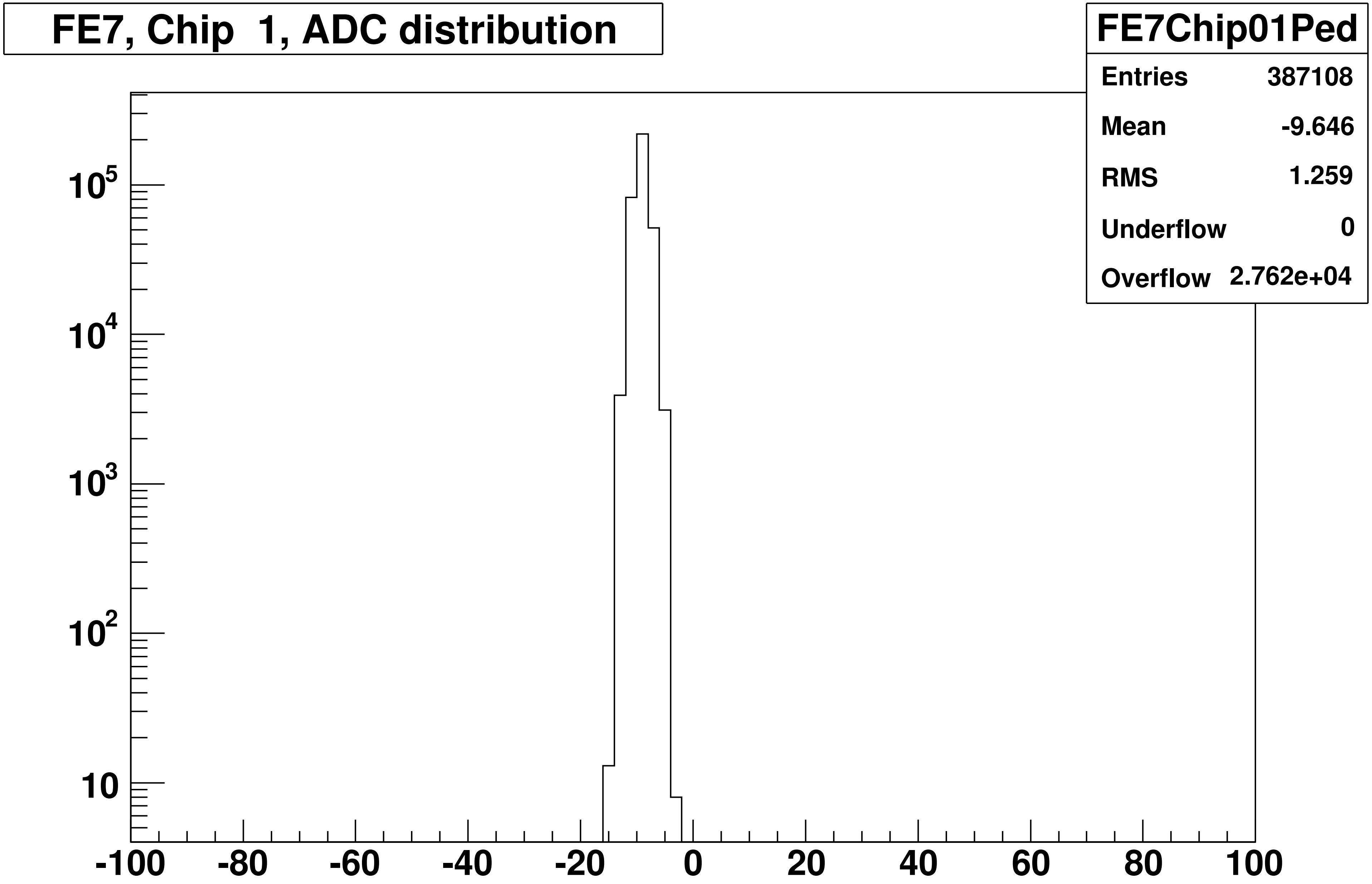

FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 387108
Mean 1.03
RMS 1.29
Underflow 0
Overflow 2.765e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100



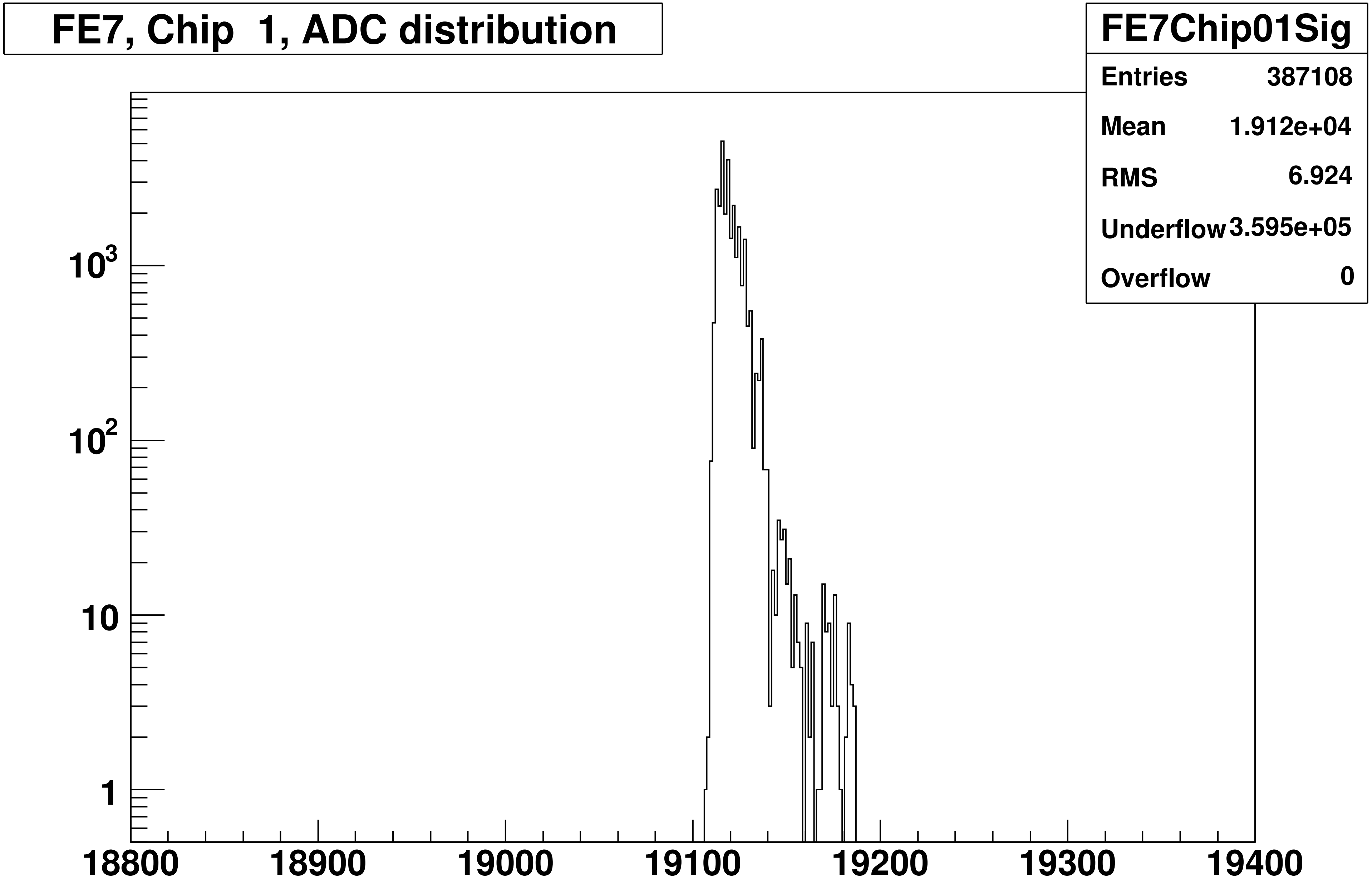
FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 387108
Mean 1.909e+04
RMS 6.98
Underflow 3.595e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

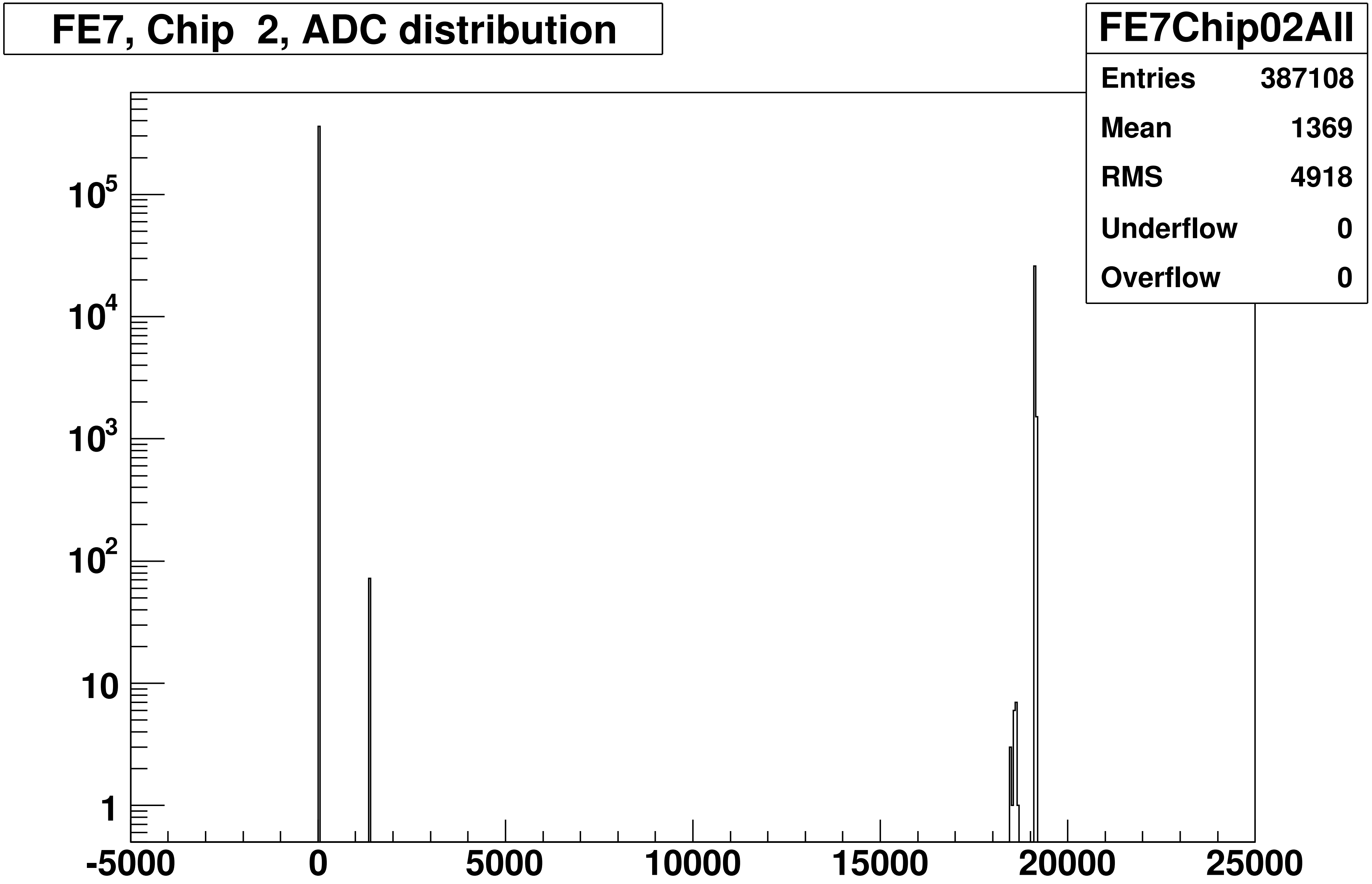


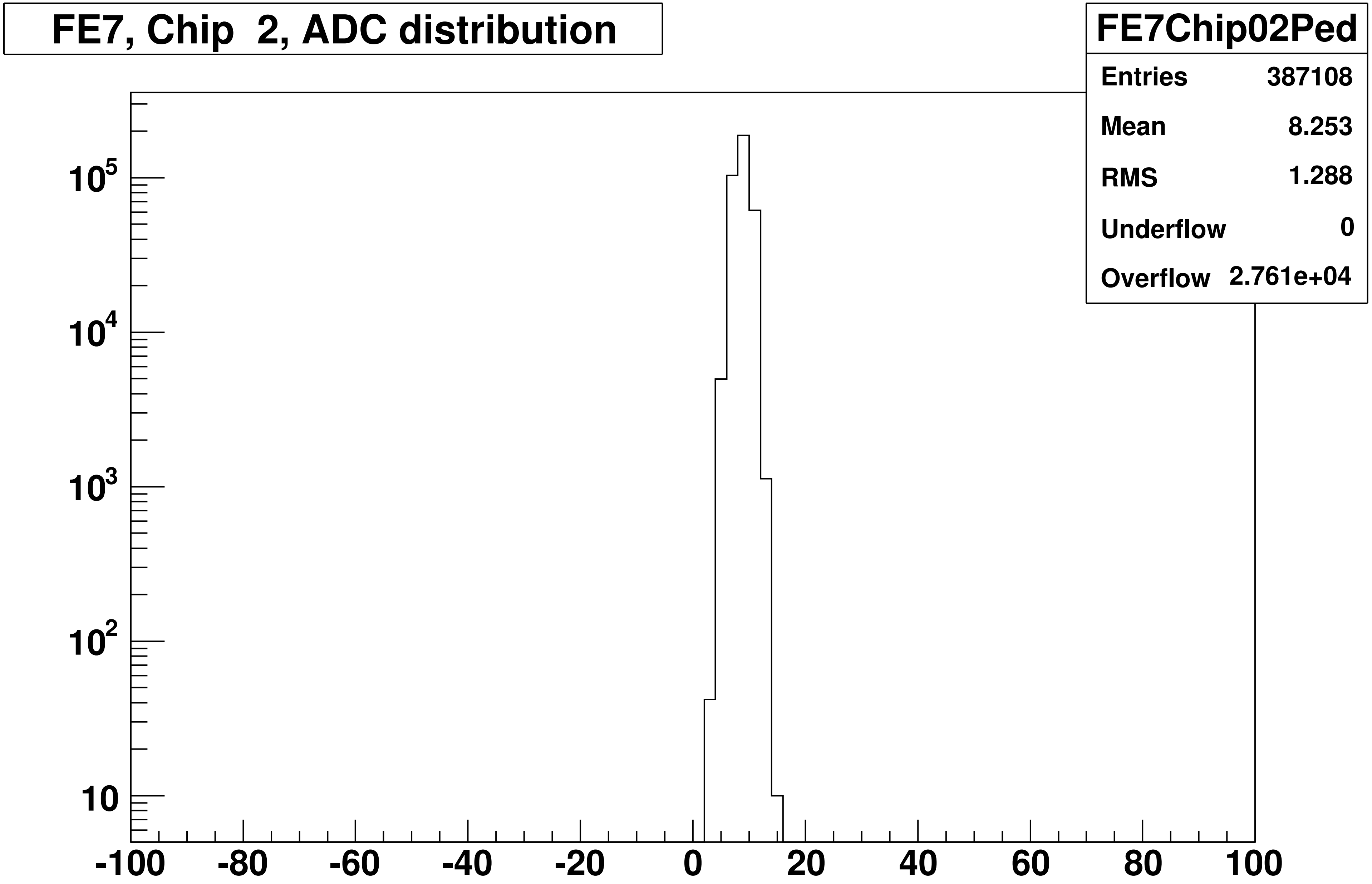
FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 387108
Mean 1355
RMS 4923
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

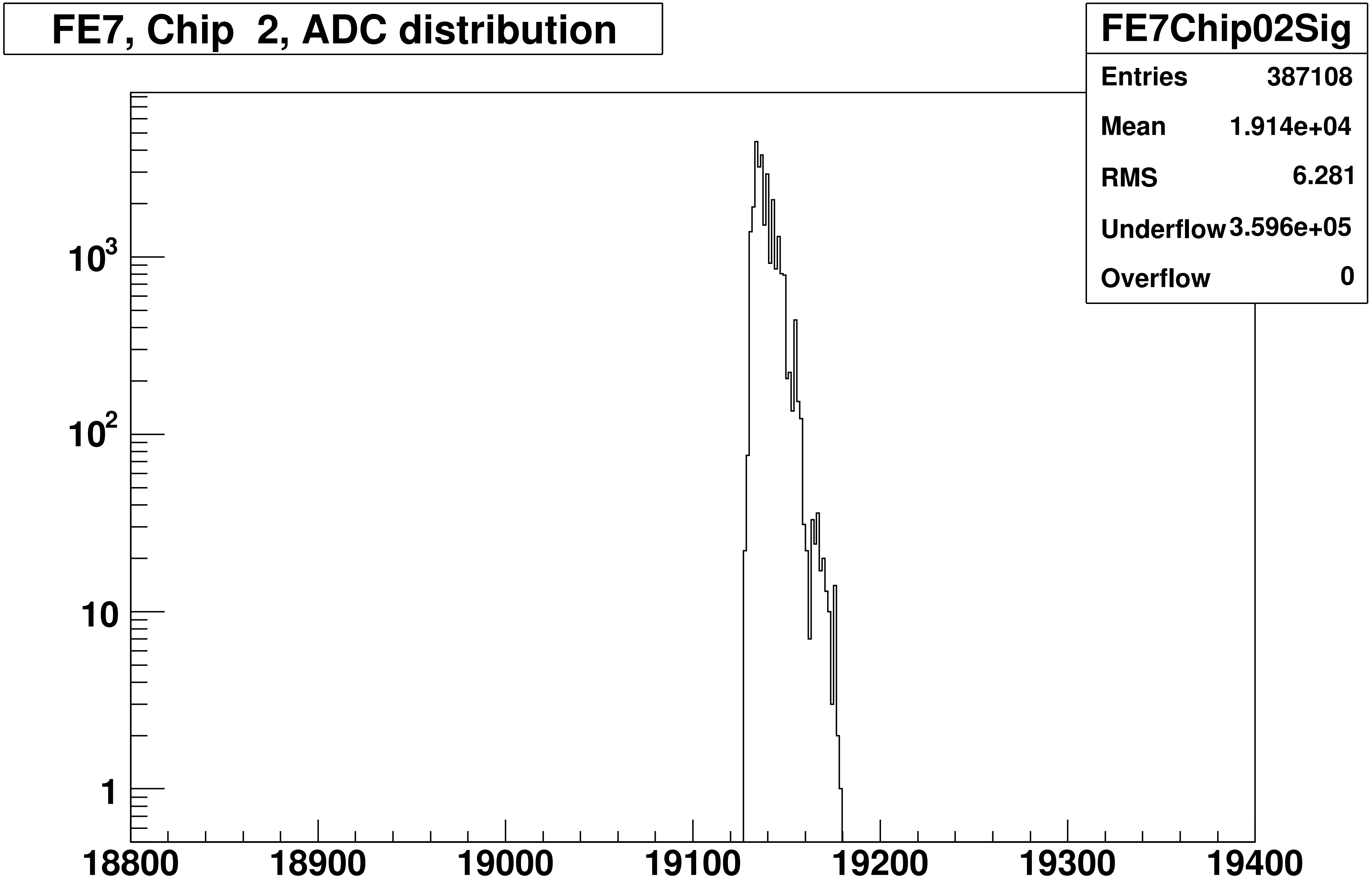


FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 387108
Mean -9.646
RMS 1.259
Underflow 0
Overflow 2.762e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

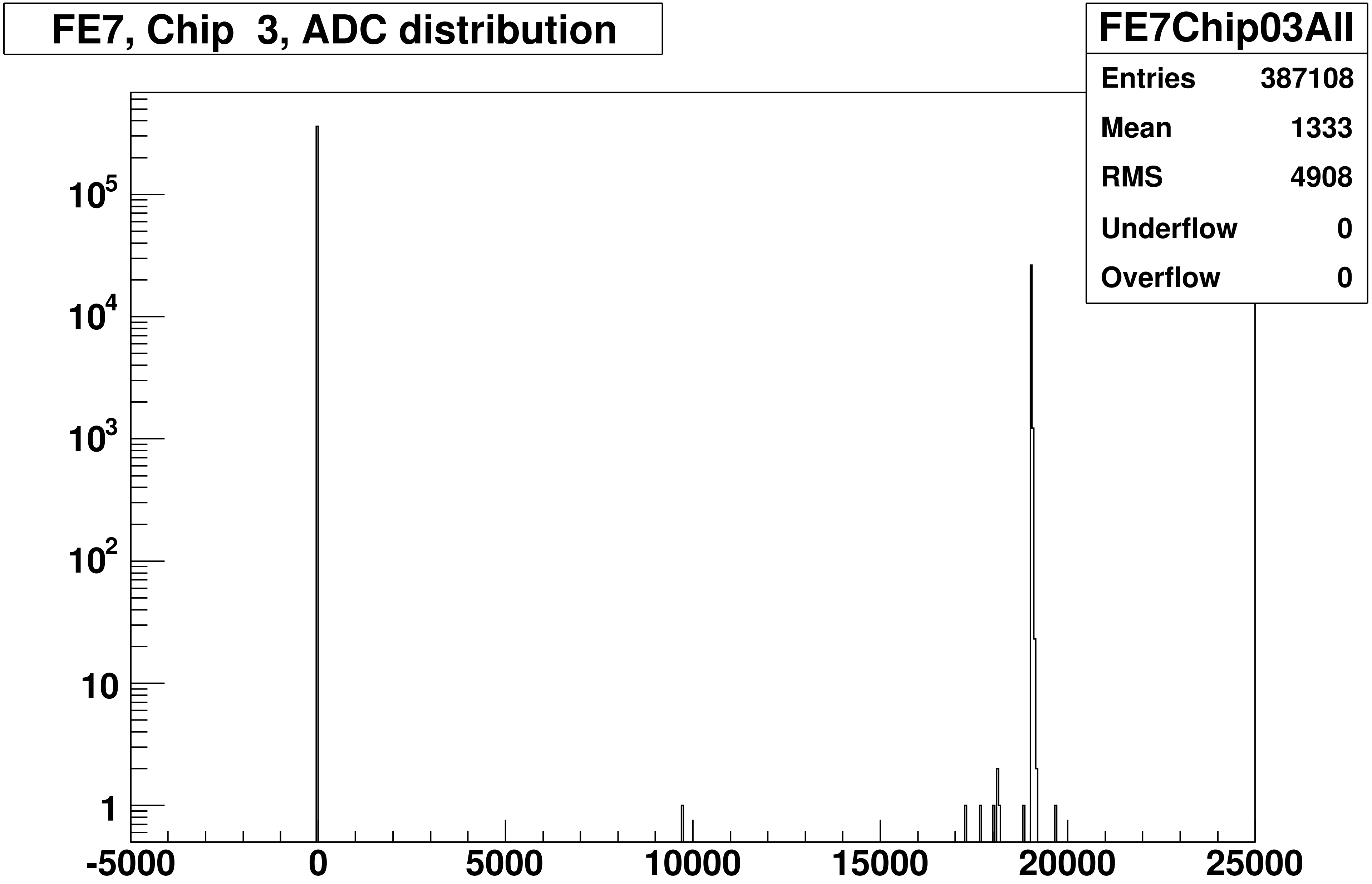

FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 387108
Mean 1.912e+04
RMS 6.924
Underflow 3.595e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 387108
Mean 1369
RMS 4918
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

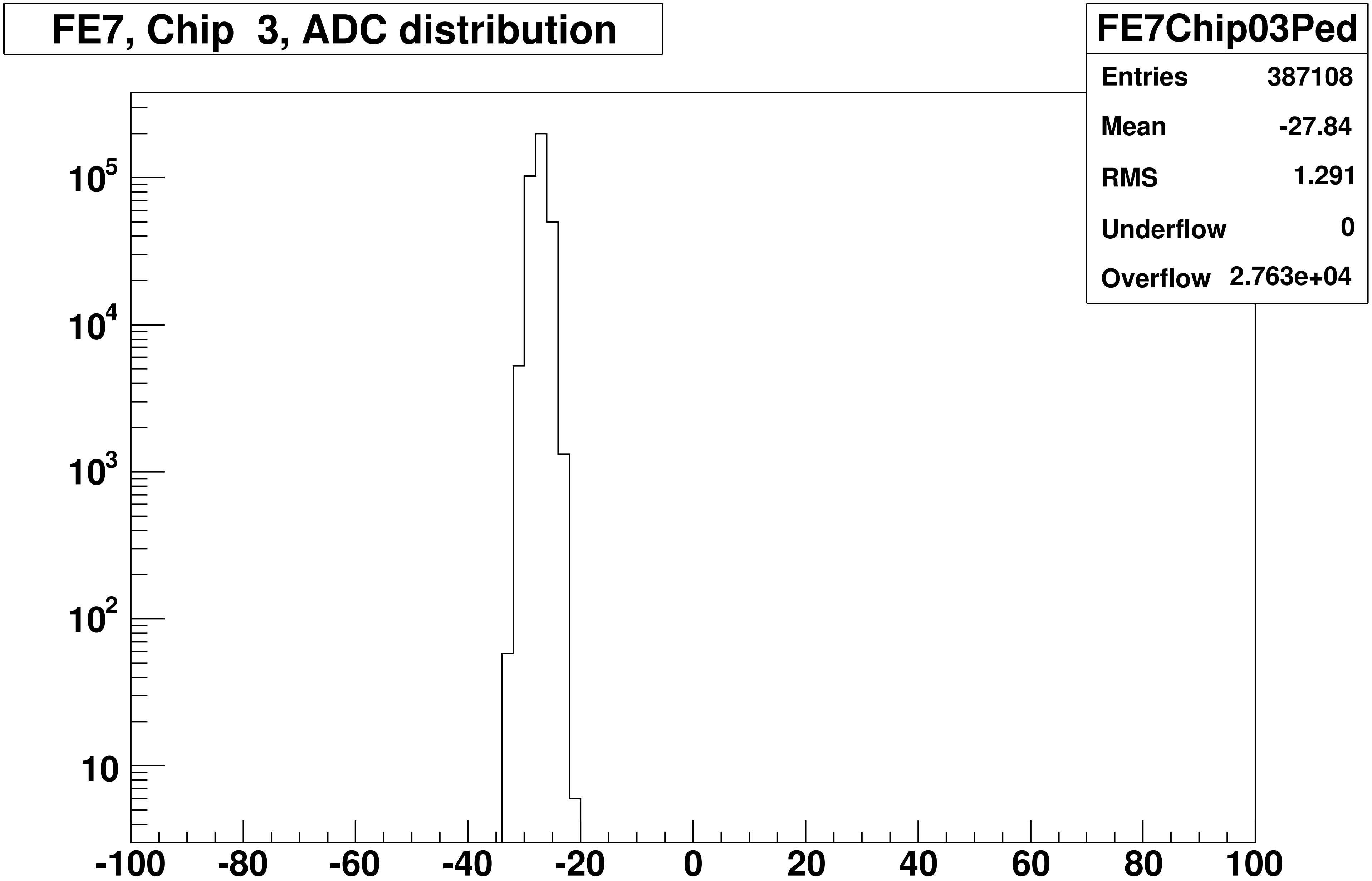

FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 387108
Mean 8.253
RMS 1.288
Underflow 0
Overflow 2.761e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

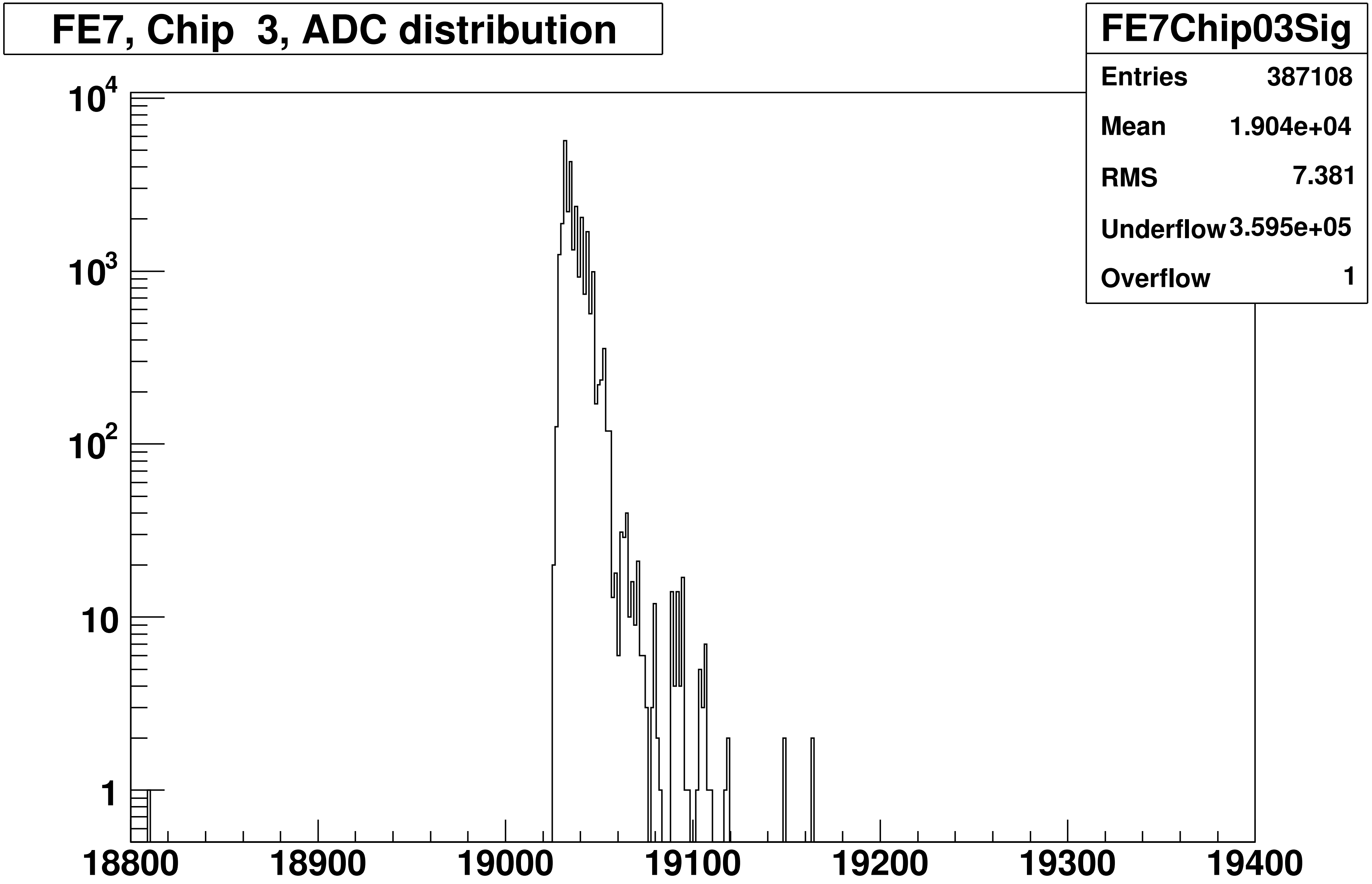


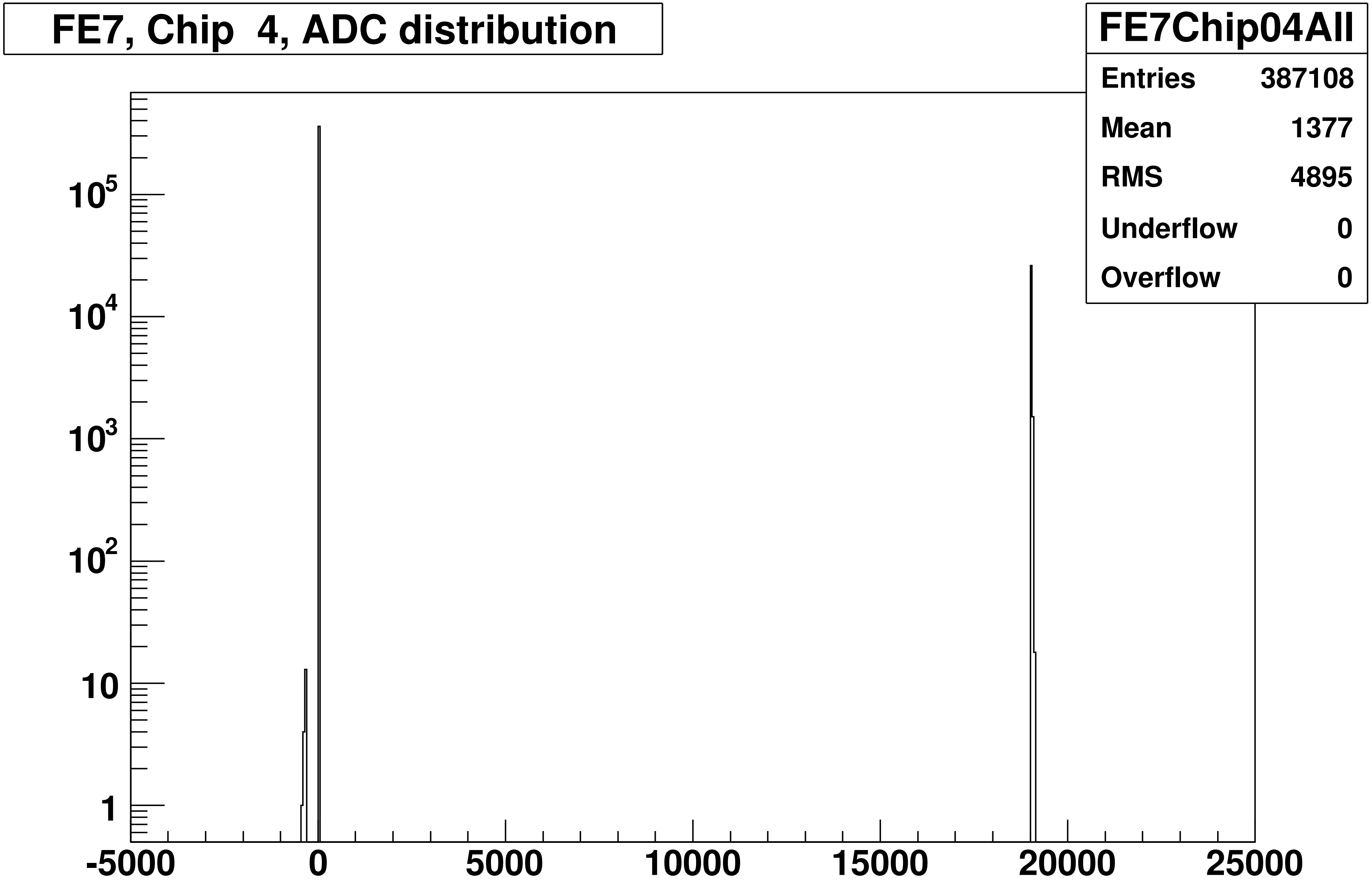
FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 387108
Mean 1.914e+04
RMS 6.281
Underflow 3.596e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

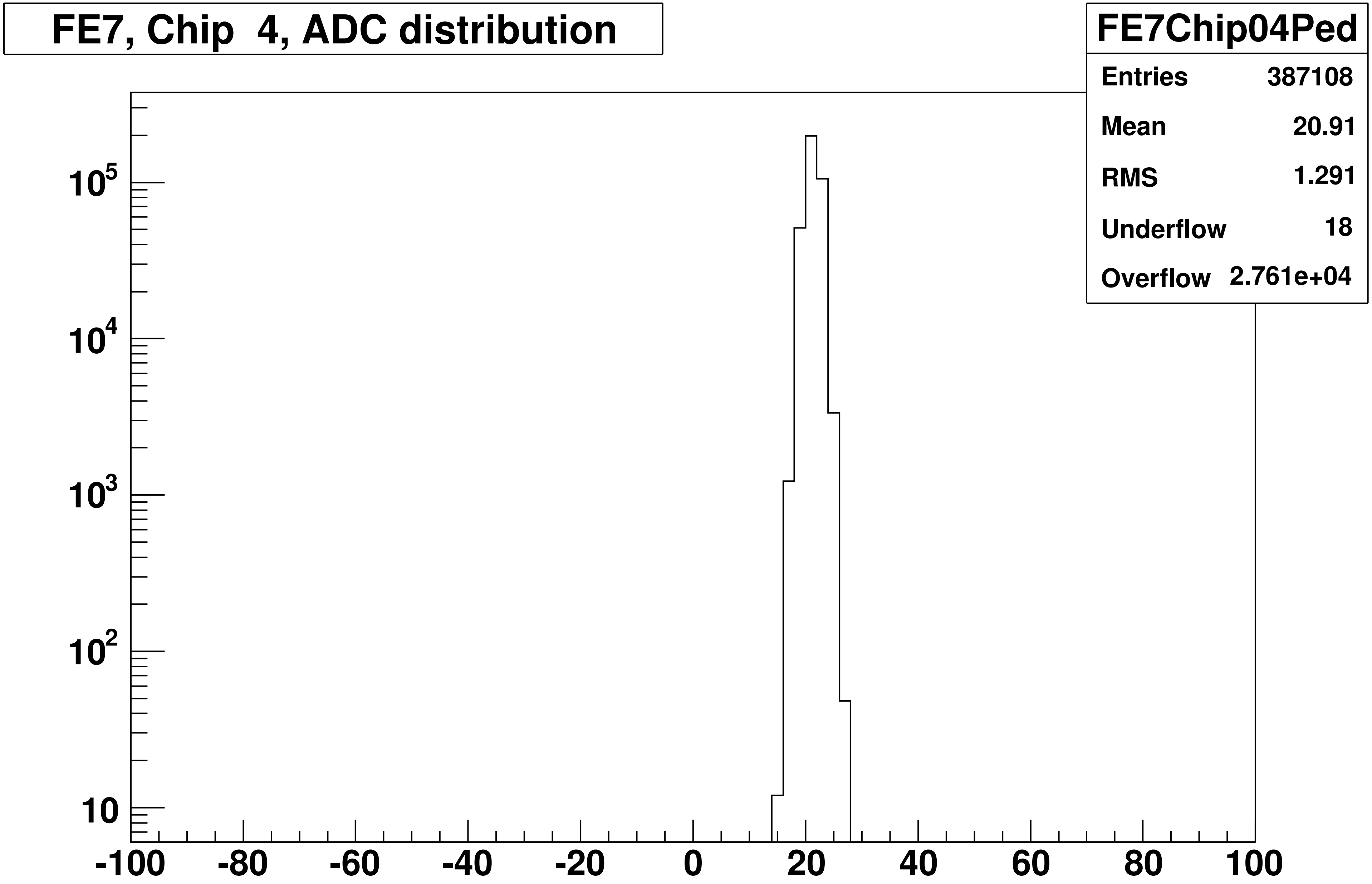


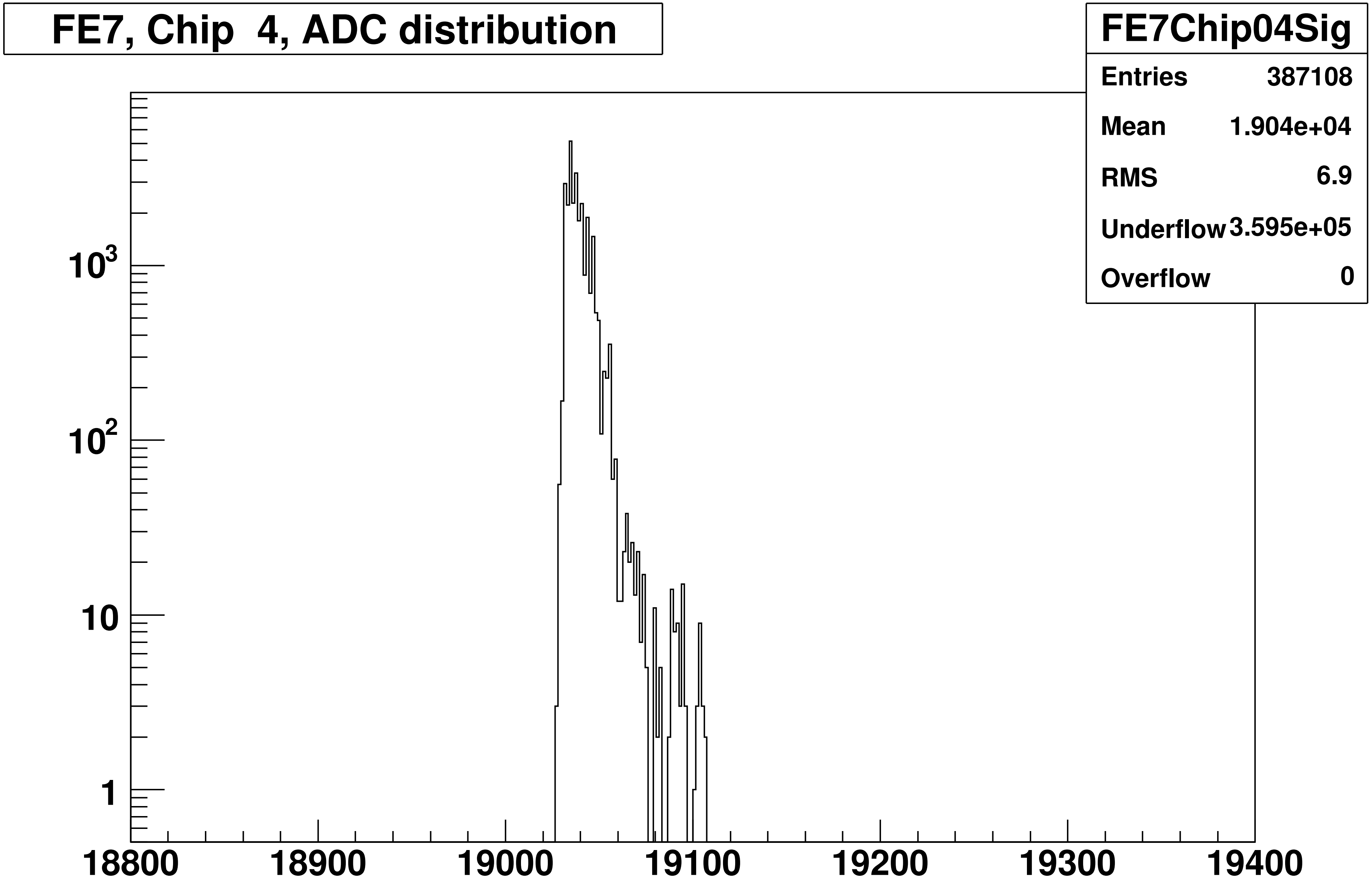
FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 387108
Mean 1333
RMS 4908
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

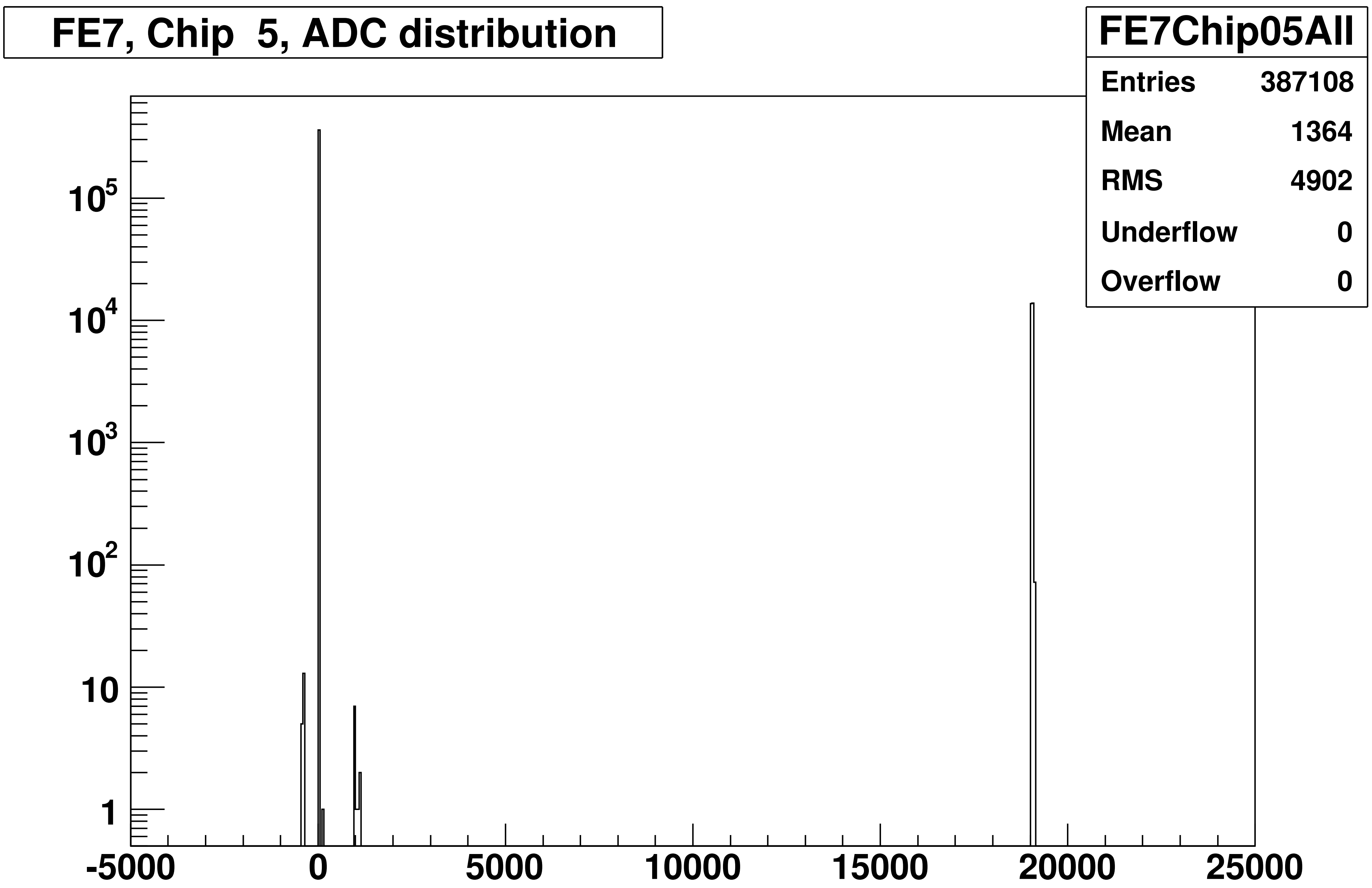

FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 387108
Mean -27.84
RMS 1.291
Underflow 0
Overflow 2.763e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

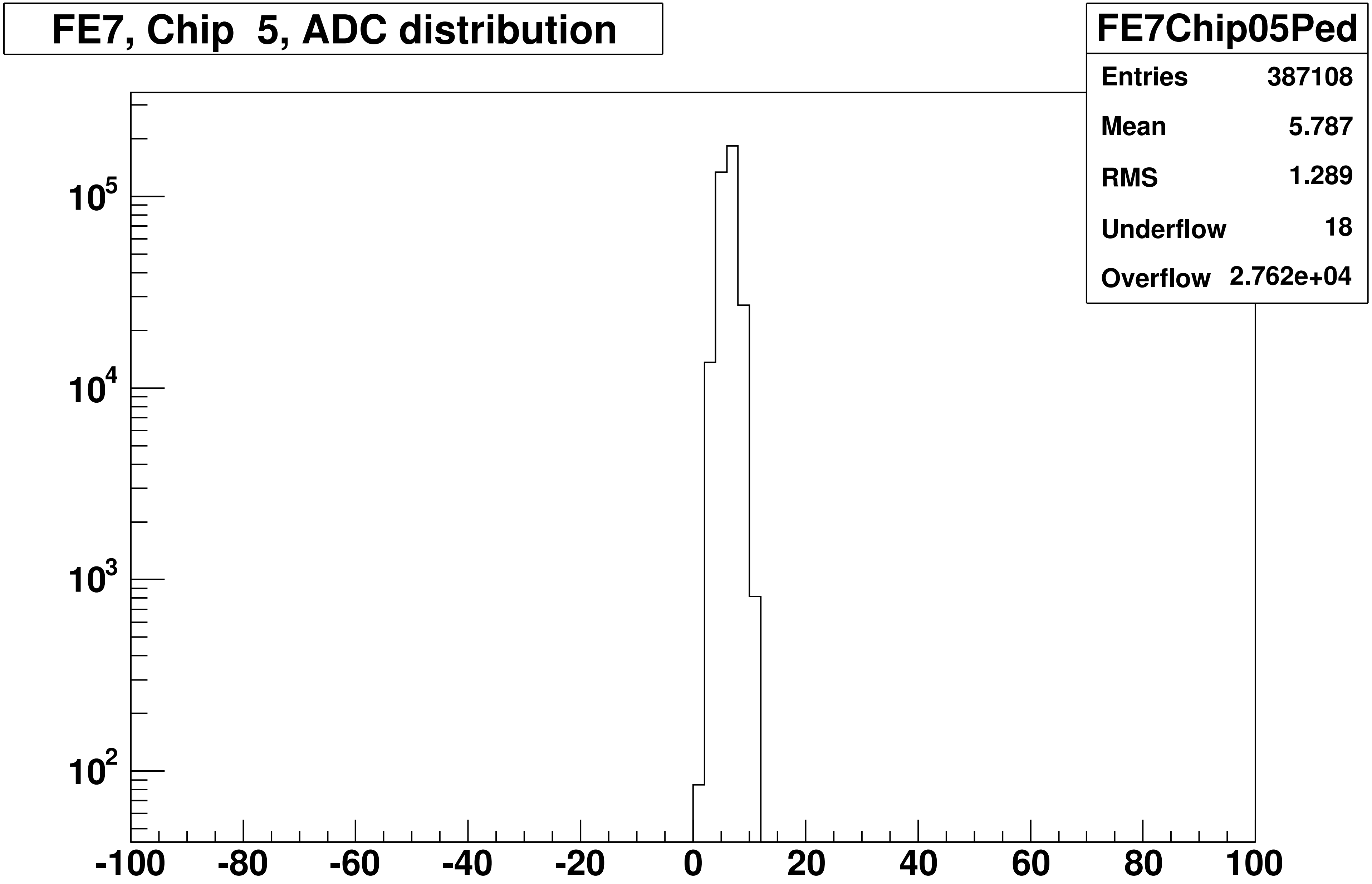

FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 387108
Mean 1.904e+04
RMS 7.381
Underflow 3.595e+05
Overflow 1
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 387108
Mean 1377
RMS 4895
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

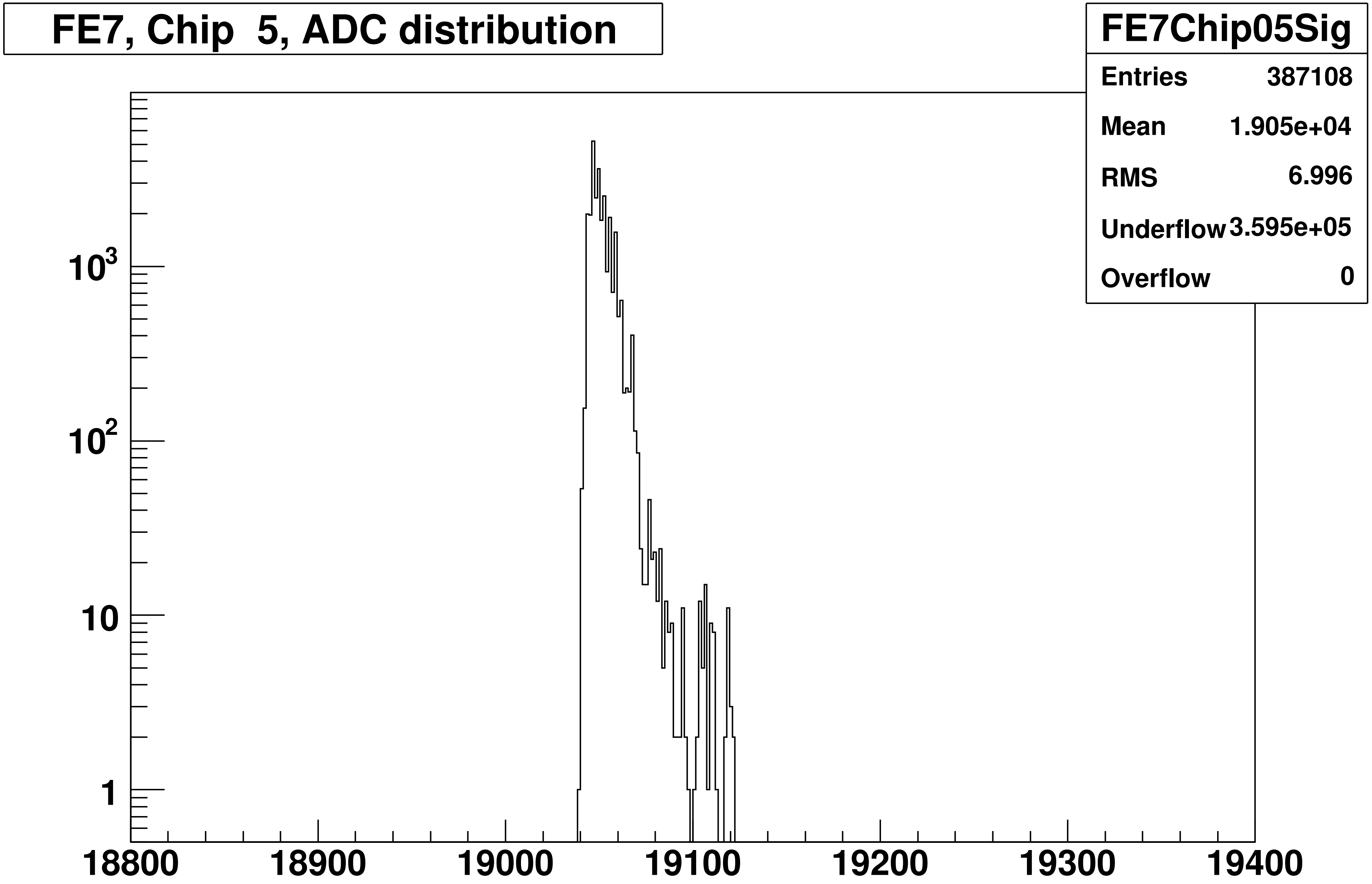

FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 387108
Mean 20.91
RMS 1.291
Underflow 18
Overflow 2.761e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

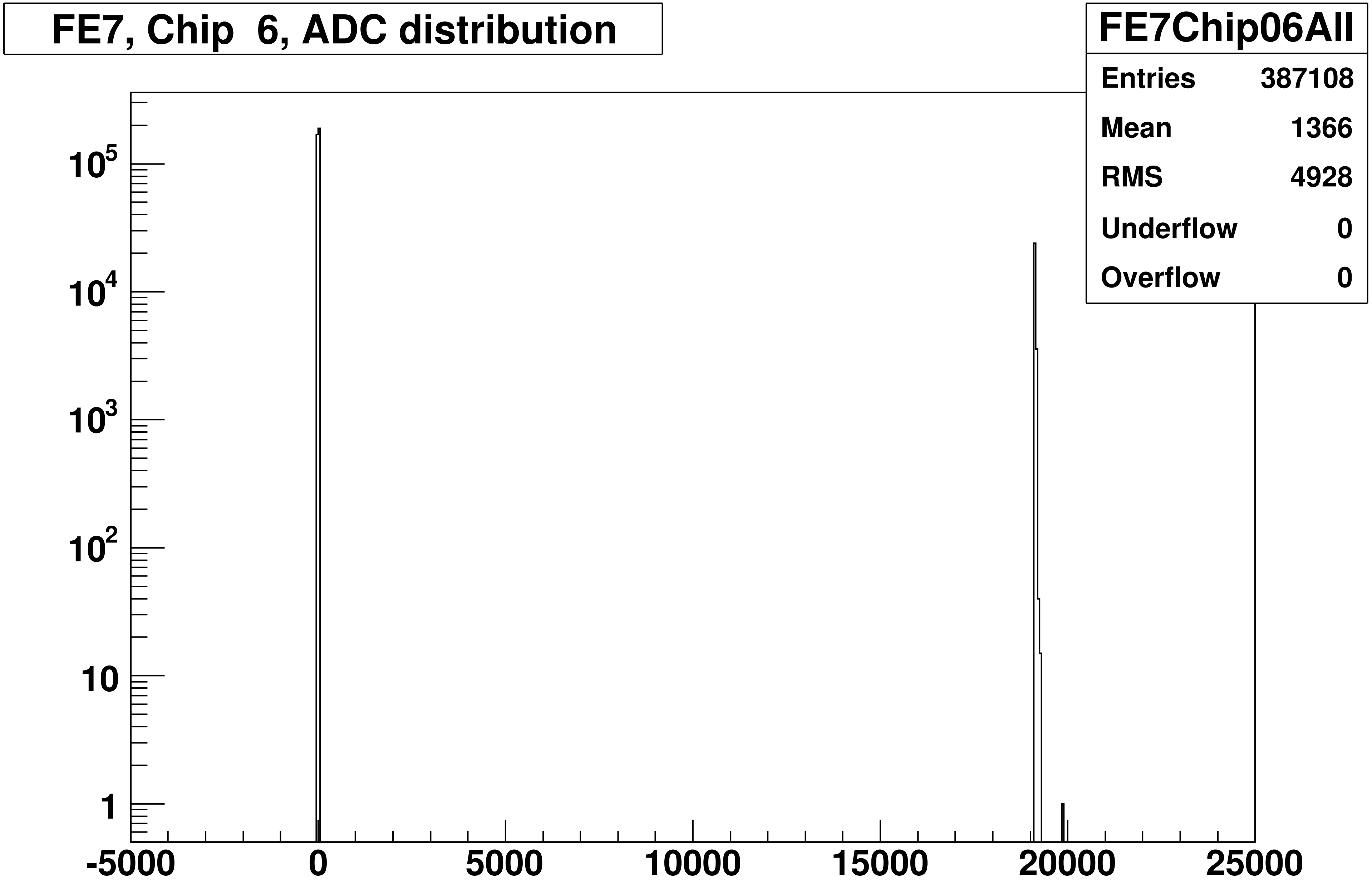

FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 387108
Mean 1.904e+04
RMS 6.9
Underflow 3.595e+05
Overflow 0
1
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 387108
Mean 1364
RMS 4902
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

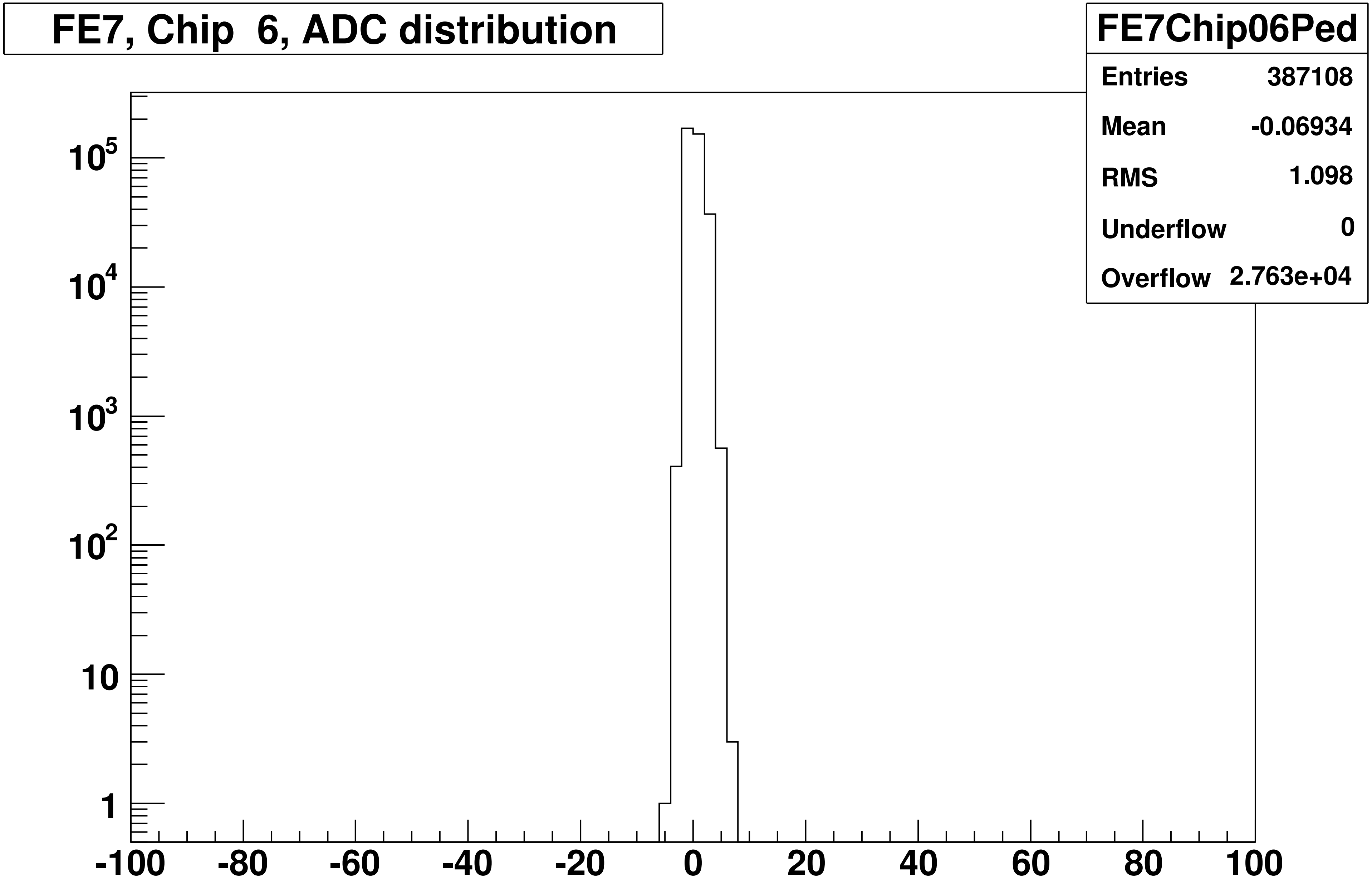


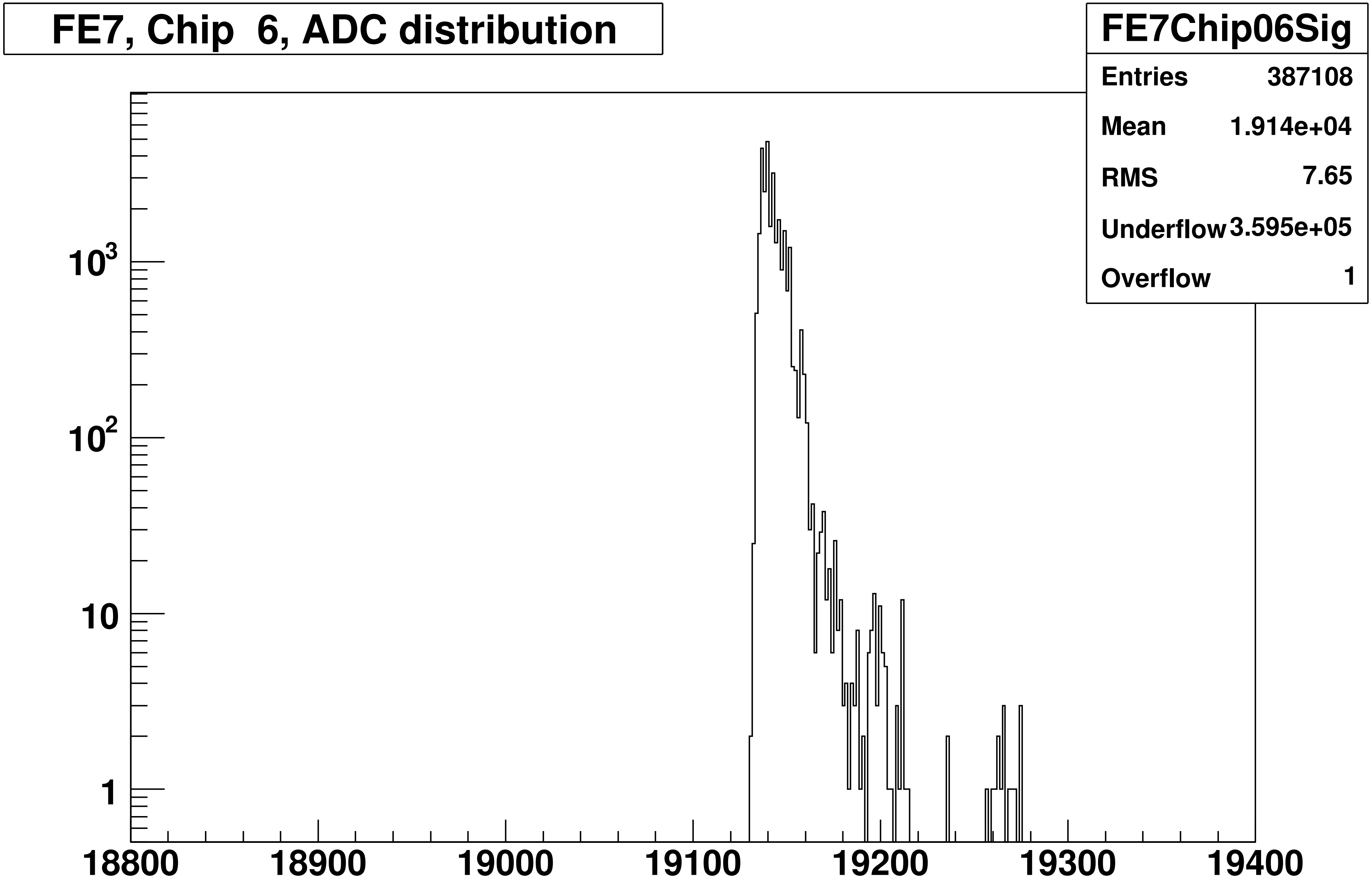
FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 387108
Mean 5.787
RMS 1.289
Underflow 18
Overflow 2.762e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 387108
Mean 1.905e+04
RMS 6.996
Underflow 3.595e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

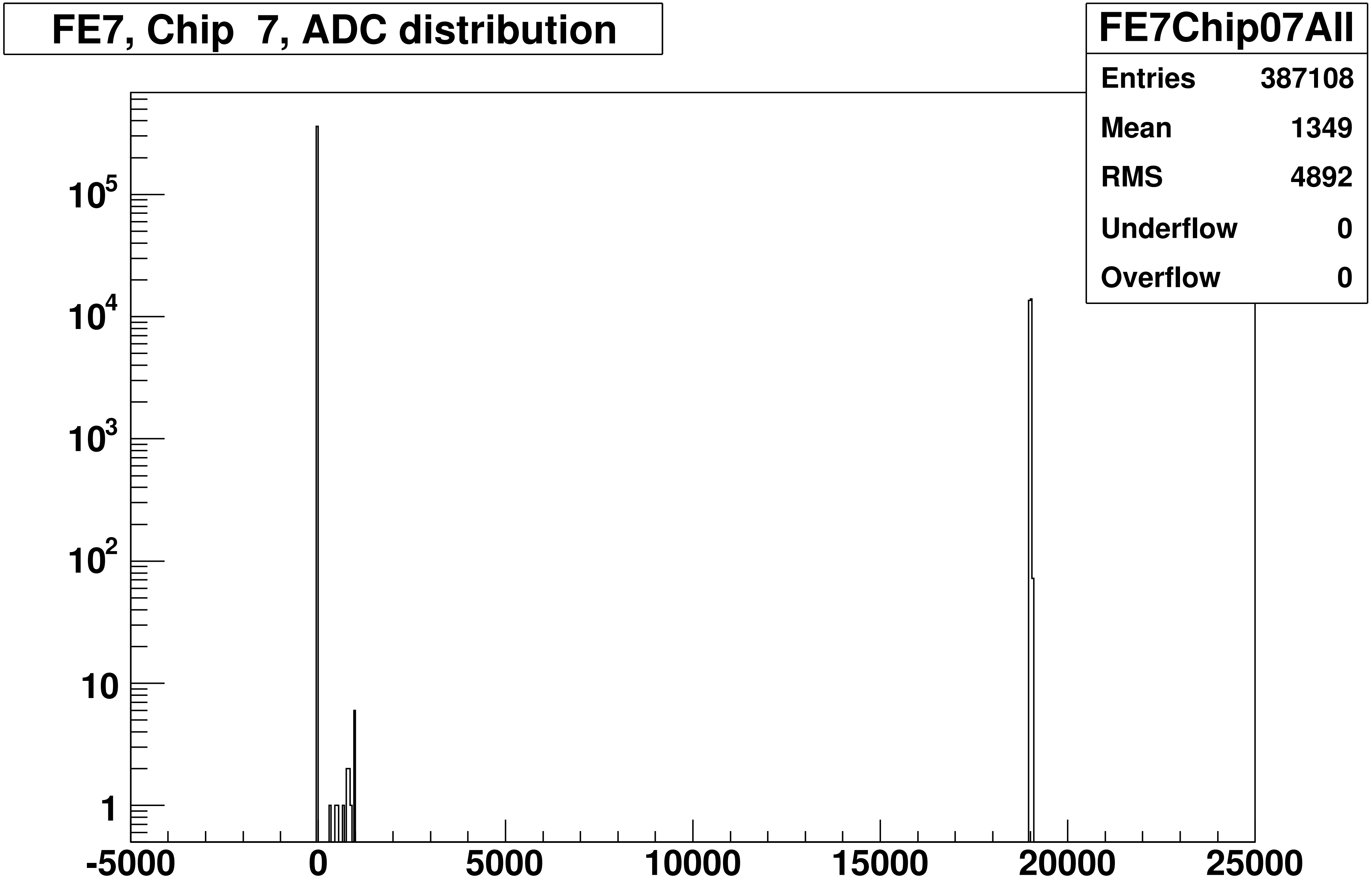


FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 387108
Mean 1366
RMS 4928
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

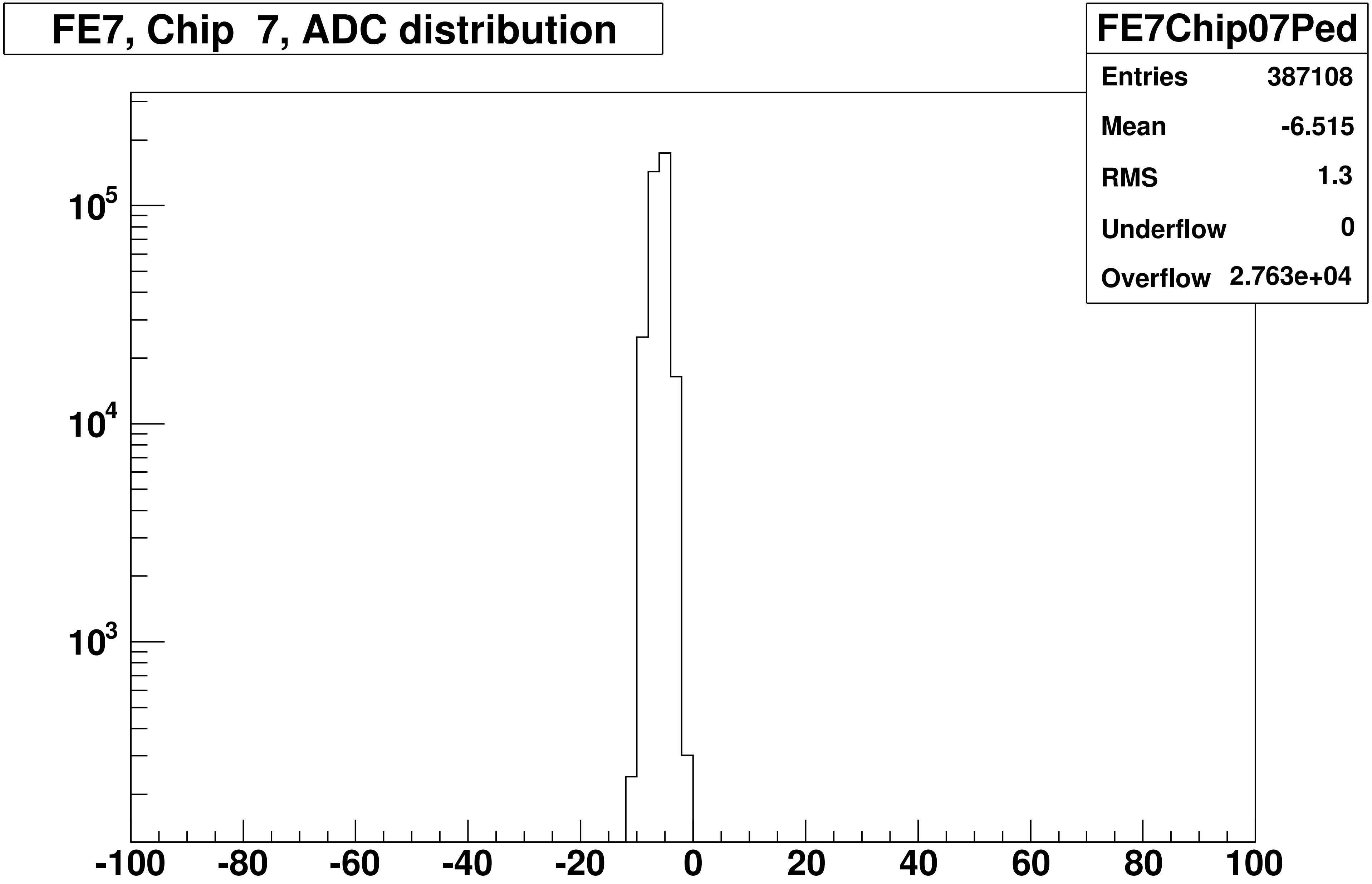

FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 387108
Mean -0.06934
RMS 1.098
Underflow 0
Overflow 2.763e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

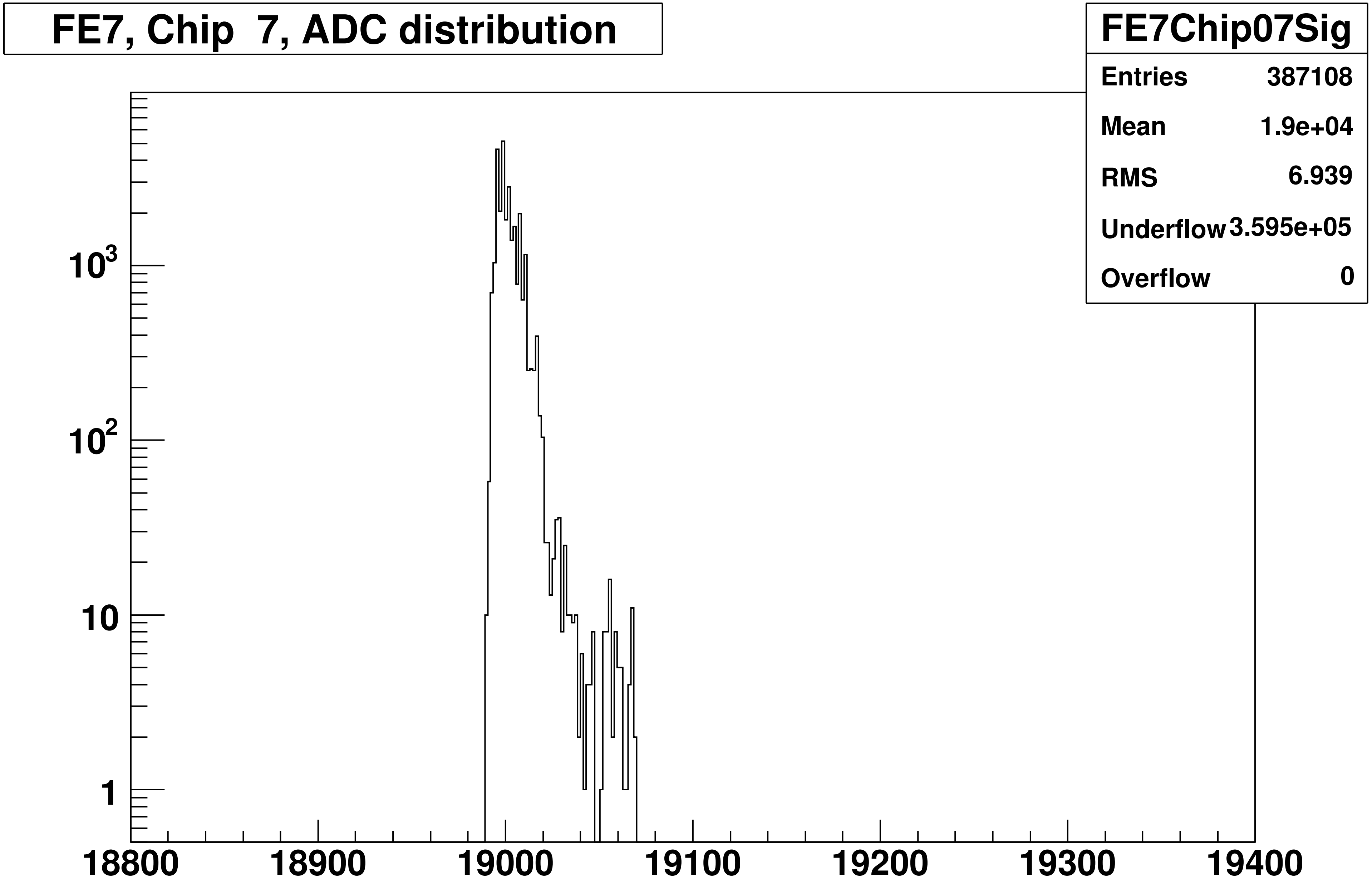


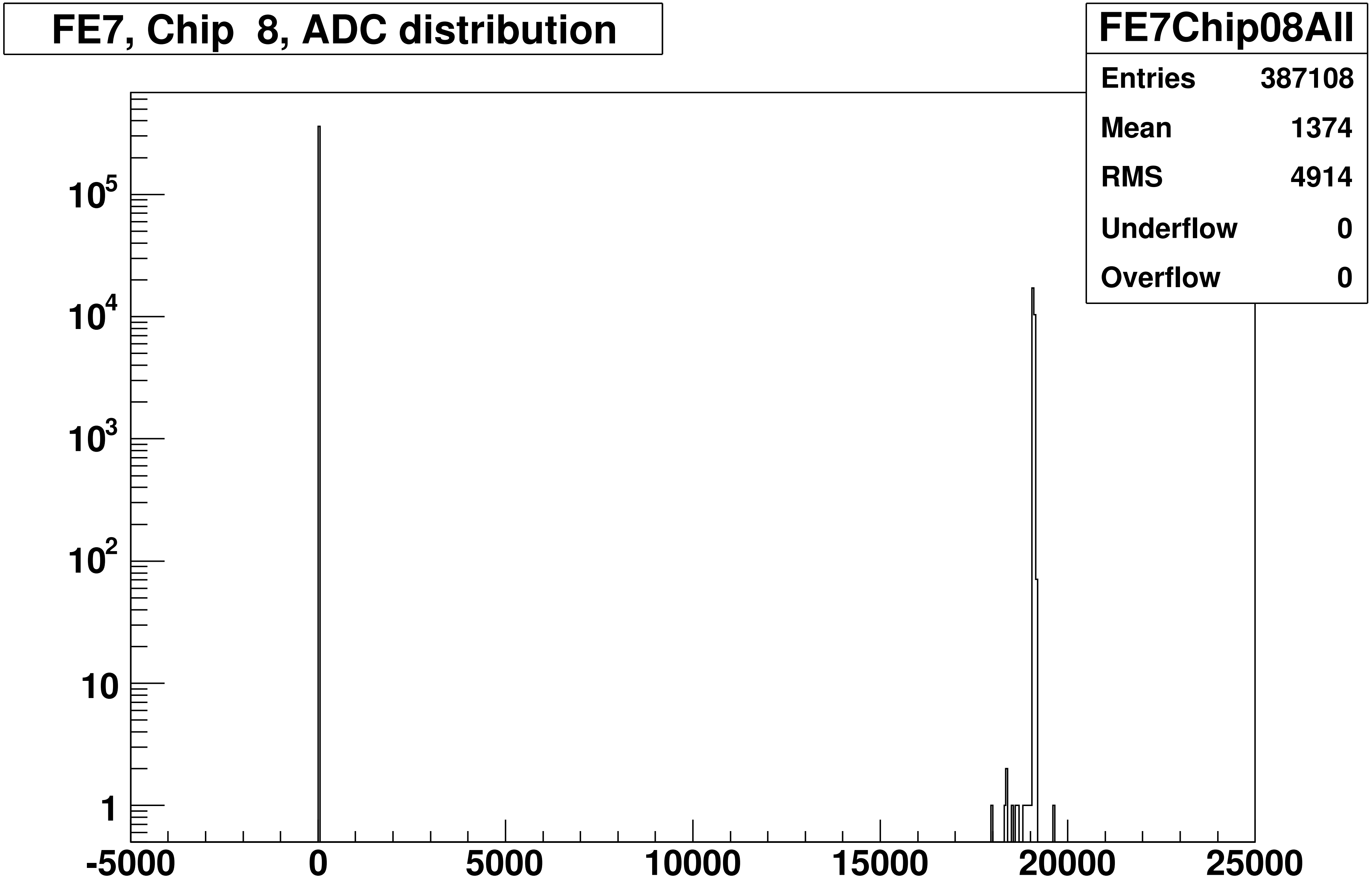
FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 387108
Mean 1.914e+04
RMS 7.65
Underflow 3.595e+05
Overflow 1
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

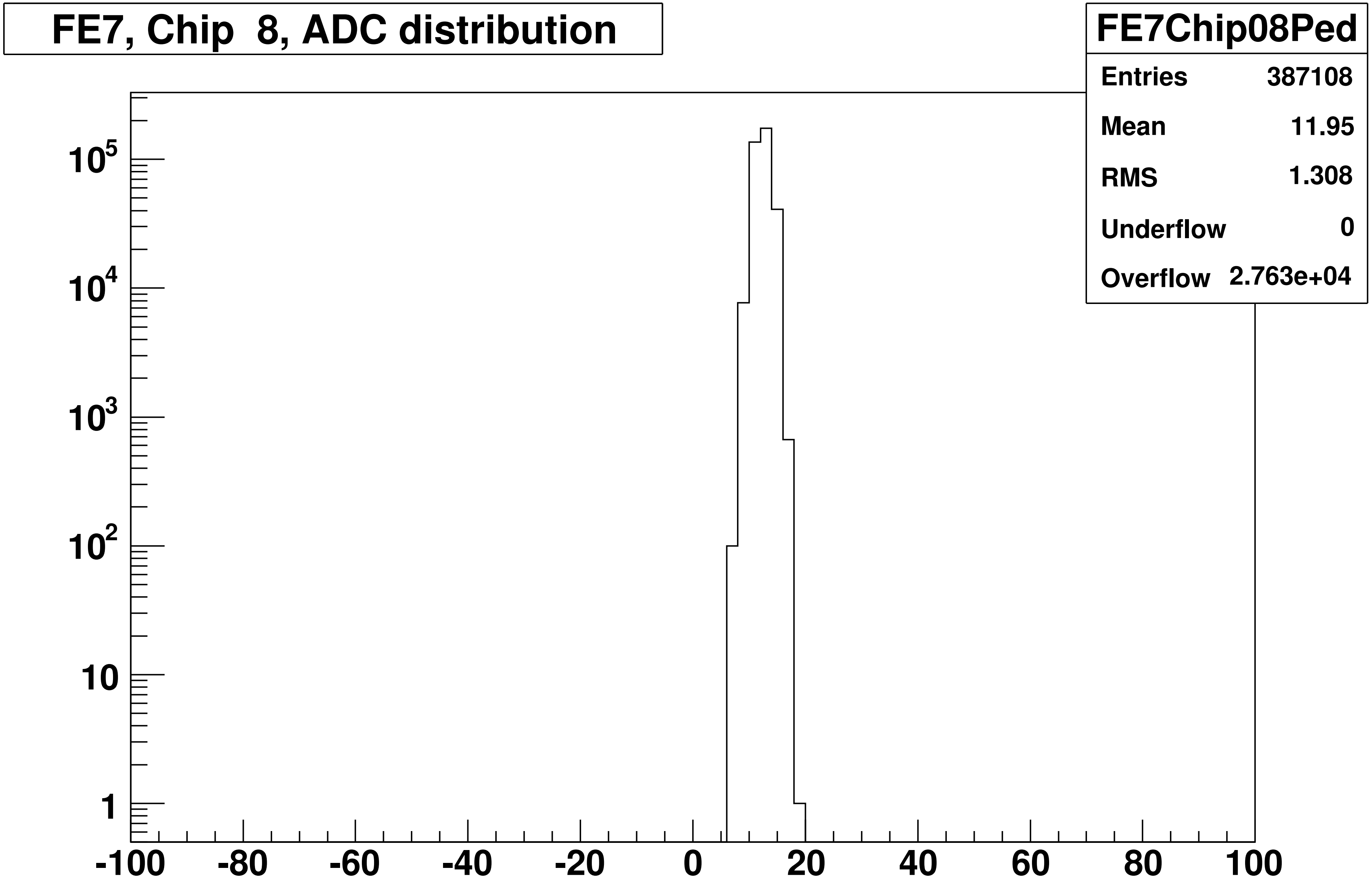


FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 387108
Mean 1349
RMS 4892
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

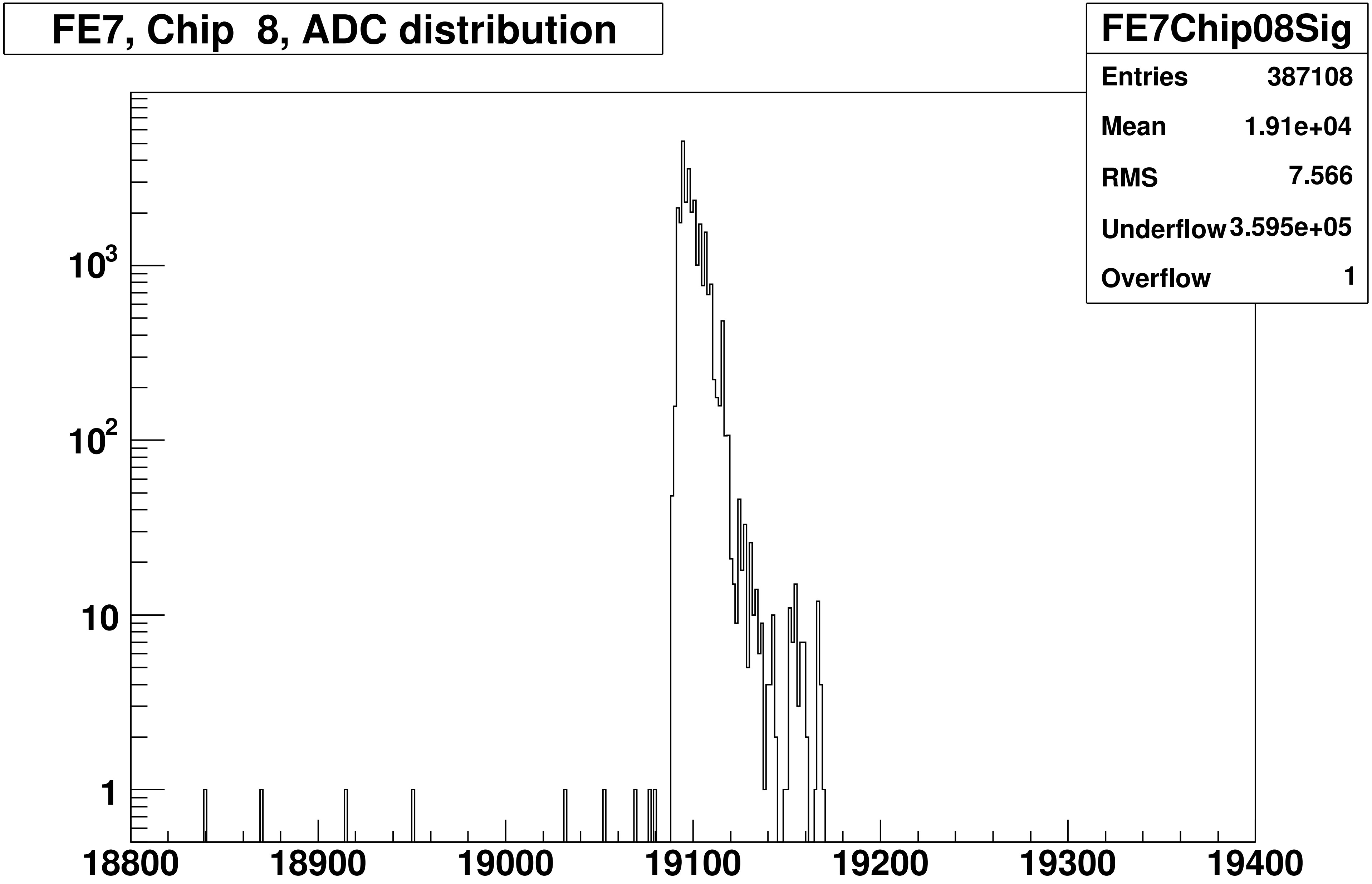

FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 387108
Mean -6.515
RMS 1.3
Underflow 0
Overflow 2.763e+04
10^5
10^4
10^3
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 387108
Mean 1.9e+04
RMS 6.939
Underflow 3.595e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

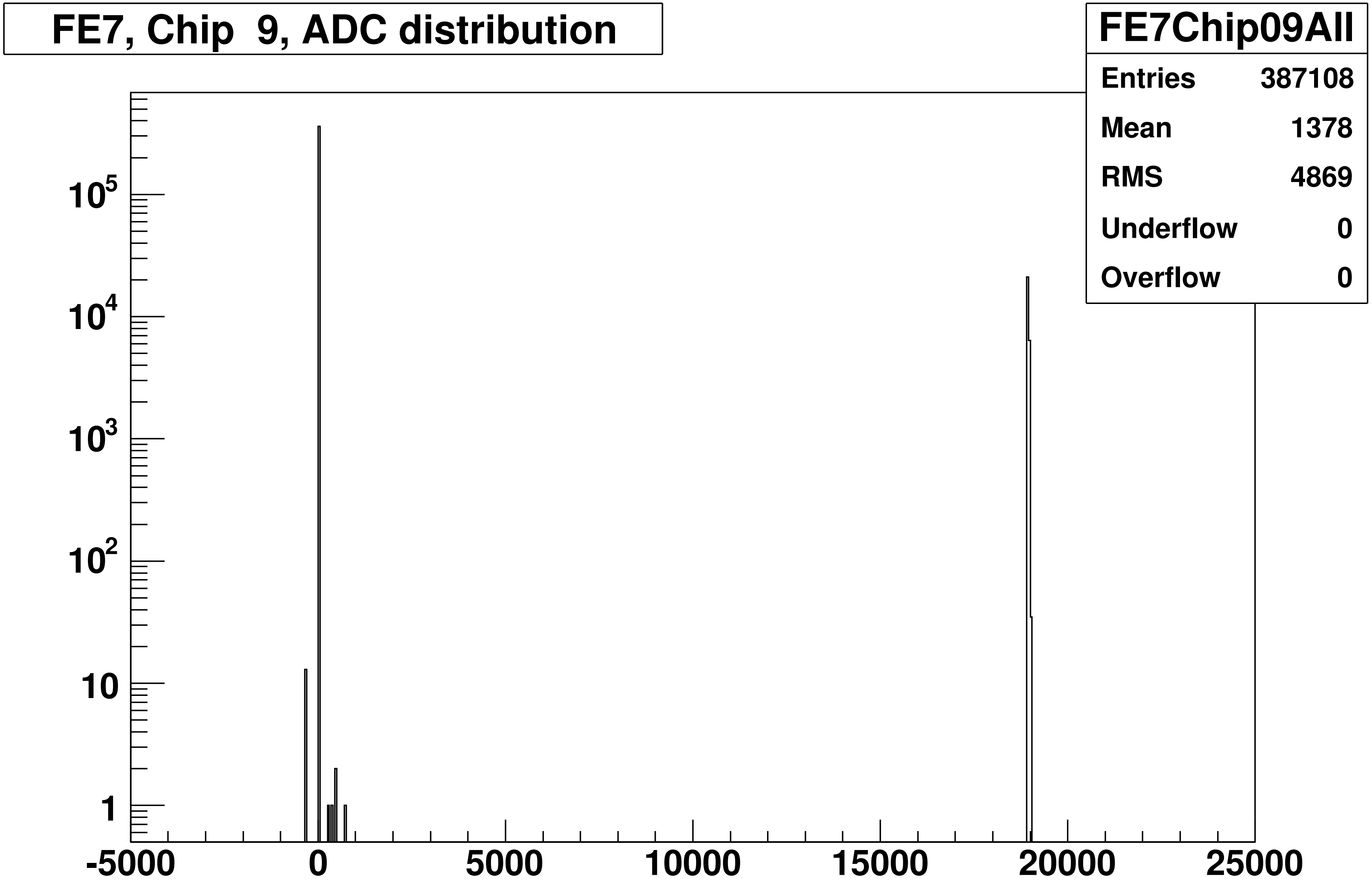

FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 387108
Mean 1374
RMS 4914
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



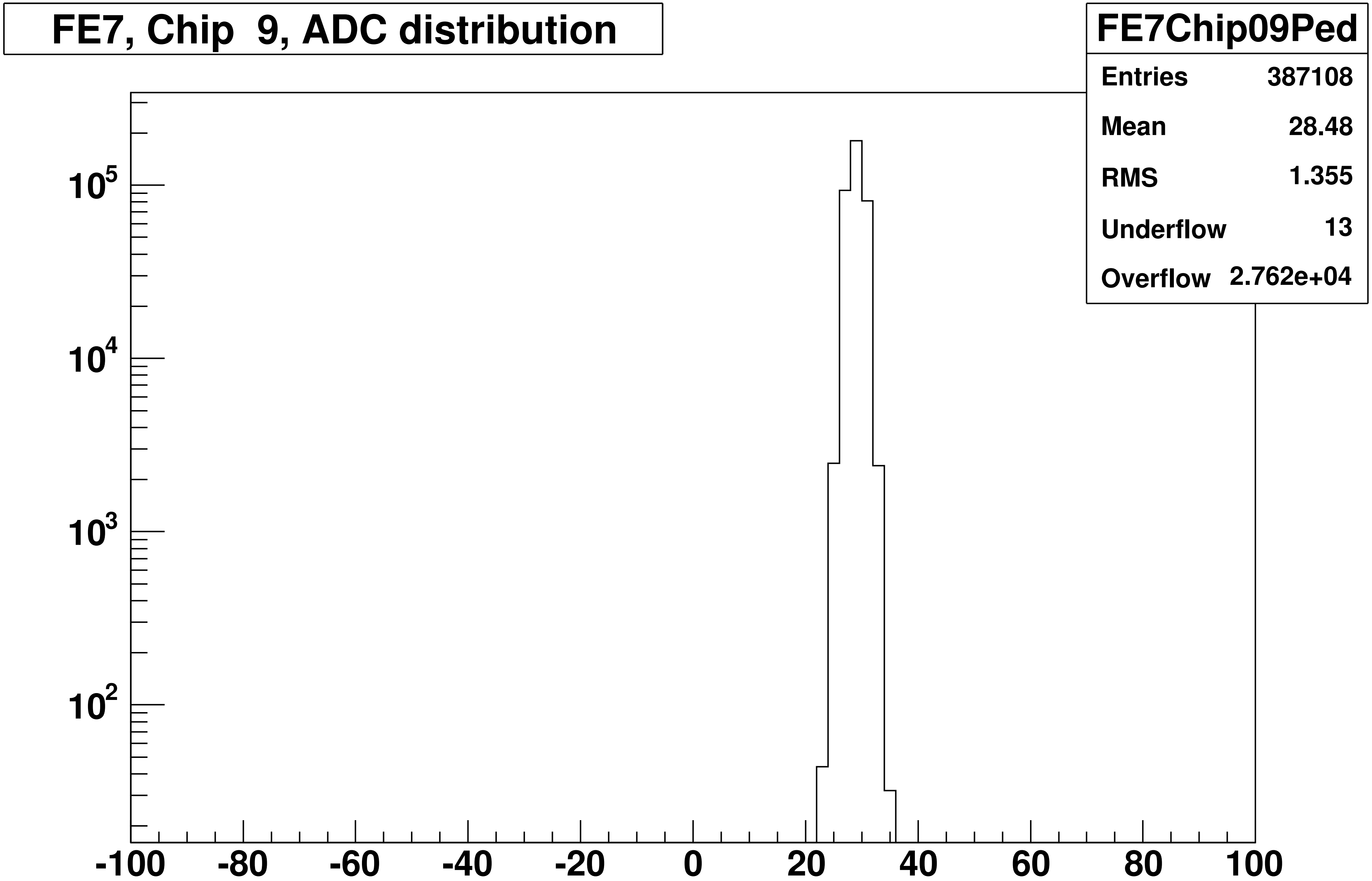
FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 387108
Mean 11.95
RMS 1.308
Underflow 0
Overflow 2.763e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

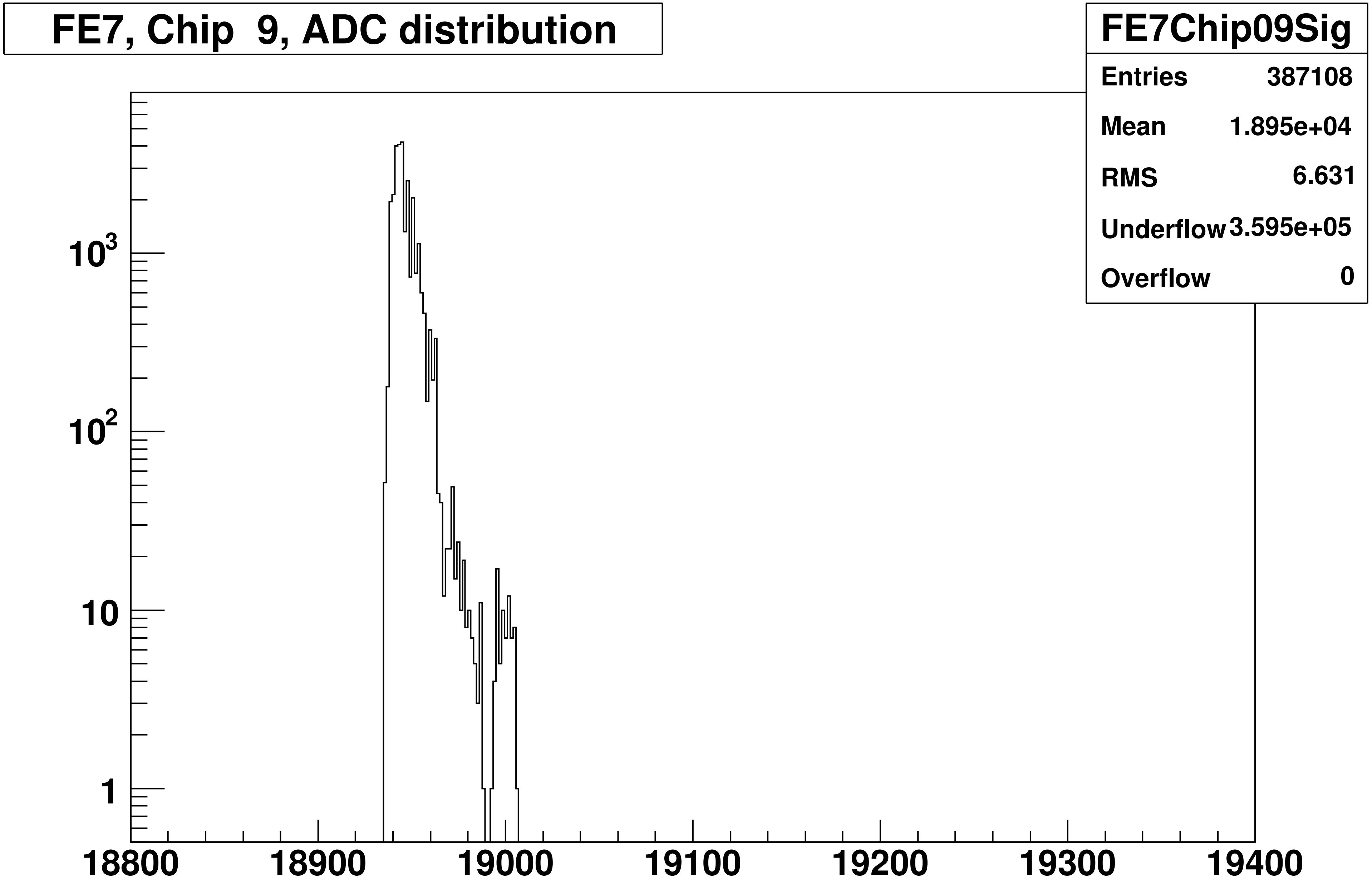


FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 387108
Mean 1.91e+04
RMS 7.566
Underflow 3.595e+05
Overflow 1
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

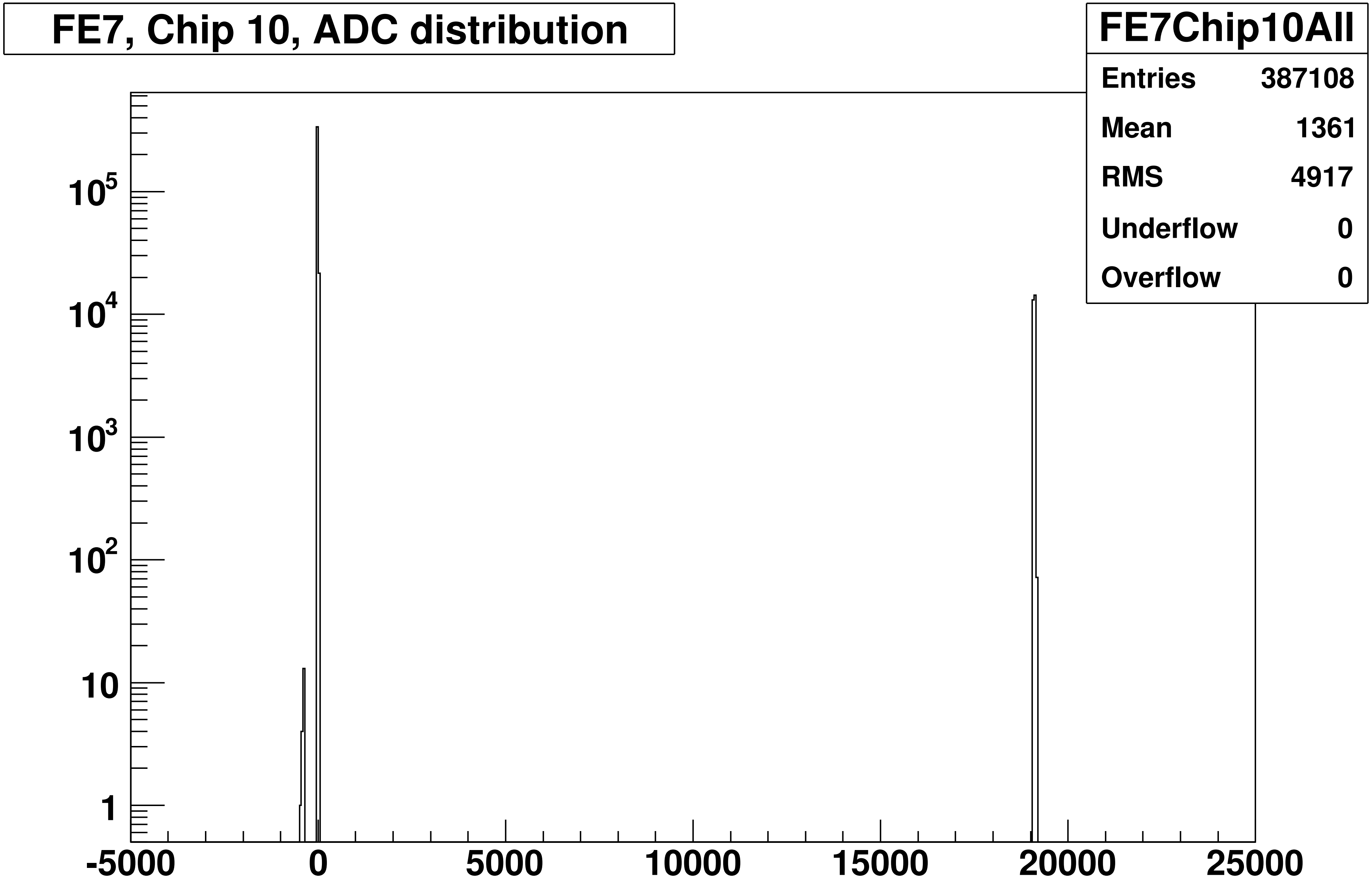


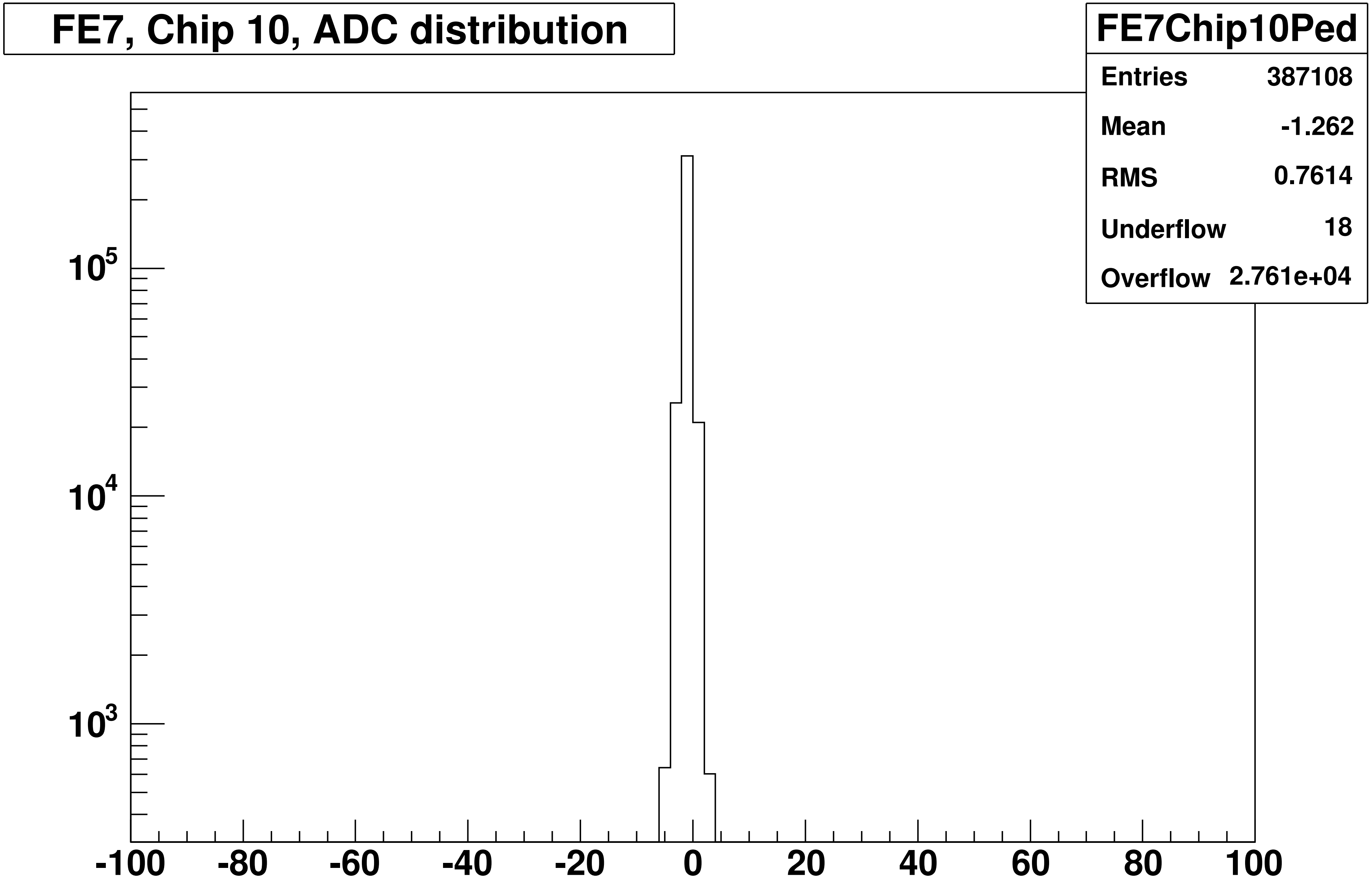
FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 387108
Mean 1378
RMS 4869
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

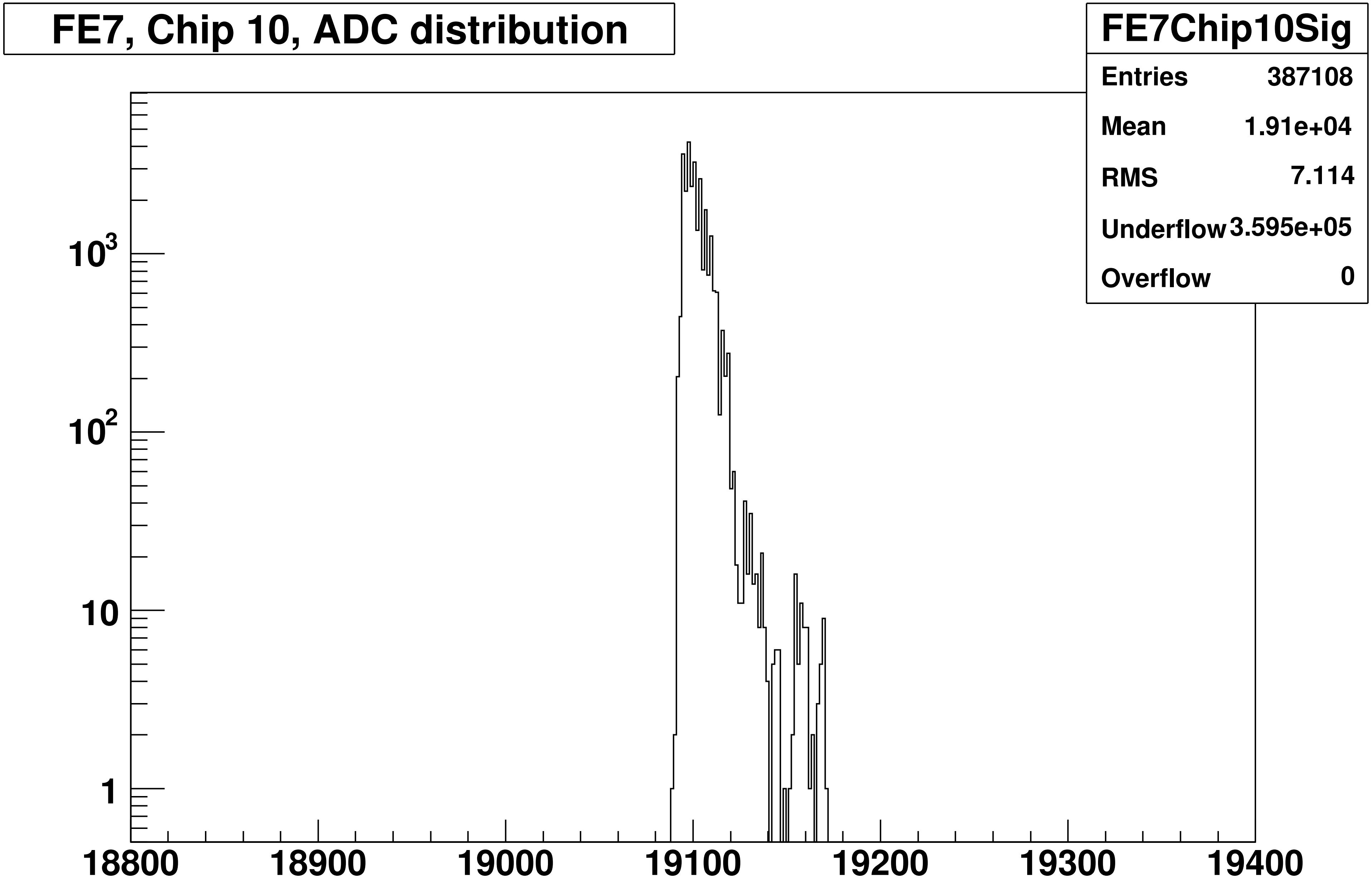

FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 387108
Mean 28.48
RMS 1.355
Underflow 13
Overflow 2.762e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

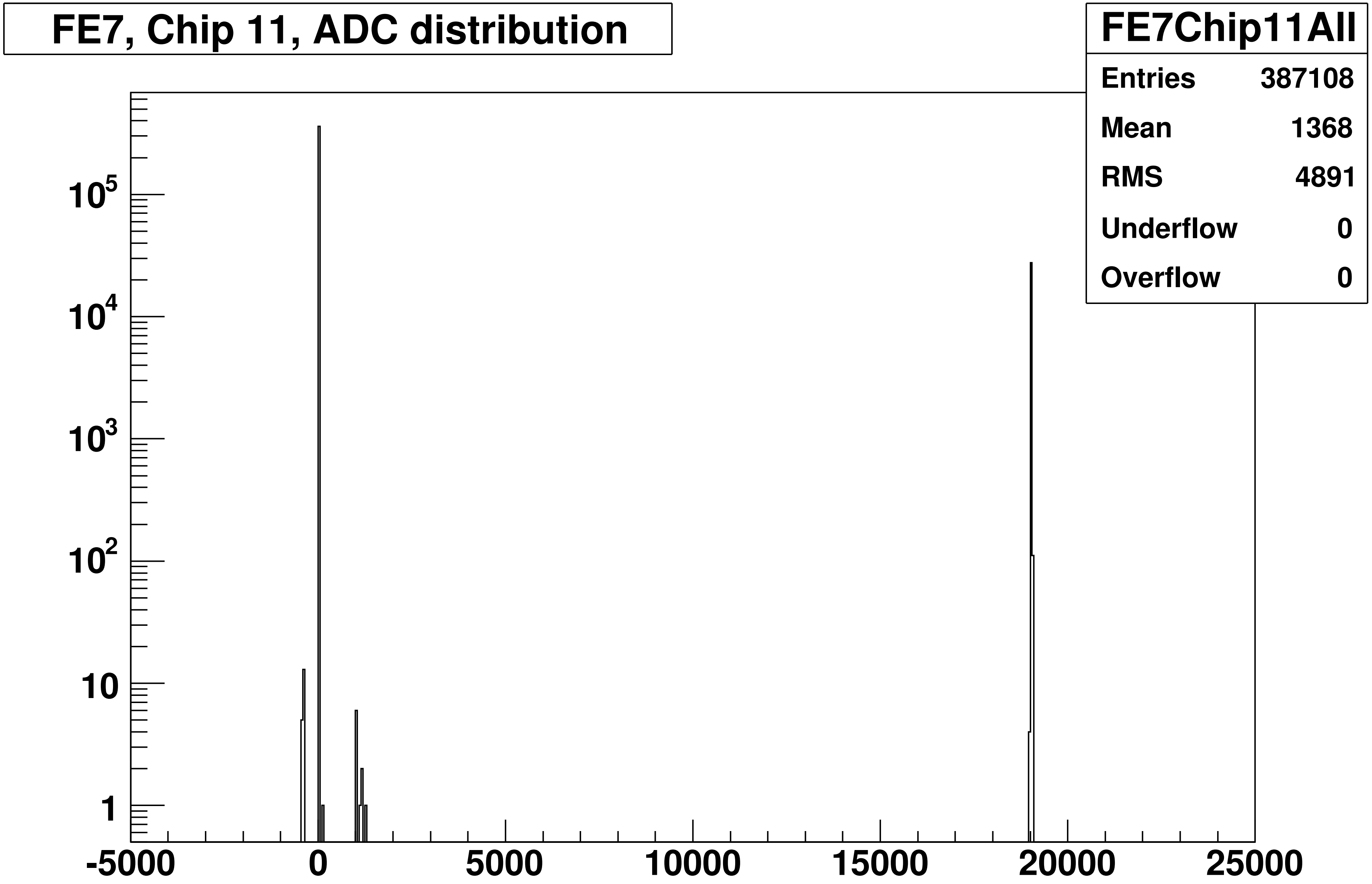


FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 387108
Mean 1.895e+04
RMS 6.631
Underflow 3.595e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

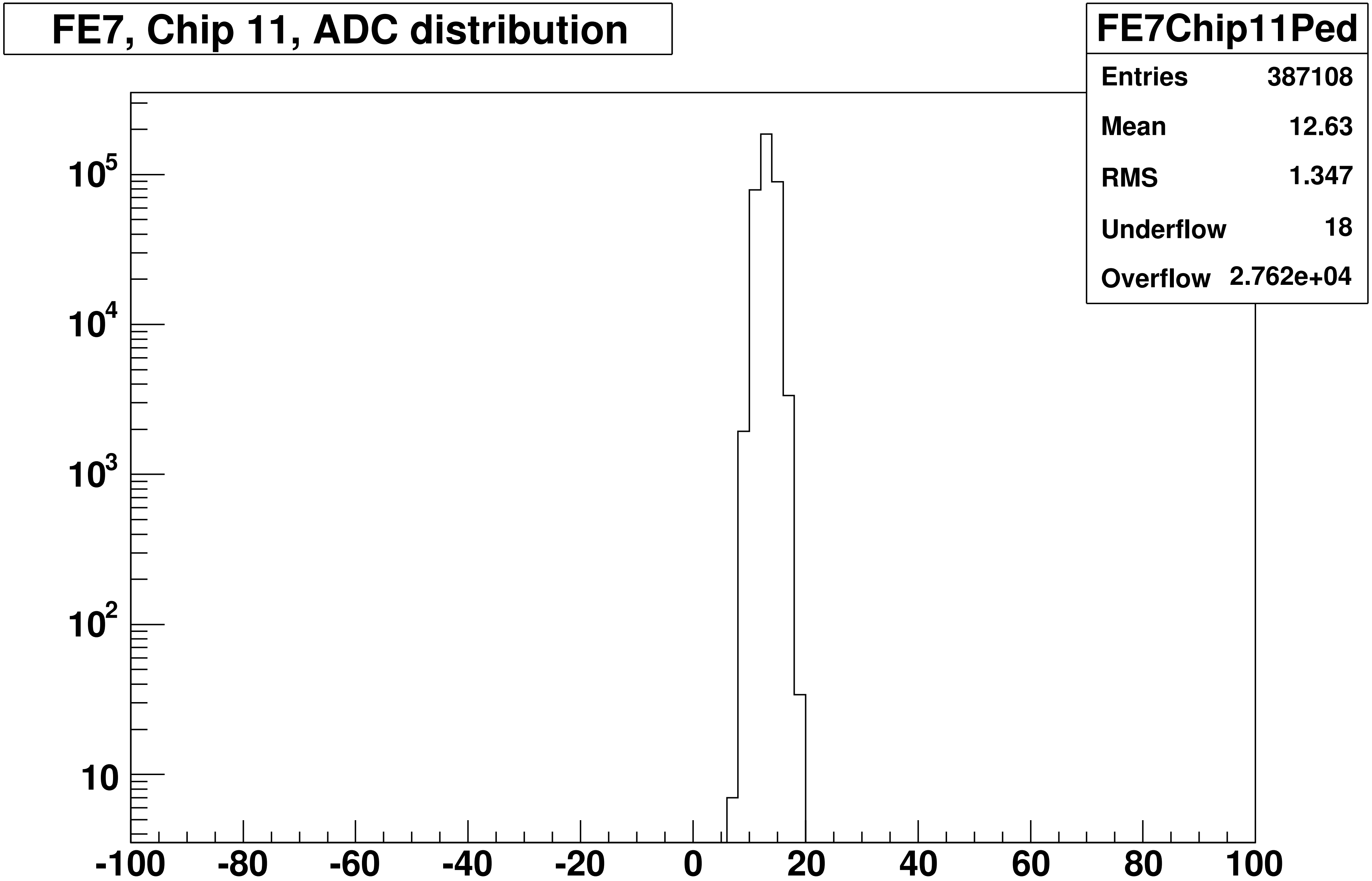

FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 387108
Mean 1361
RMS 4917
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

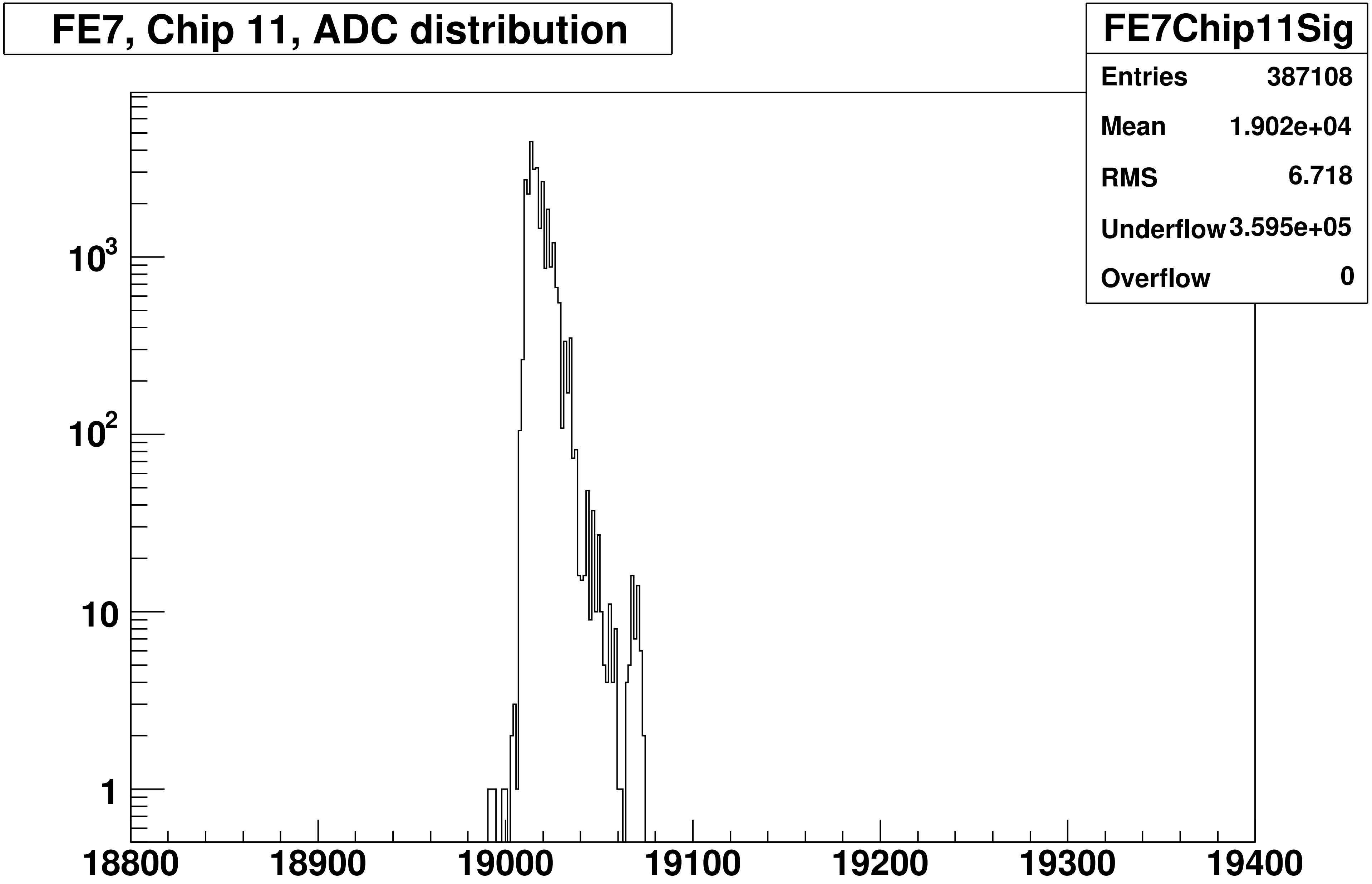

FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 387108
Mean -1.262
RMS 0.7614
Underflow 18
Overflow 2.761e+04
10^5
10^4
10^3
-100 -80 -60 -40 -20 0 20 40 60 80 100

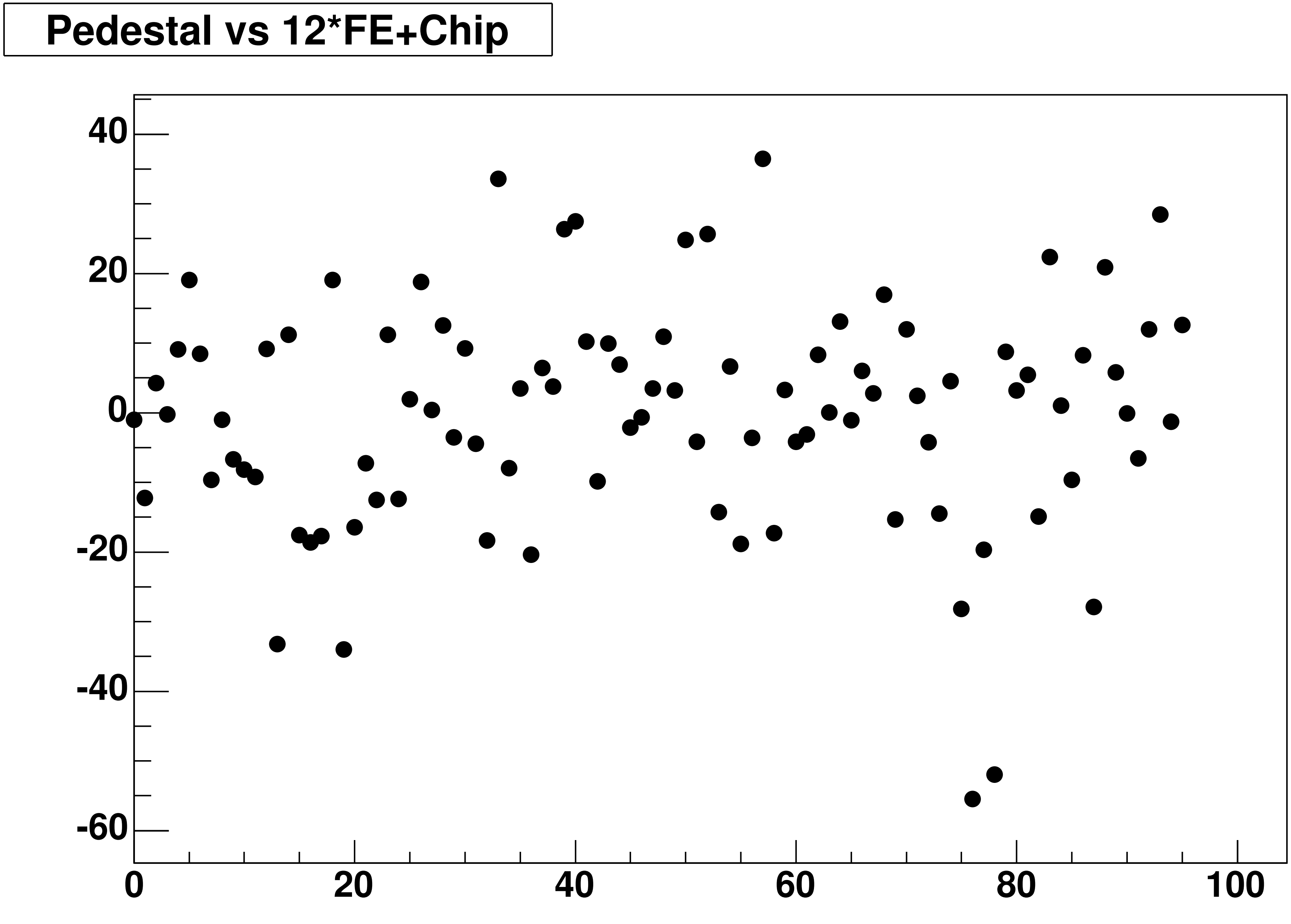

FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 387108
Mean 1.91e+04
RMS 7.114
Underflow 3.595e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



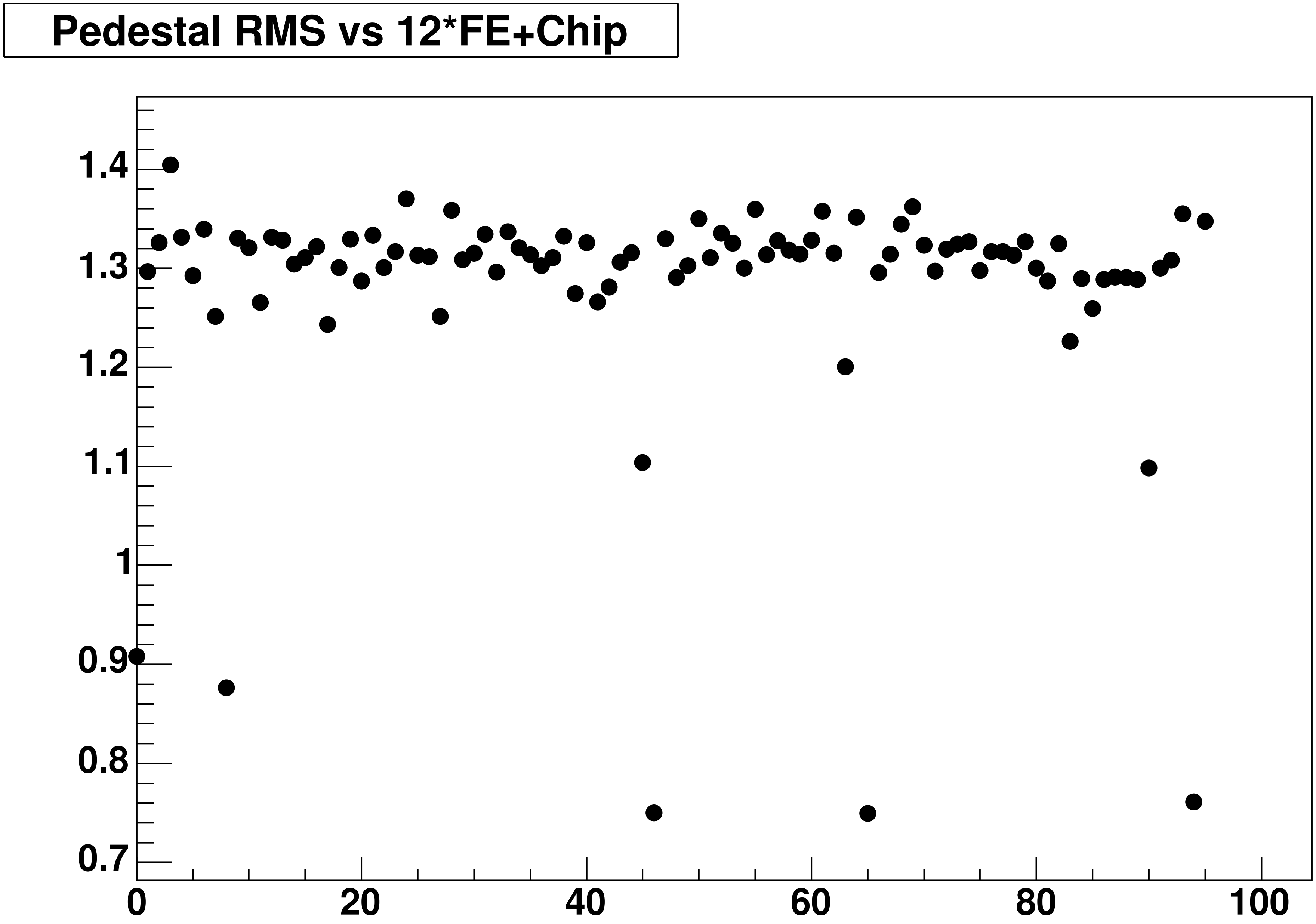
FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 387108
Mean 1368
RMS 4891
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 387108
Mean 12.63
RMS 1.347
Underflow 18
Overflow 2.762e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

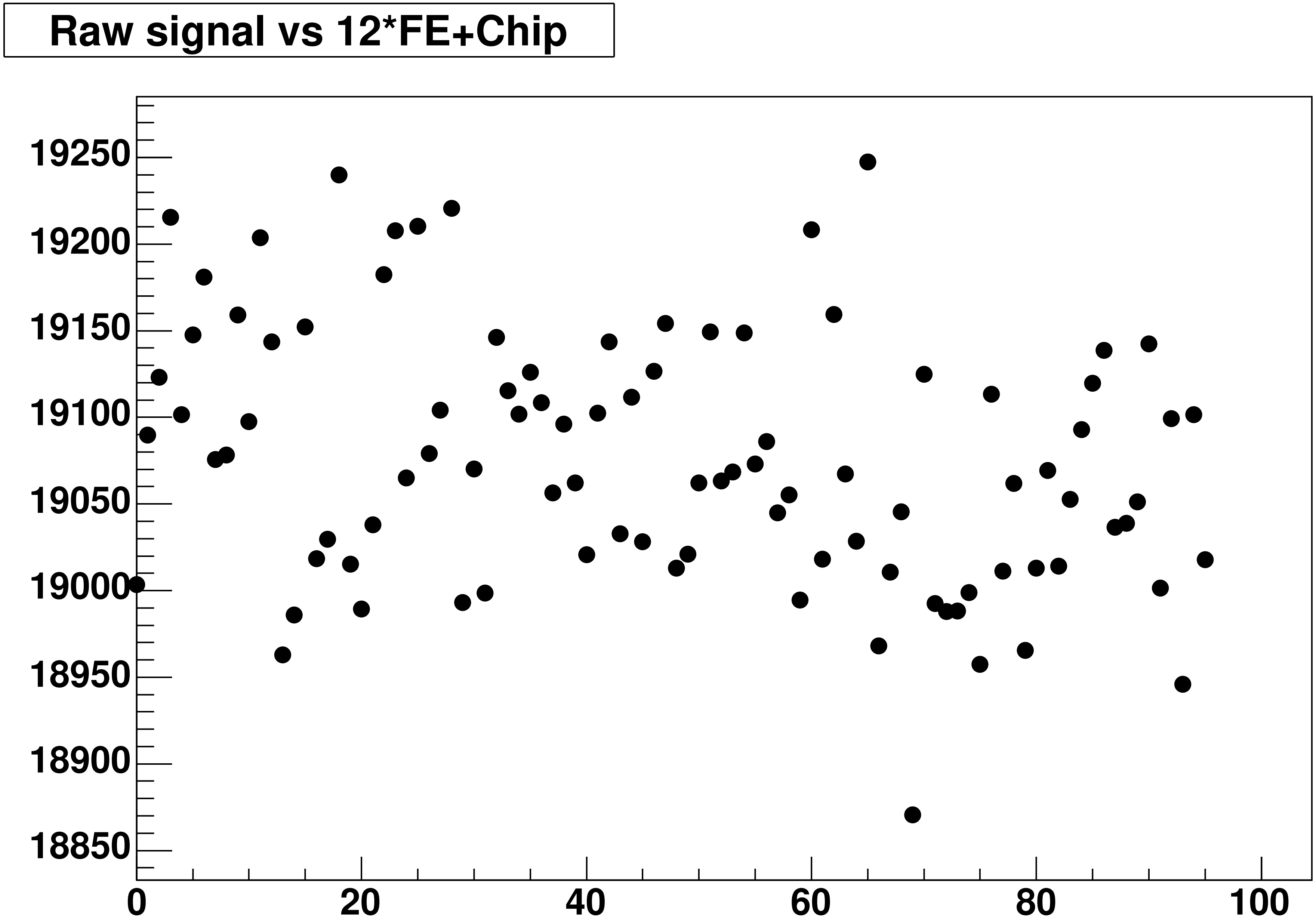

FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 387108
Mean 1.902e+04
RMS 6.718
Underflow 3.595e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

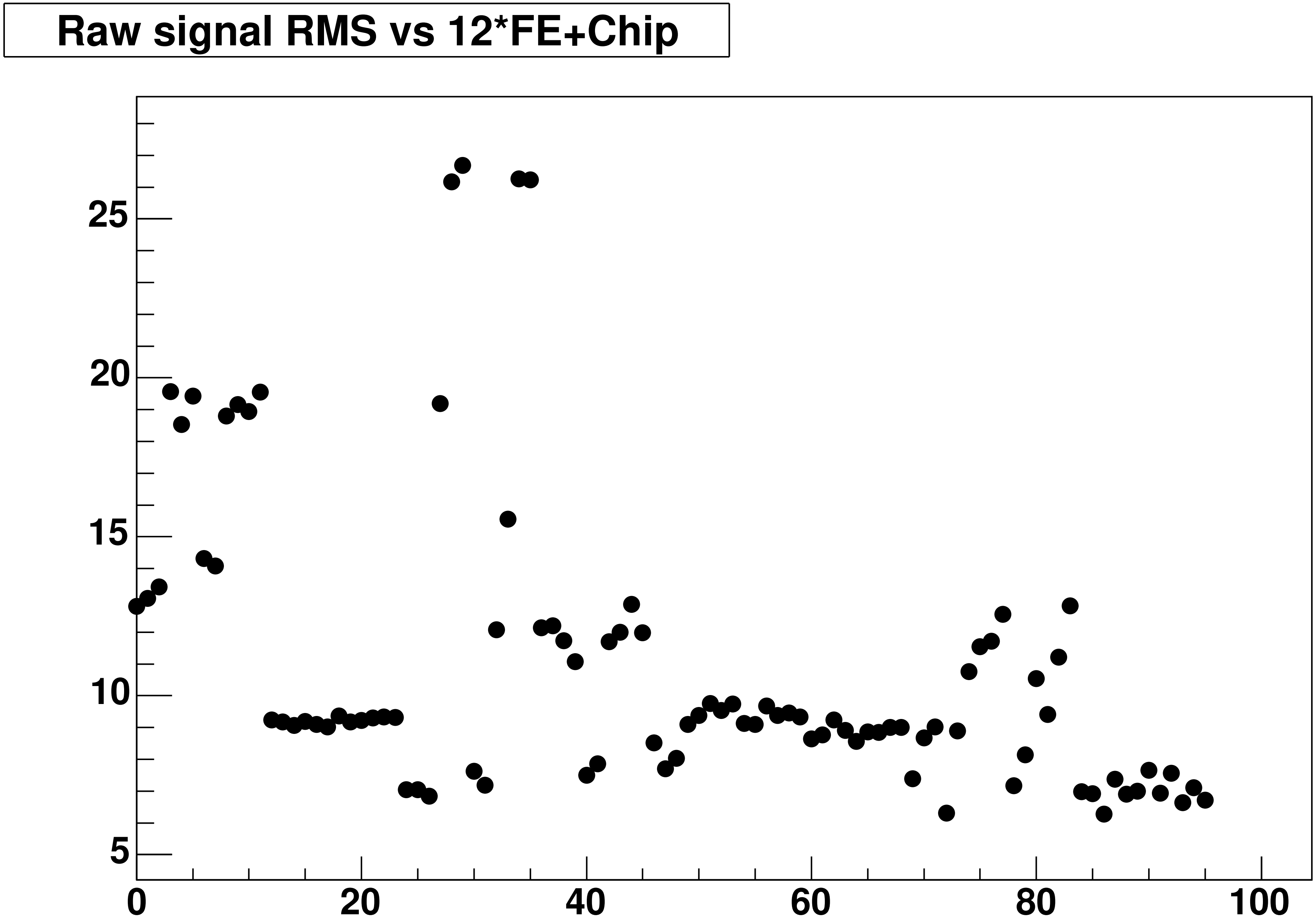


Pedestal vs 12*FE+Chip
40
20
0
-20
-40
-60
0
20
40
60
80
100

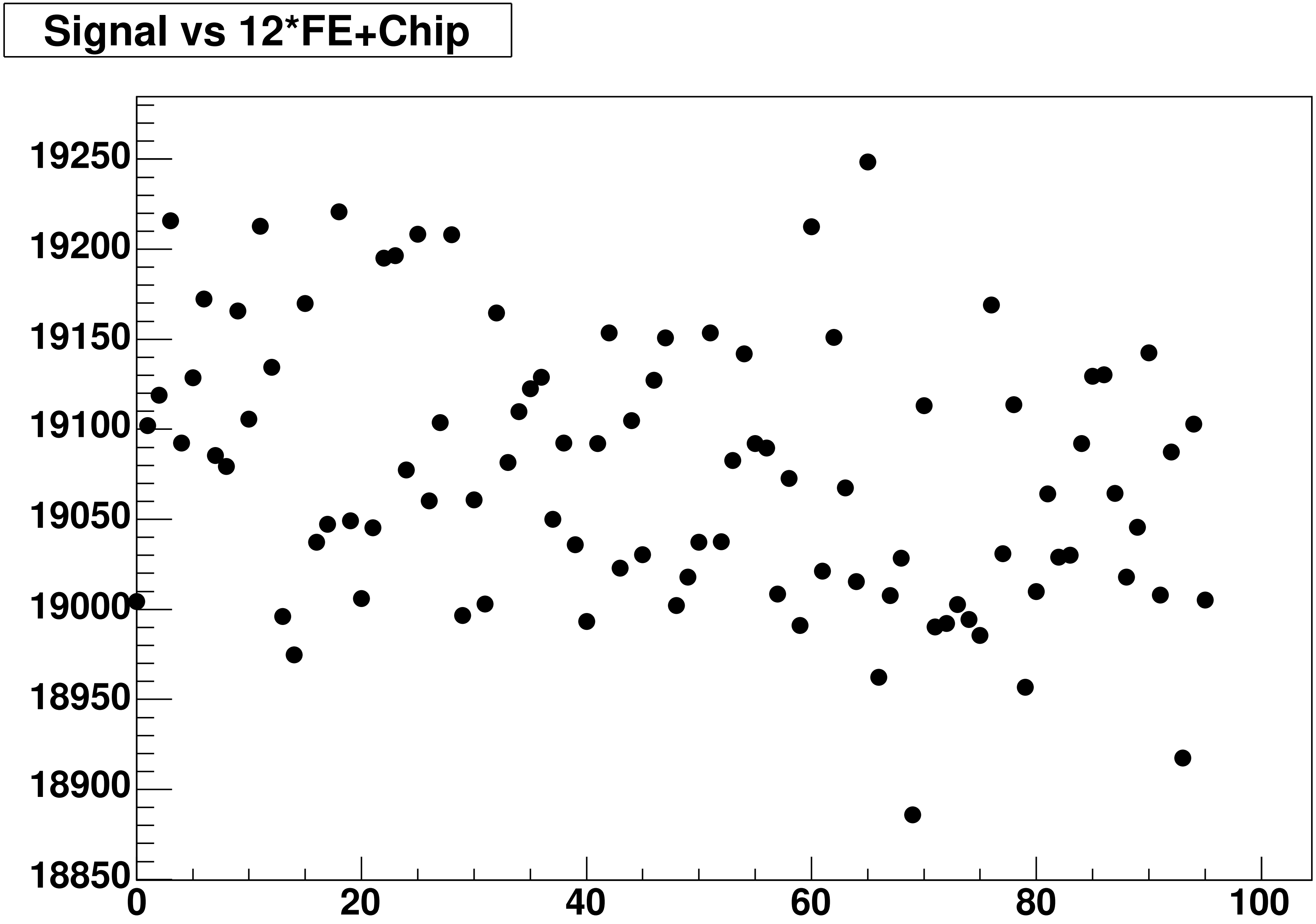


Pedestal RMS vs 12*FE+Chip
1.4
1.3
1.2
1.1
1
0.9
0.8
0.7
0
20
40
60
80
100

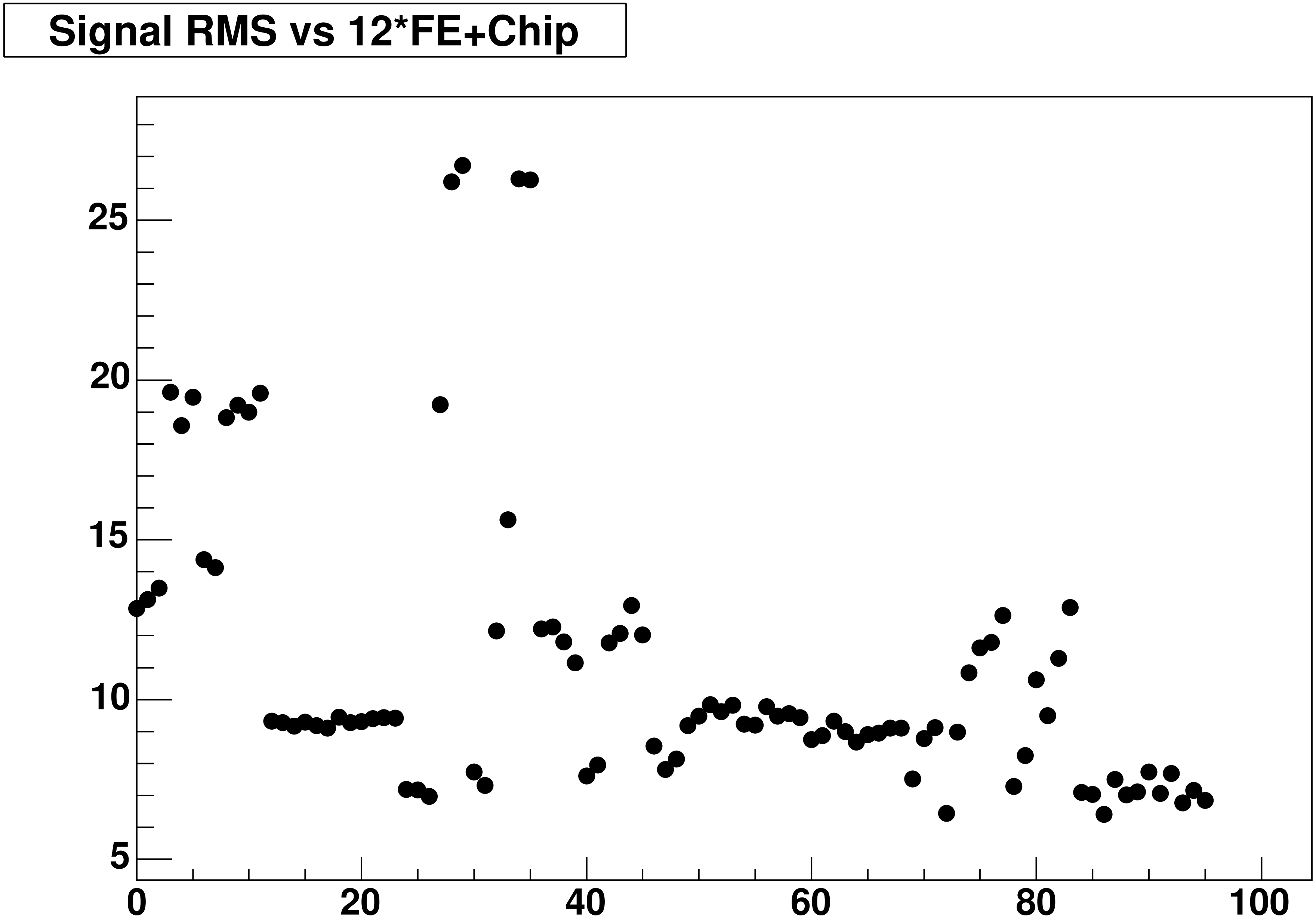

Raw signal vs 12*FE+Chip
19250
19200
19150
19100
19050
19000
18950
18900
18850
0
20
40
60
80
100



Raw signal RMS vs 12*FE+Chip
25
20
15
10
5
0
20
40
60
80
100



Signal vs 12*FE+Chip
19250
19200
19150
19100
19050
19000
18950
18900
18850
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
25
20
15
10
5
0
20
40
60
80
100