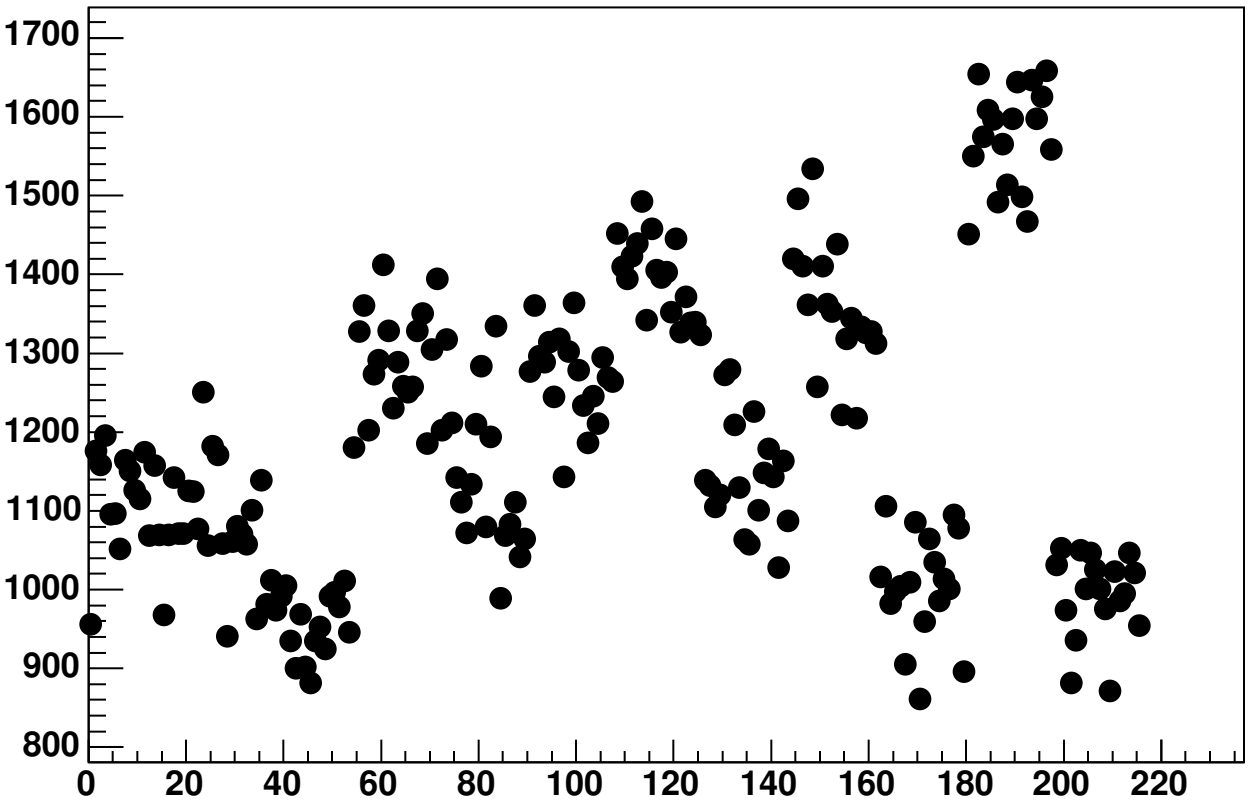
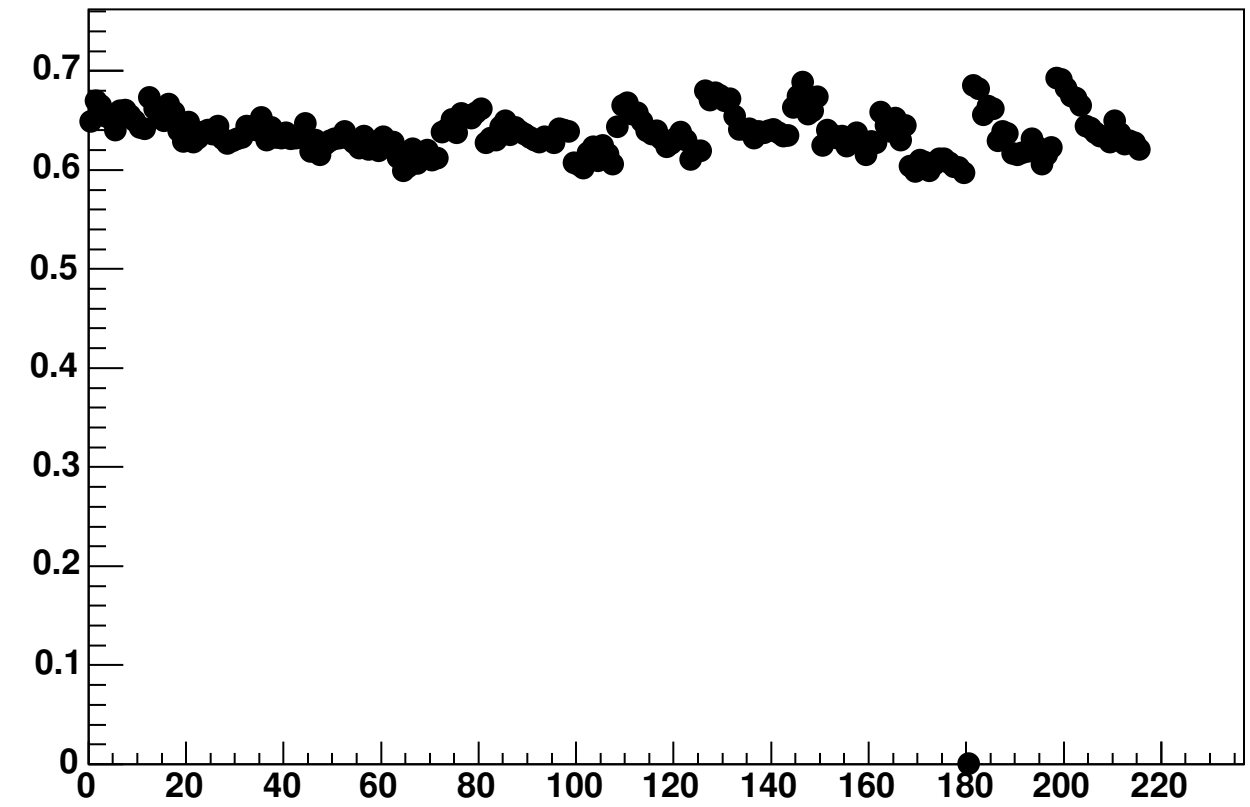
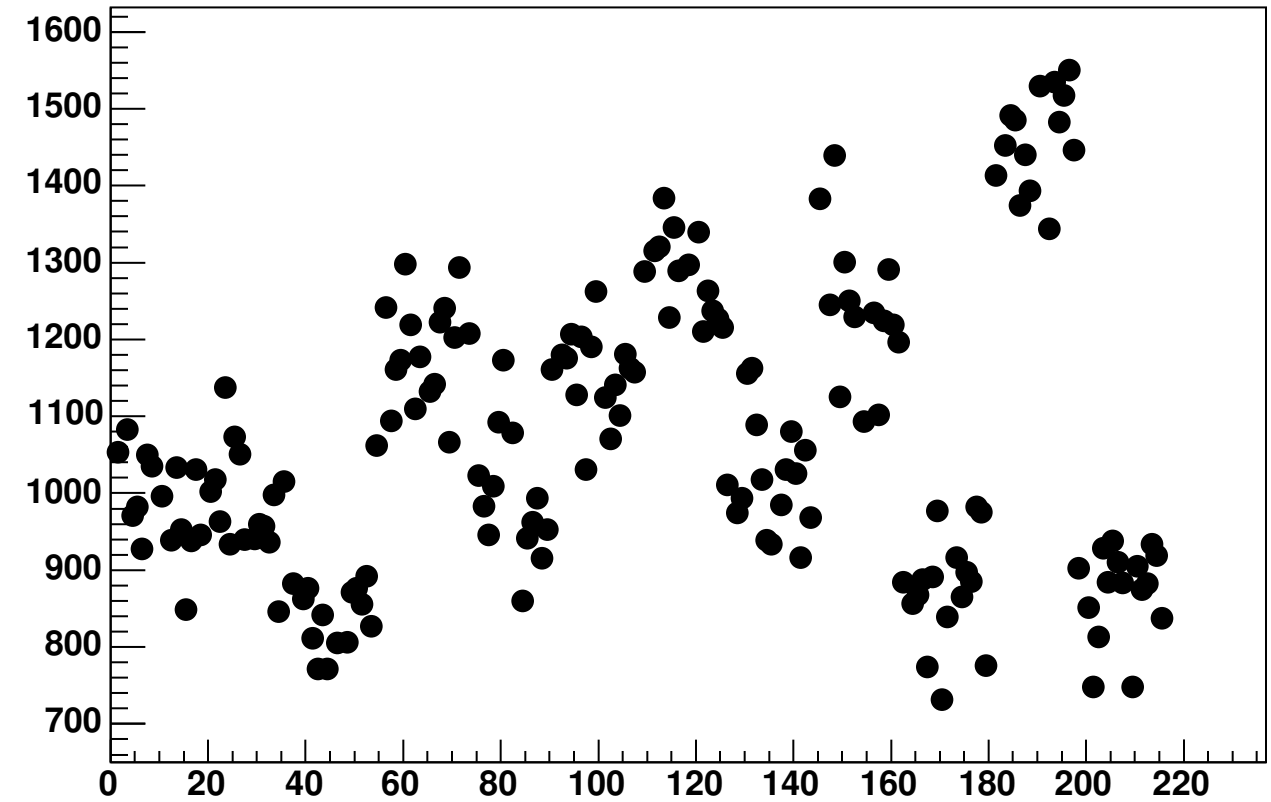
Channel Enabled, Hold=30, Fit Intercept vs 18*Chip+Chan



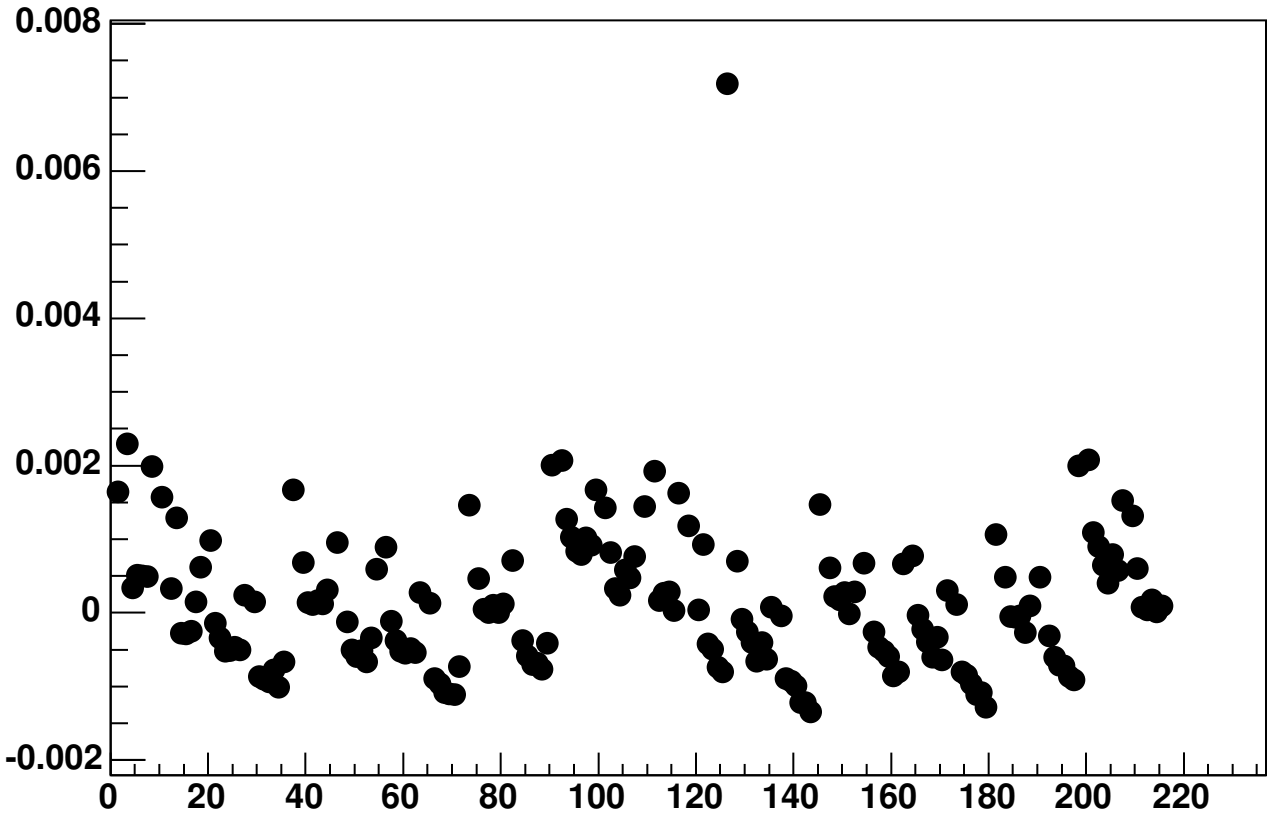
Channel Enabled, Hold=30, Fit Slope vs 18*Chip+Chan



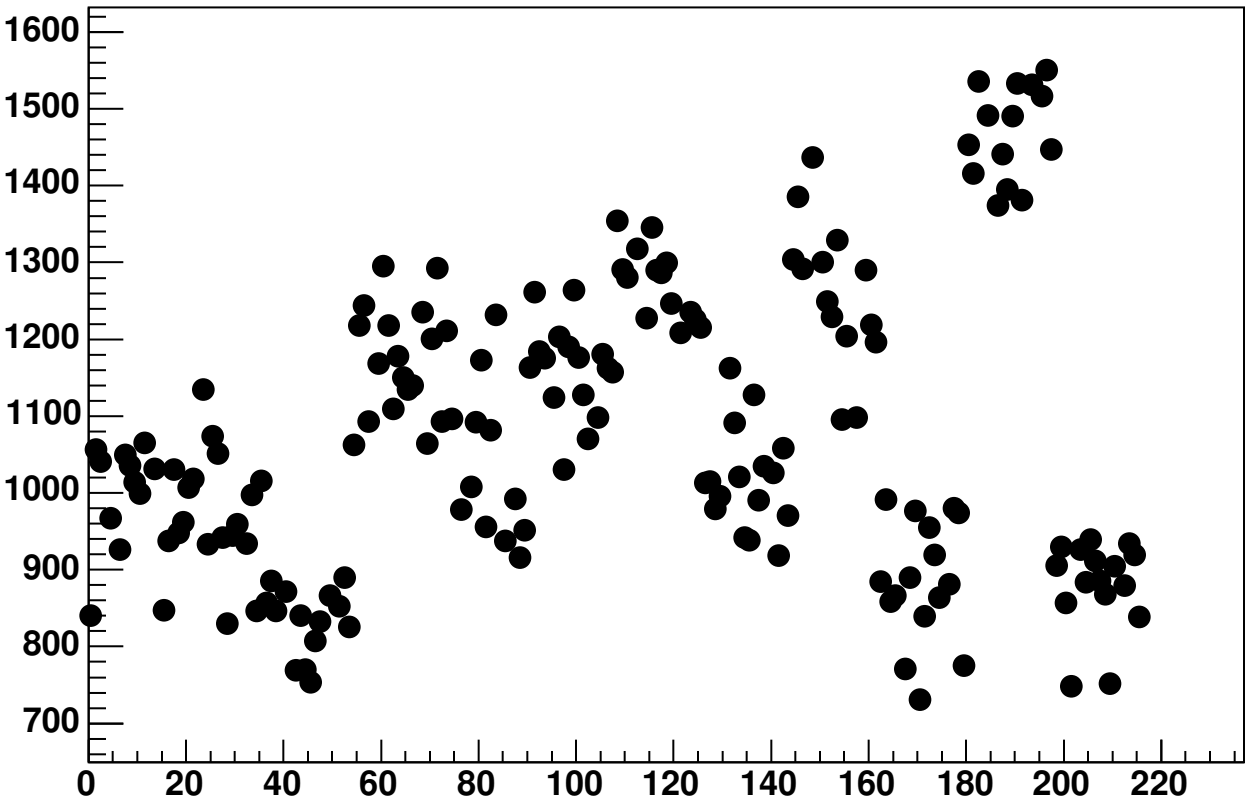
Disabled 0, Hold=30, Fit Intercept vs 18*Chip+Chan



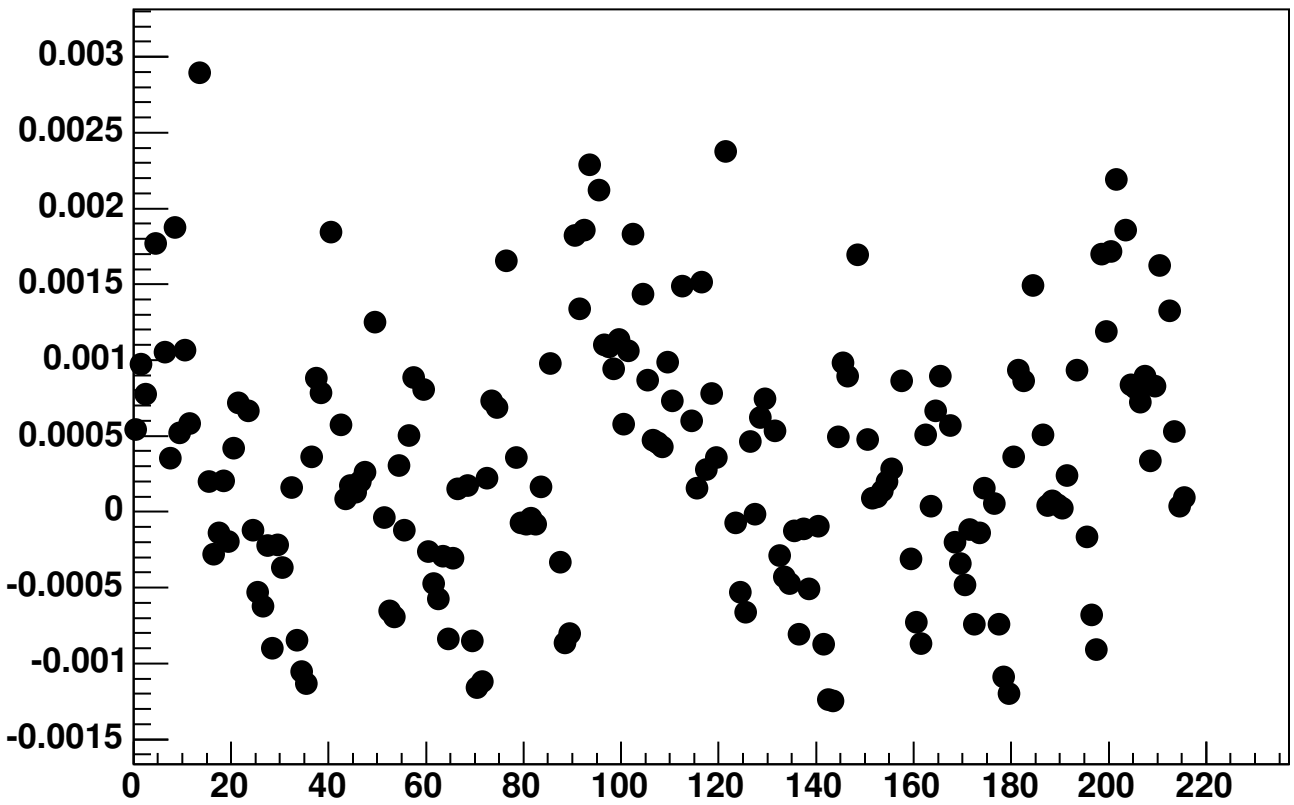
Disabled 0, Hold=30, Fit Slope vs 18*Chip+Chan



Disabled 1, Hold=30, Fit Intercept vs 18*Chip+Chan



Disabled 1, Hold=30, Fit Slope vs 18*Chip+Chan



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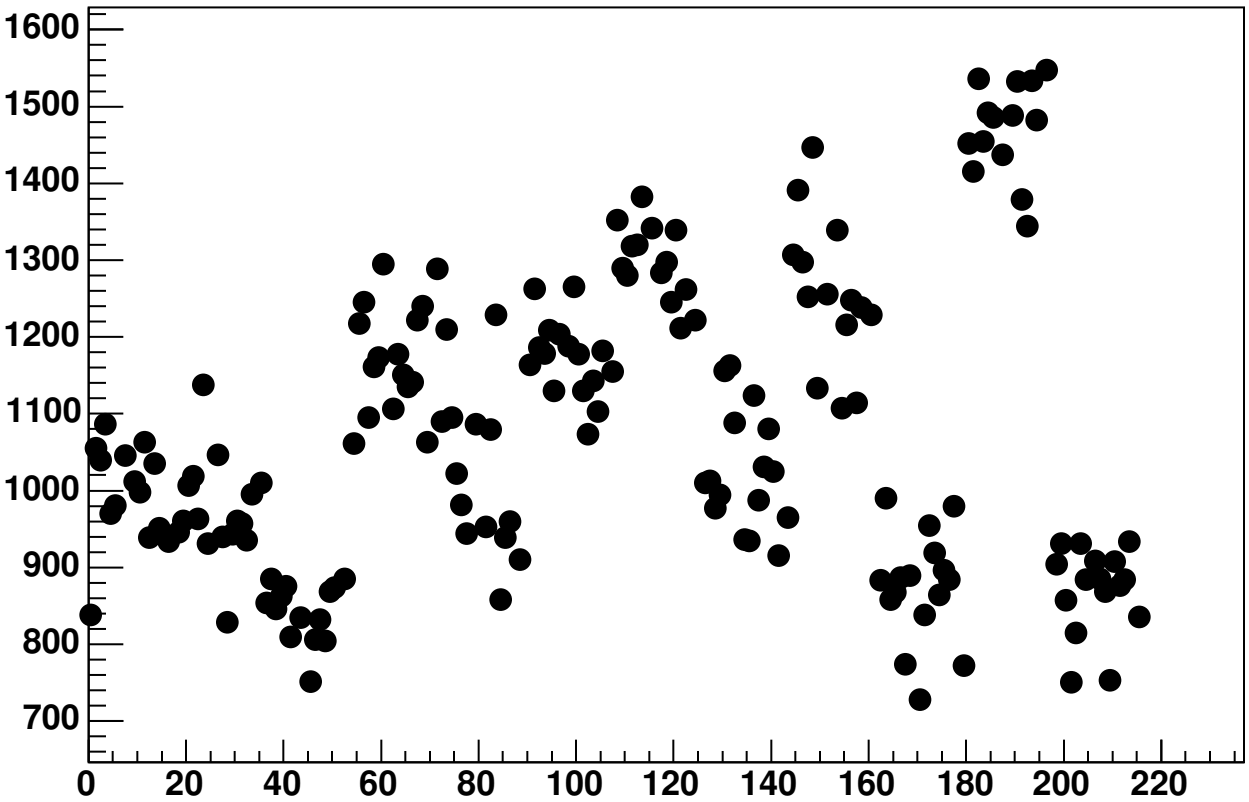
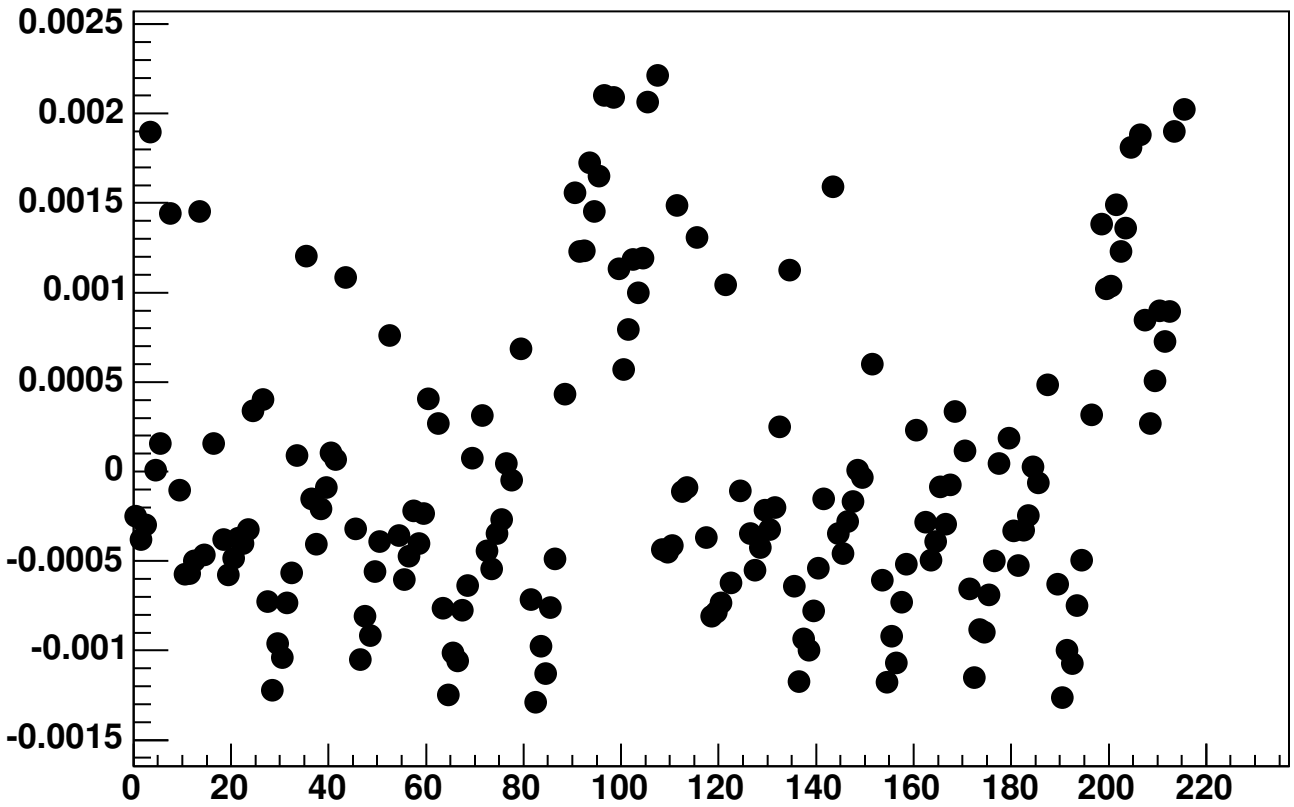
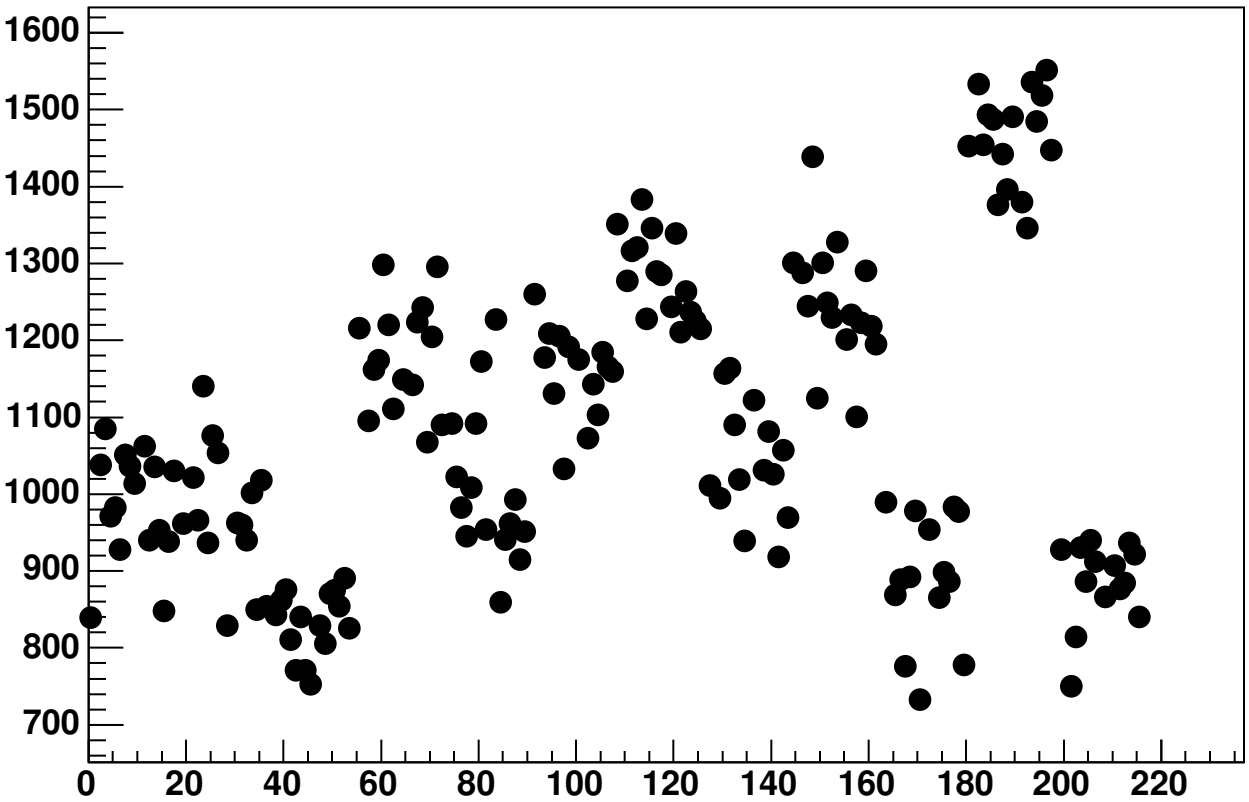


Disabled 2, Hold=30, Fit Slope vs 18*Chip+Chan



Disabled 3, Hold=30, Fit Intercept vs 18*Chip+Chan



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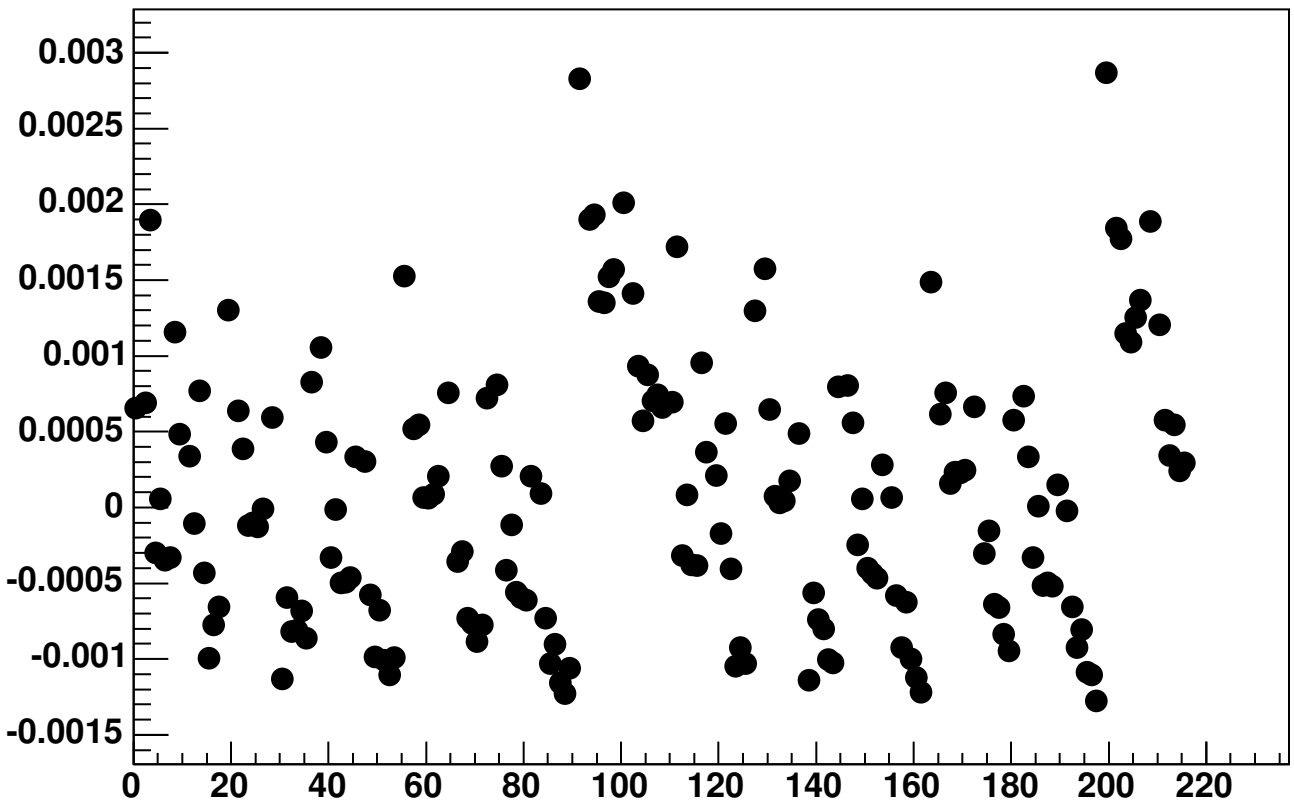
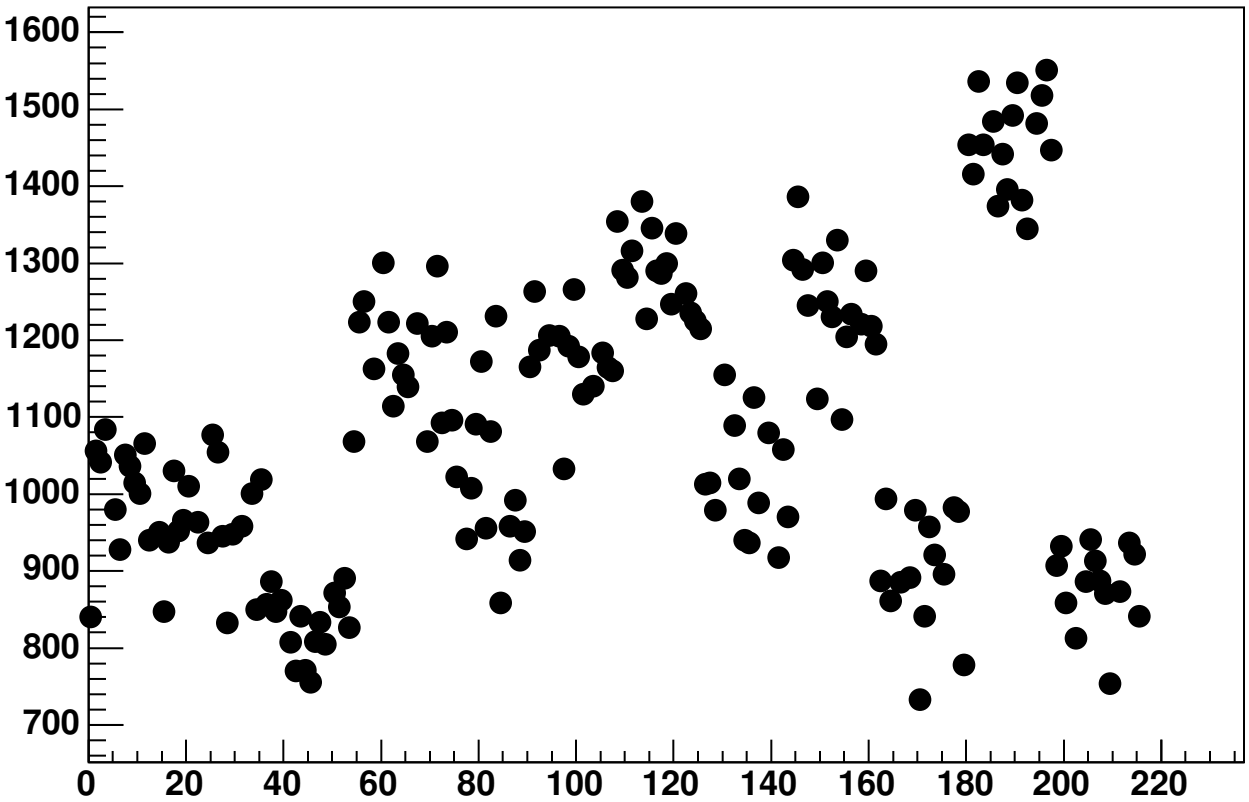
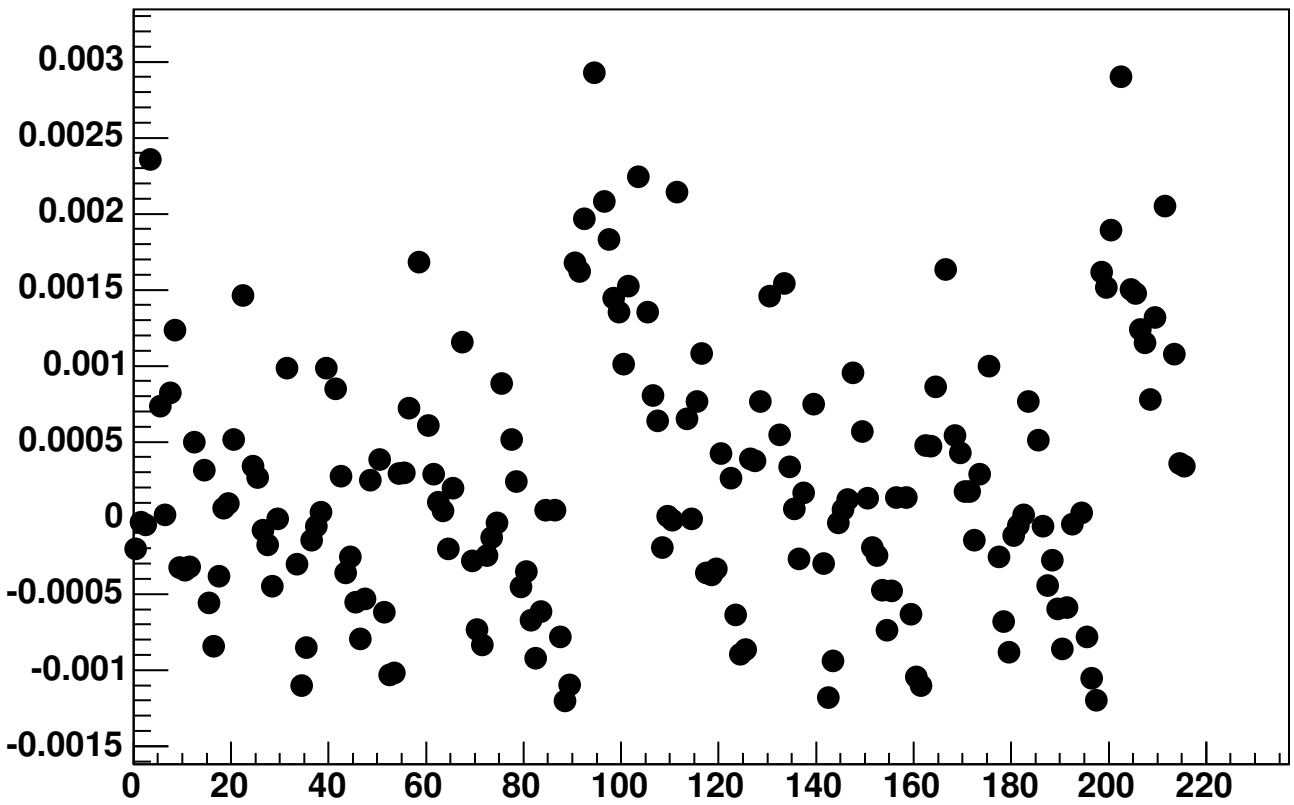


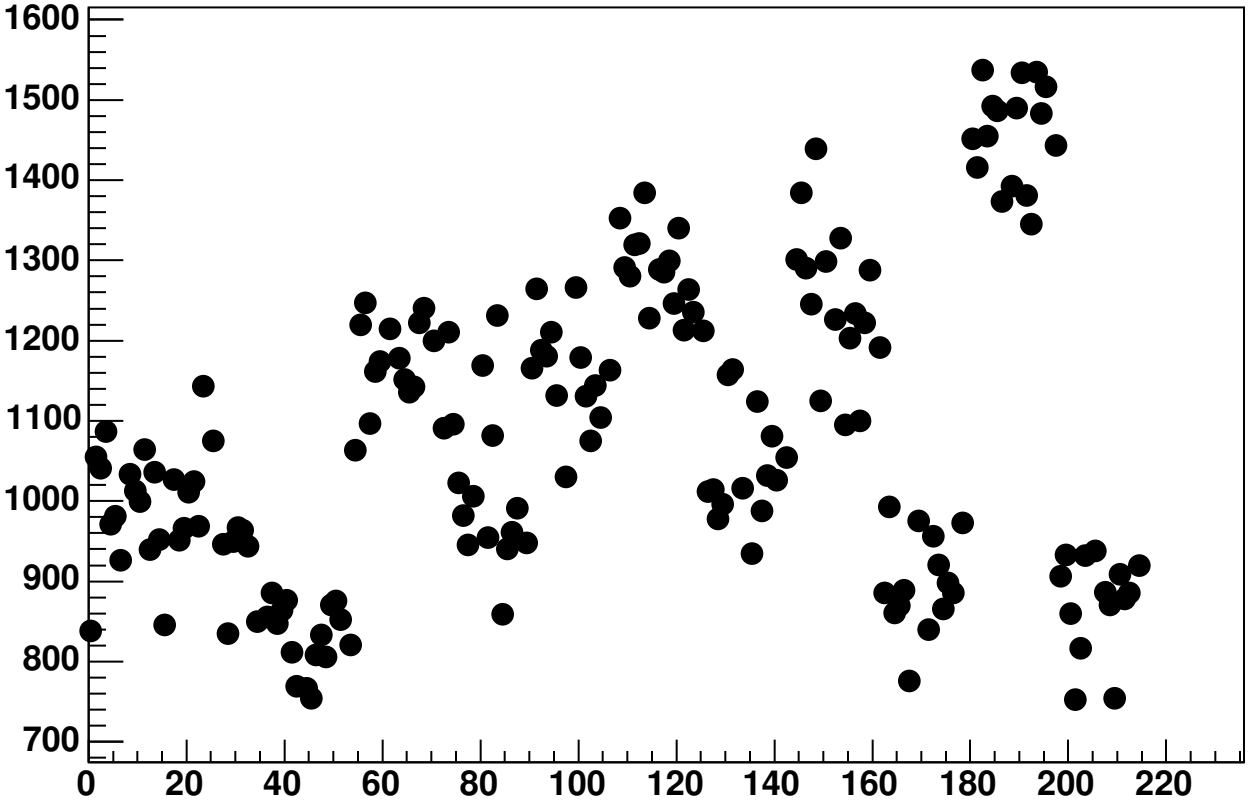
Disabled 4, Hold=30, Fit Intercept vs 18*Chip+Chan



Disabled 4, Hold=30, Fit Slope vs 18*Chip+Chan



Disabled 5, Hold=30, Fit Intercept vs 18*Chip+Chan



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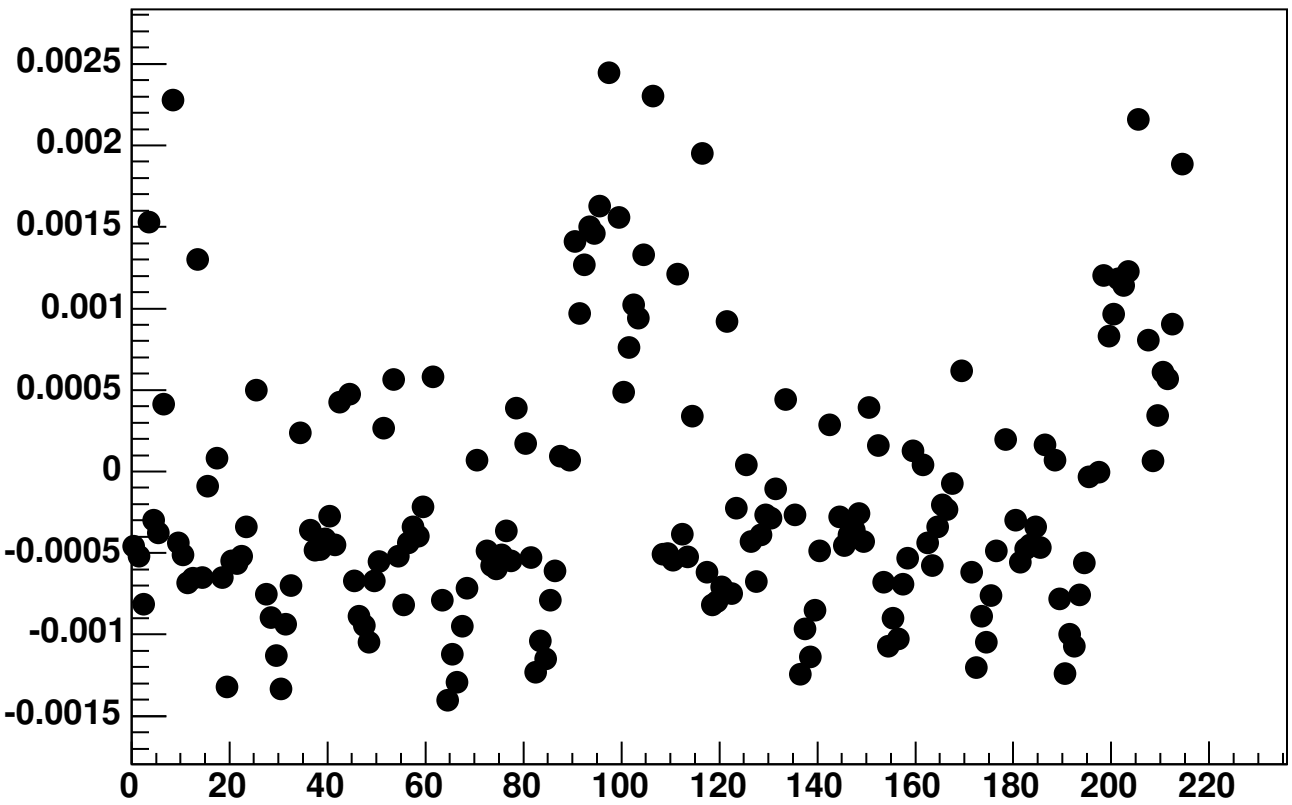
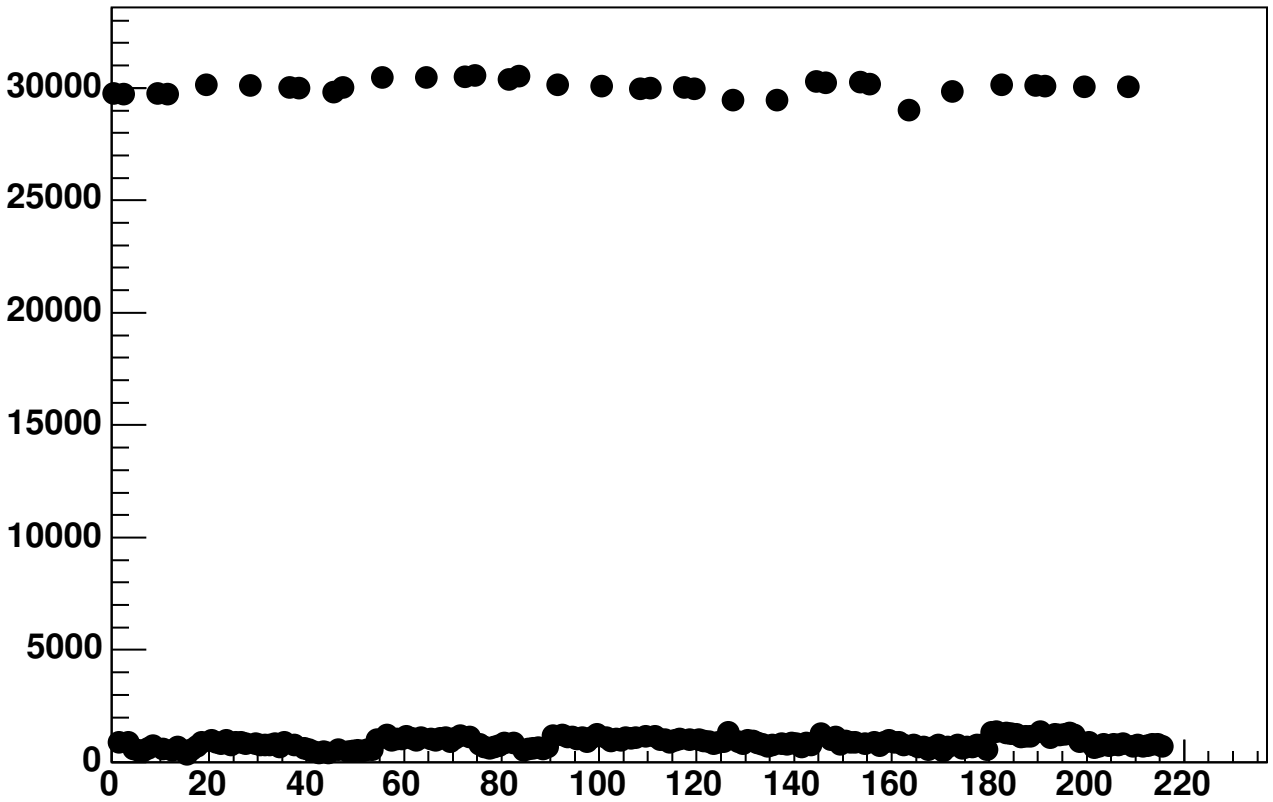
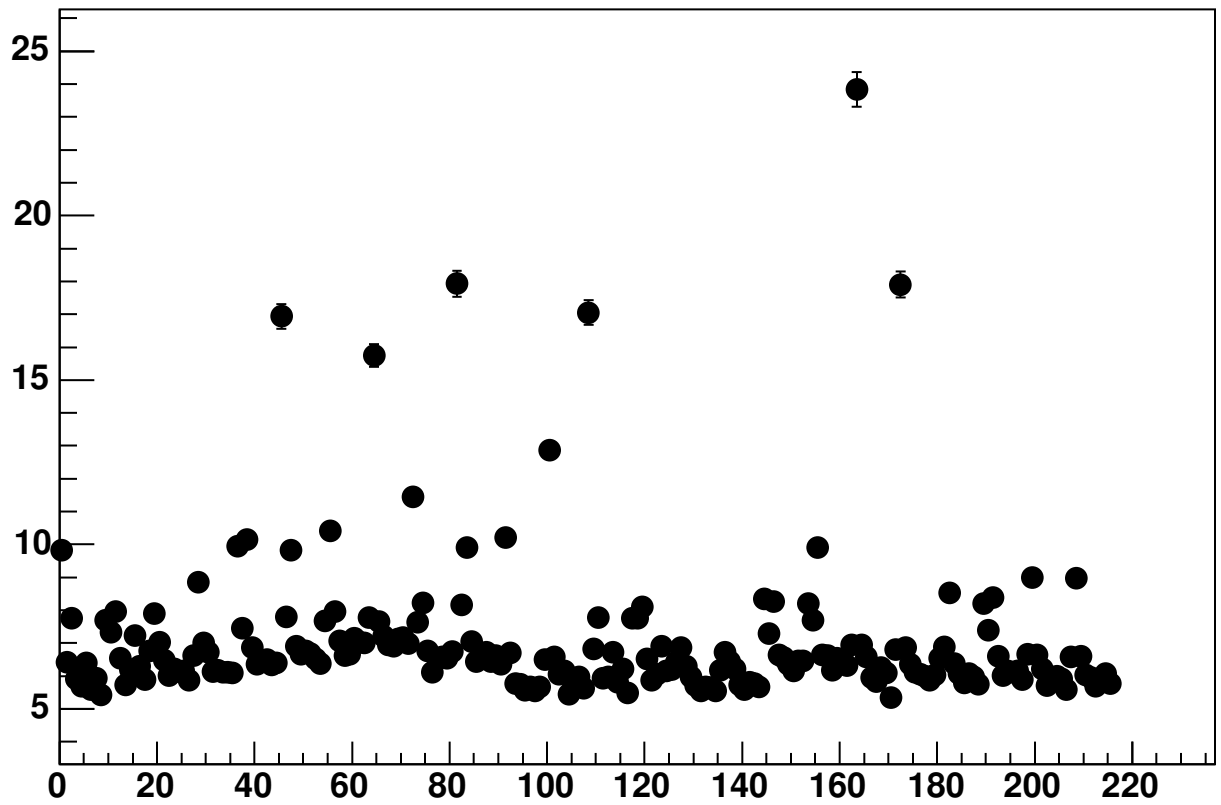


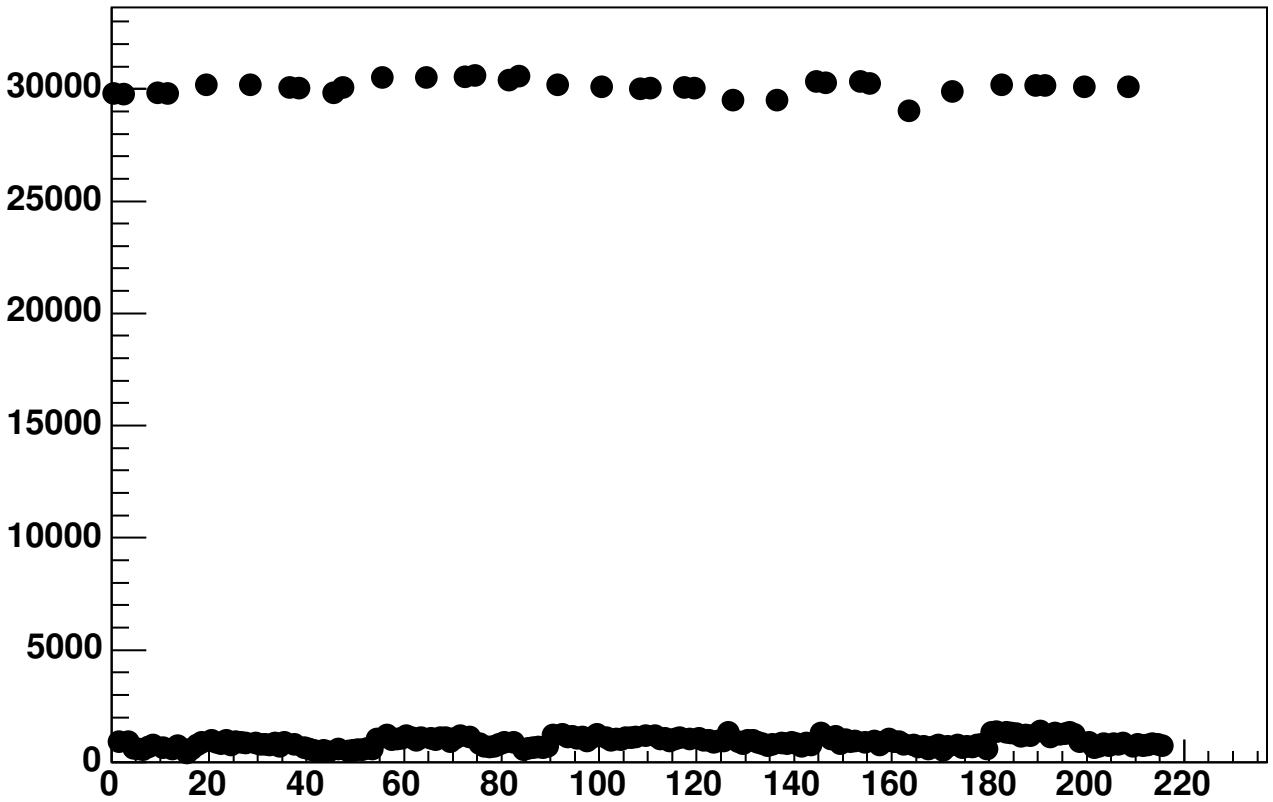
Enable 0, Hold=30, DAC=63488, ADC Mean vs 18*Chip+Chan



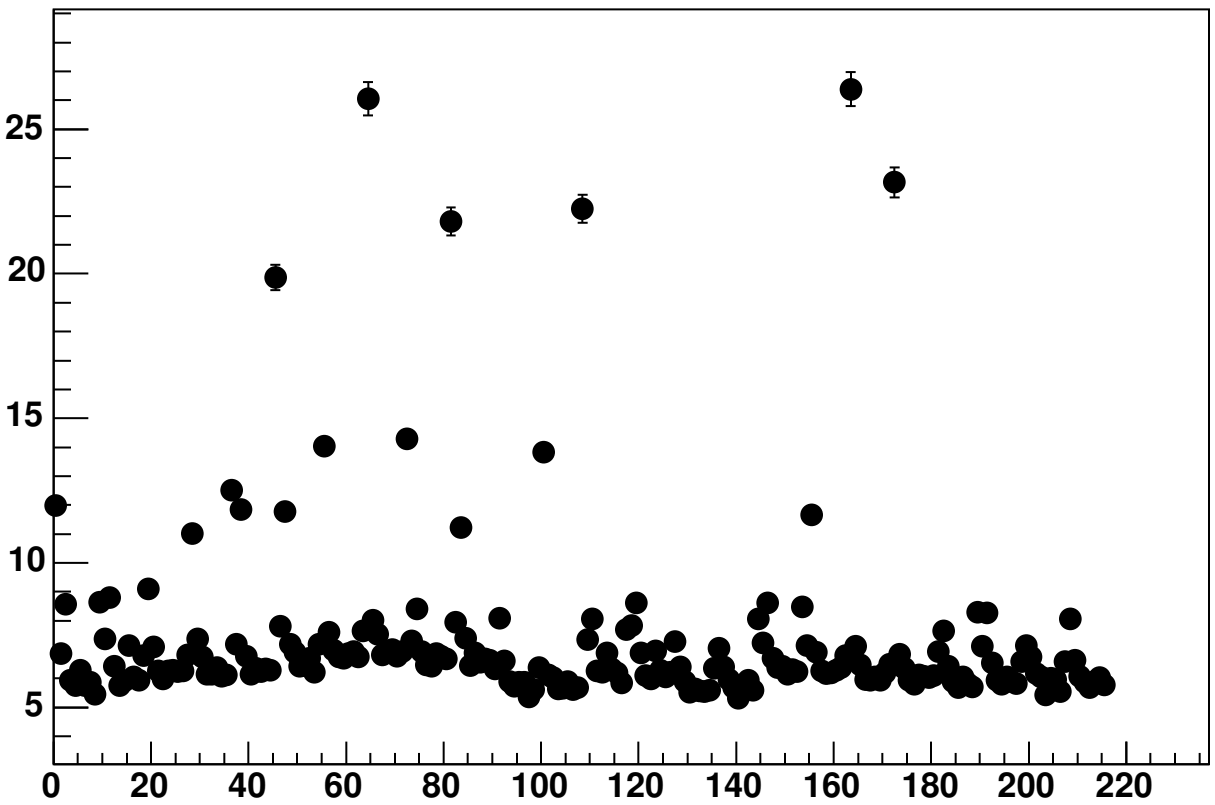
Enable 0, Hold=30, DAC=63488, ADC Noise vs 18*Chip+Chan



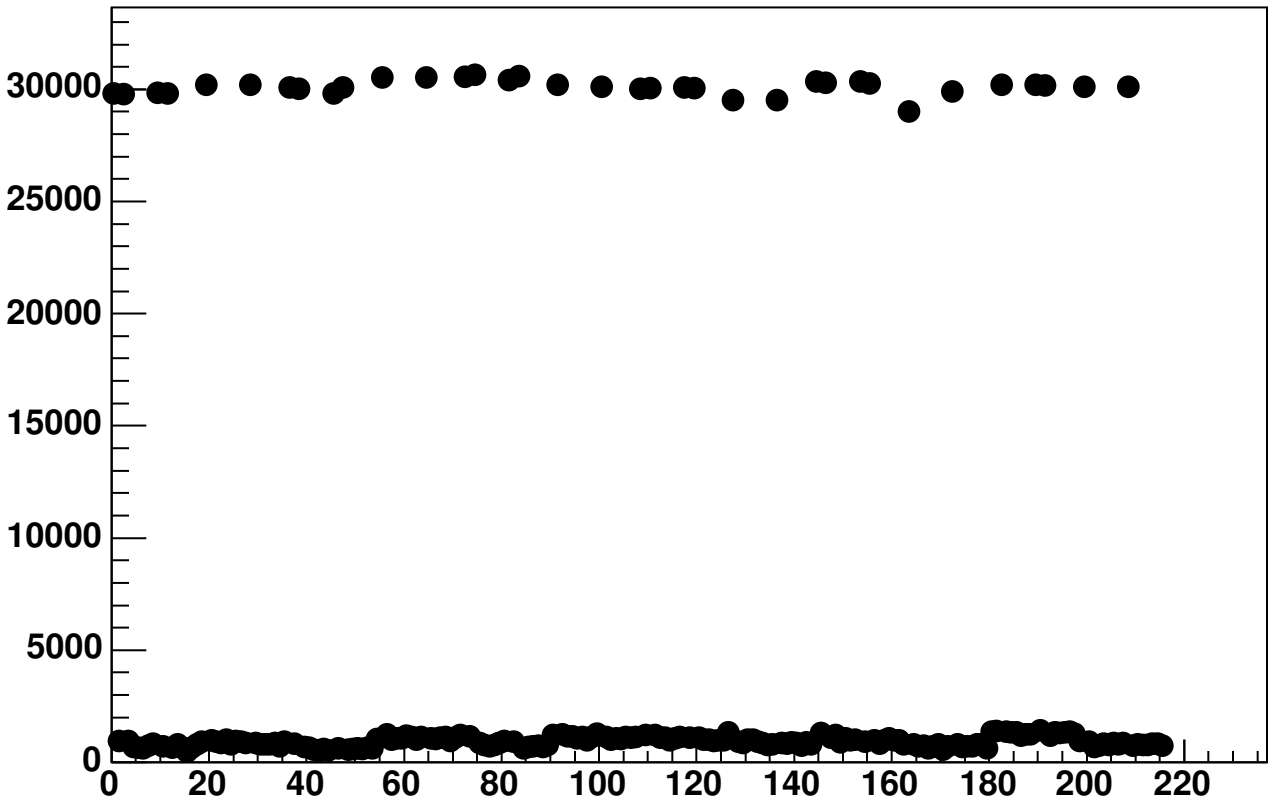
Enable 0, Hold=30, DAC=61440, ADC Mean vs 18*Chip+Chan



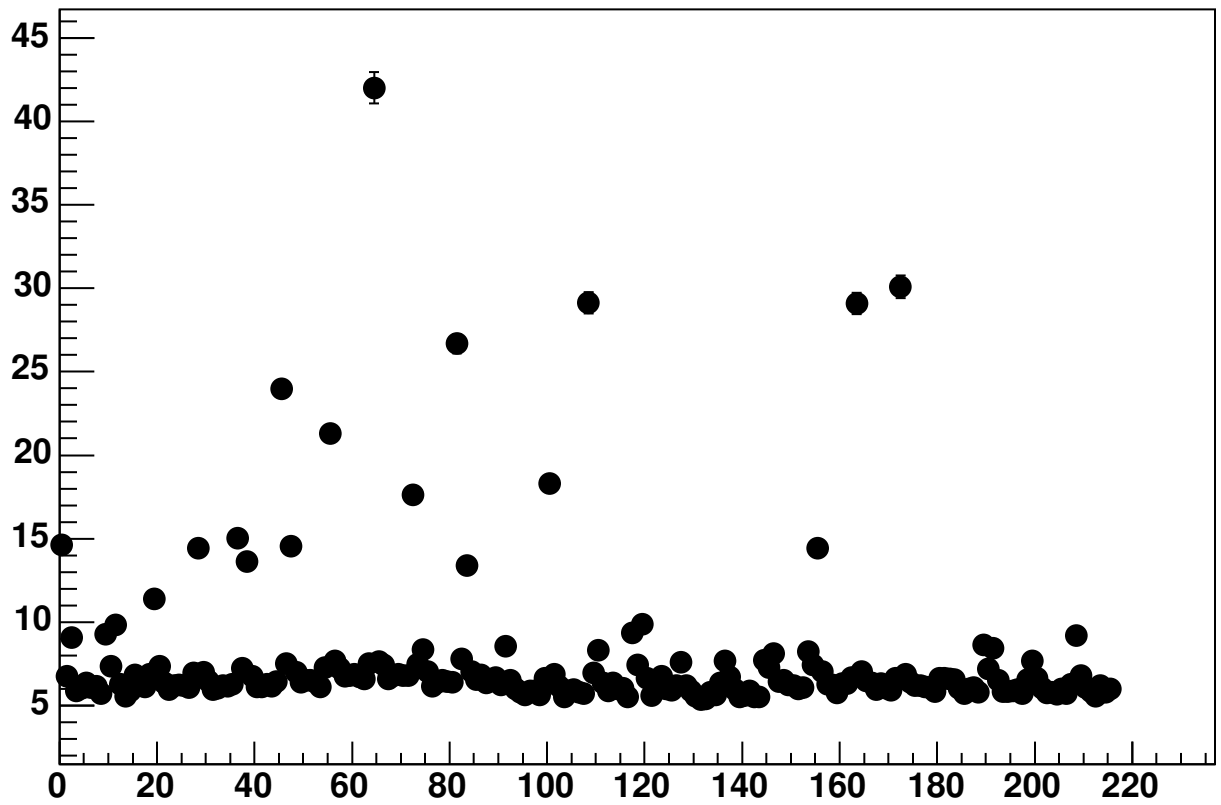
Enable 0, Hold=30, DAC=61440, ADC Noise vs 18*Chip+Chan



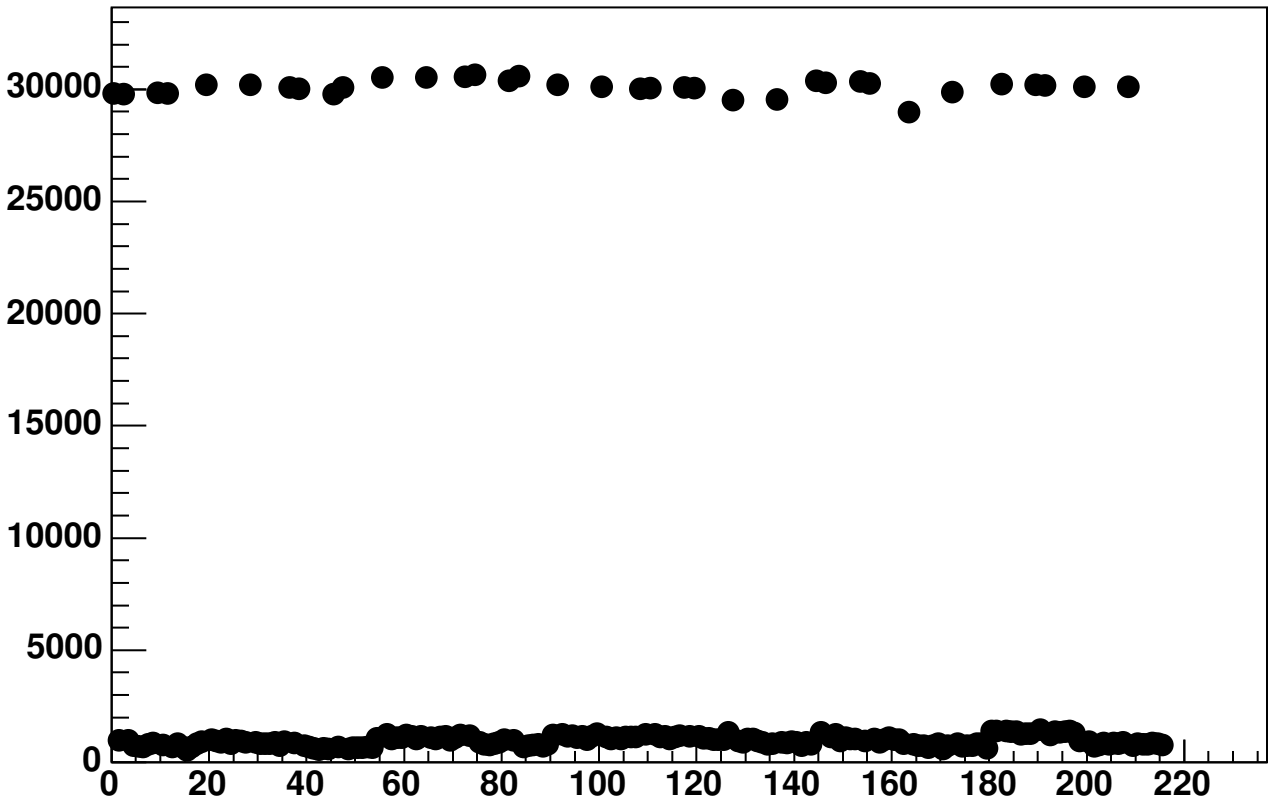
Enable 0, Hold=30, DAC=59392, ADC Mean vs 18*Chip+Chan



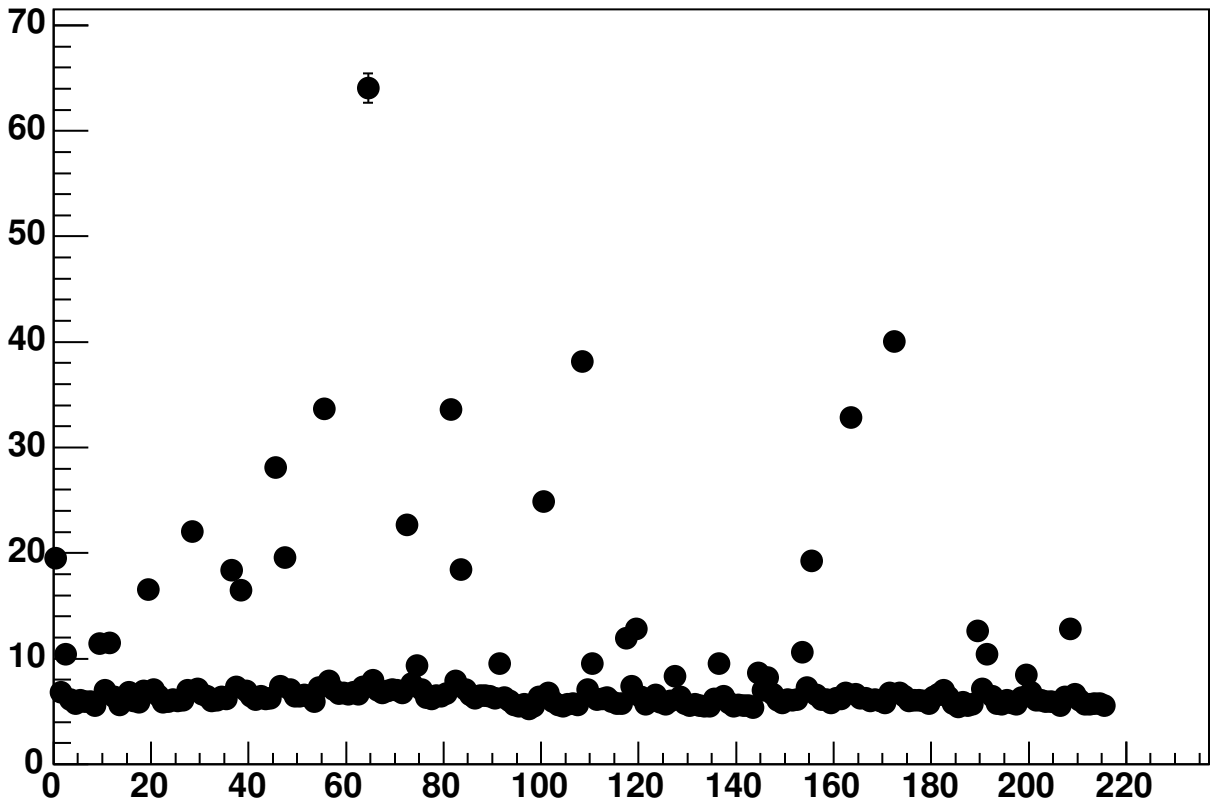
Enable 0, Hold=30, DAC=59392, ADC Noise vs 18*Chip+Chan



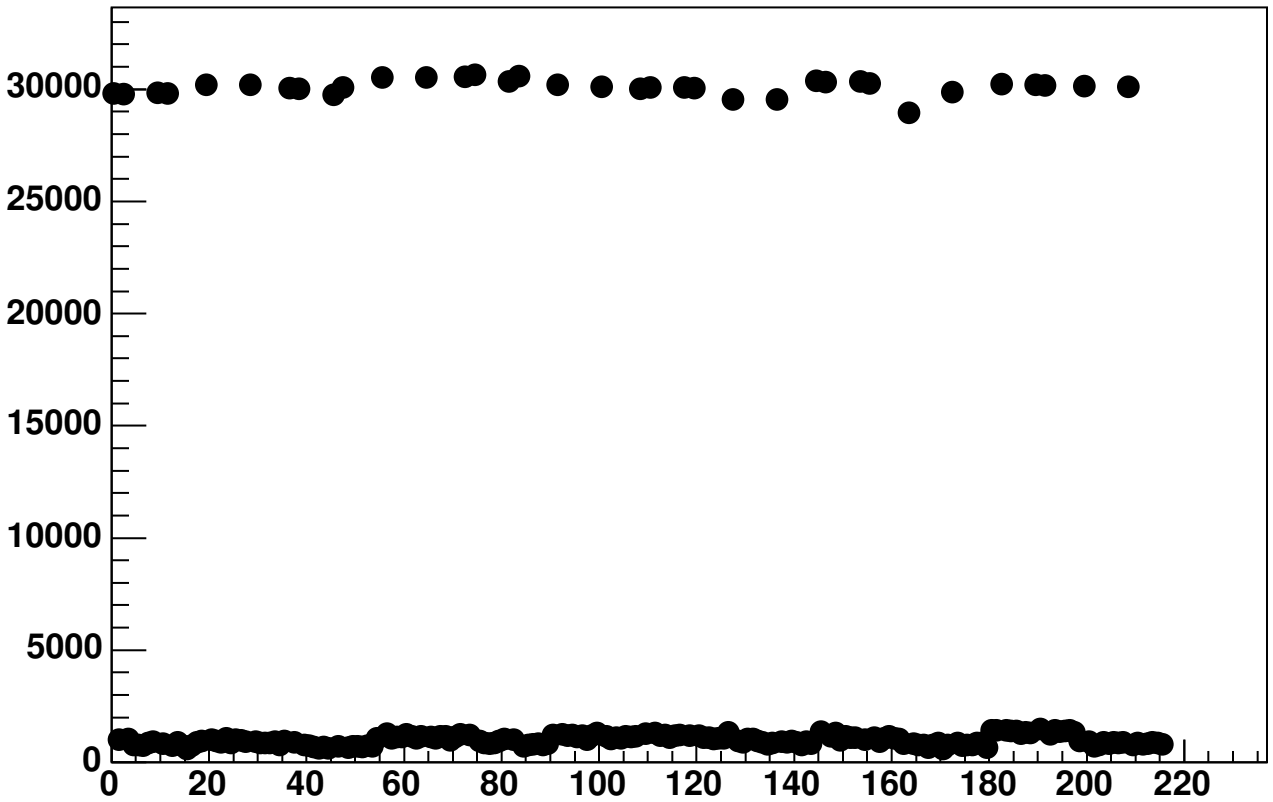
Enable 0, Hold=30, DAC=57344, ADC Mean vs 18*Chip+Chan



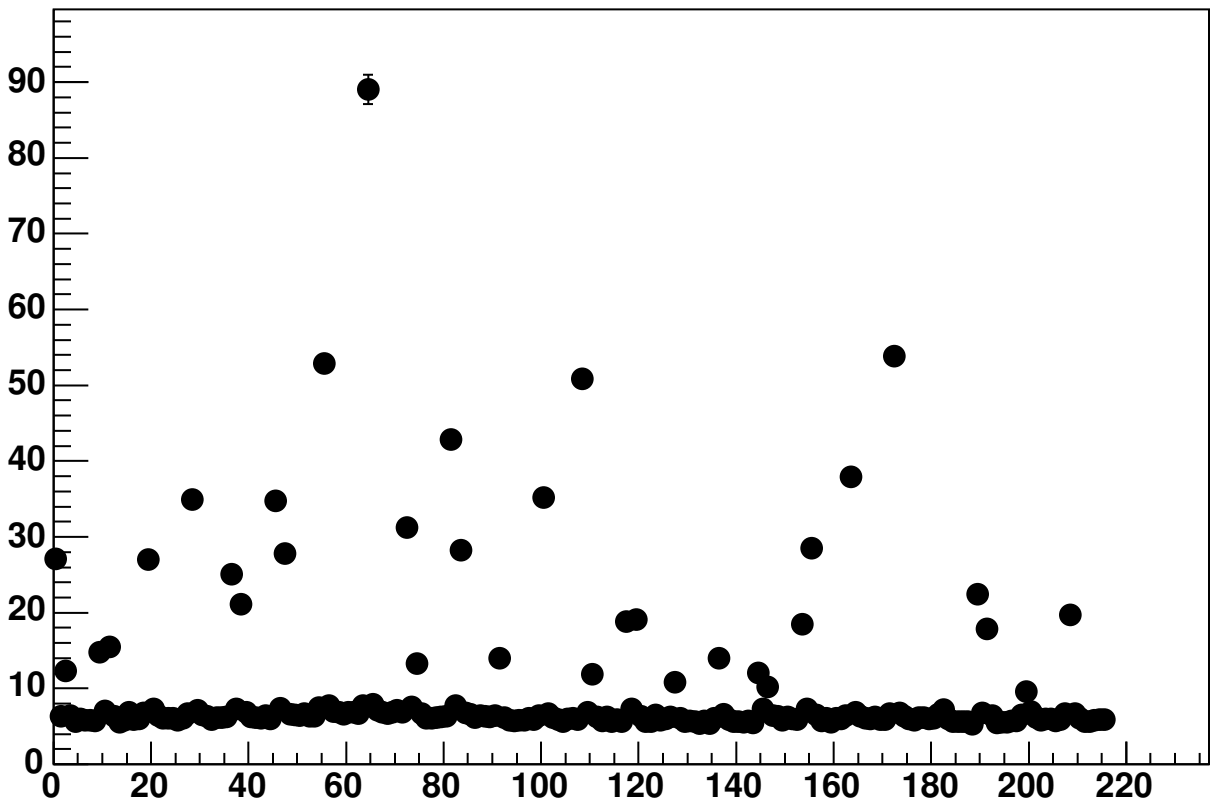
Enable 0, Hold=30, DAC=57344, ADC Noise vs 18*Chip+Chan



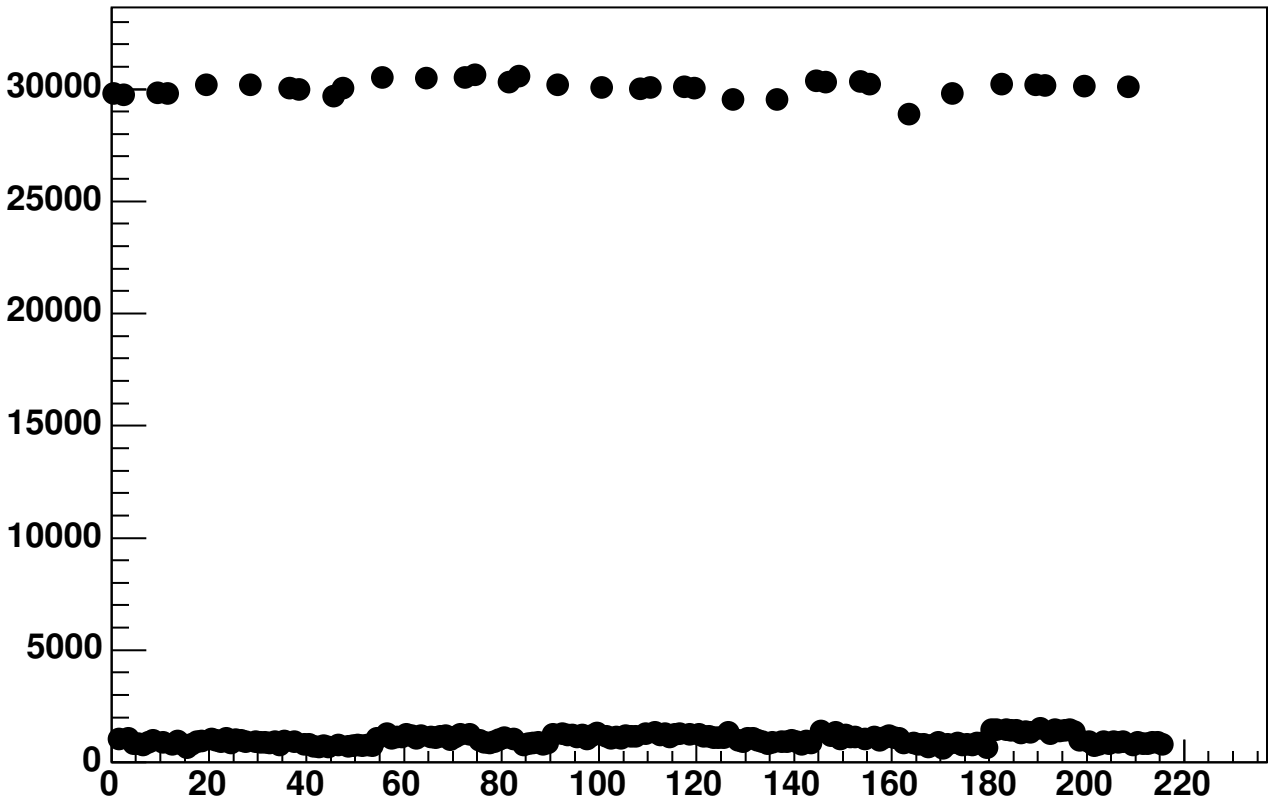
Enable 0, Hold=30, DAC=55296, ADC Mean vs 18*Chip+Chan



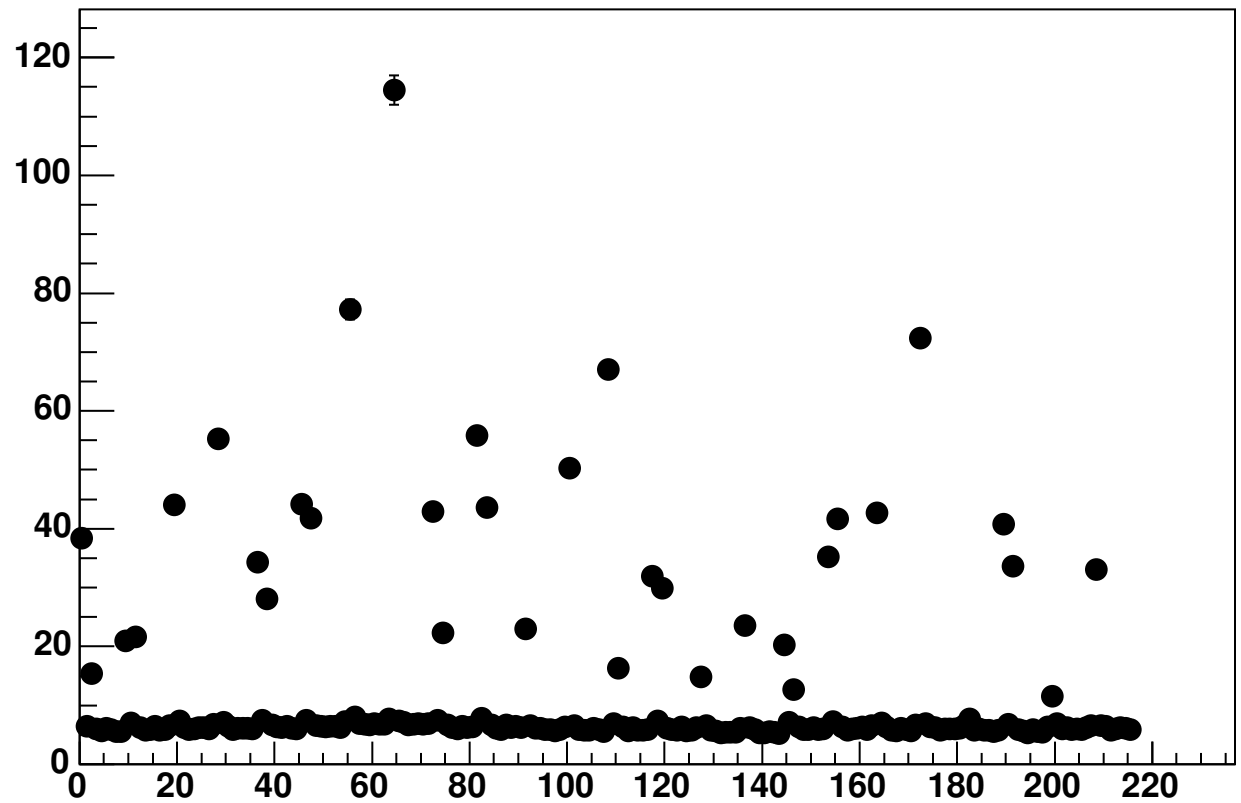
Enable 0, Hold=30, DAC=55296, ADC Noise vs 18*Chip+Chan



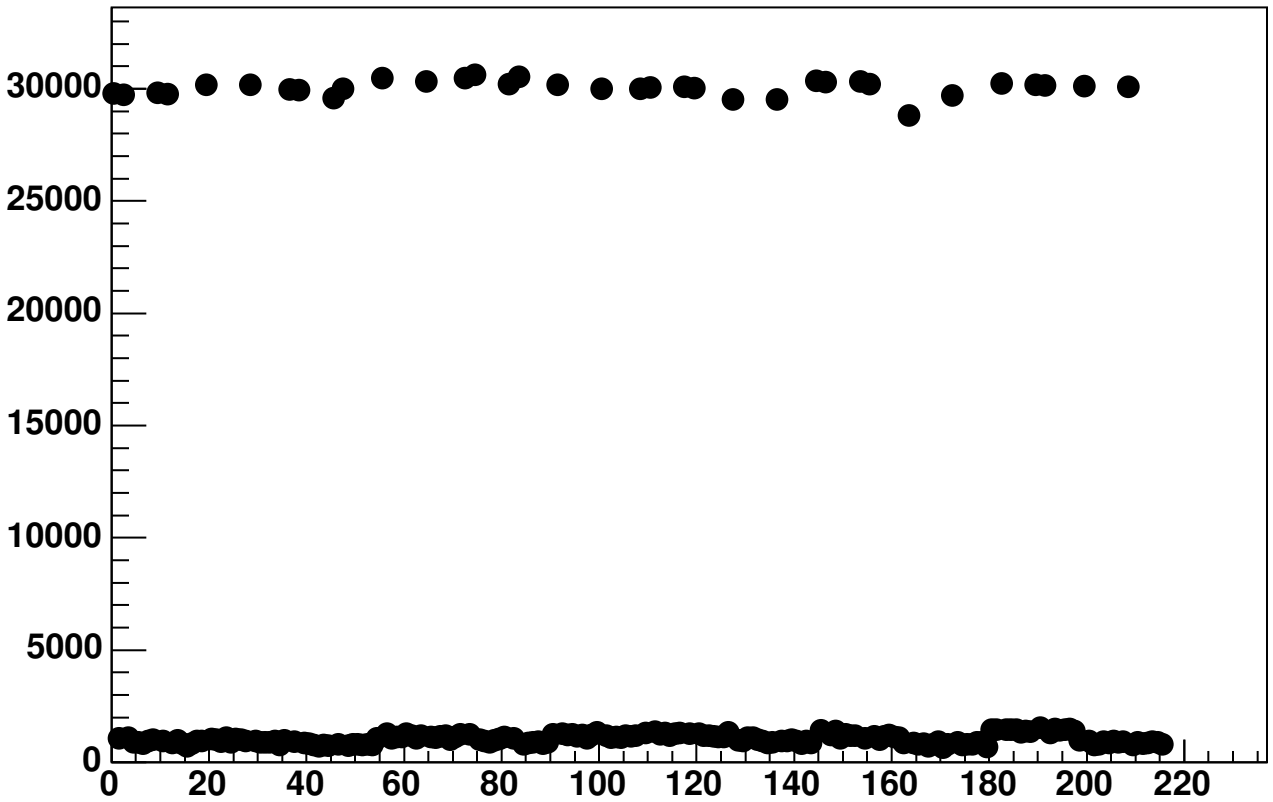
Enable 0, Hold=30, DAC=53248, ADC Mean vs 18*Chip+Chan



Enable 0, Hold=30, DAC=53248, ADC Noise vs 18*Chip+Chan



Enable 0, Hold=30, DAC=51200, ADC Mean vs 18*Chip+Chan



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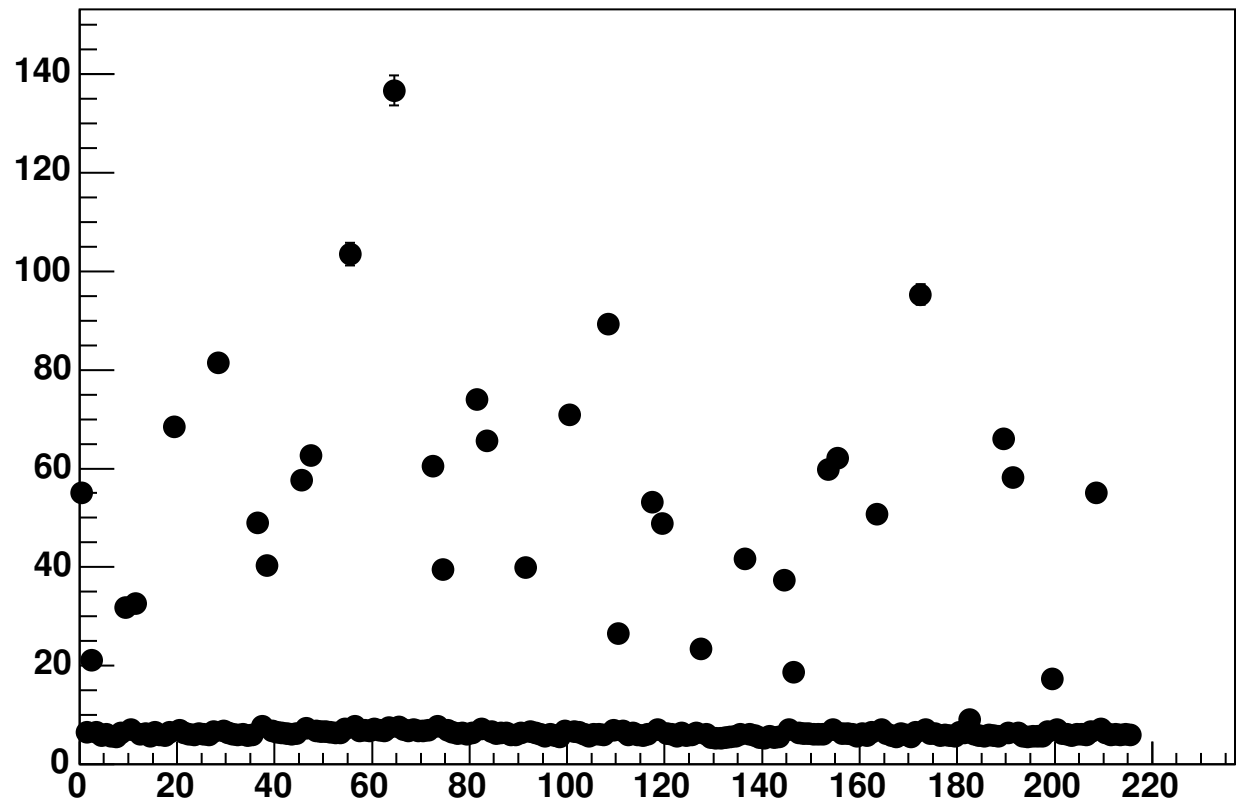
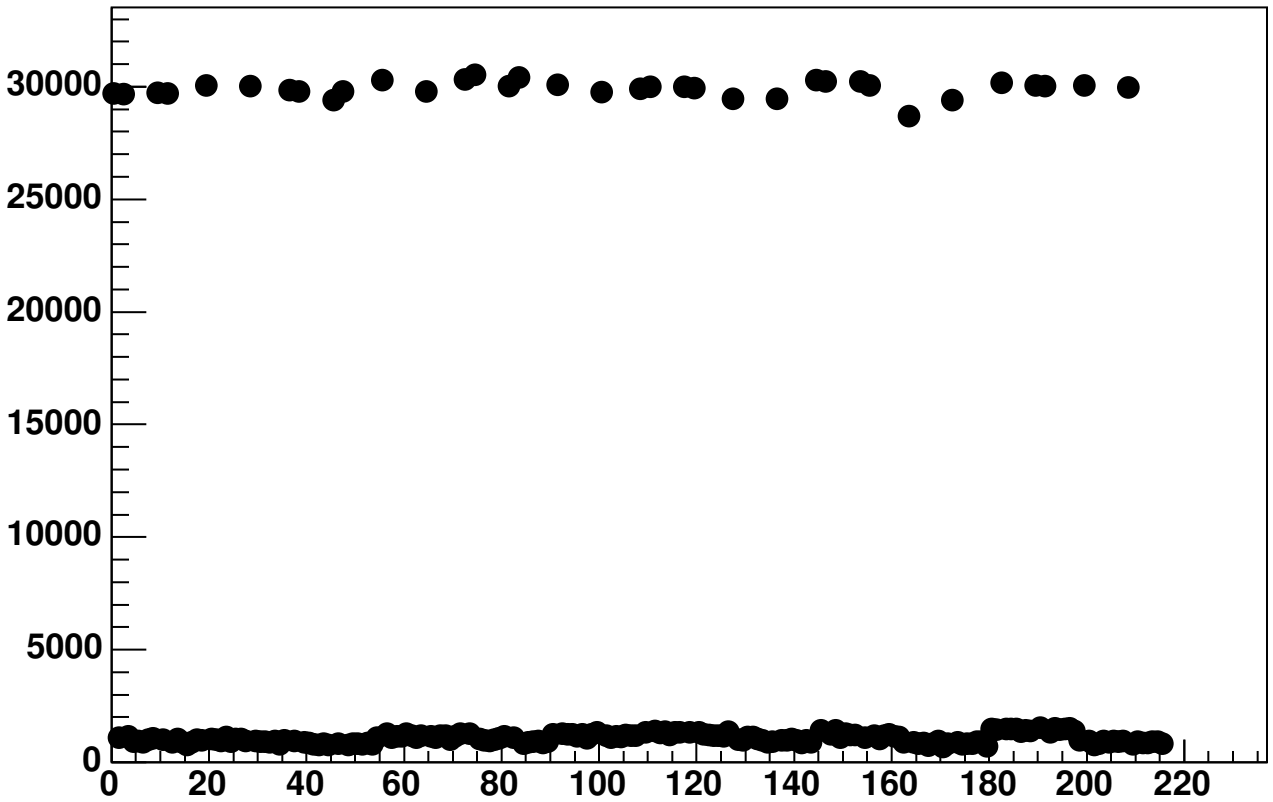
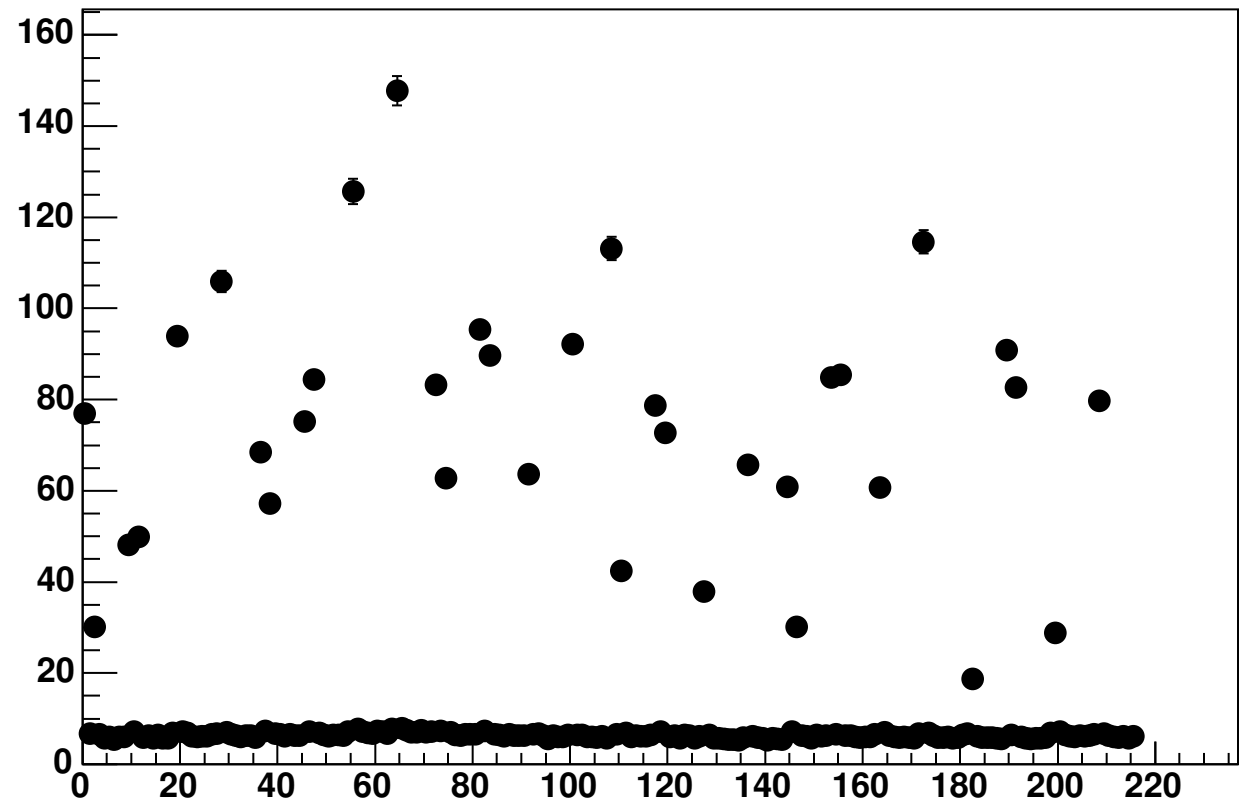


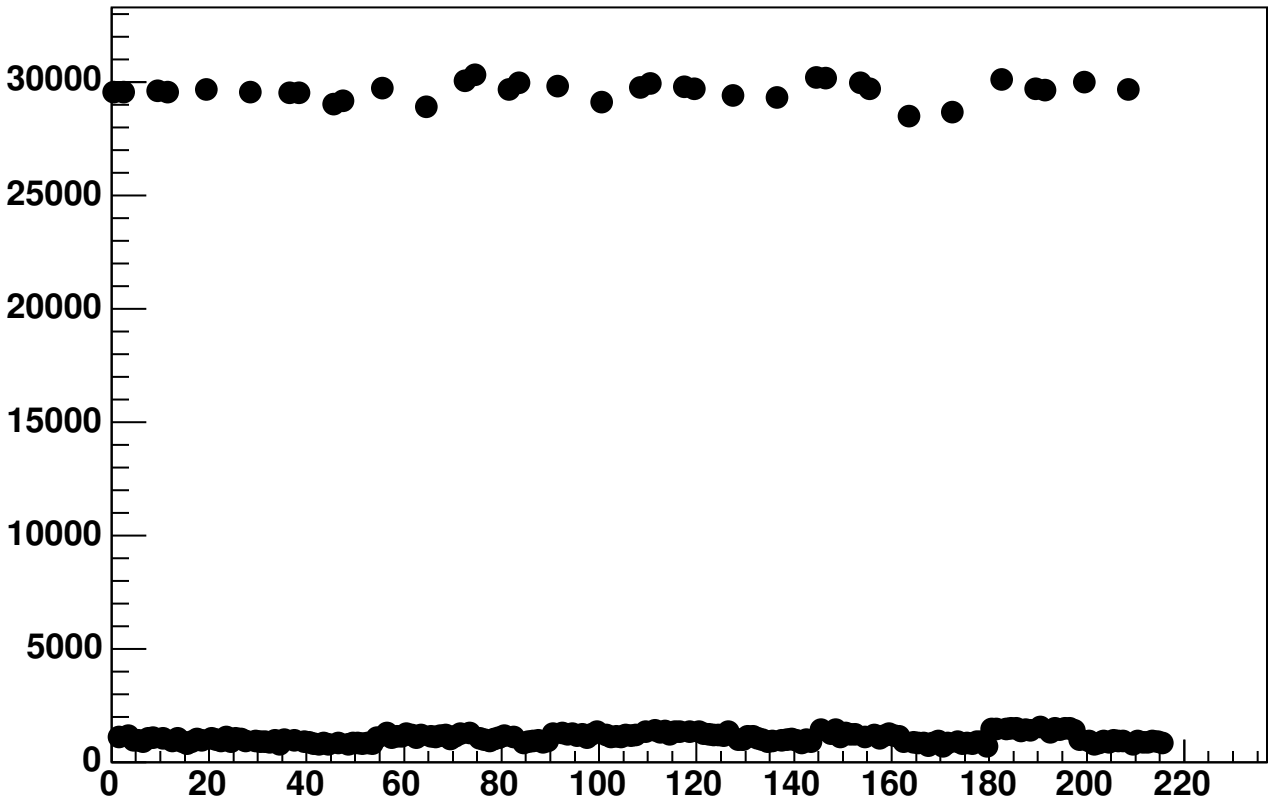
Enable 0, Hold=30, DAC=49152, ADC Mean vs 18*Chip+Chan



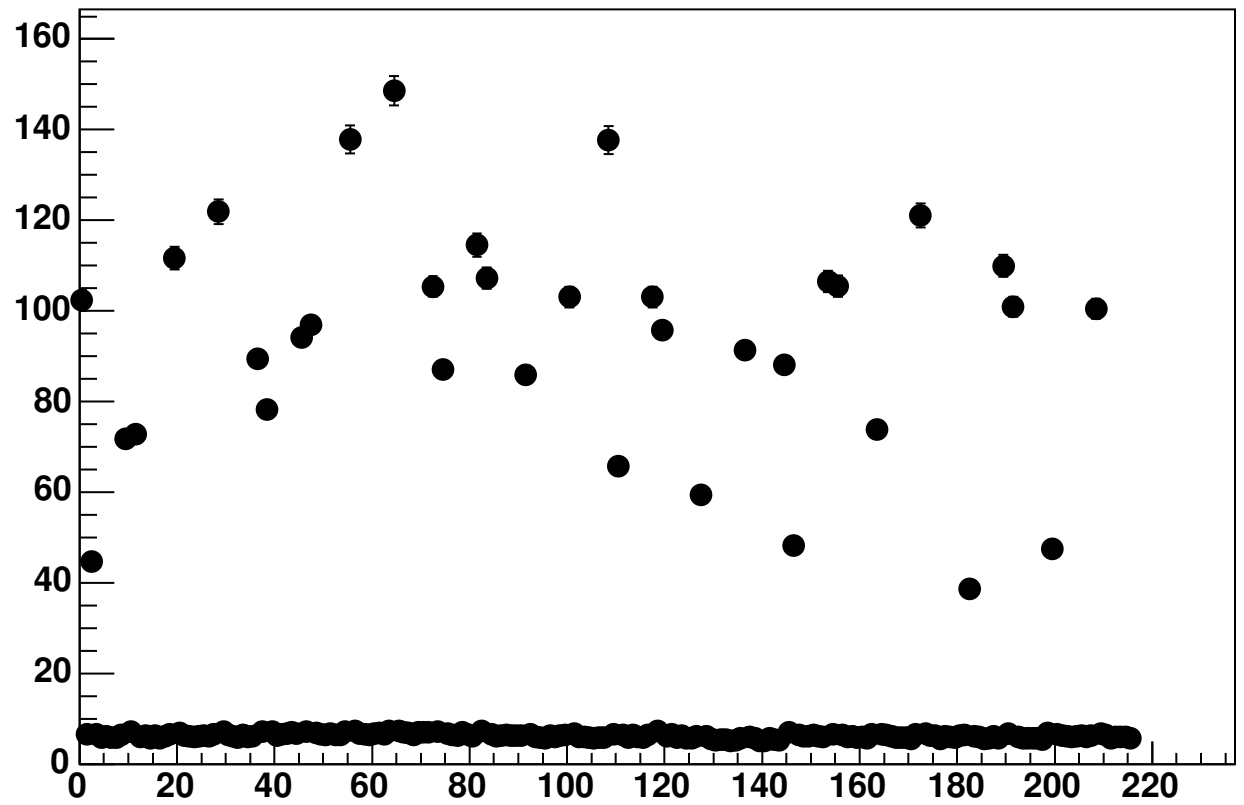
Enable 0, Hold=30, DAC=49152, ADC Noise vs 18*Chip+Chan



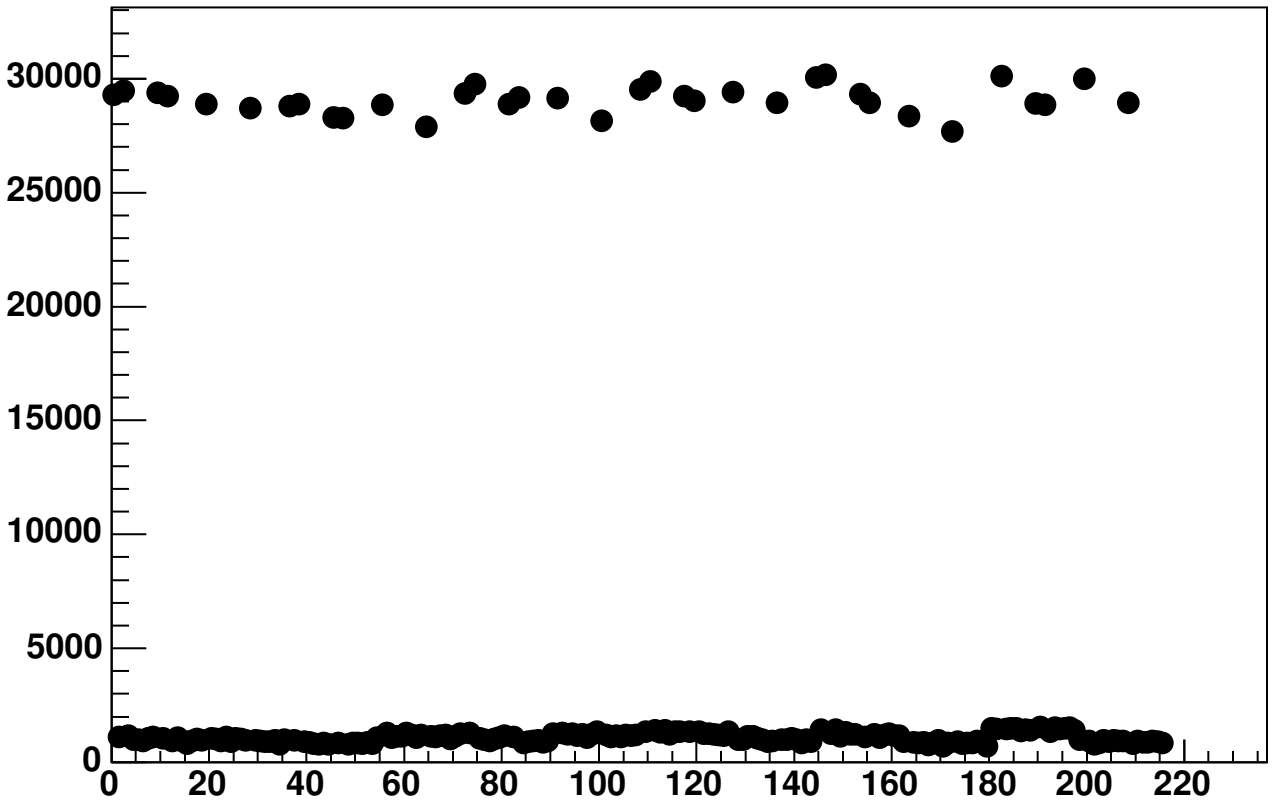
Enable 0, Hold=30, DAC=47104, ADC Mean vs 18*Chip+Chan



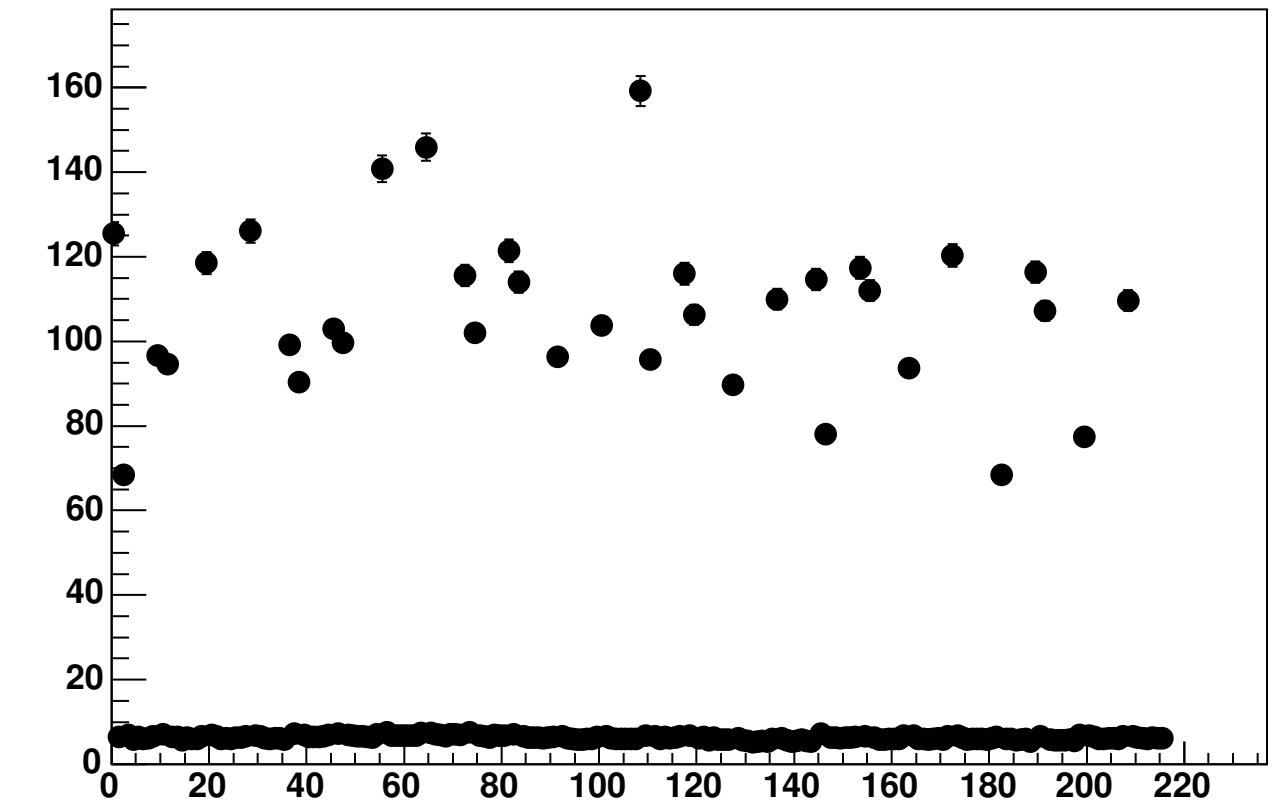
Enable 0, Hold=30, DAC=47104, ADC Noise vs 18*Chip+Chan



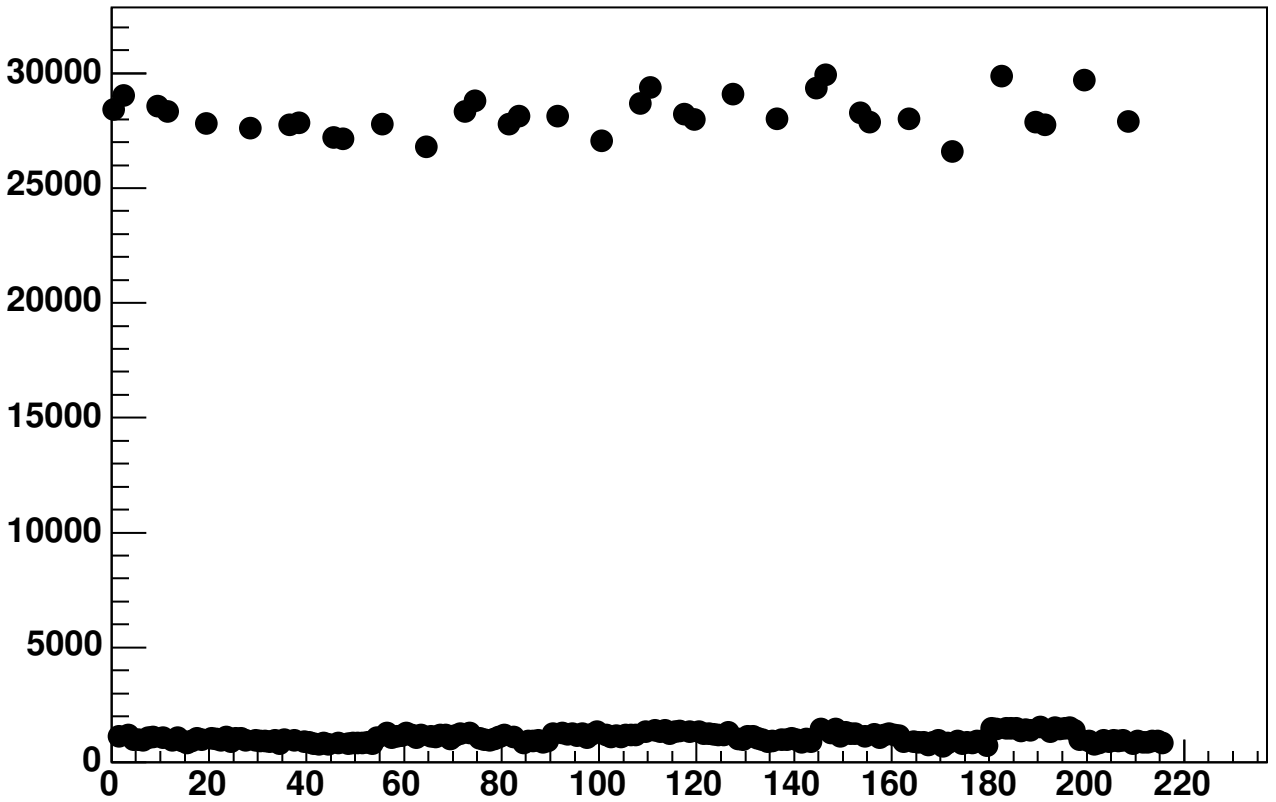
Enable 0, Hold=30, DAC=45056, ADC Mean vs 18*Chip+Chan



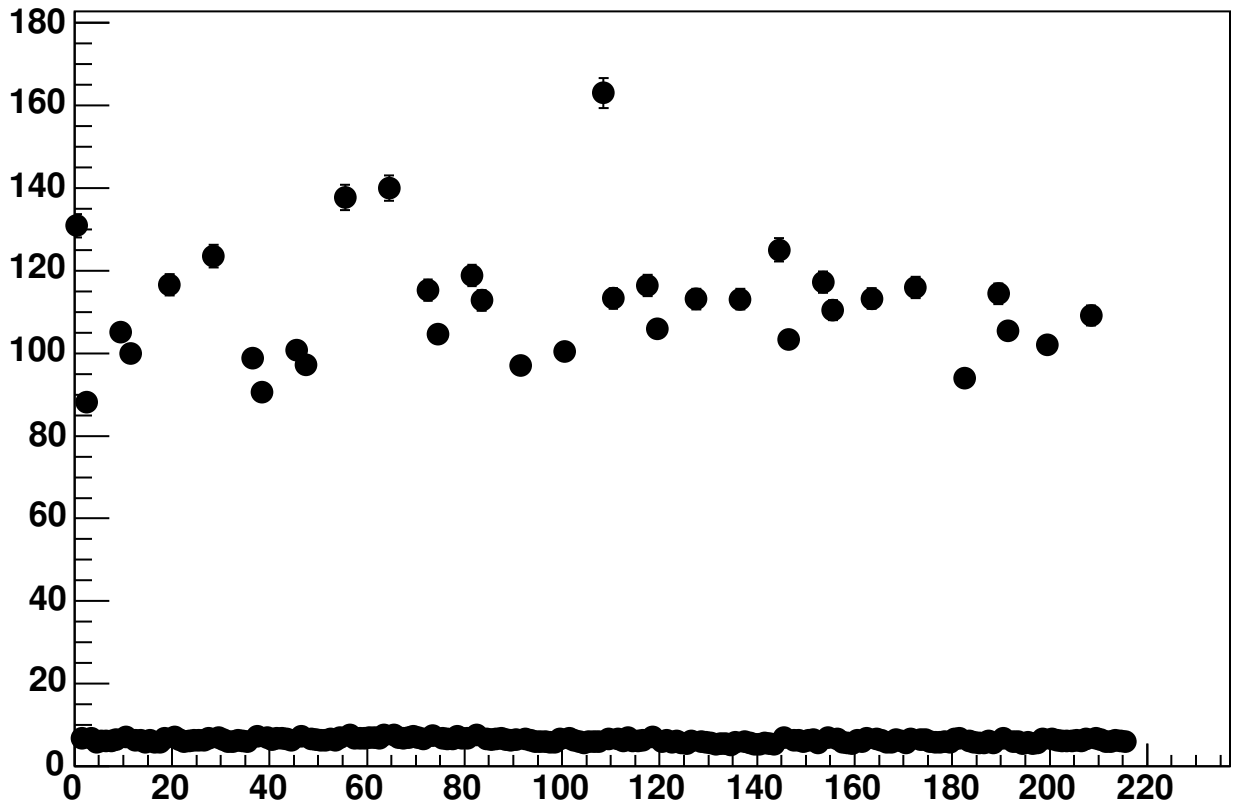
Enable 0, Hold=30, DAC=45056, ADC Noise vs 18*Chip+Chan



Enable 0, Hold=30, DAC=43008, ADC Mean vs 18*Chip+Chan



Enable 0, Hold=30, DAC=43008, ADC Noise vs 18*Chip+Chan



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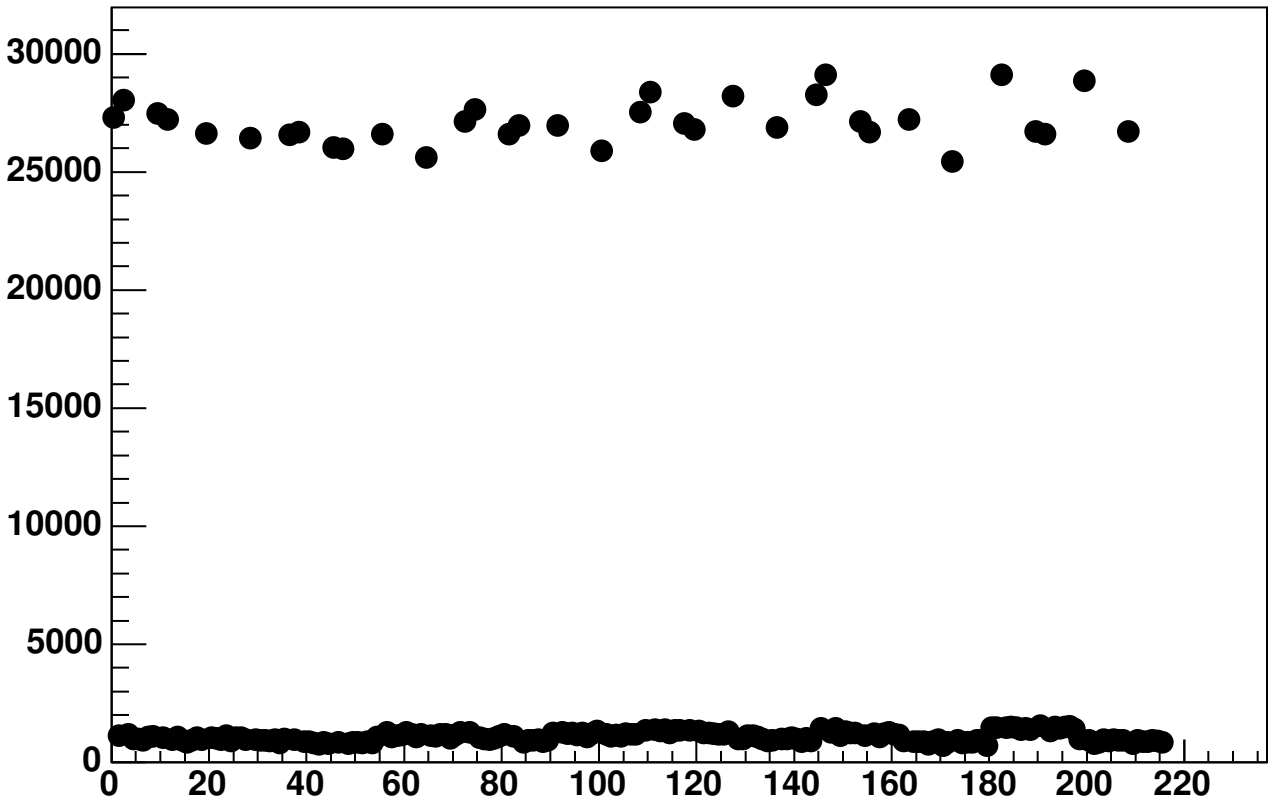
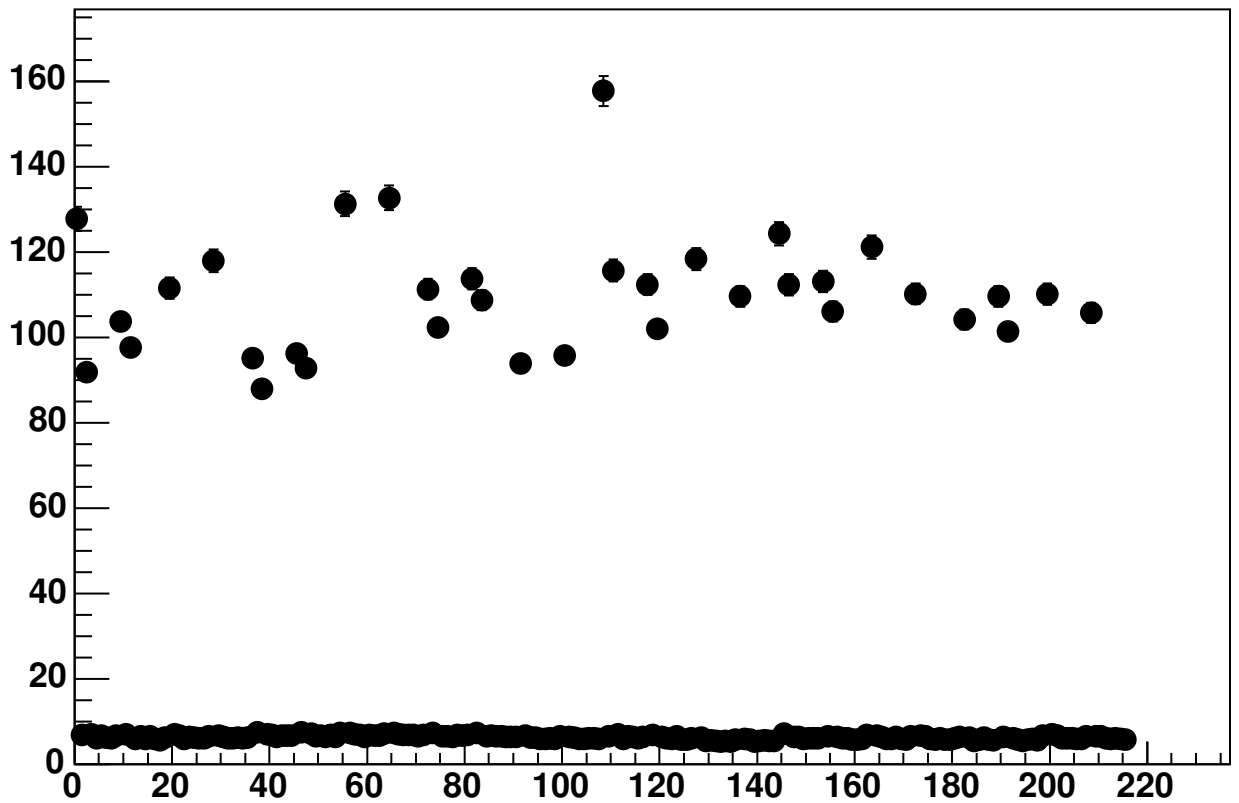
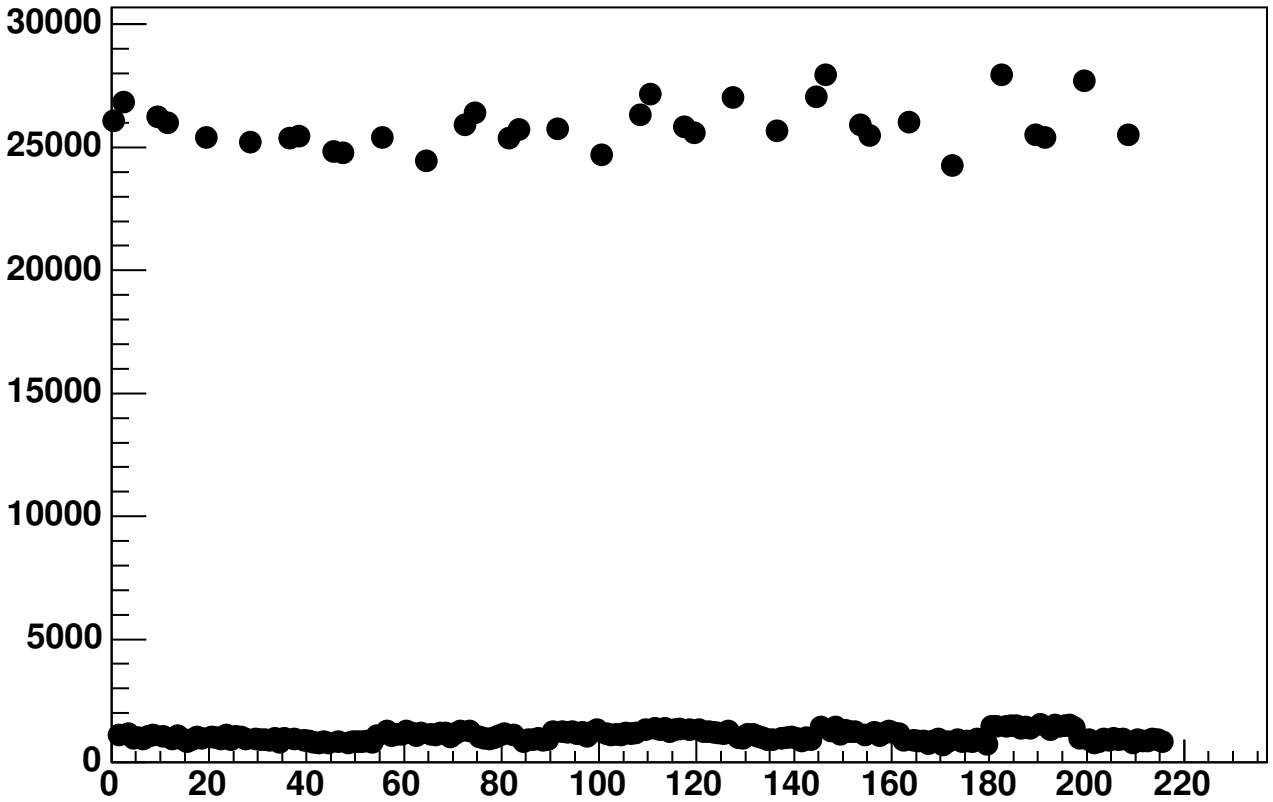


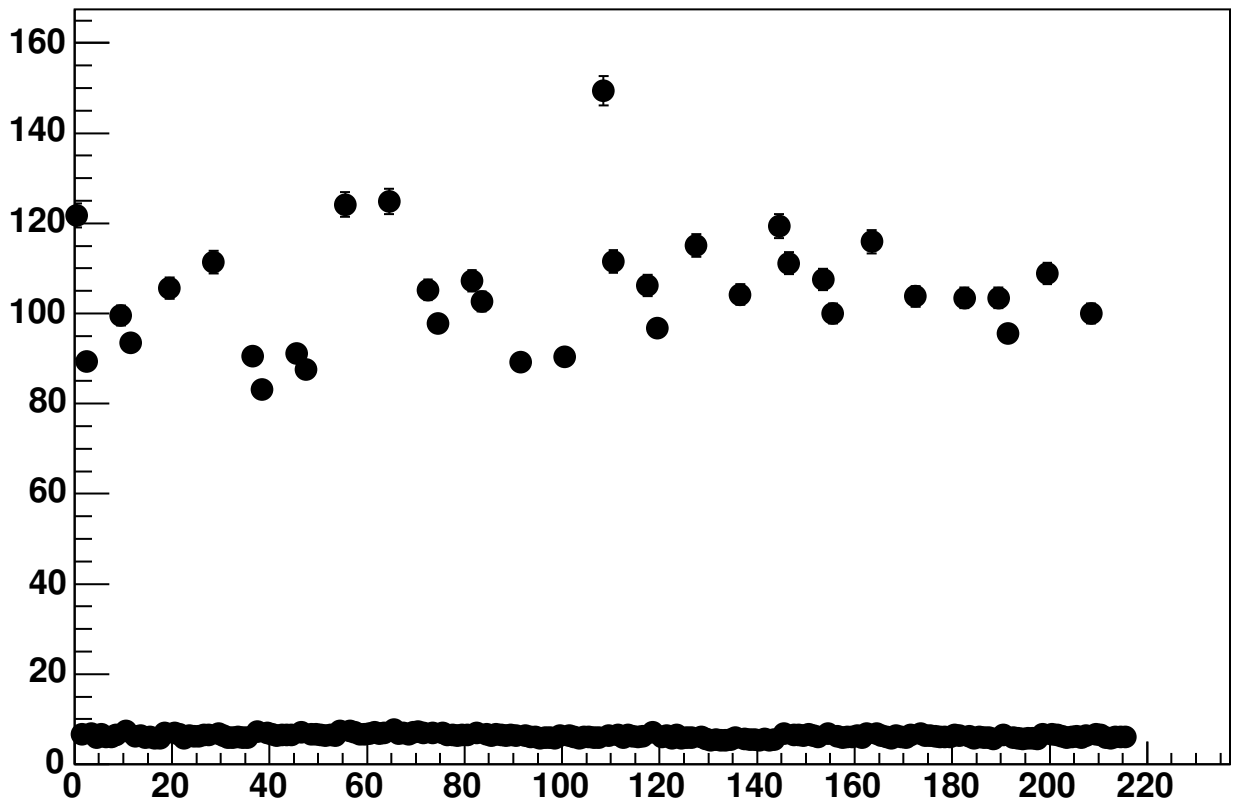
Enable 0, Hold=30, DAC=40960, ADC Noise vs 18*Chip+Chan



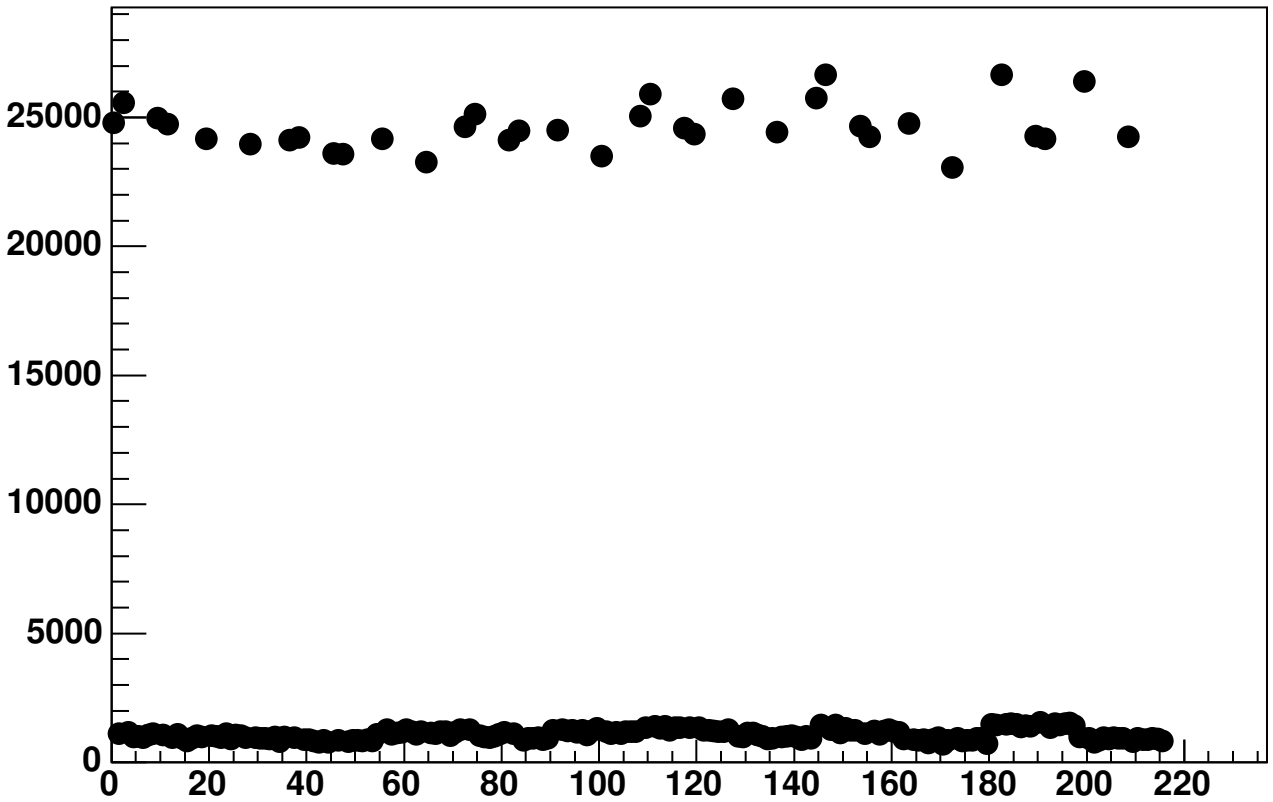
Enable 0, Hold=30, DAC=38912, ADC Mean vs 18*Chip+Chan



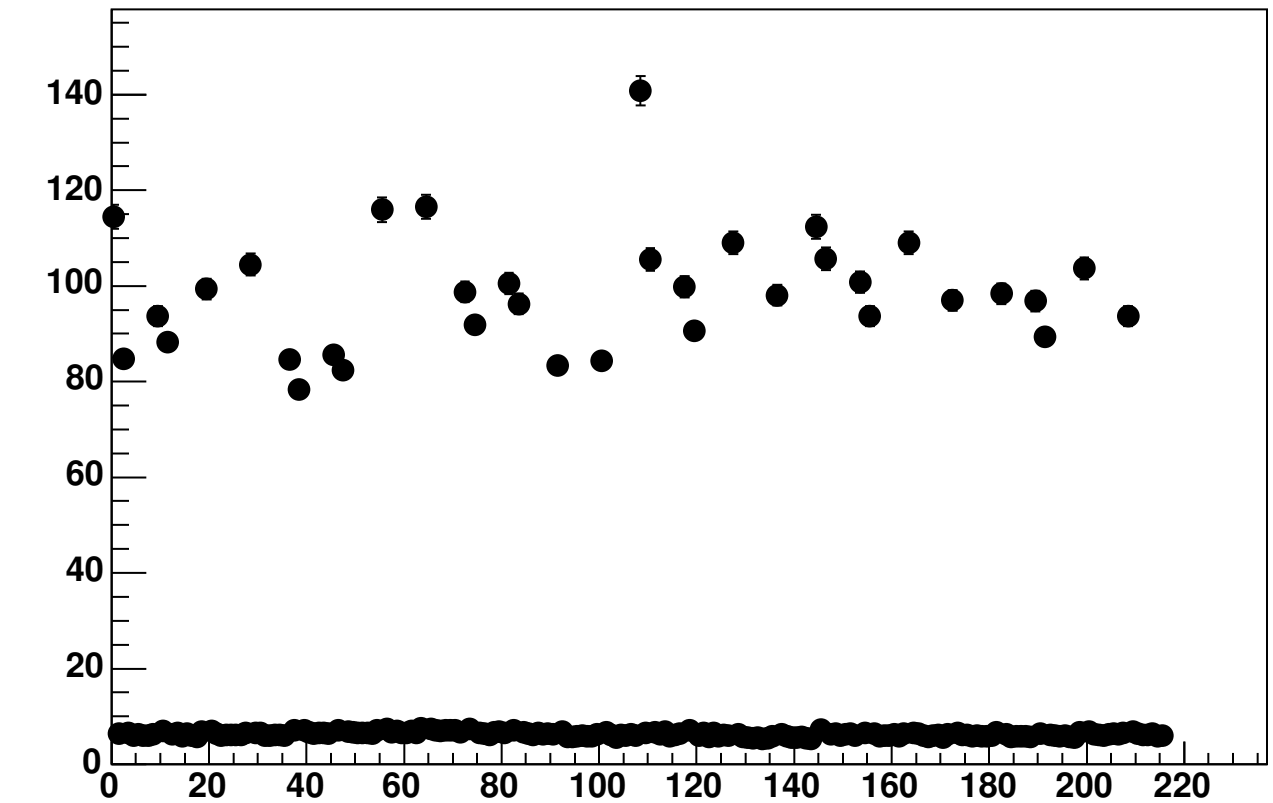
Enable 0, Hold=30, DAC=38912, ADC Noise vs 18*Chip+Chan



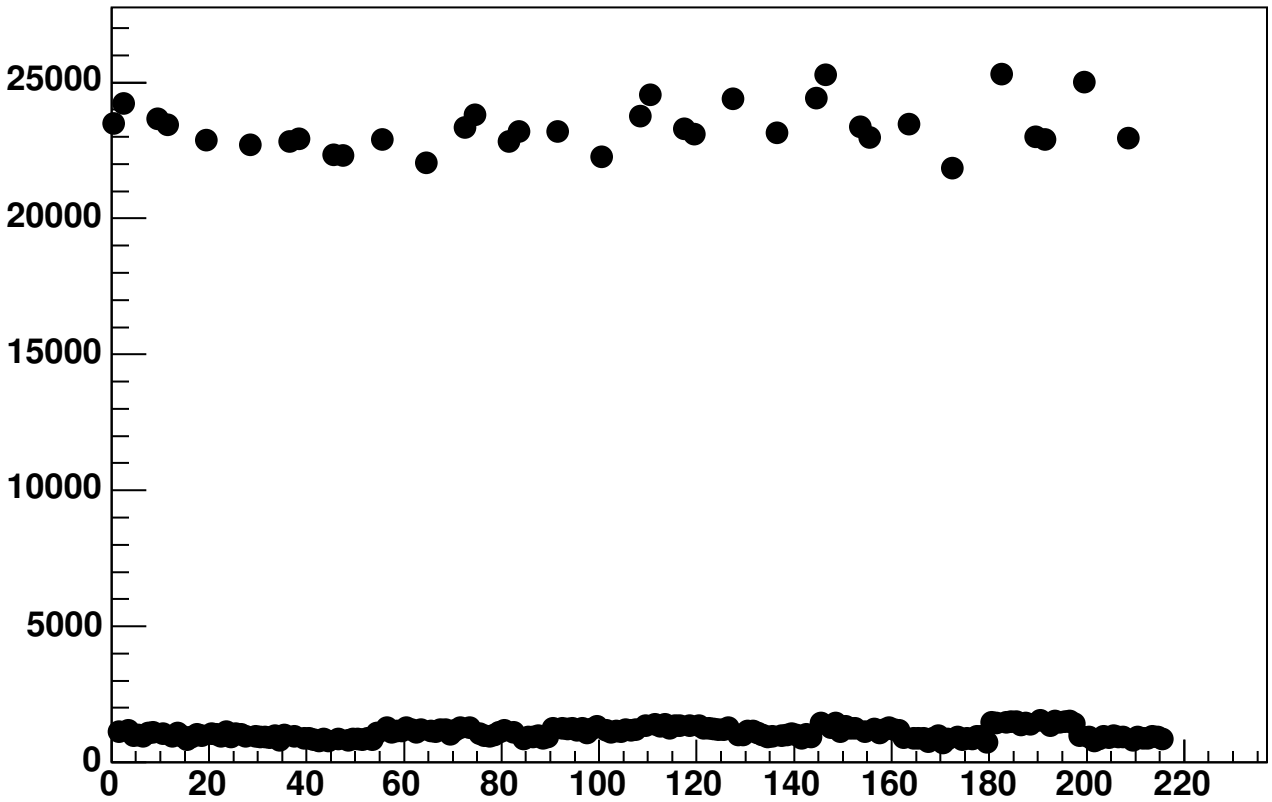
Enable 0, Hold=30, DAC=36864, ADC Mean vs 18*Chip+Chan



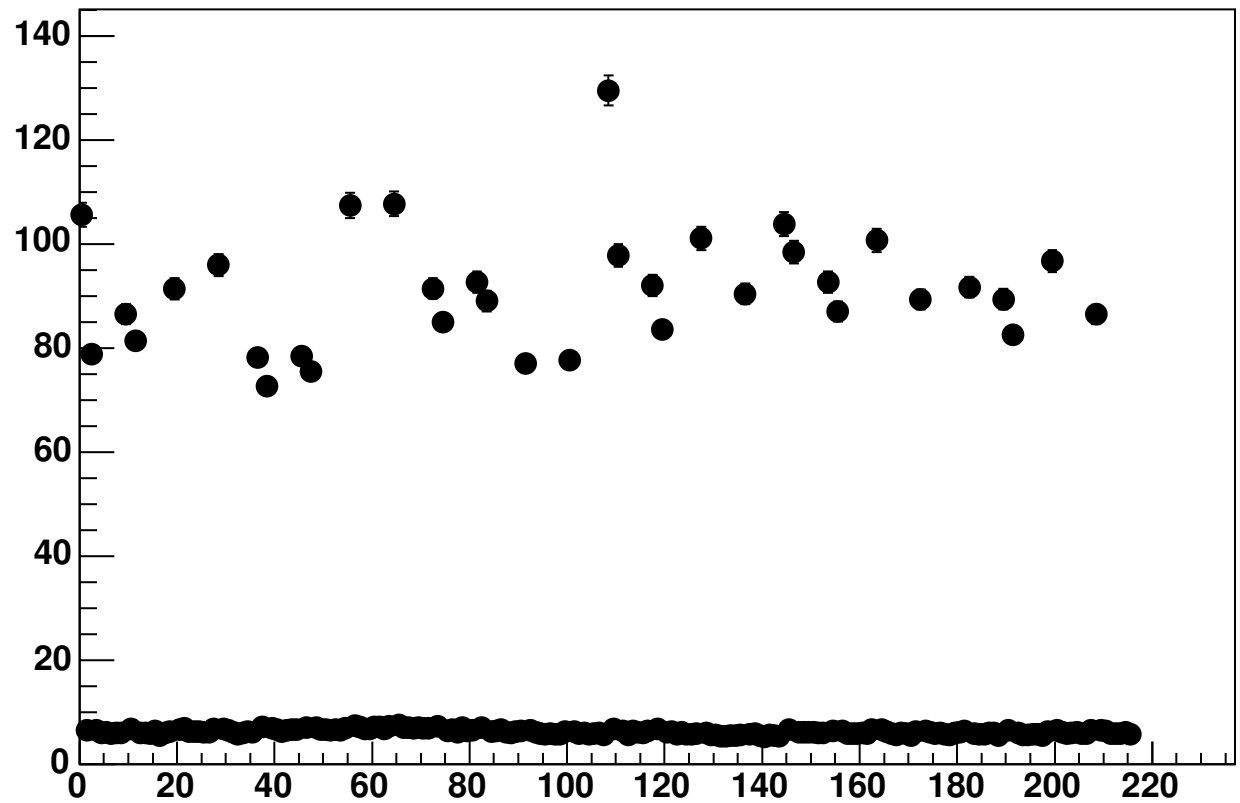
Enable 0, Hold=30, DAC=36864, ADC Noise vs 18*Chip+Chan



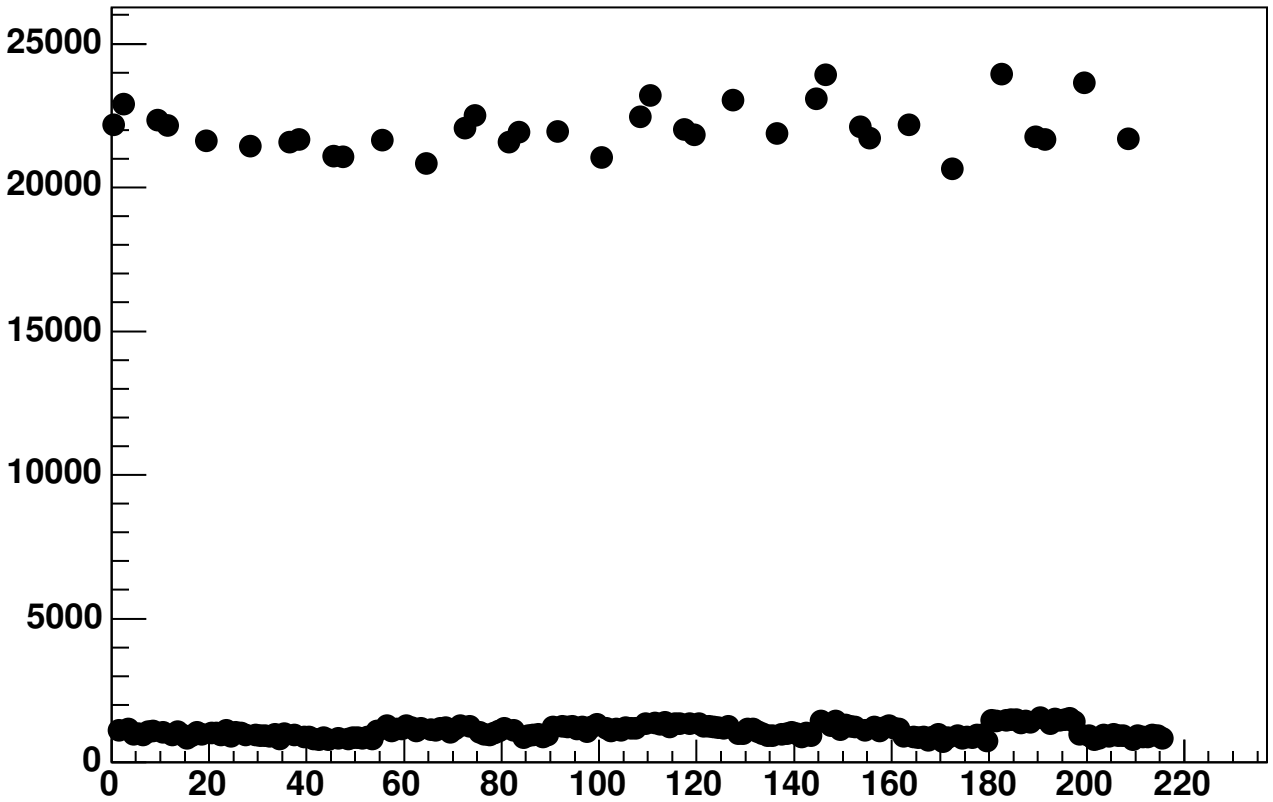
Enable 0, Hold=30, DAC=34816, ADC Mean vs 18*Chip+Chan



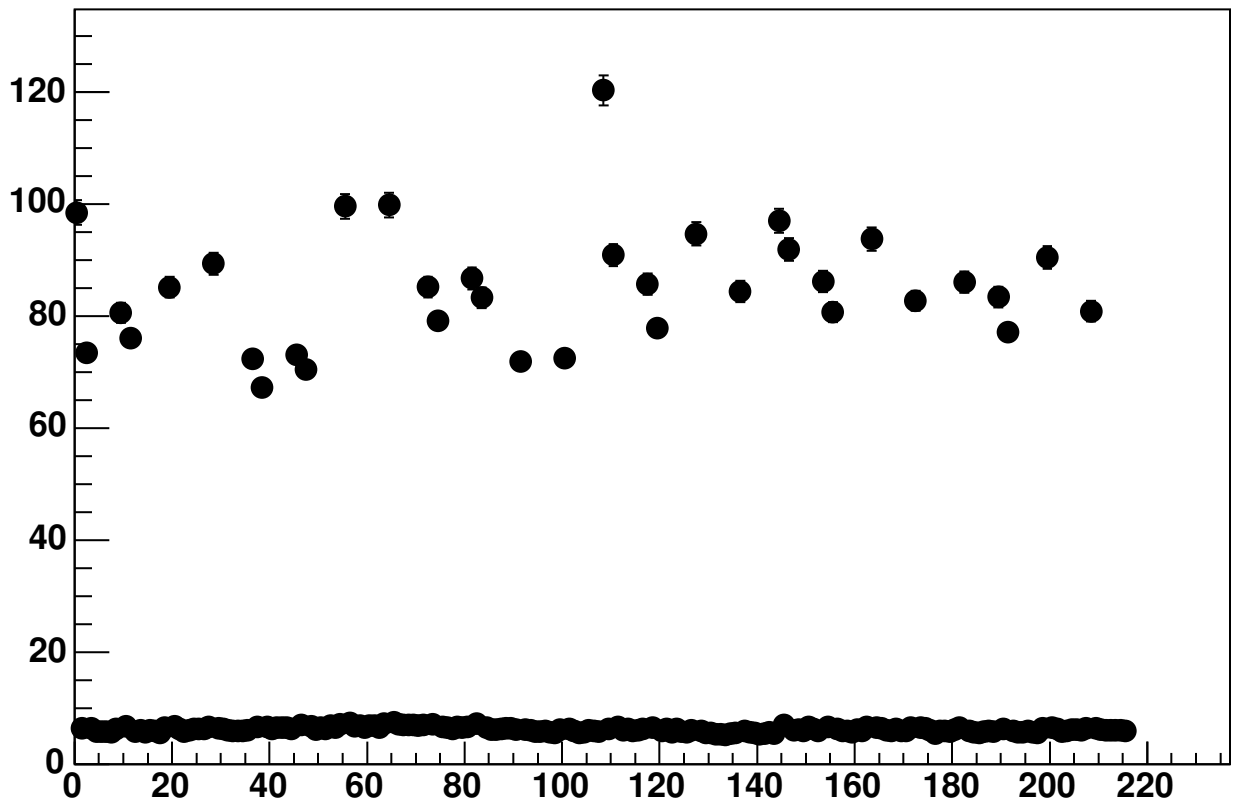
Enable 0, Hold=30, DAC=34816, ADC Noise vs 18*Chip+Chan



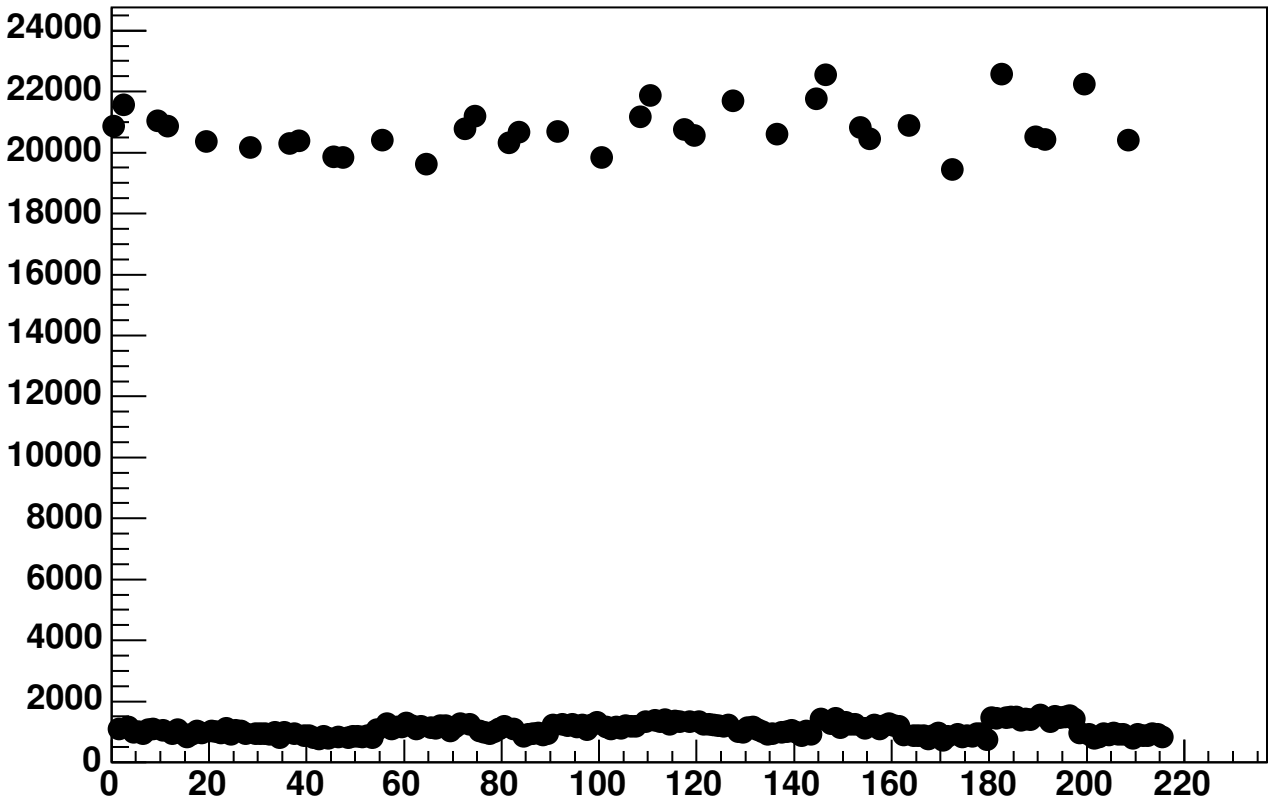
Enable 0, Hold=30, DAC=32768, ADC Mean vs 18*Chip+Chan



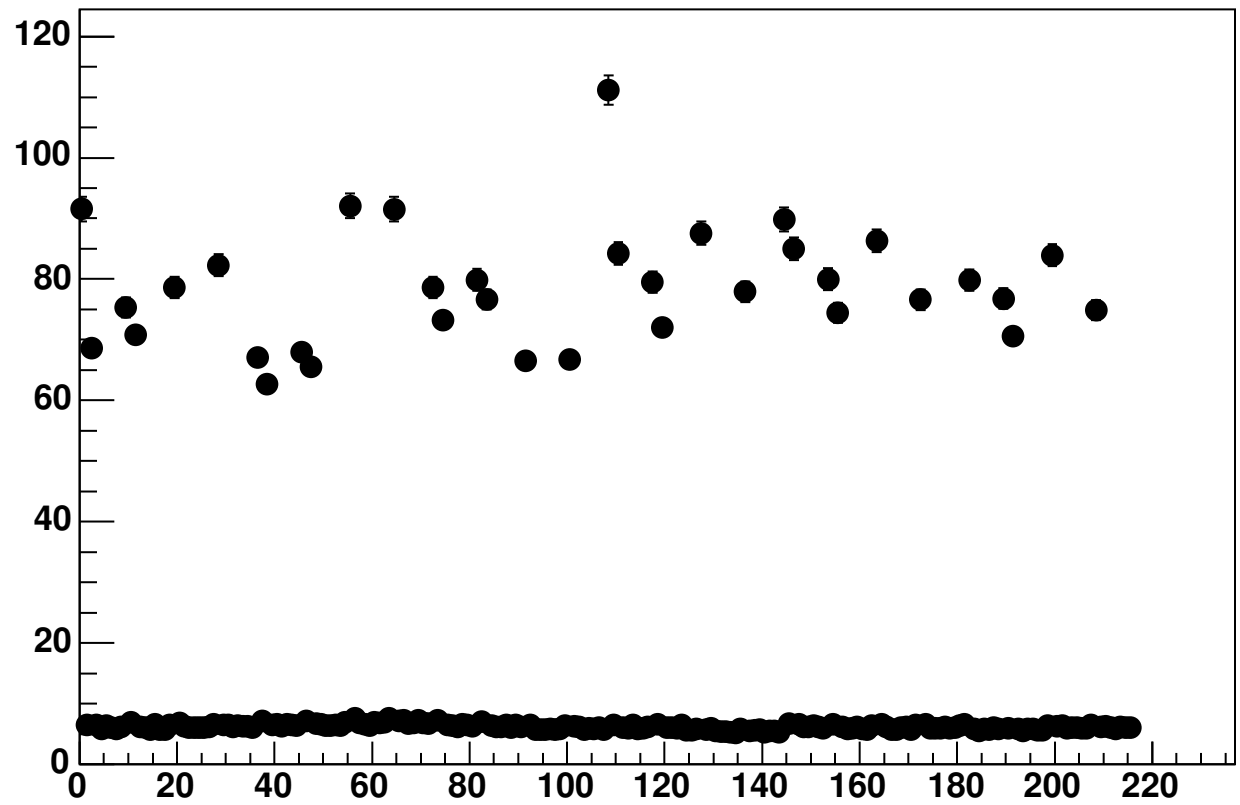
Enable 0, Hold=30, DAC=32768, ADC Noise vs 18*Chip+Chan



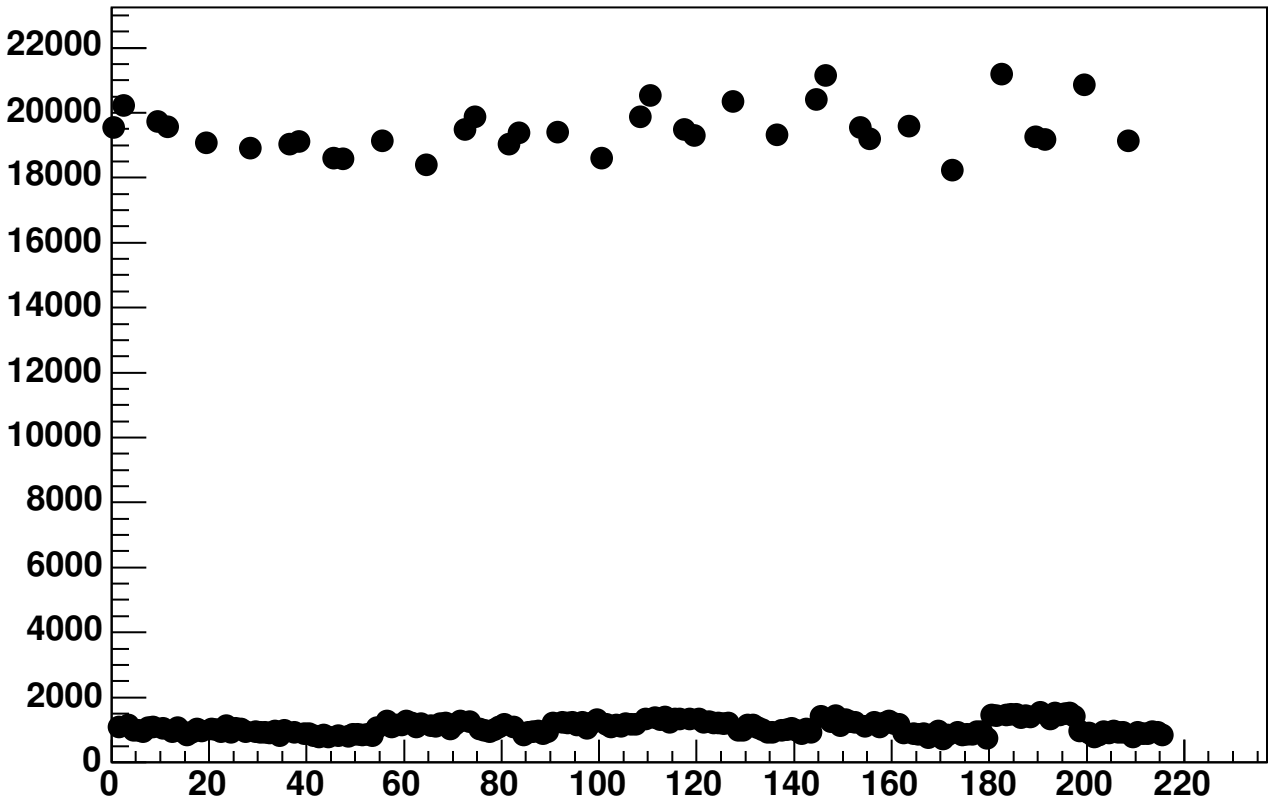
Enable 0, Hold=30, DAC=30720, ADC Mean vs 18*Chip+Chan



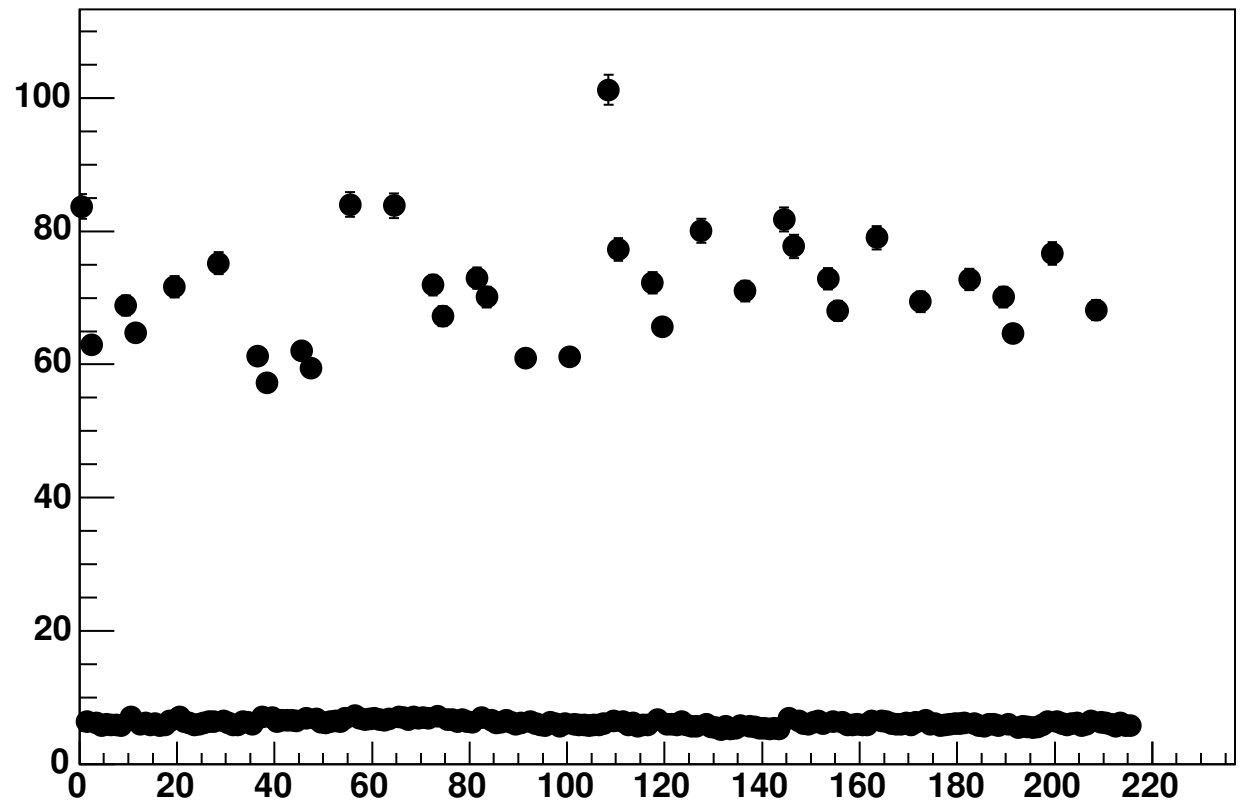
Enable 0, Hold=30, DAC=30720, ADC Noise vs 18*Chip+Chan



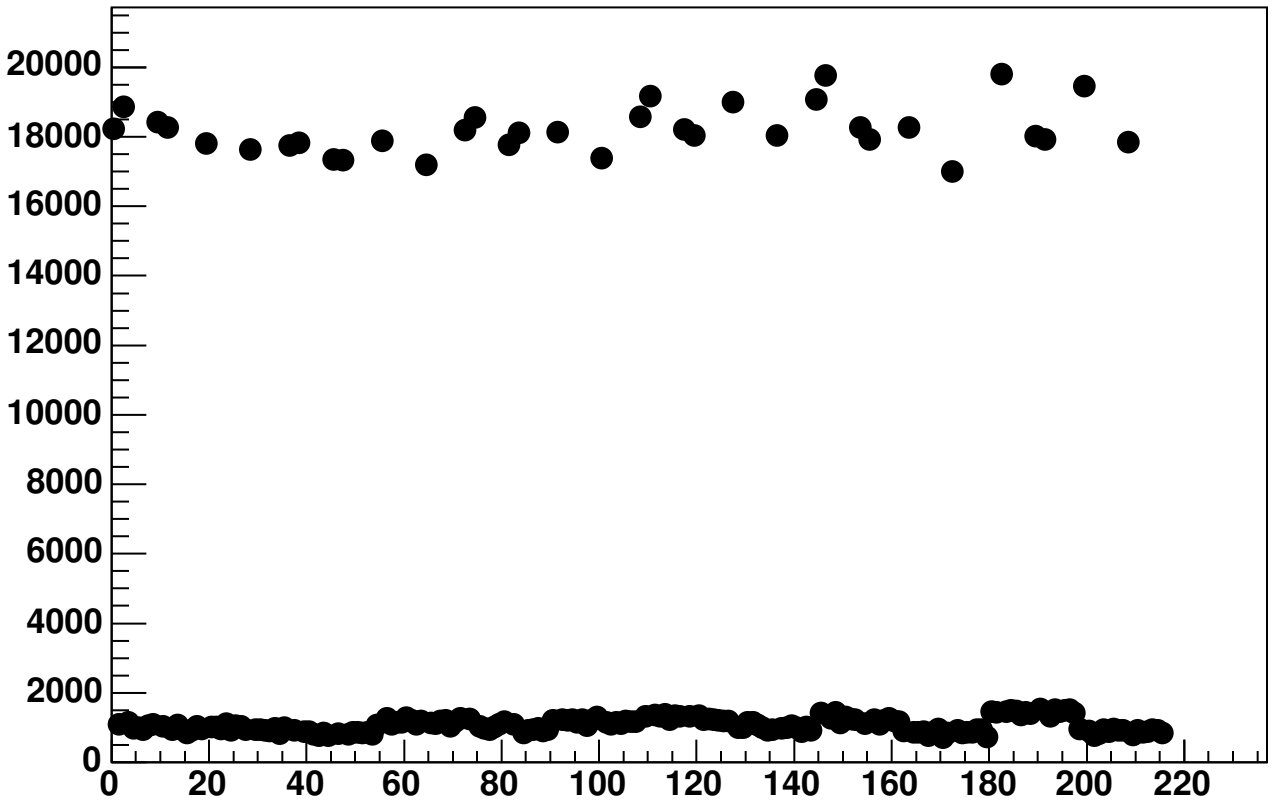
Enable 0, Hold=30, DAC=28672, ADC Mean vs 18*Chip+Chan



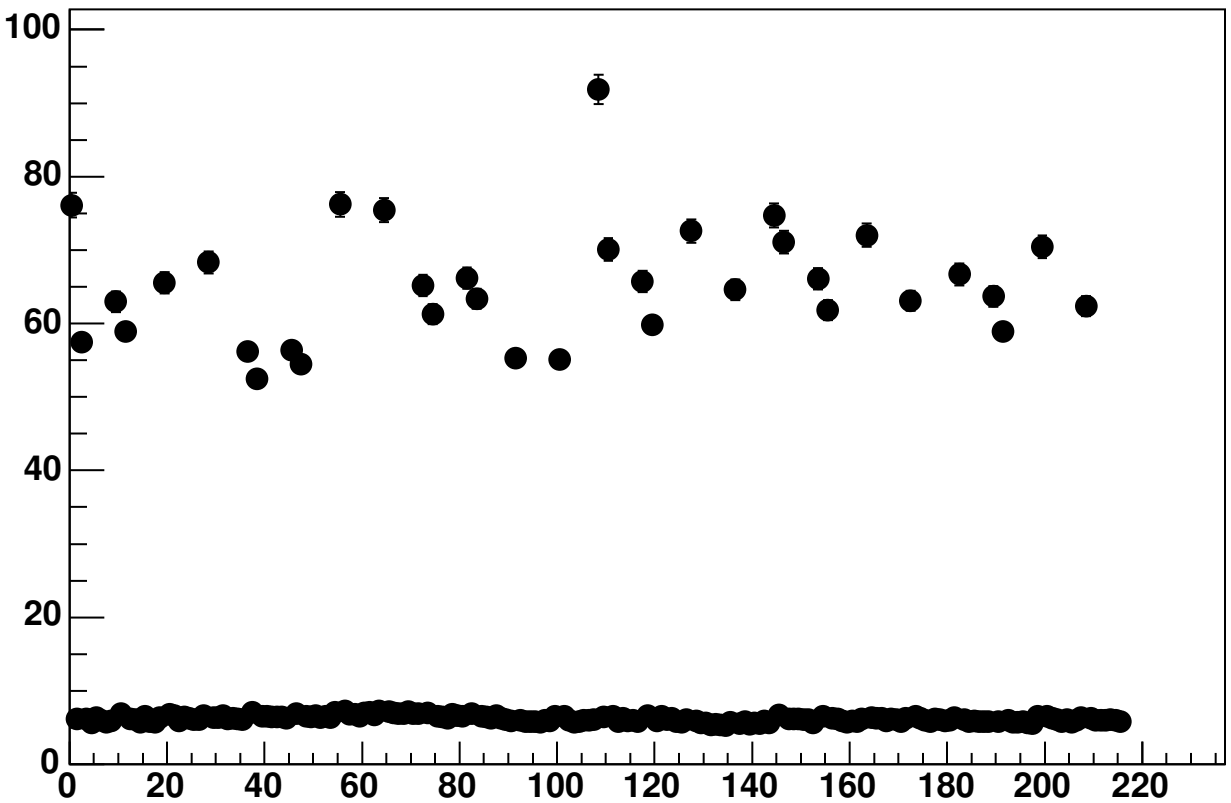
Enable 0, Hold=30, DAC=28672, ADC Noise vs 18*Chip+Chan



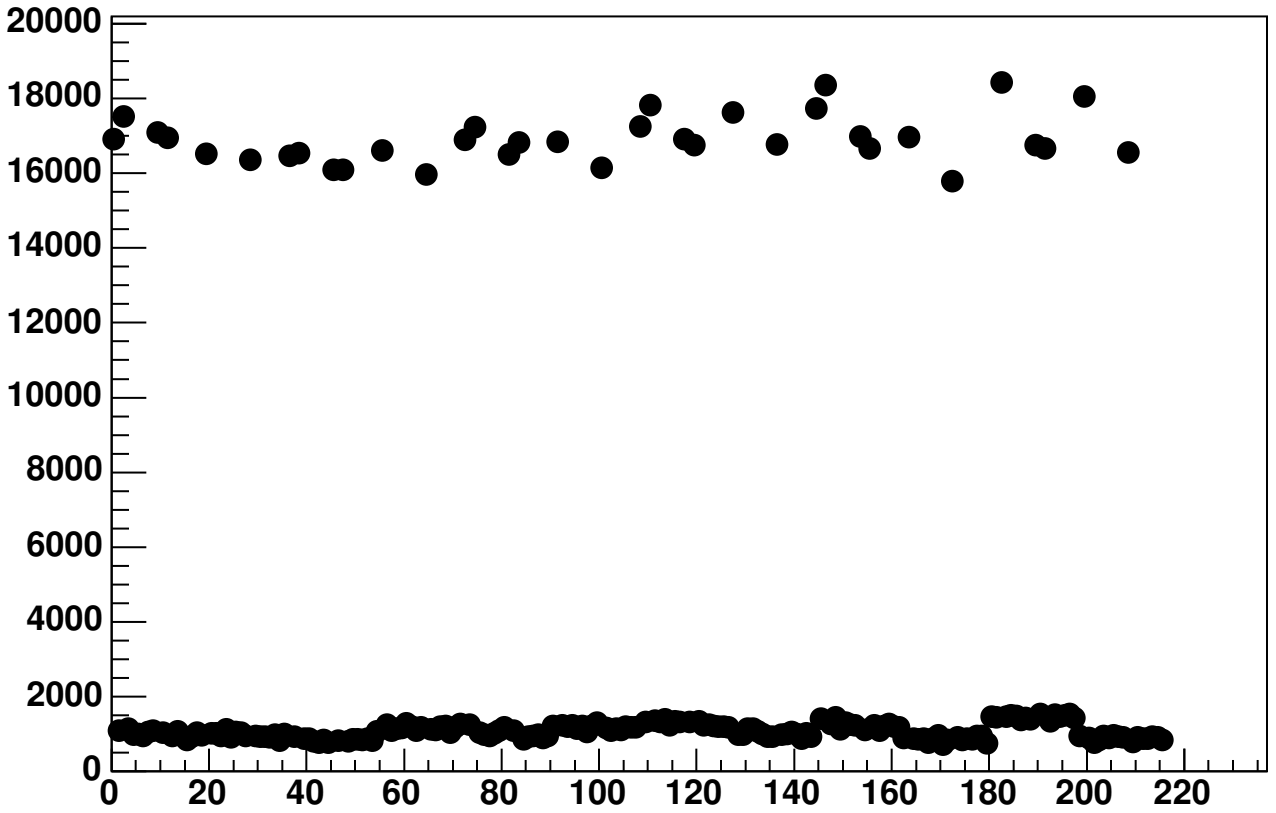
Enable 0, Hold=30, DAC=26624, ADC Mean vs 18*Chip+Chan



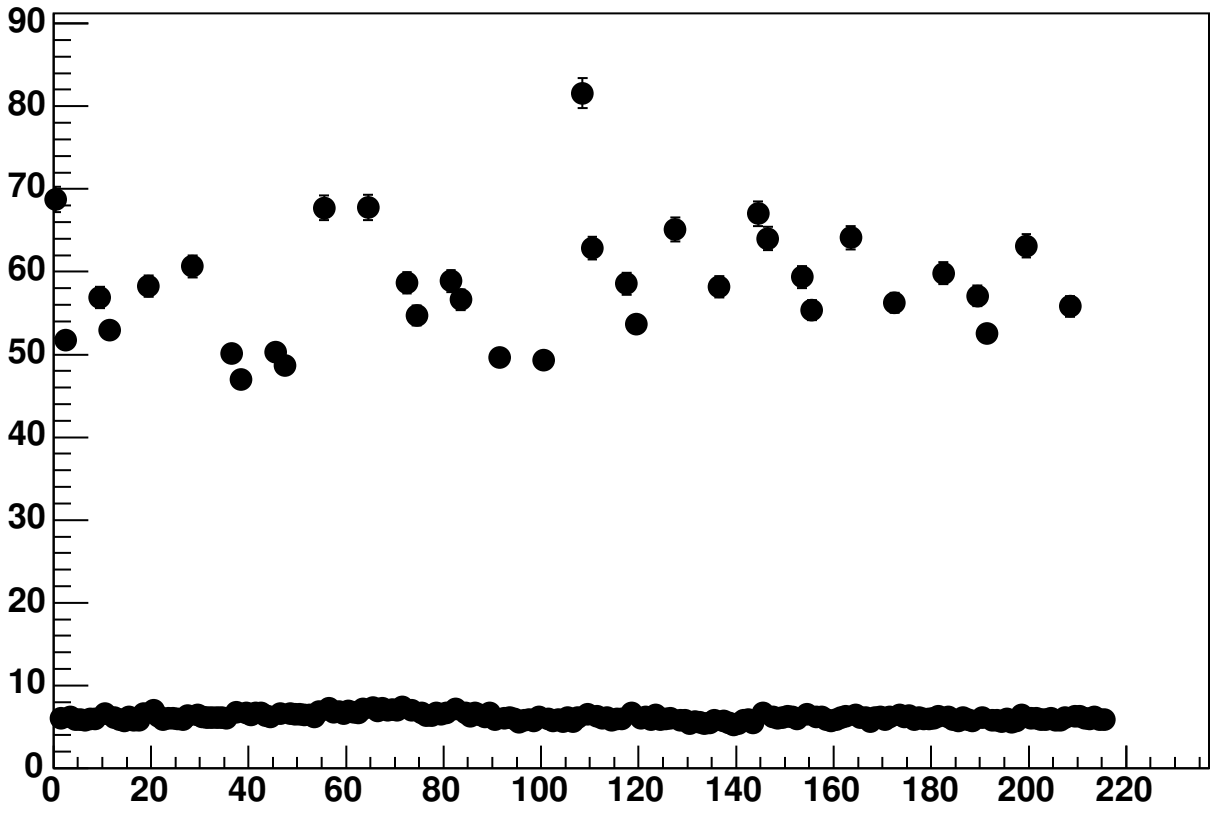
Enable 0, Hold=30, DAC=26624, ADC Noise vs 18*Chip+Chan



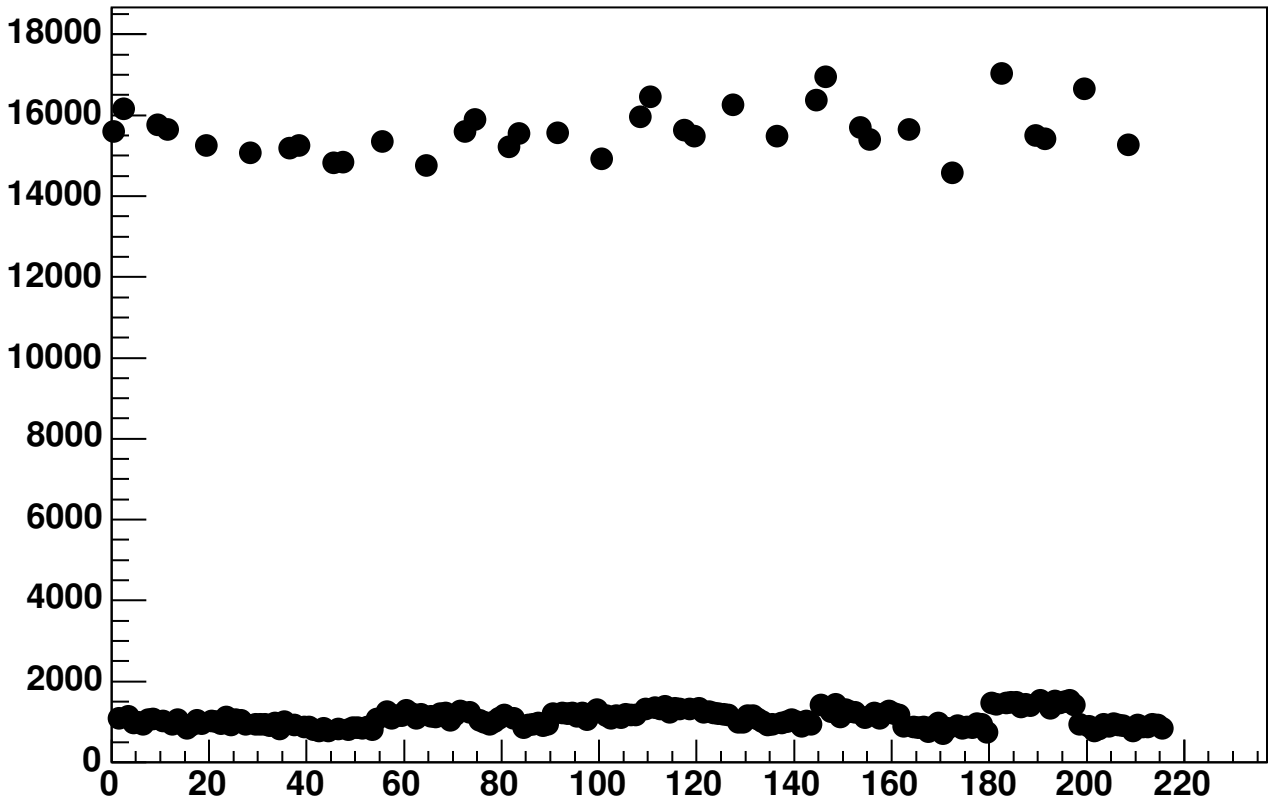
Enable 0, Hold=30, DAC=24576, ADC Mean vs 18*Chip+Chan



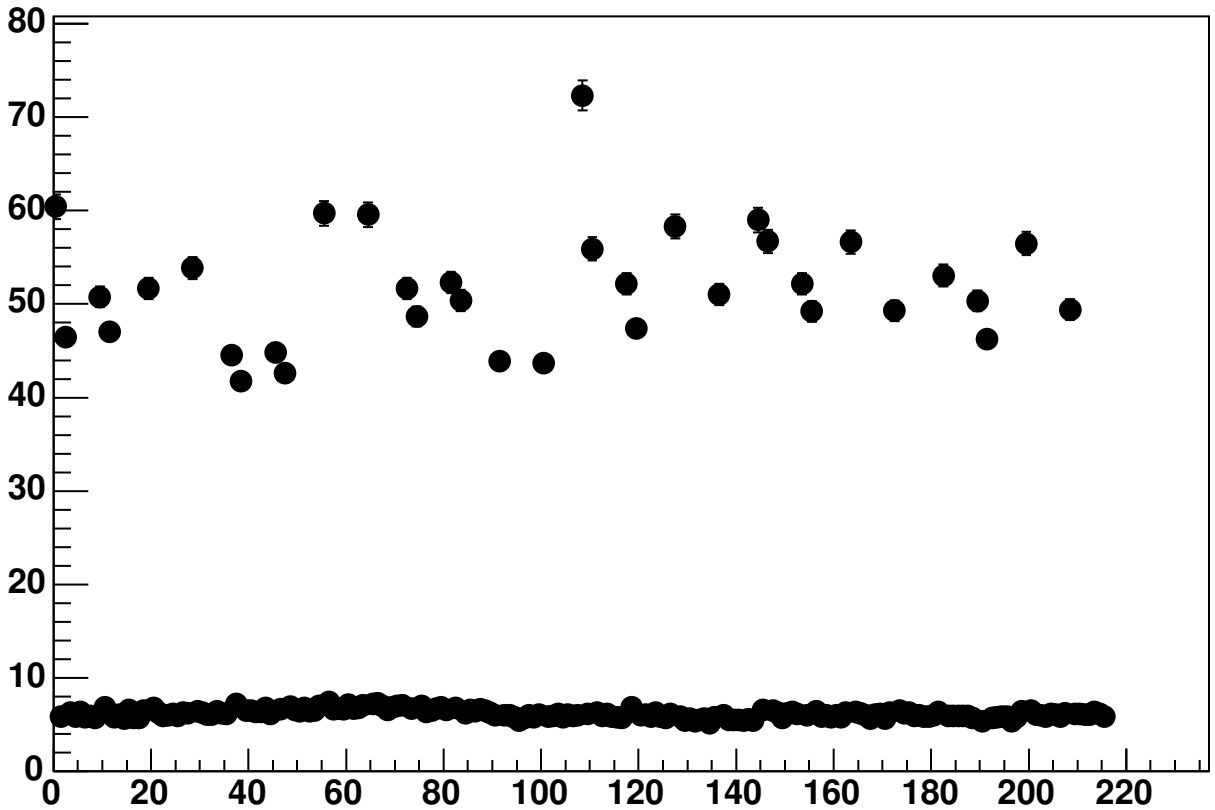
Enable 0, Hold=30, DAC=24576, ADC Noise vs 18*Chip+Chan



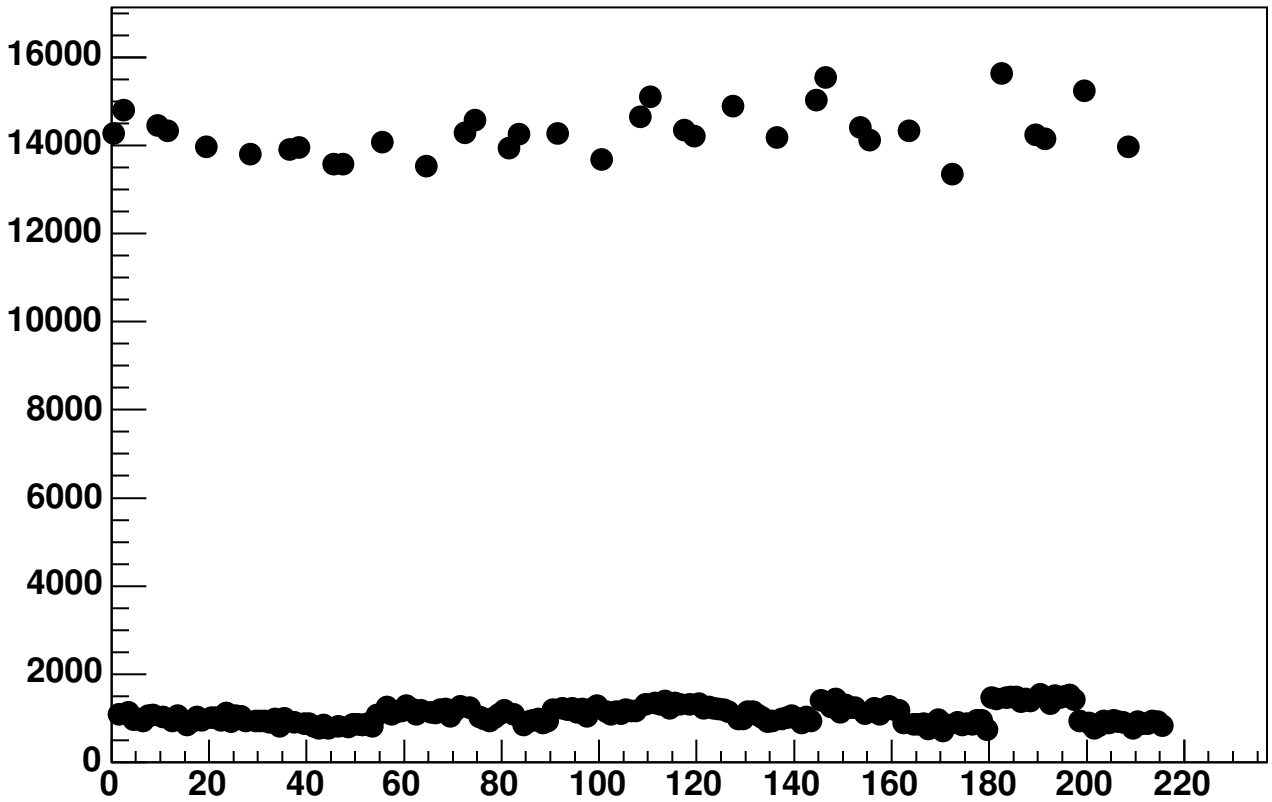
Enable 0, Hold=30, DAC=22528, ADC Mean vs 18*Chip+Chan



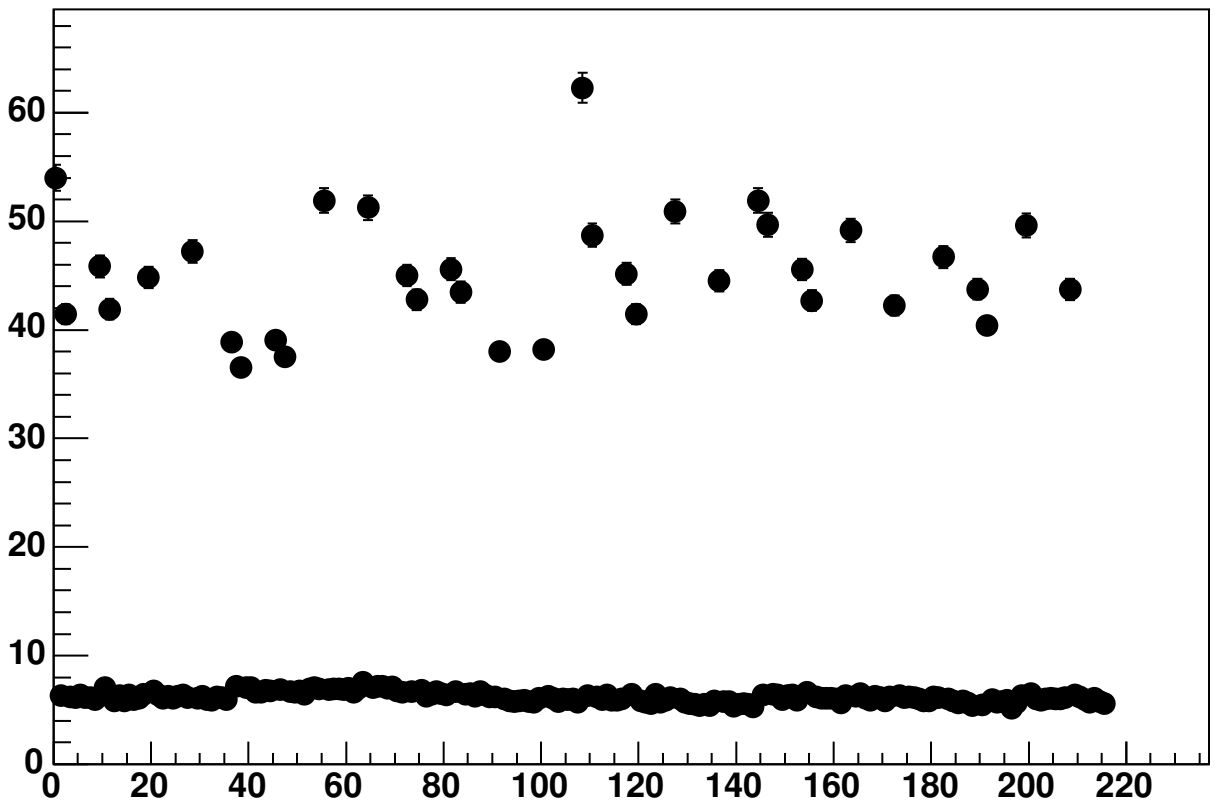
Enable 0, Hold=30, DAC=22528, ADC Noise vs 18*Chip+Chan



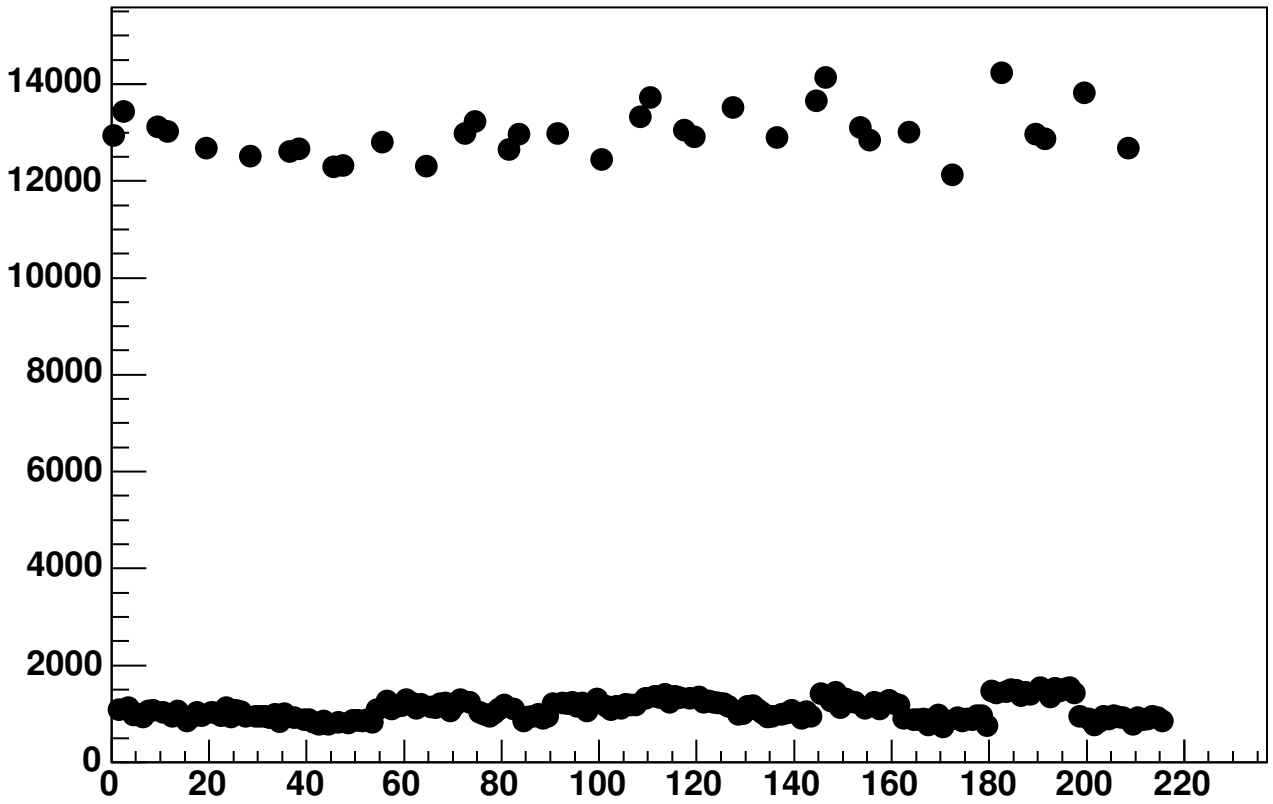
Enable 0, Hold=30, DAC=20480, ADC Mean vs 18*Chip+Chan



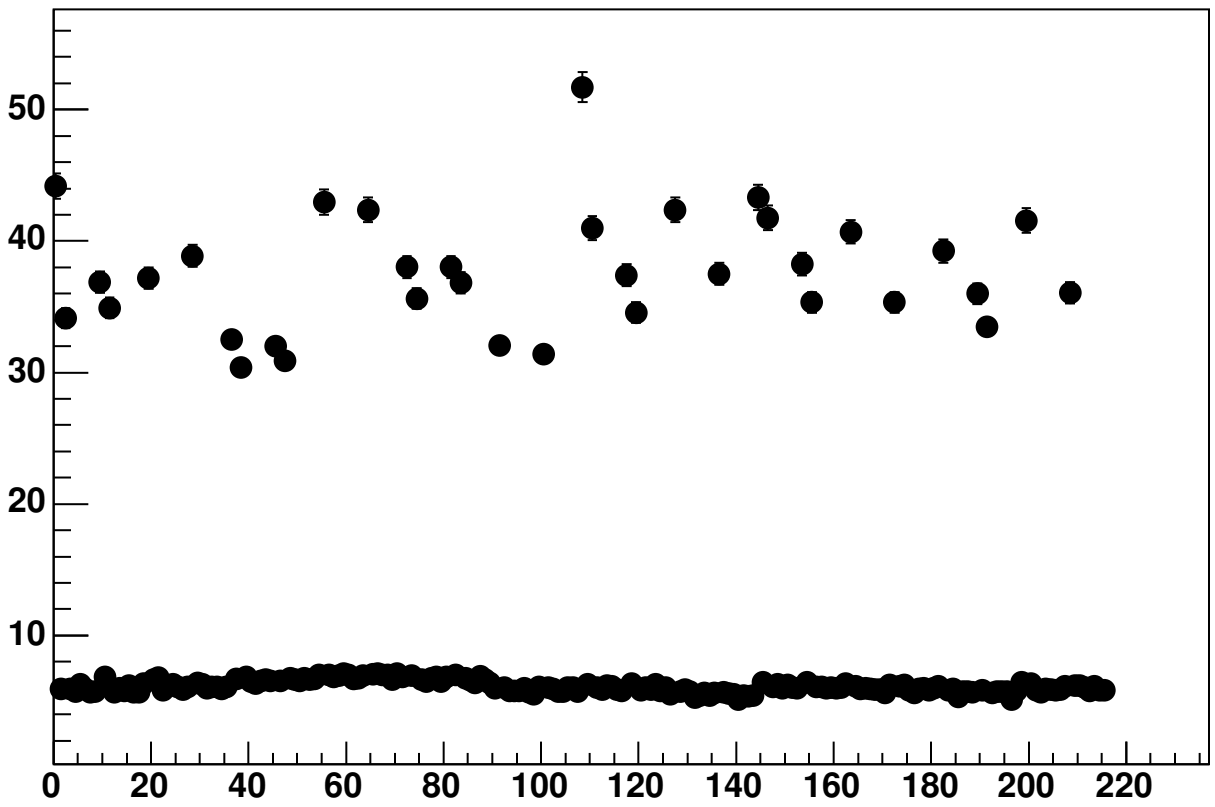
Enable 0, Hold=30, DAC=20480, ADC Noise vs 18*Chip+Chan



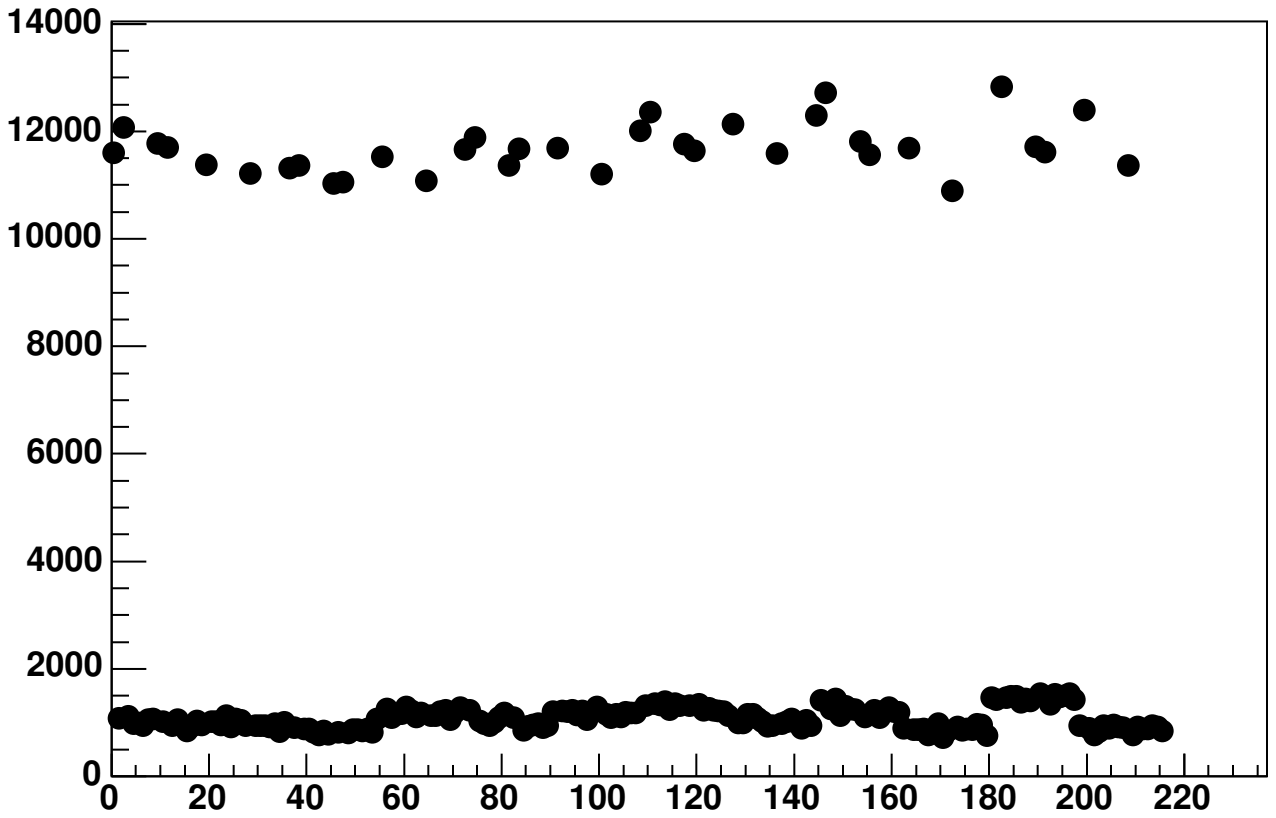
Enable 0, Hold=30, DAC=18432, ADC Mean vs 18*Chip+Chan



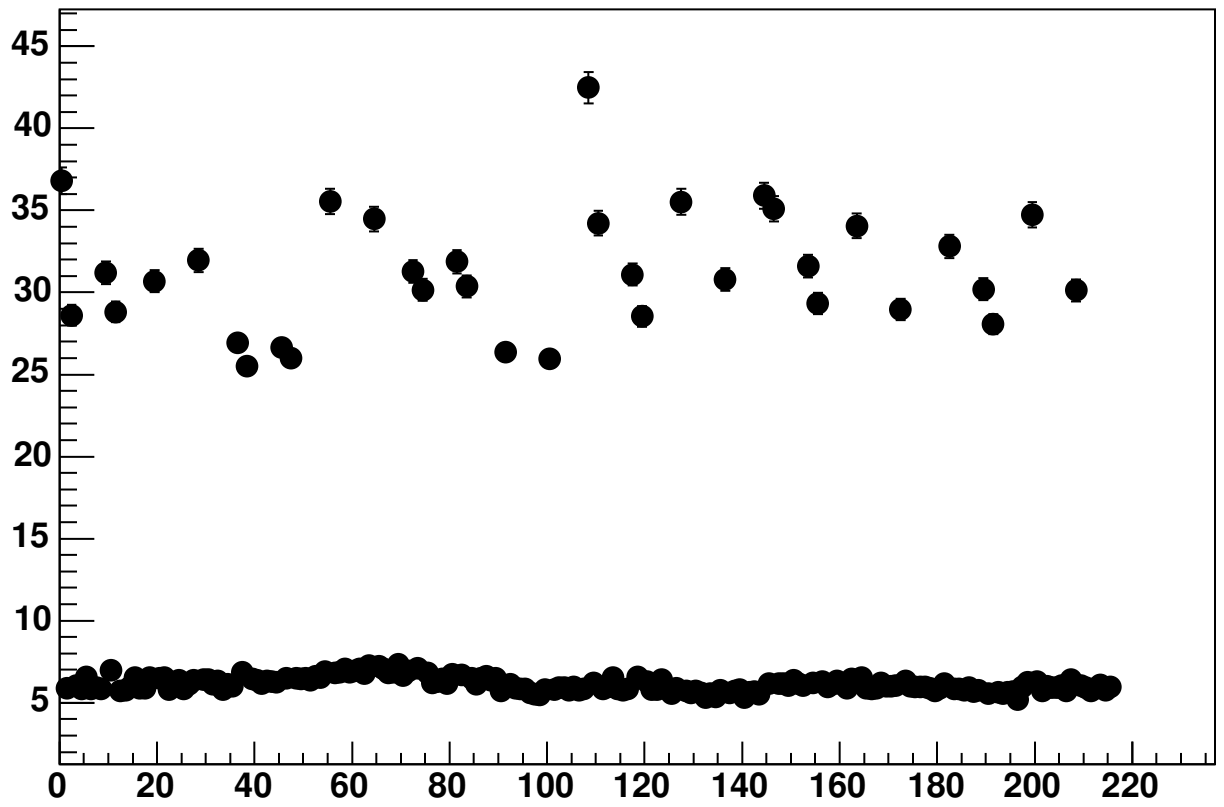
Enable 0, Hold=30, DAC=18432, ADC Noise vs 18*Chip+Chan



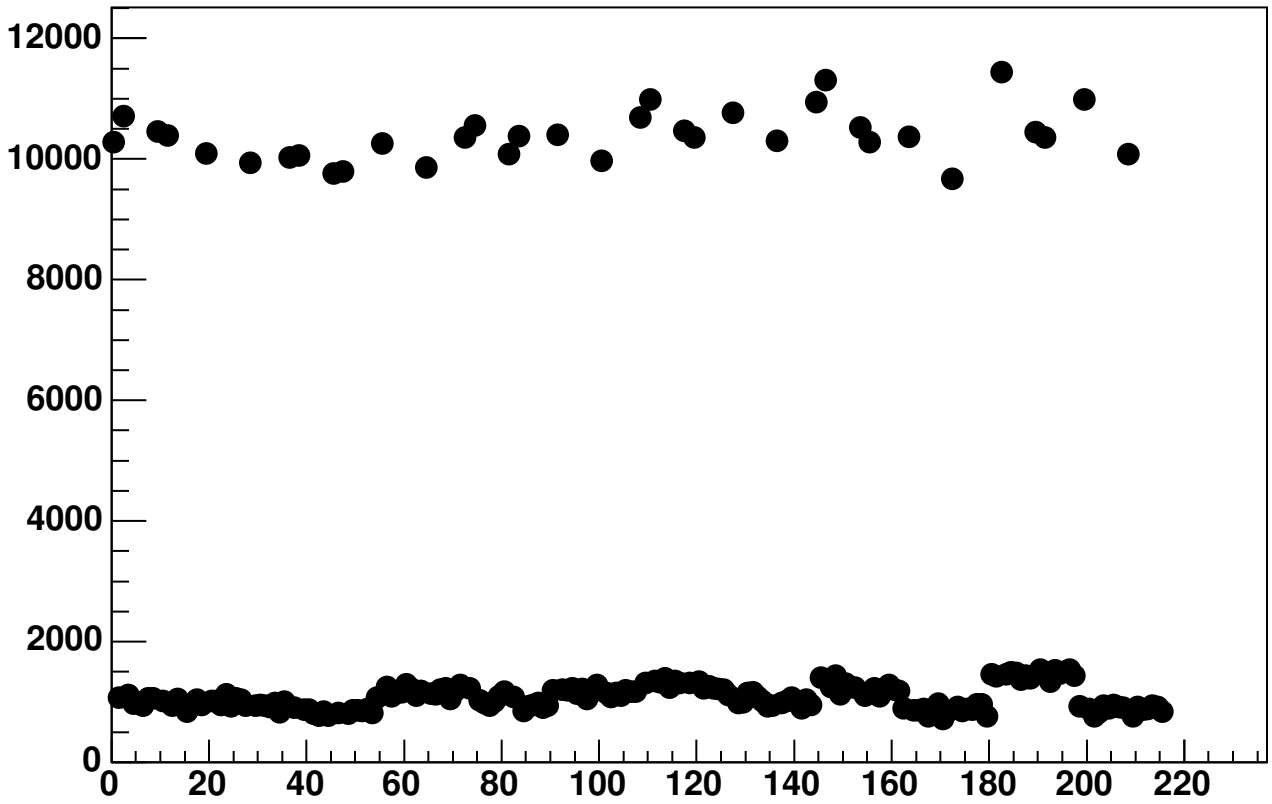
Enable 0, Hold=30, DAC=16384, ADC Mean vs 18*Chip+Chan



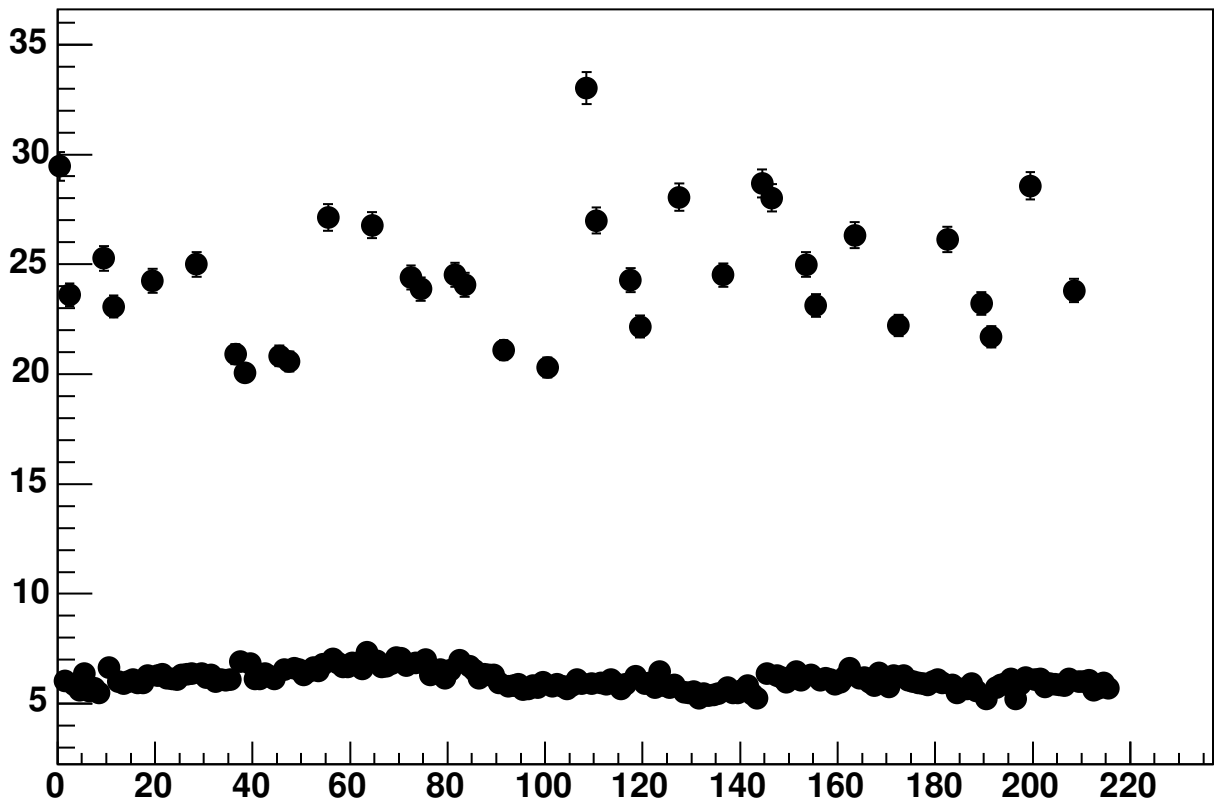
Enable 0, Hold=30, DAC=16384, ADC Noise vs 18*Chip+Chan



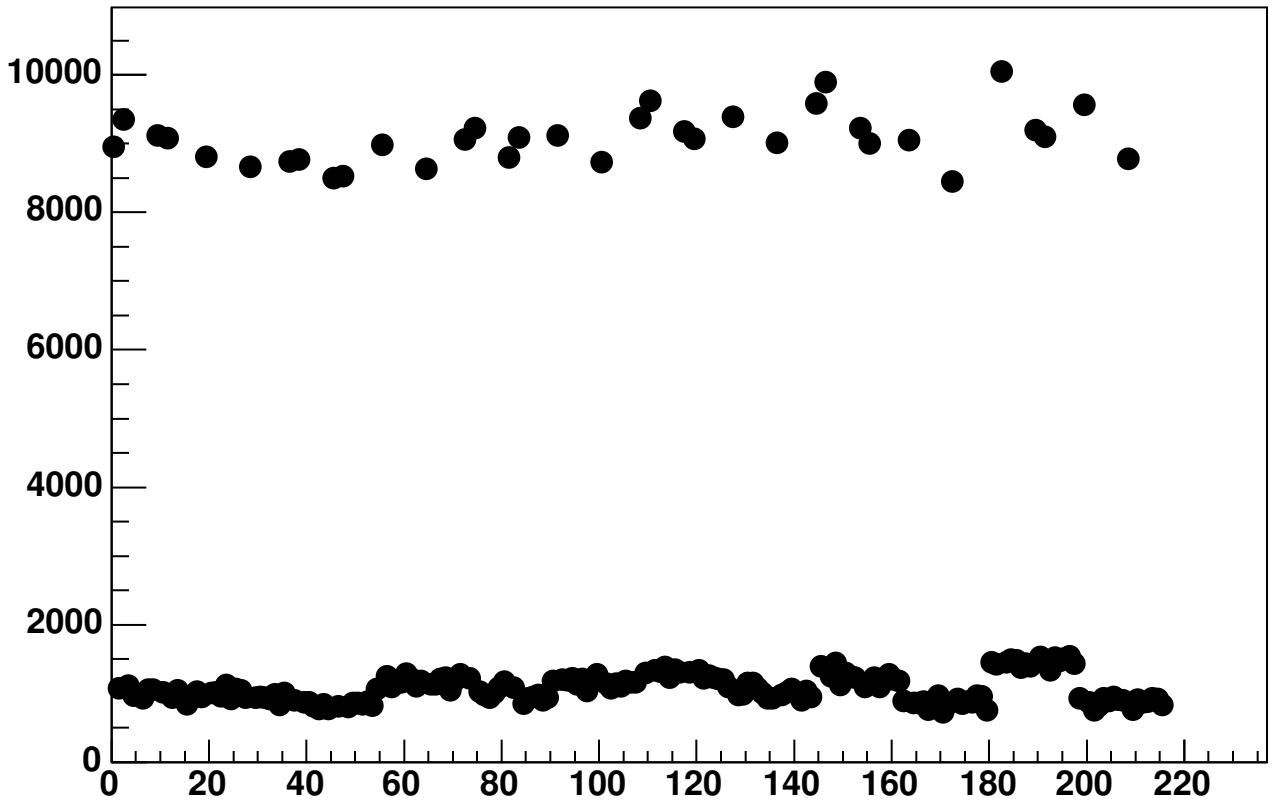
Enable 0, Hold=30, DAC=14336, ADC Mean vs 18*Chip+Chan



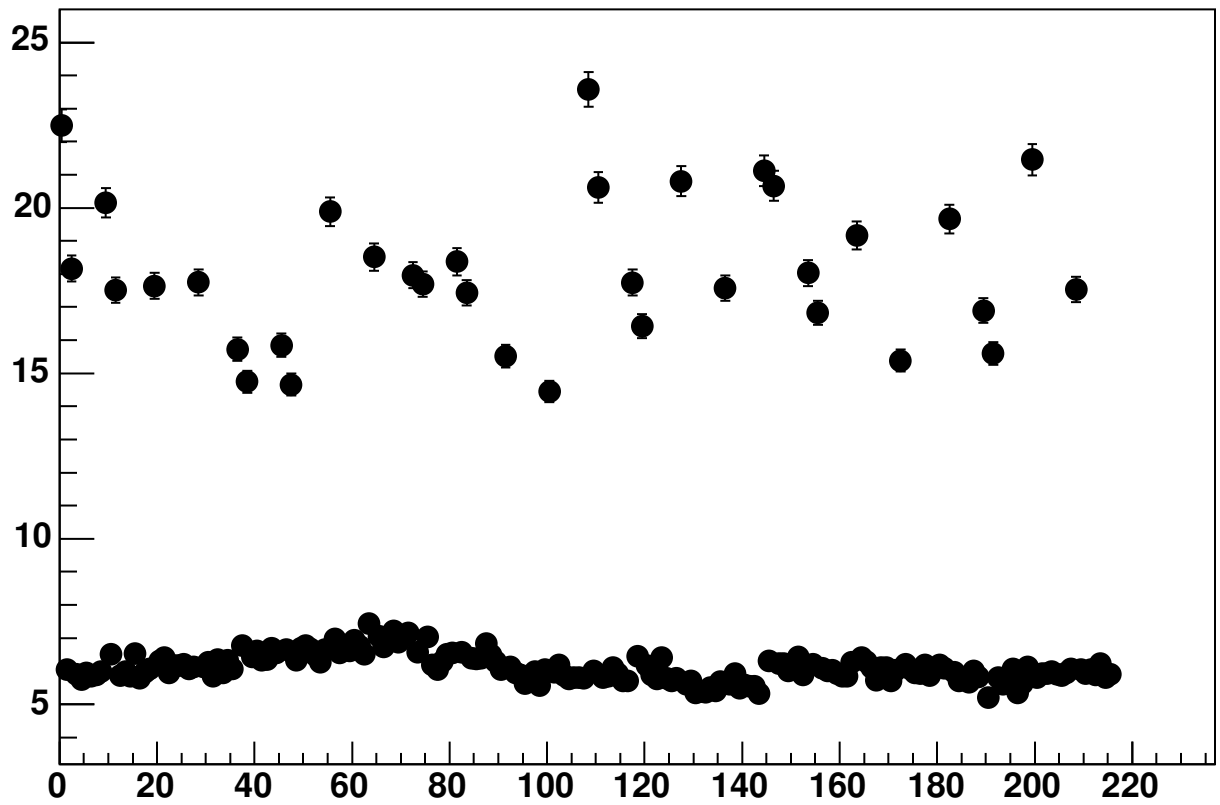
Enable 0, Hold=30, DAC=14336, ADC Noise vs 18*Chip+Chan



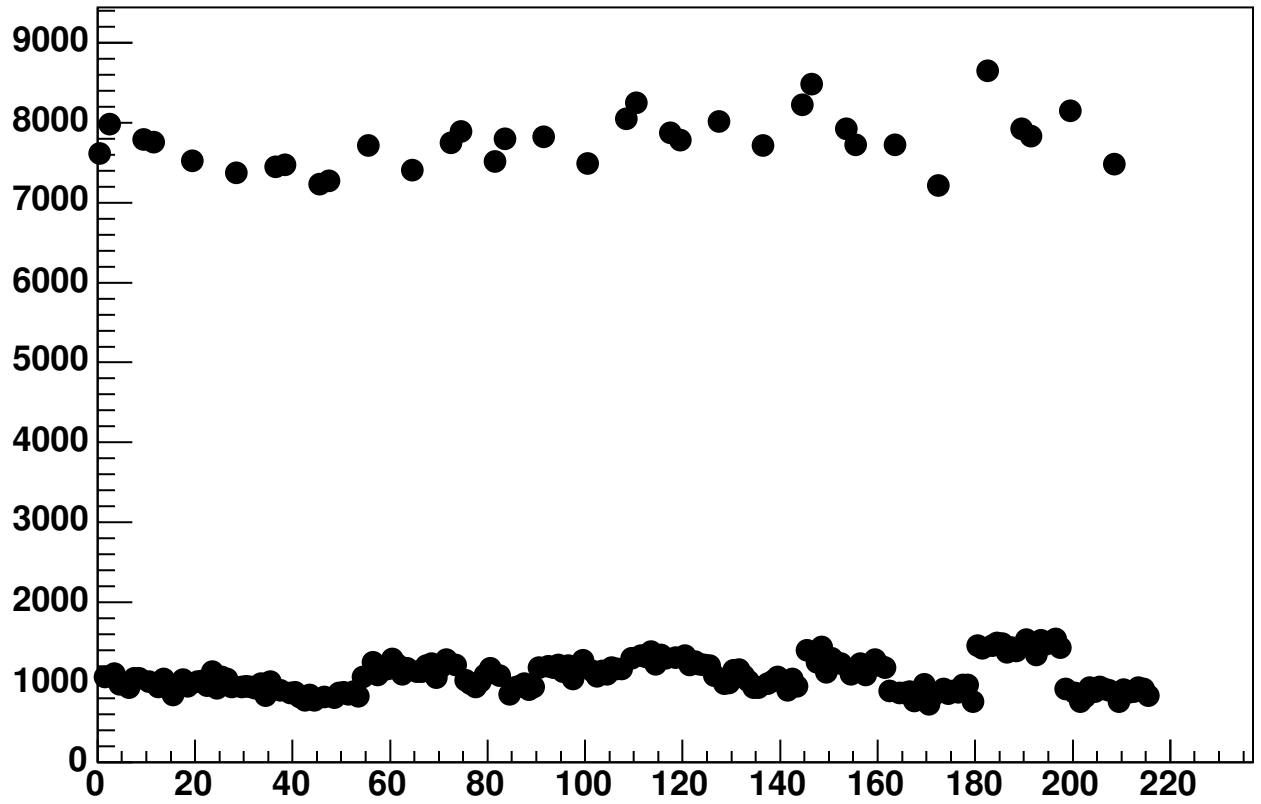
Enable 0, Hold=30, DAC=12288, ADC Mean vs 18*Chip+Chan



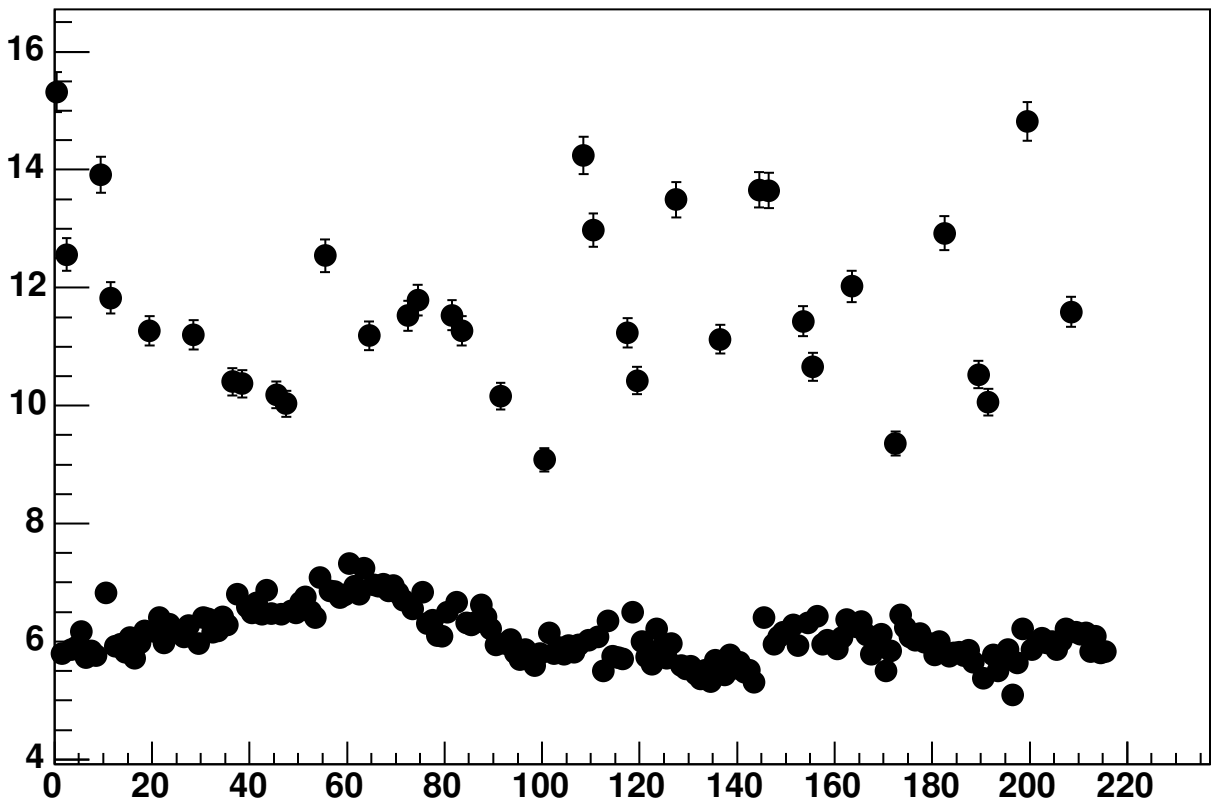
Enable 0, Hold=30, DAC=12288, ADC Noise vs 18*Chip+Chan



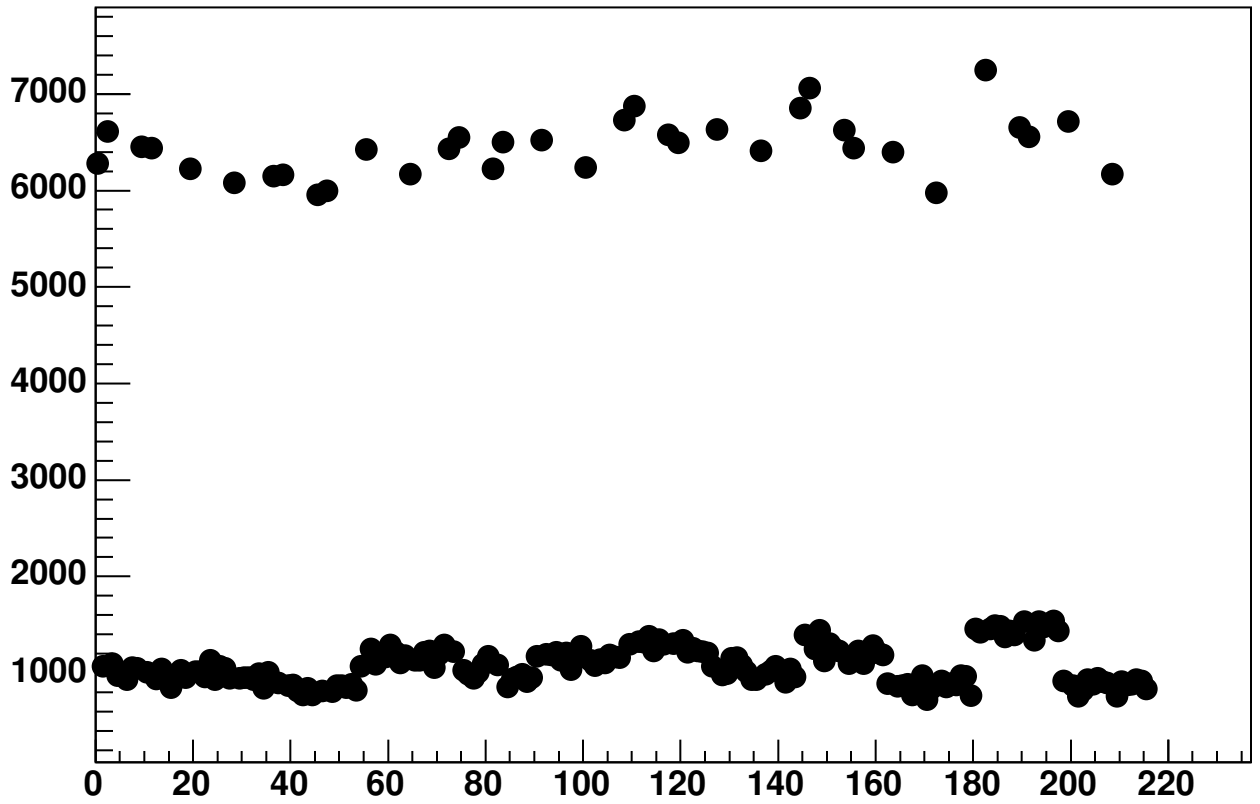
Enable 0, Hold=30, DAC=10240, ADC Mean vs 18*Chip+Chan



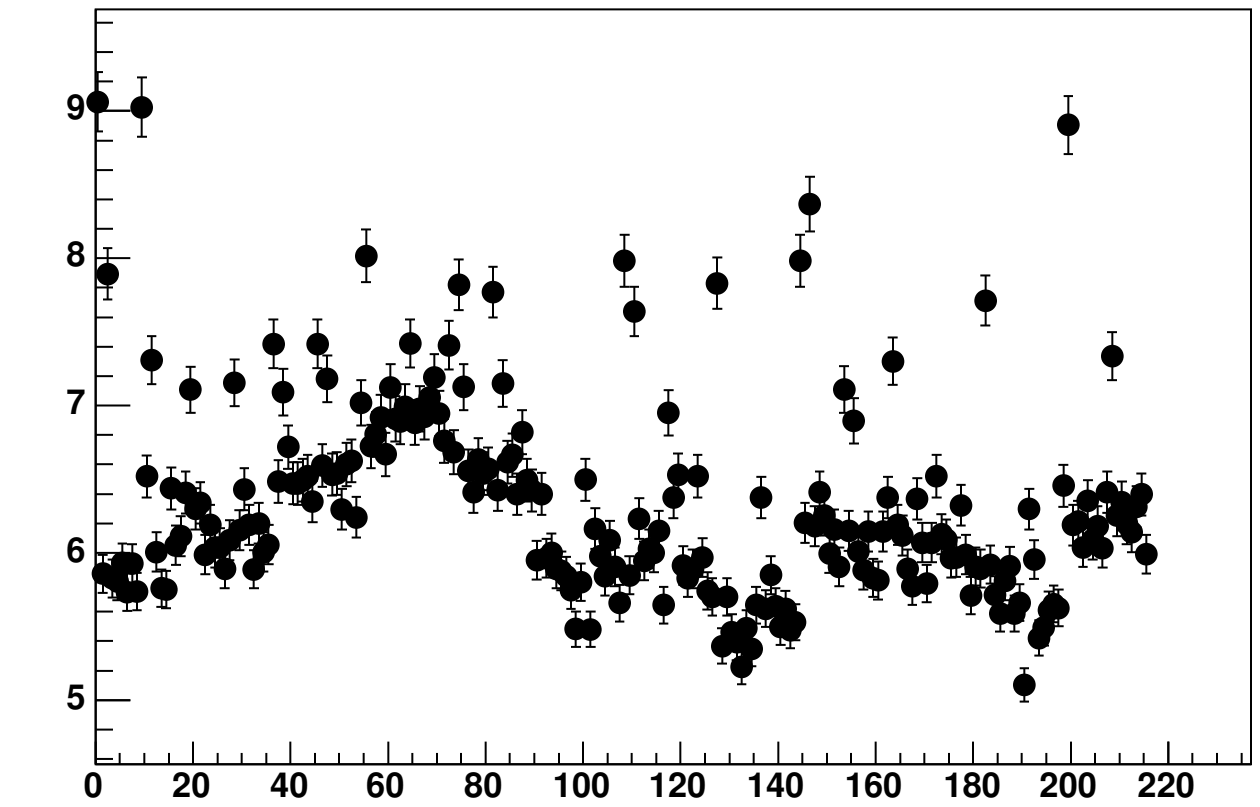
Enable 0, Hold=30, DAC=10240, ADC Noise vs 18*Chip+Chan



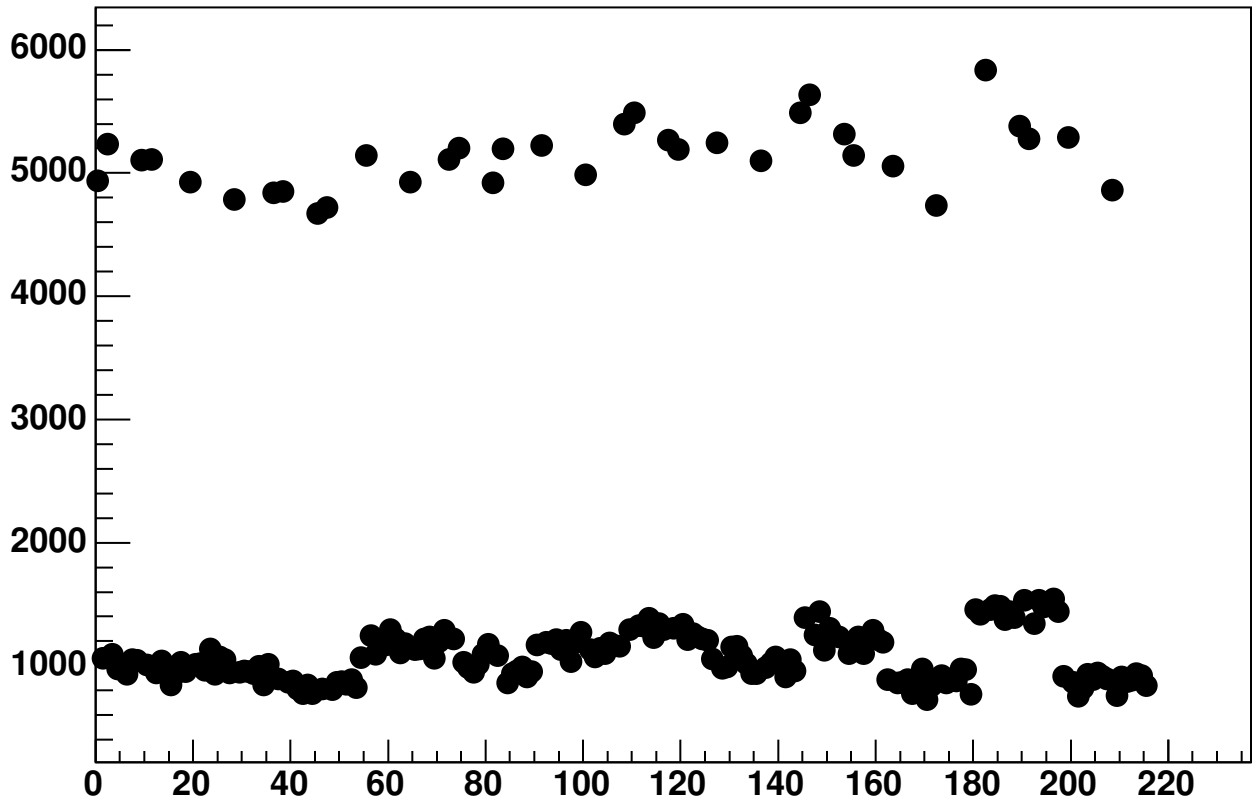
Enable 0, Hold=30, DAC=8192, ADC Mean vs 18*Chip+Chan



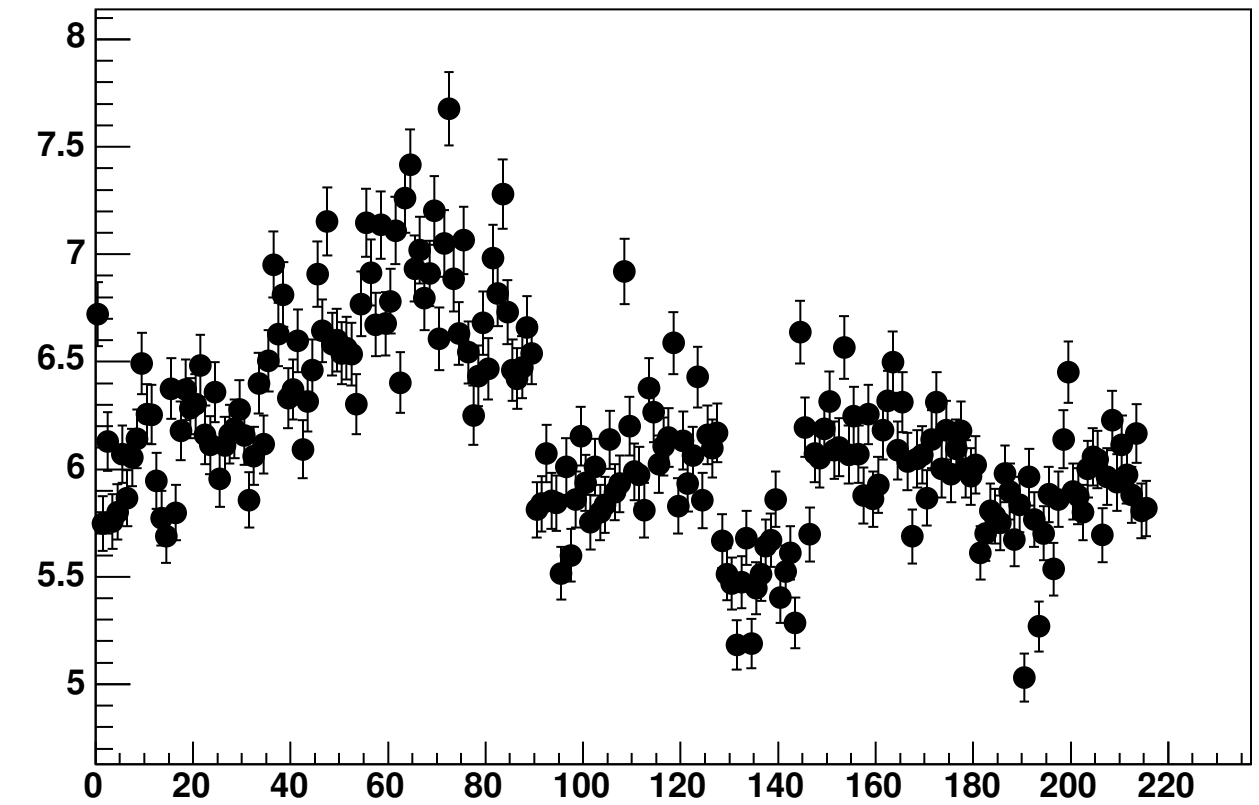
Enable 0, Hold=30, DAC=8192, ADC Noise vs 18*Chip+Chan



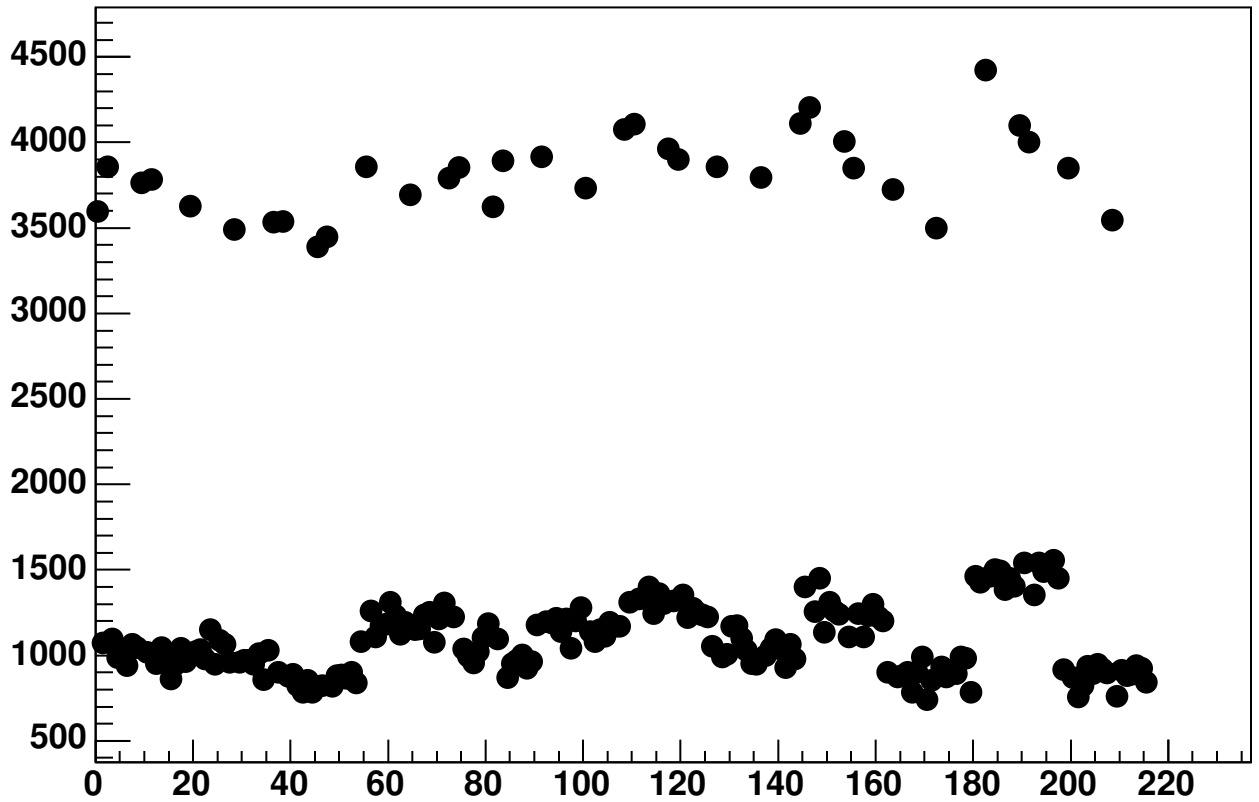
Enable 0, Hold=30, DAC=6144, ADC Mean vs 18*Chip+Chan



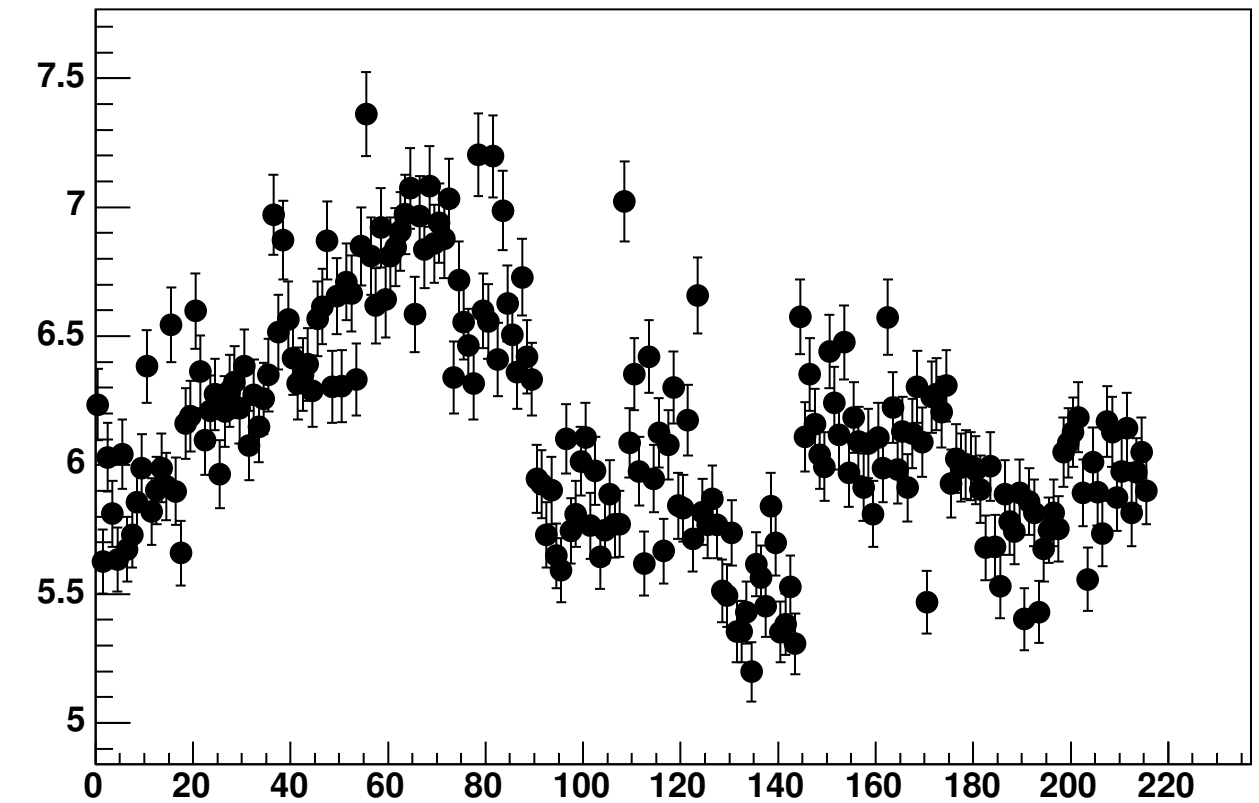
Enable 0, Hold=30, DAC=6144, ADC Noise vs 18*Chip+Chan



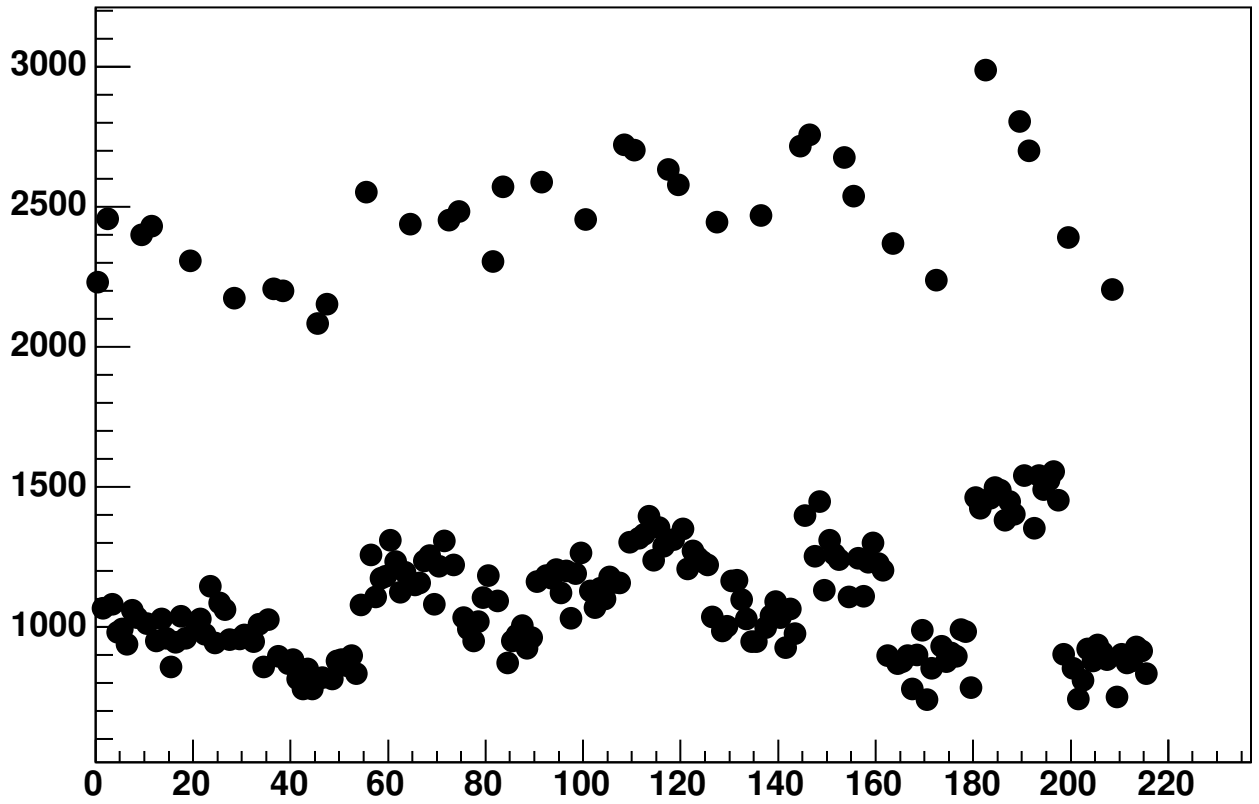
Enable 0, Hold=30, DAC=4096, ADC Mean vs 18*Chip+Chan



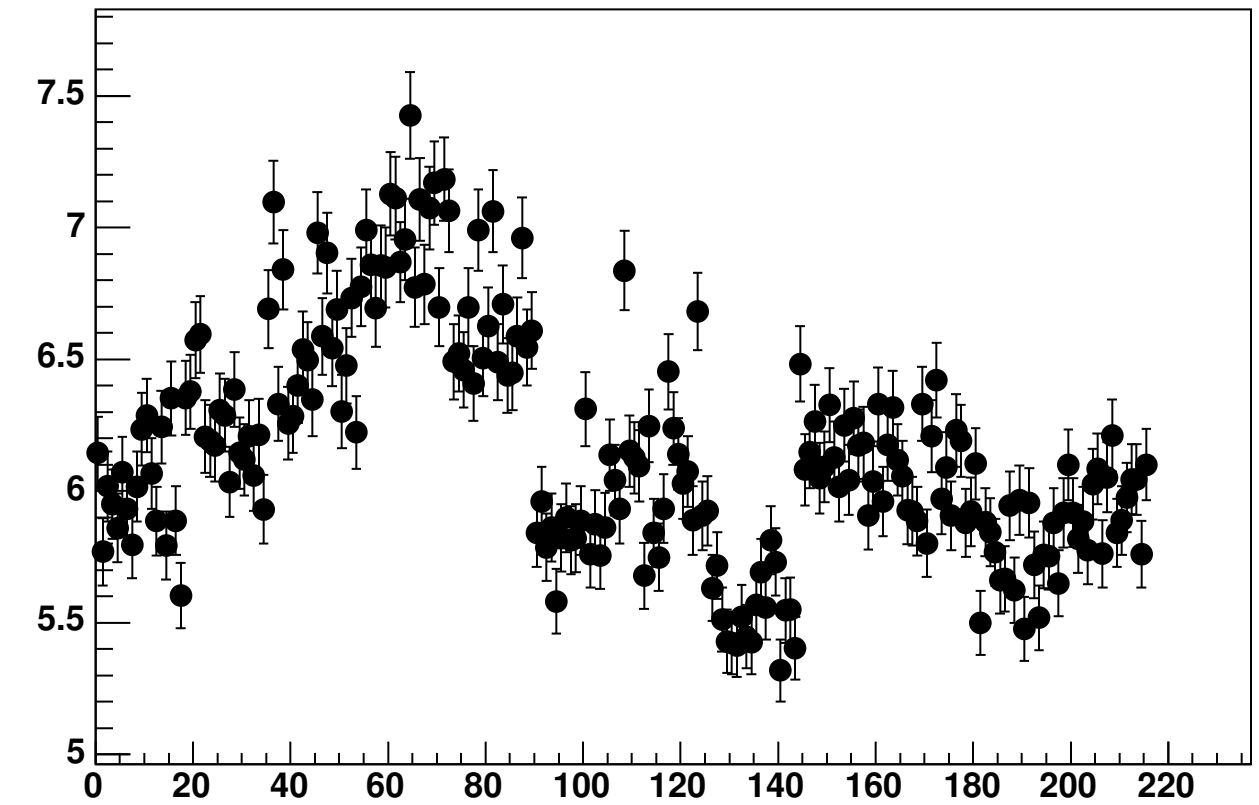
Enable 0, Hold=30, DAC=4096, ADC Noise vs 18*Chip+Chan



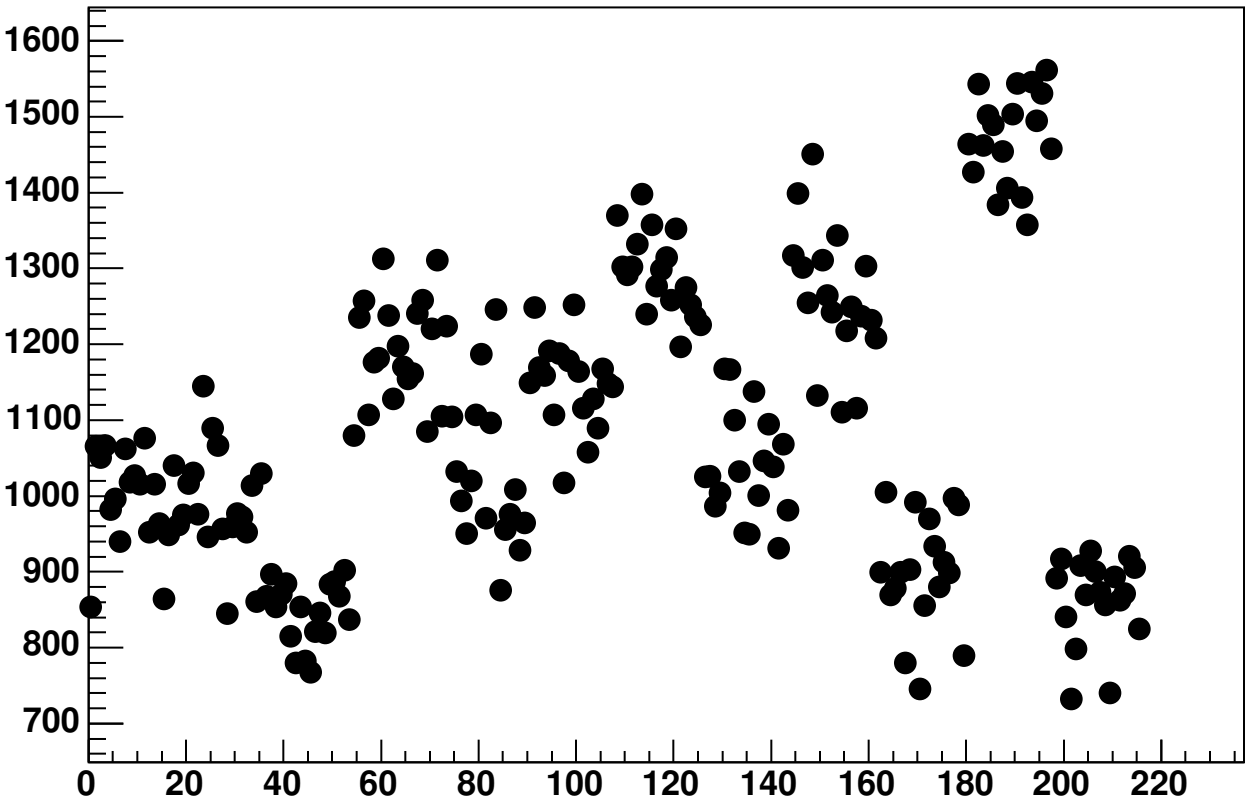
Enable 0, Hold=30, DAC=2048, ADC Mean vs 18*Chip+Chan



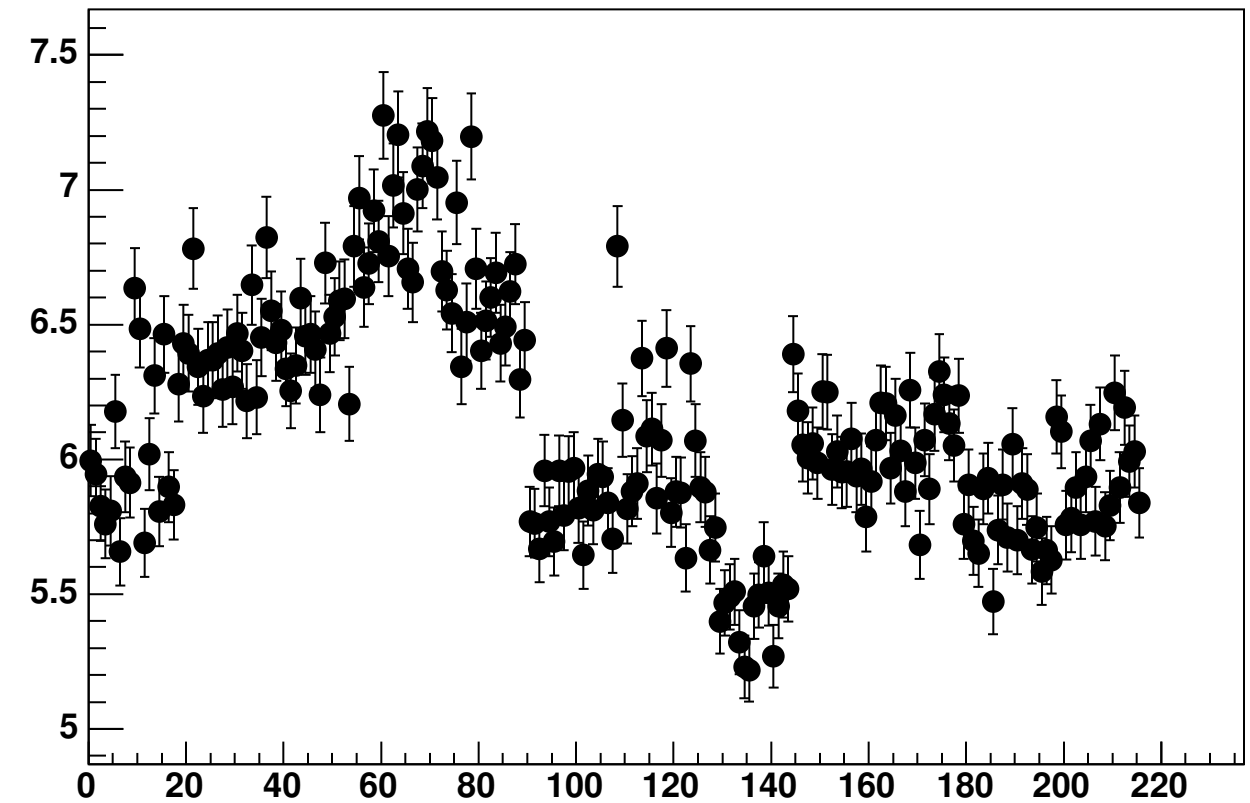
Enable 0, Hold=30, DAC=2048, ADC Noise vs 18*Chip+Chan



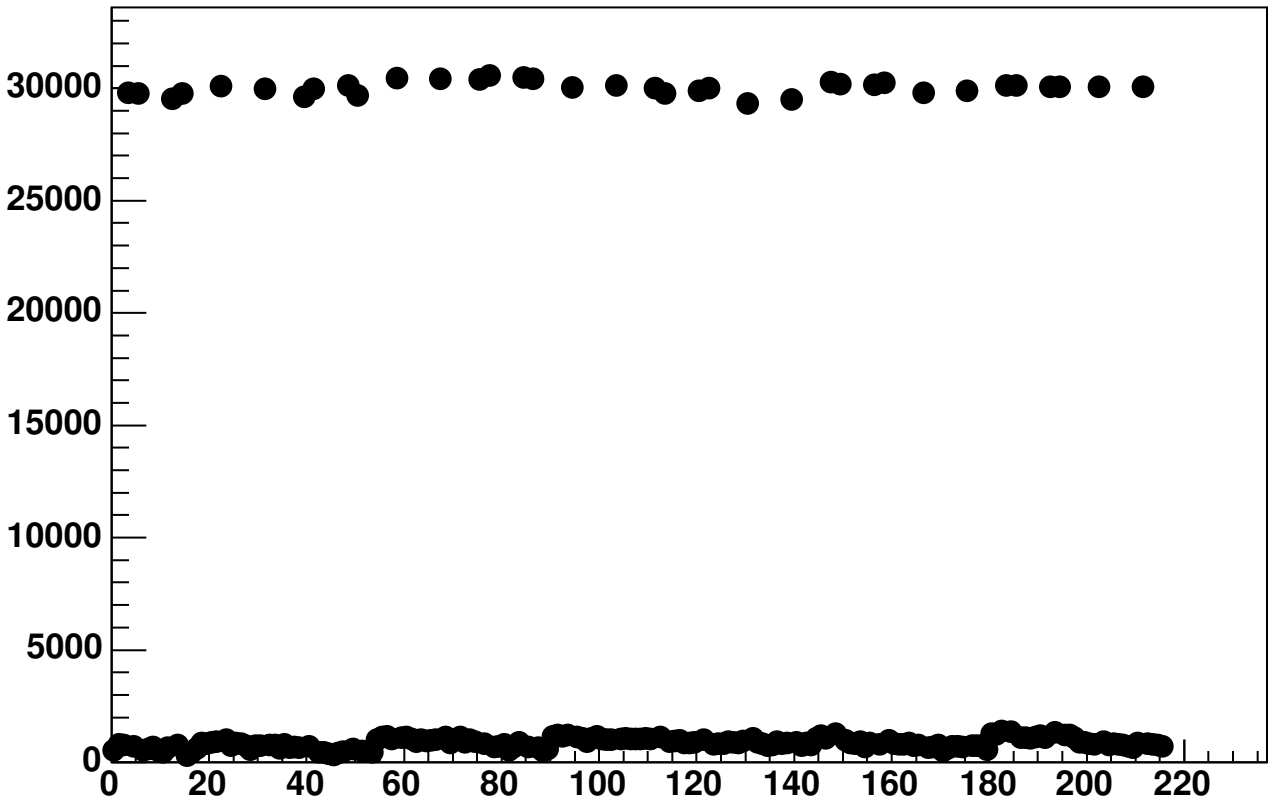
Enable 0, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



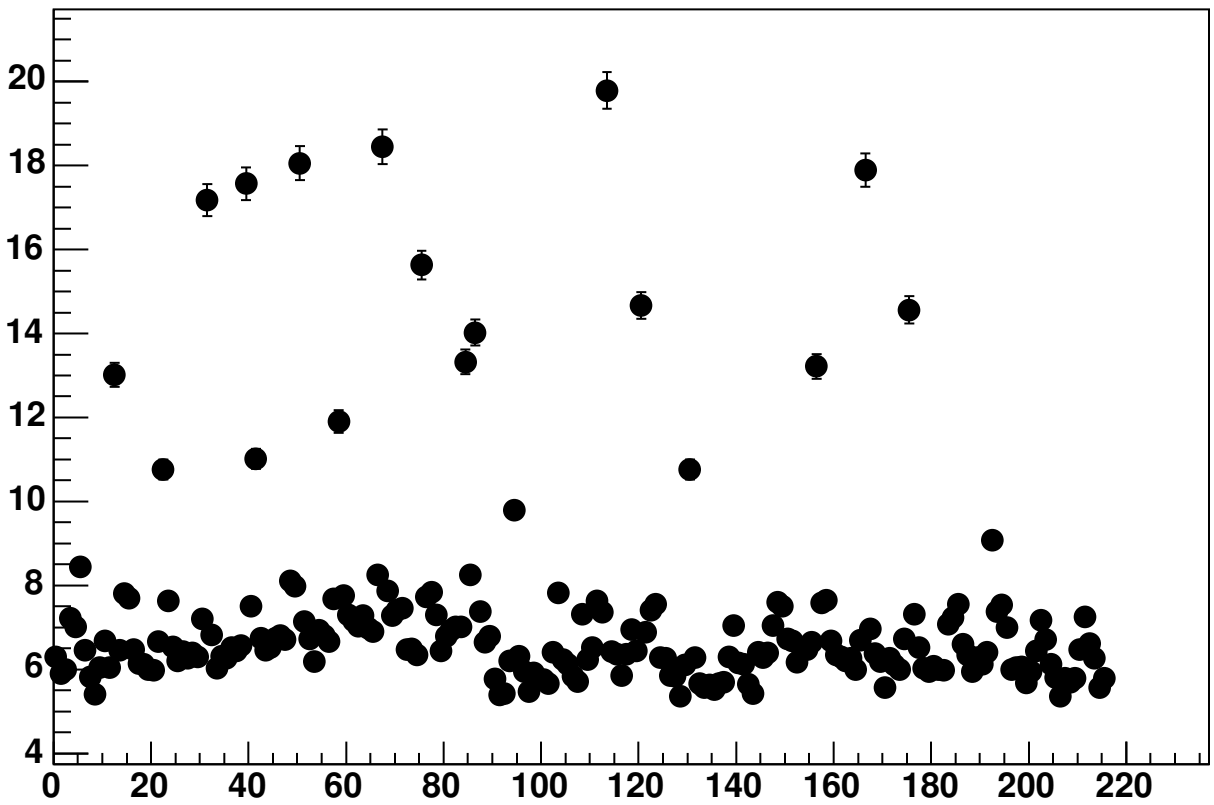
Enable 0, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



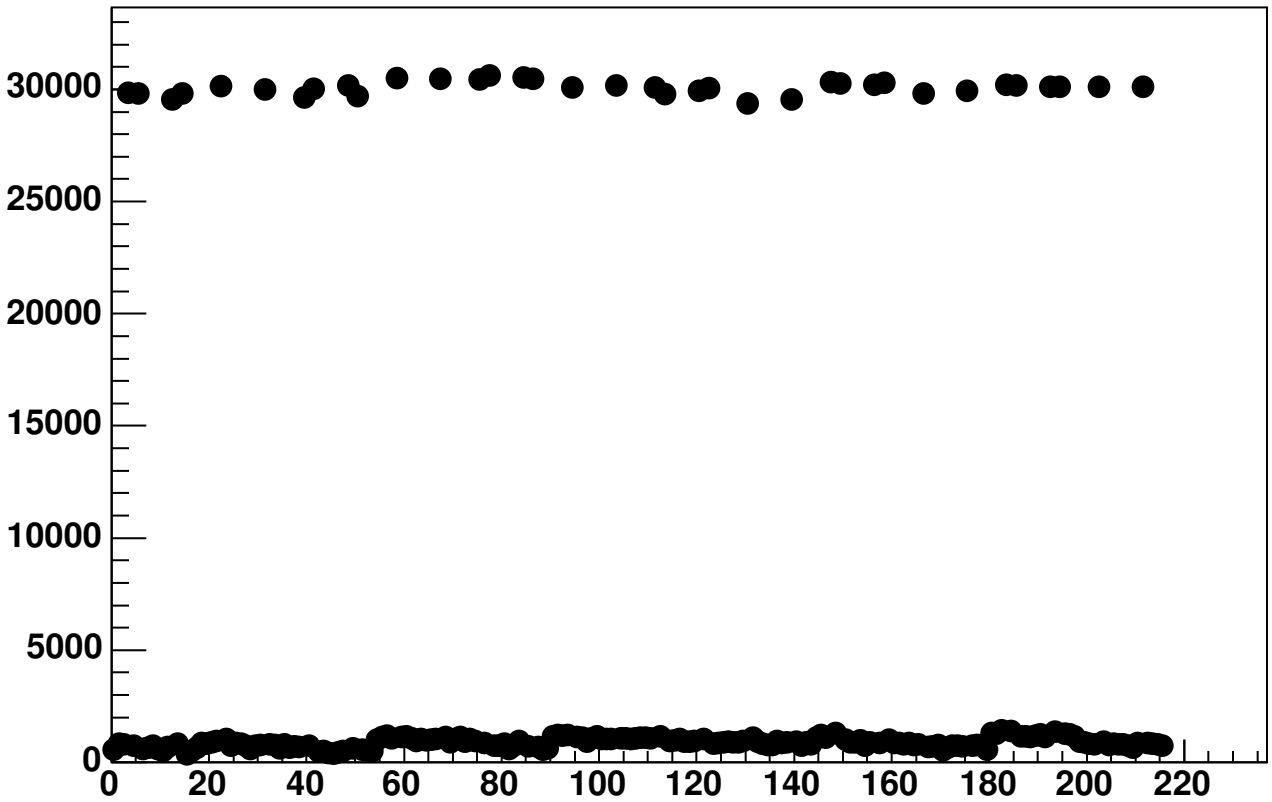
Enable 1, Hold=30, DAC=63488, ADC Mean vs 18*Chip+Chan



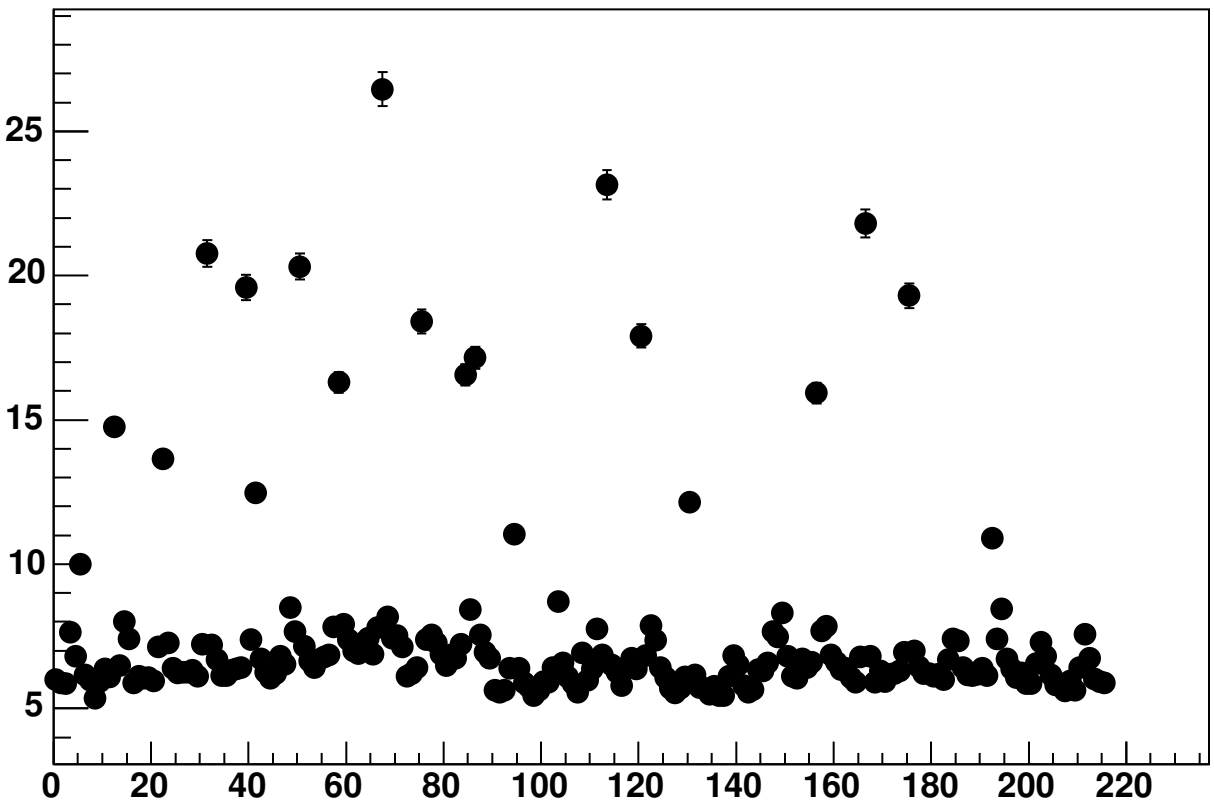
Enable 1, Hold=30, DAC=63488, ADC Noise vs 18*Chip+Chan



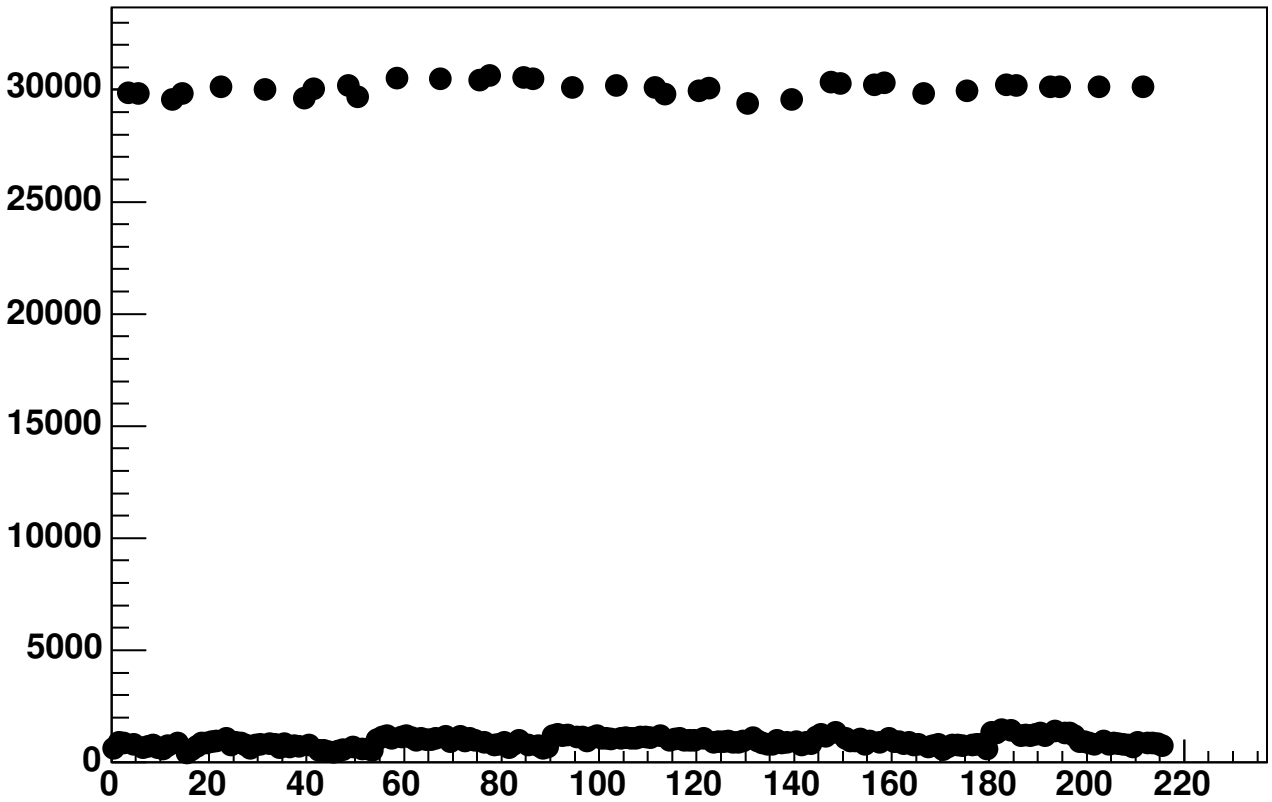
Enable 1, Hold=30, DAC=61440, ADC Mean vs 18*Chip+Chan



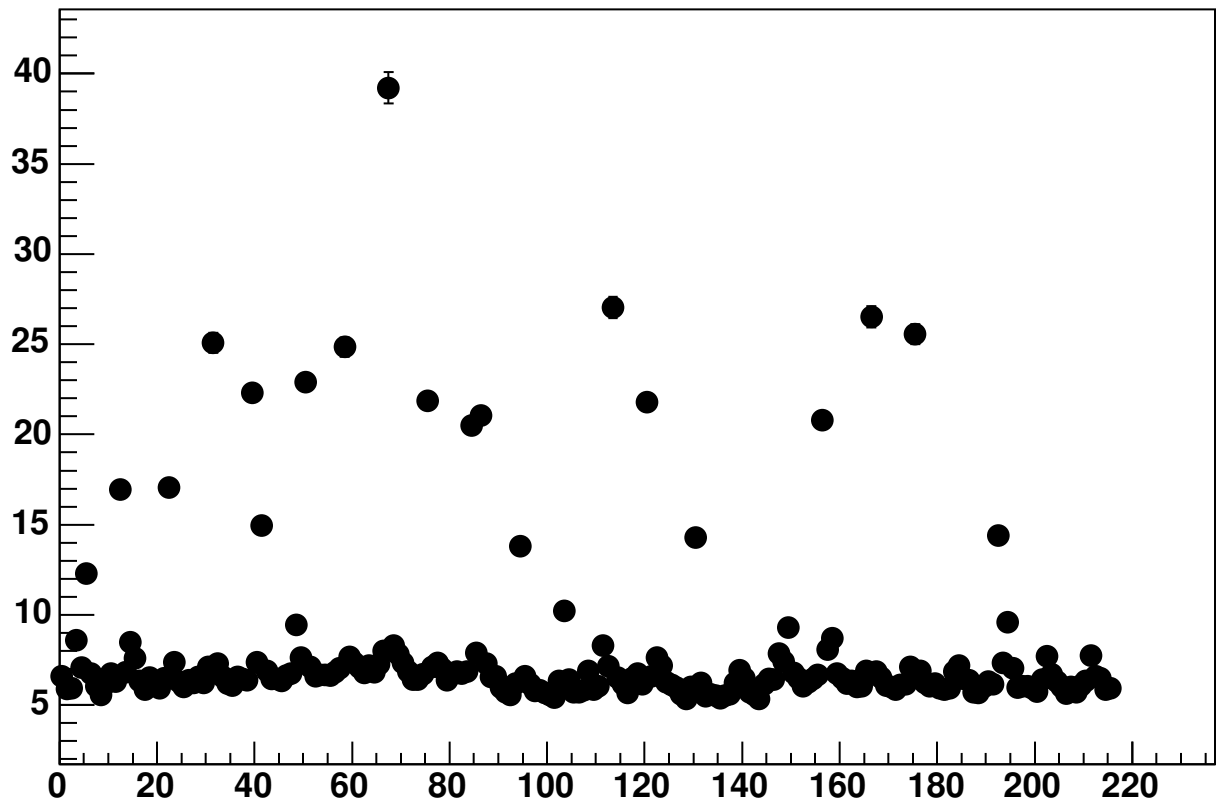
Enable 1, Hold=30, DAC=61440, ADC Noise vs 18*Chip+Chan



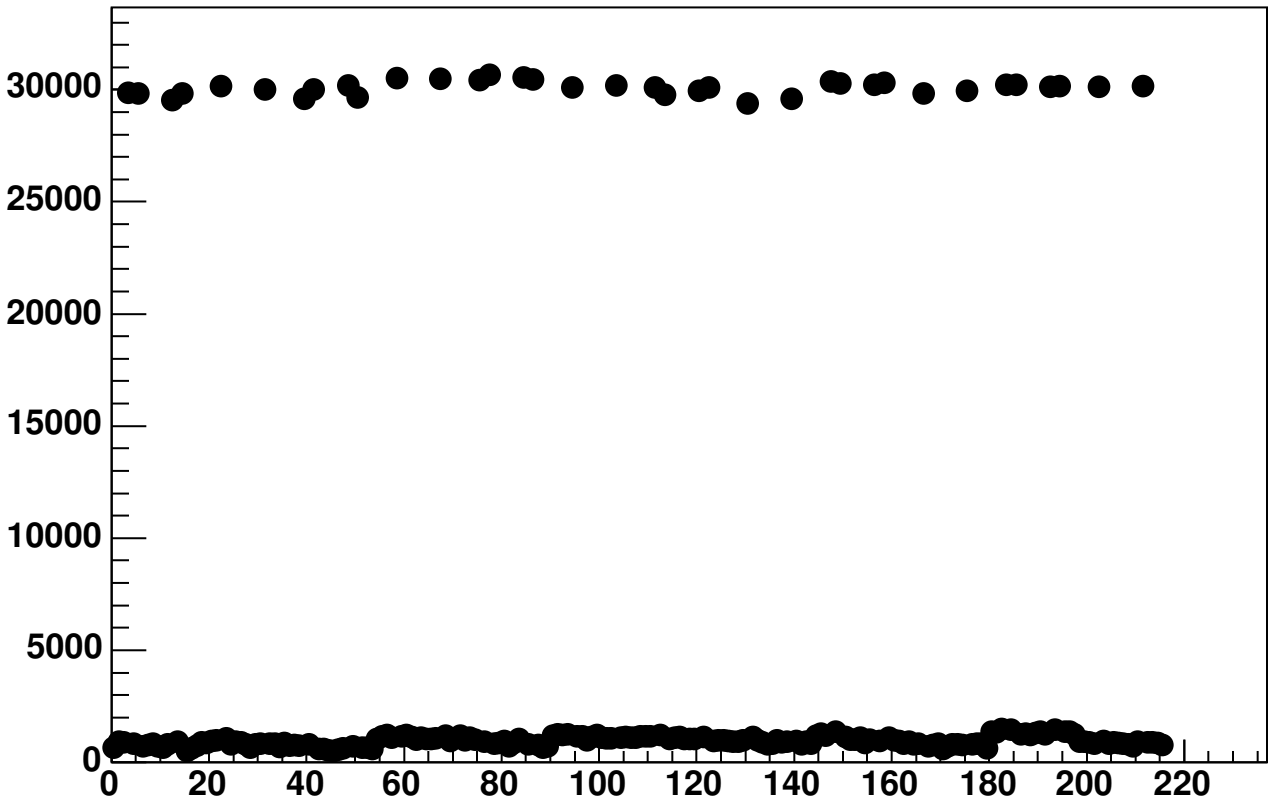
Enable 1, Hold=30, DAC=59392, ADC Mean vs 18*Chip+Chan



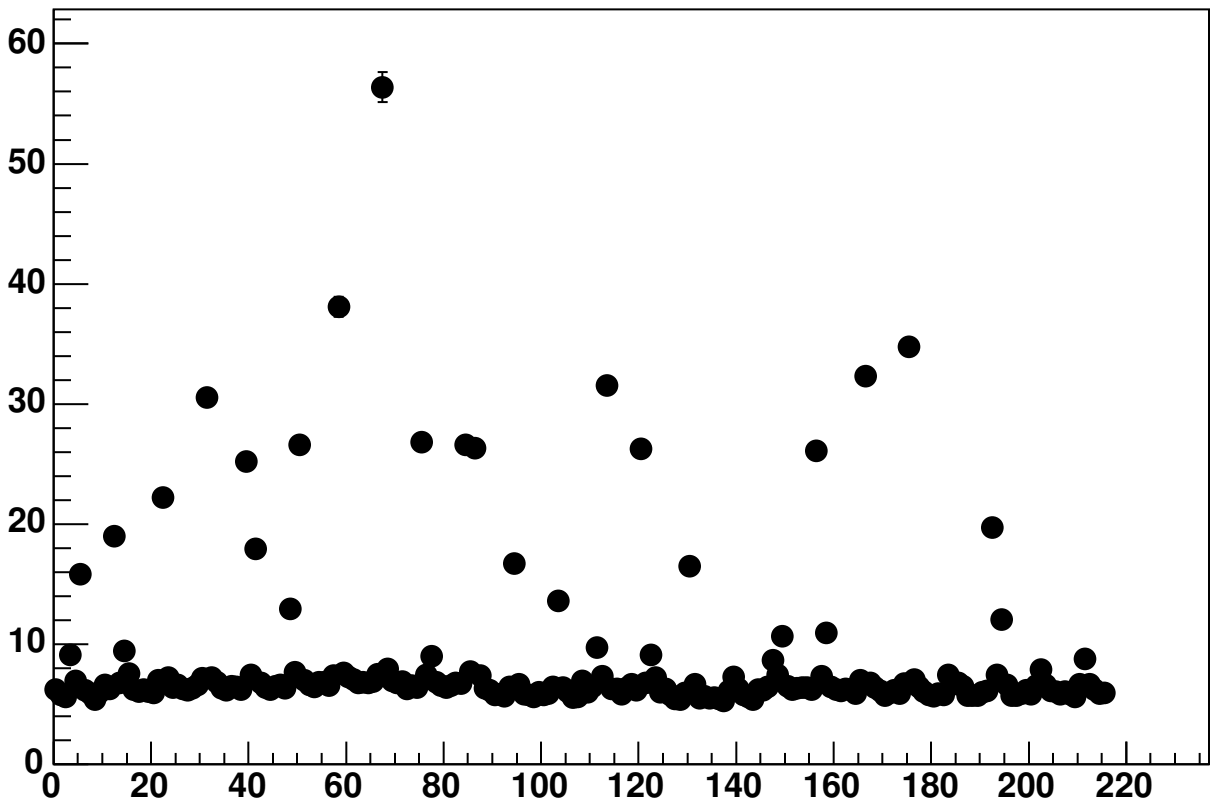
Enable 1, Hold=30, DAC=59392, ADC Noise vs 18*Chip+Chan



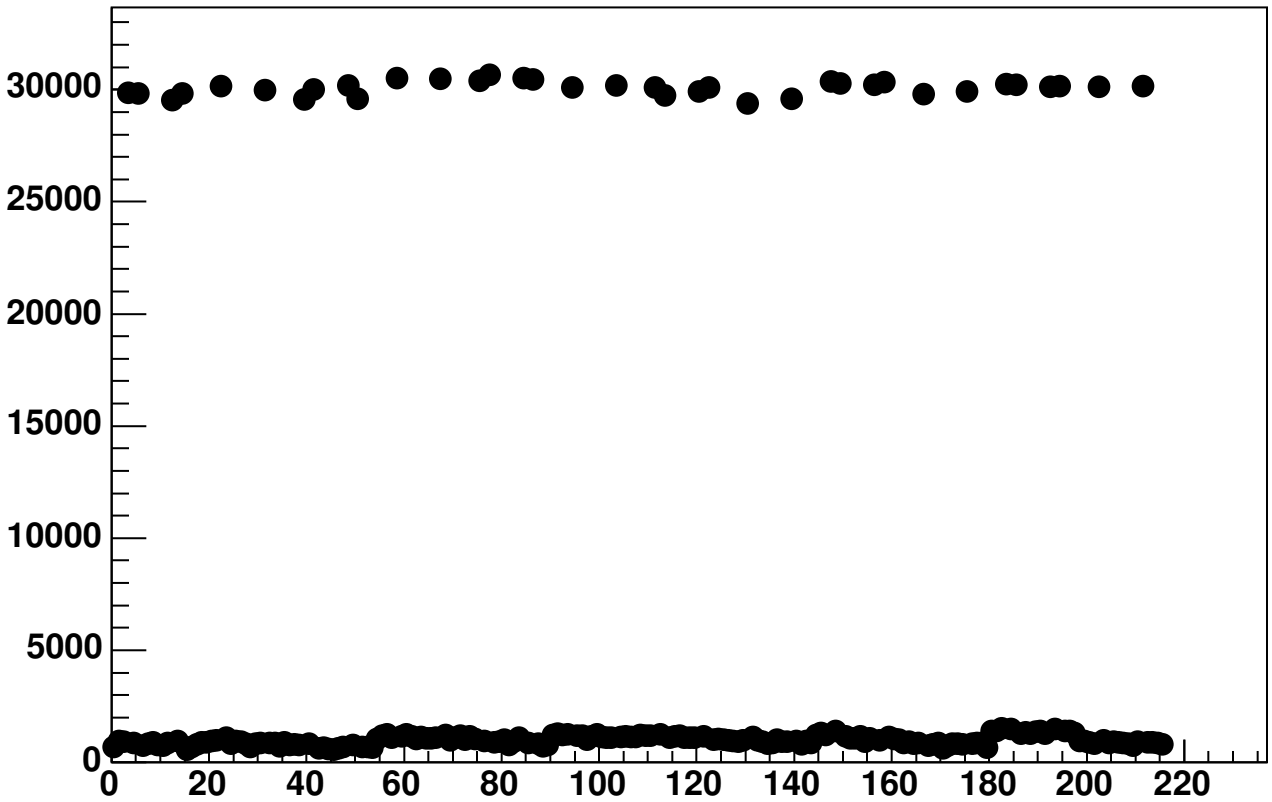
Enable 1, Hold=30, DAC=57344, ADC Mean vs 18*Chip+Chan



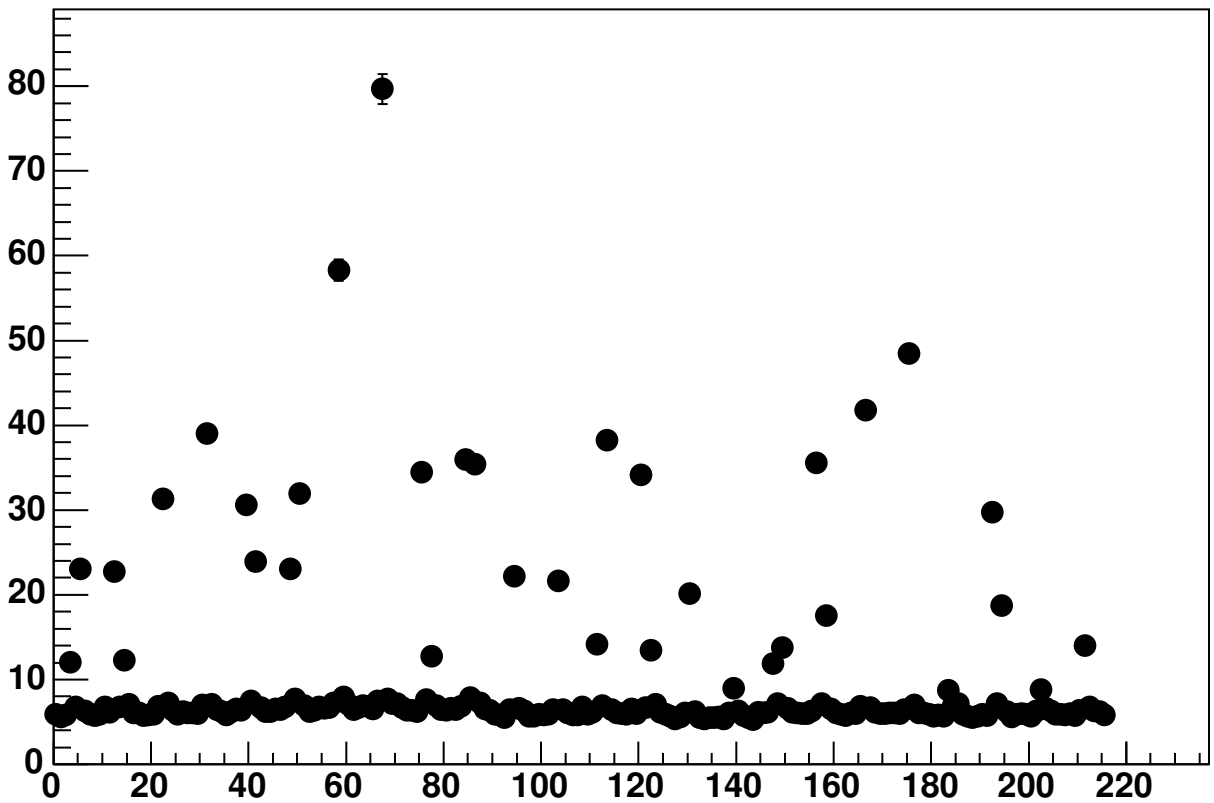
Enable 1, Hold=30, DAC=57344, ADC Noise vs 18*Chip+Chan



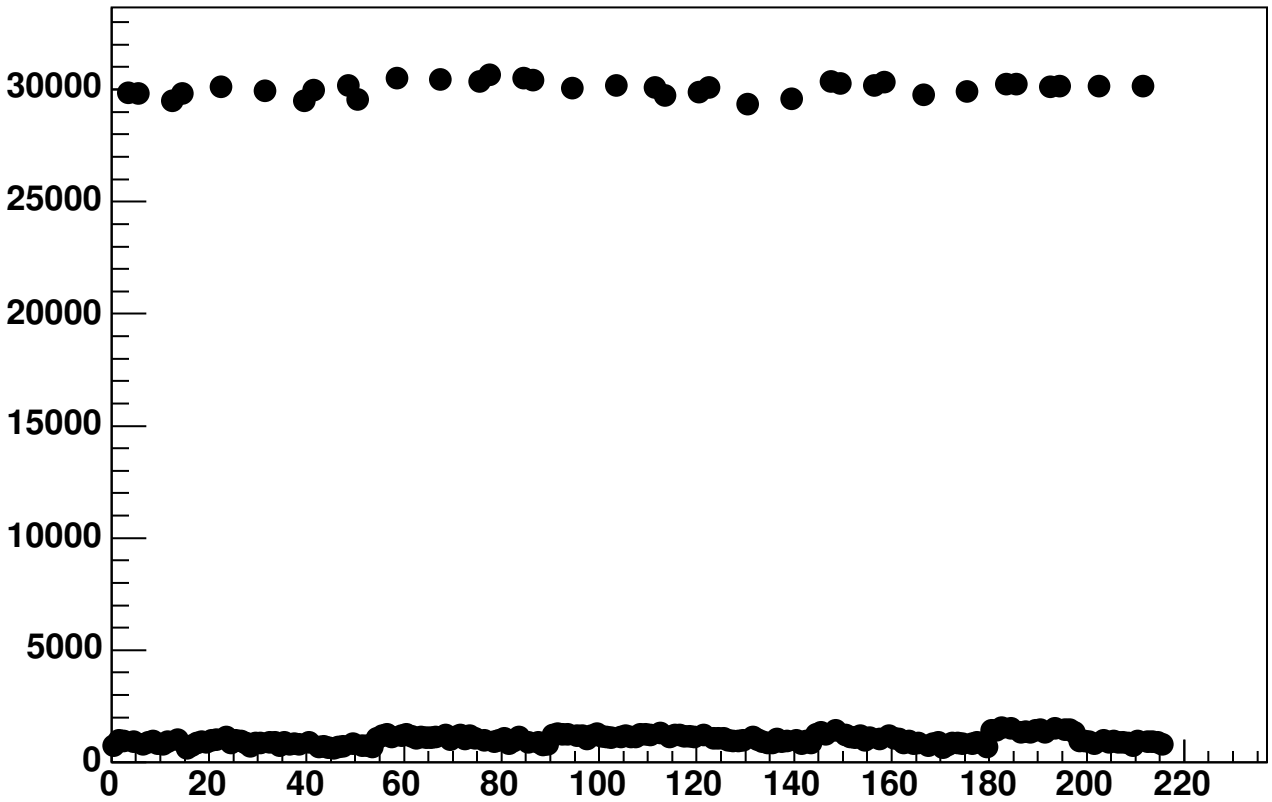
Enable 1, Hold=30, DAC=55296, ADC Mean vs 18*Chip+Chan



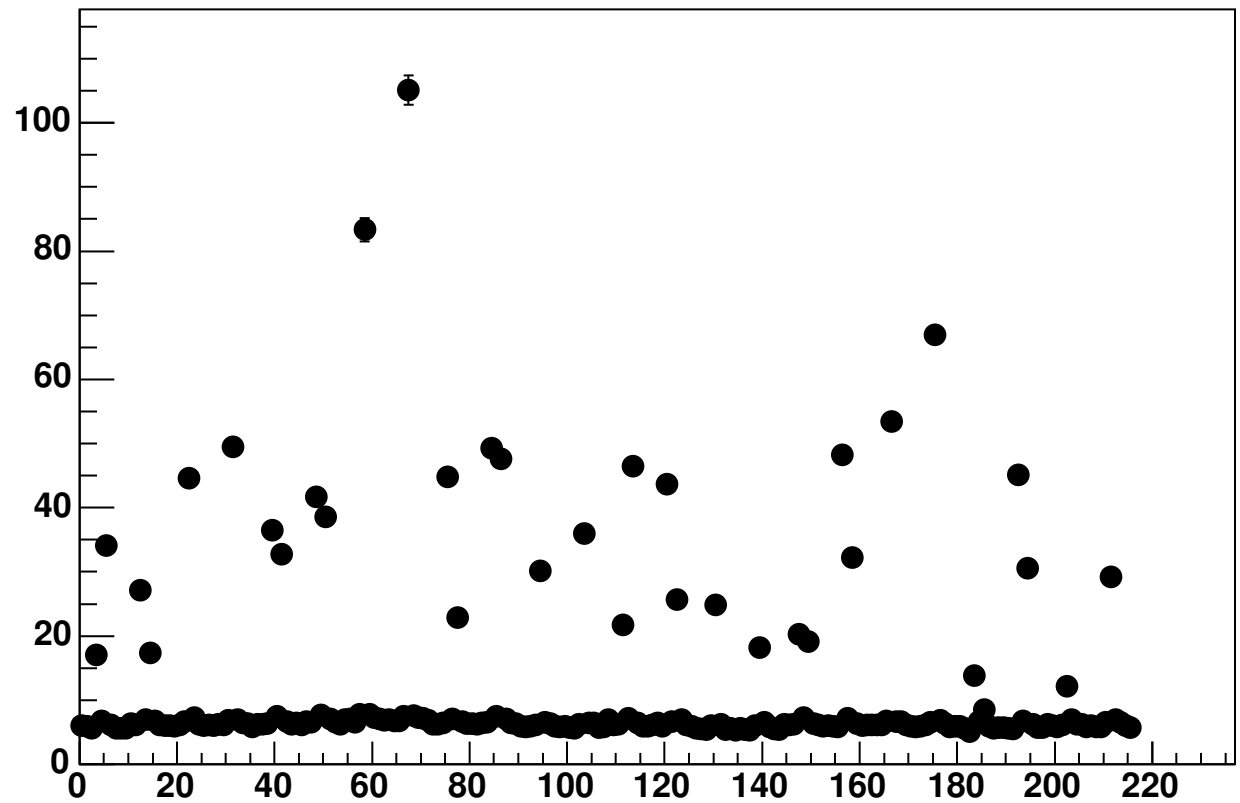
Enable 1, Hold=30, DAC=55296, ADC Noise vs 18*Chip+Chan



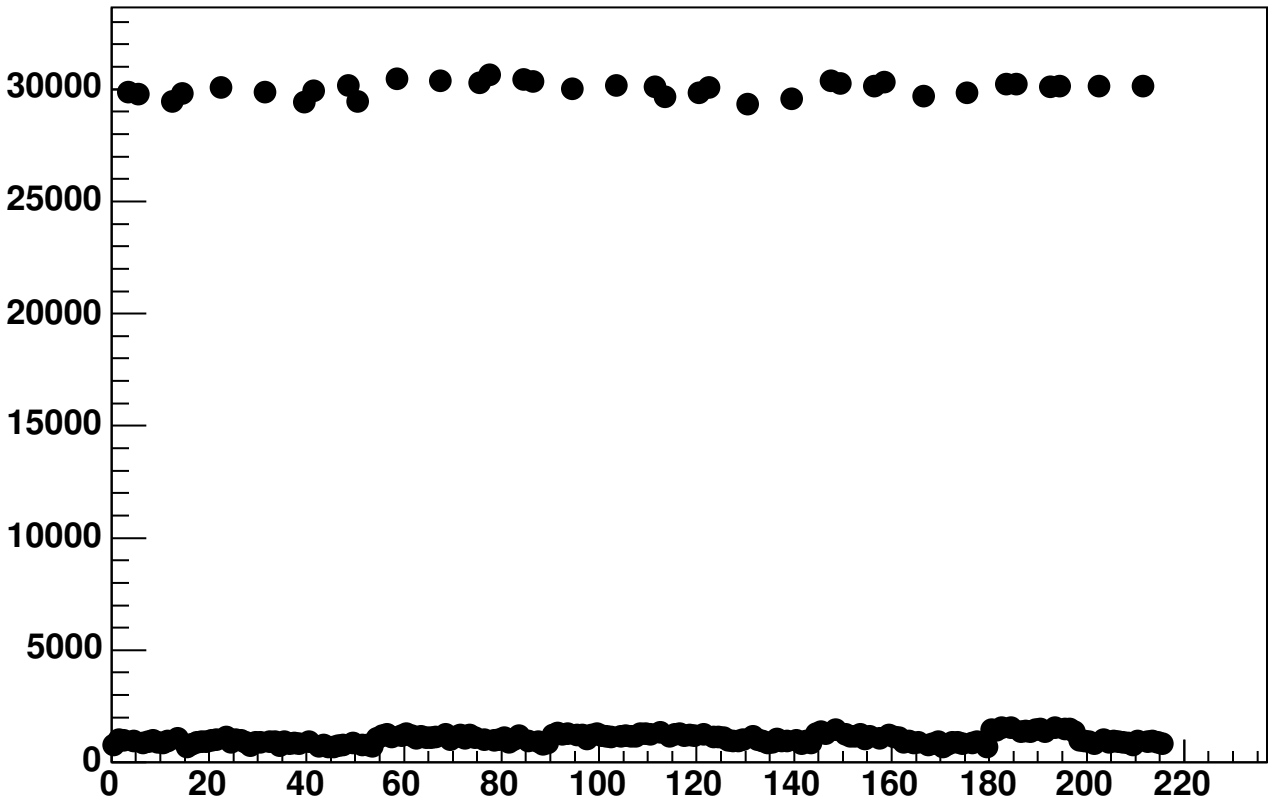
Enable 1, Hold=30, DAC=53248, ADC Mean vs 18*Chip+Chan



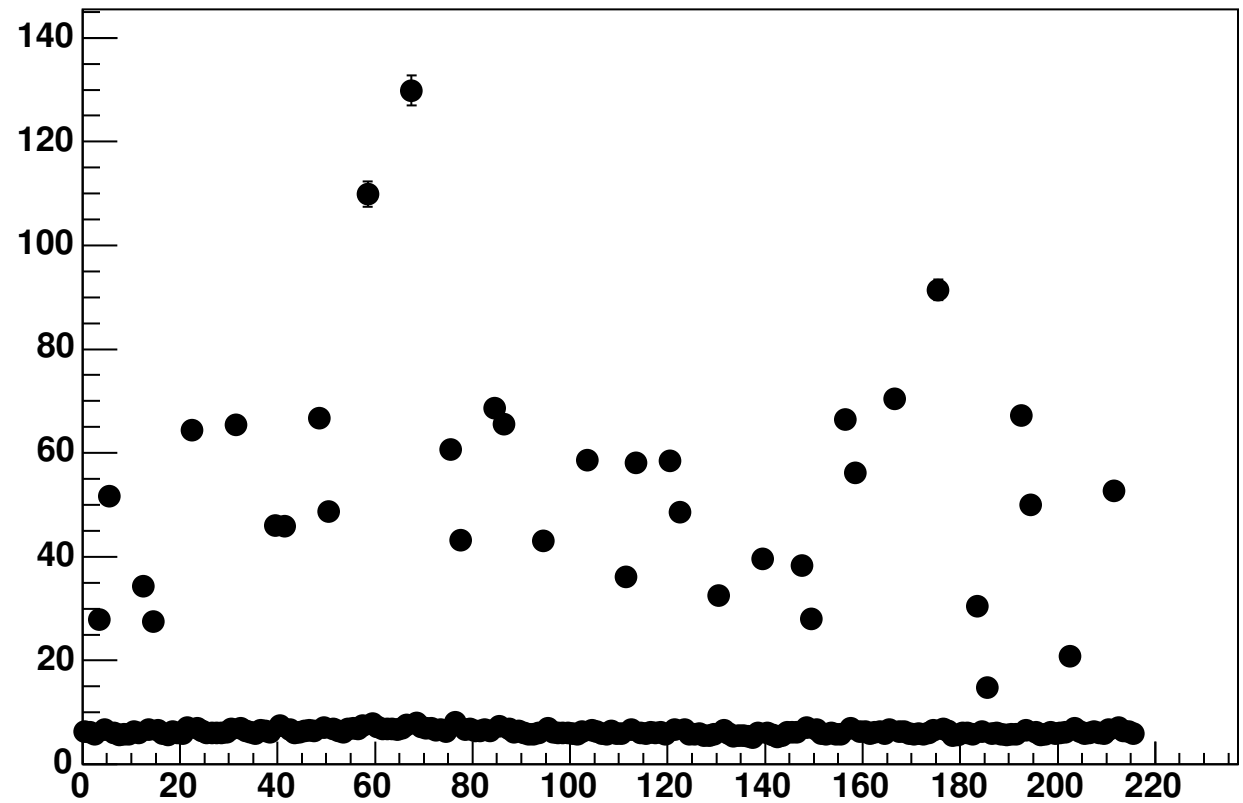
Enable 1, Hold=30, DAC=53248, ADC Noise vs 18*Chip+Chan



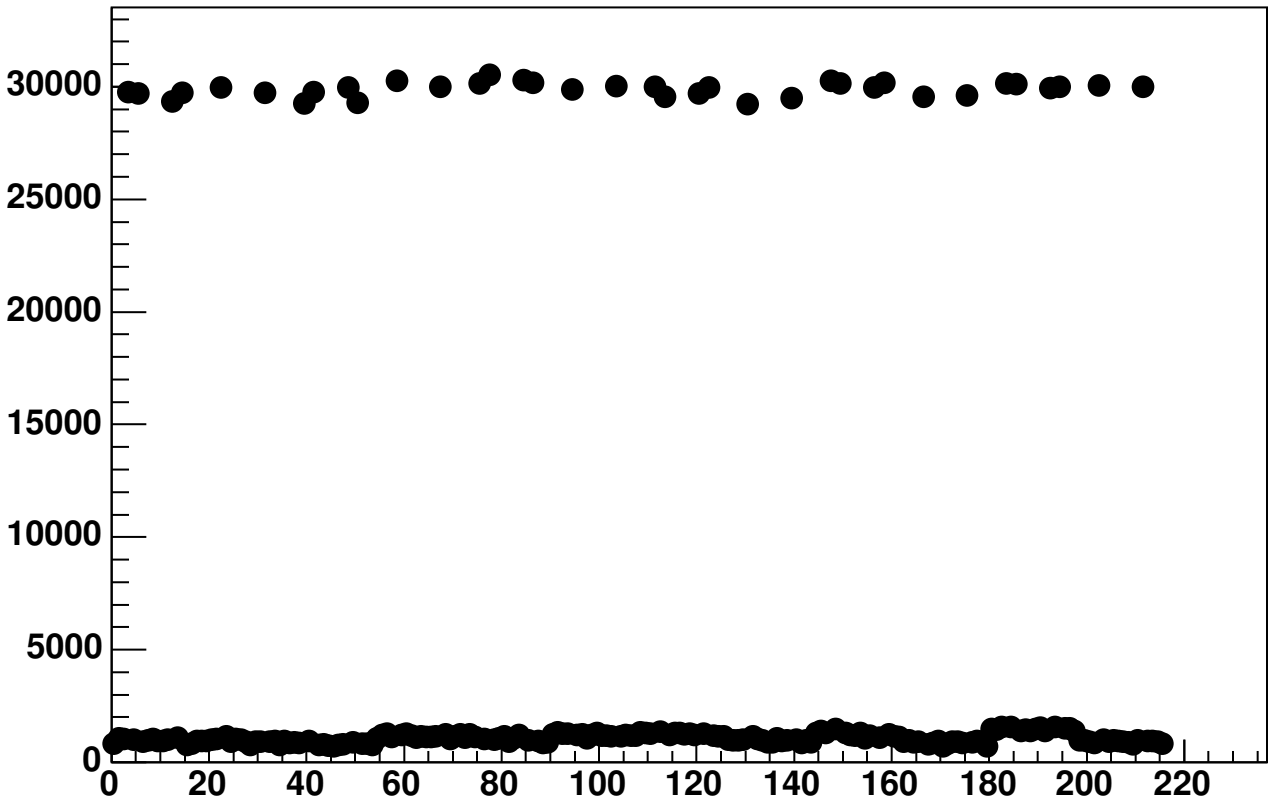
Enable 1, Hold=30, DAC=51200, ADC Mean vs 18*Chip+Chan



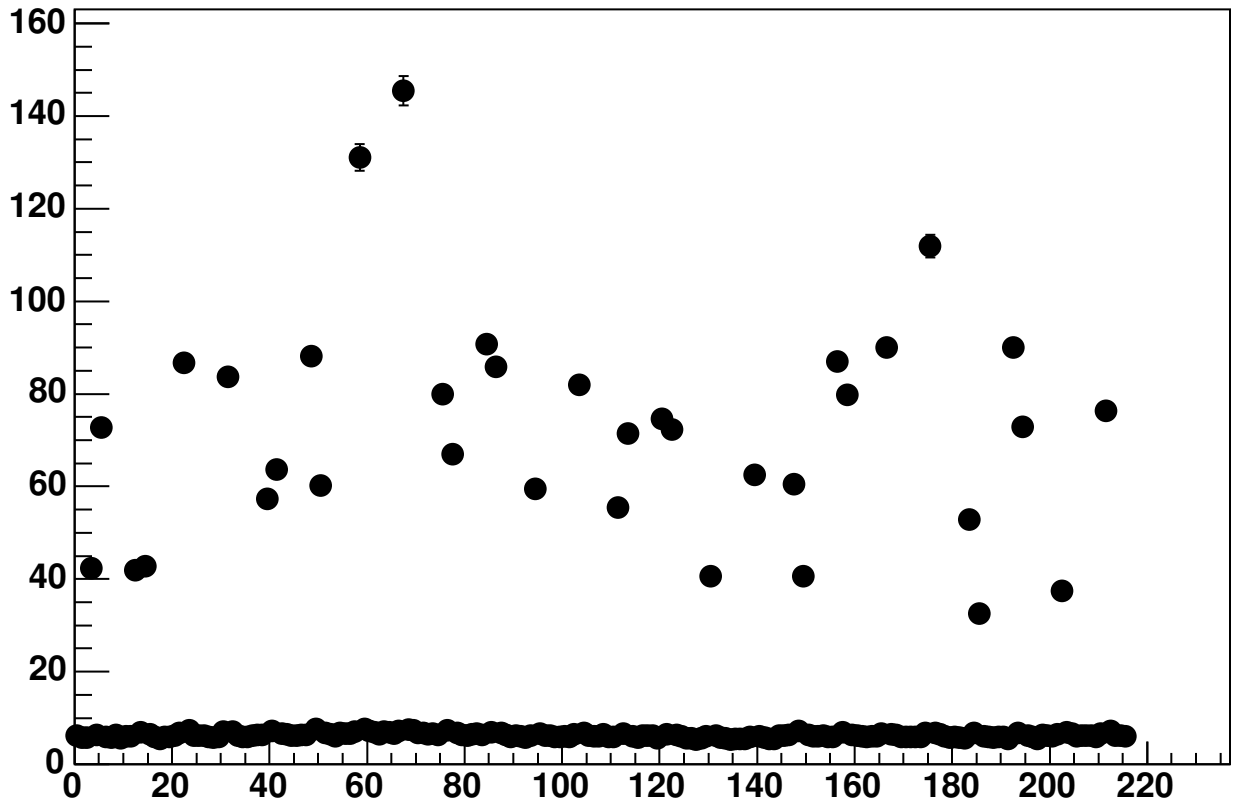
Enable 1, Hold=30, DAC=51200, ADC Noise vs 18*Chip+Chan



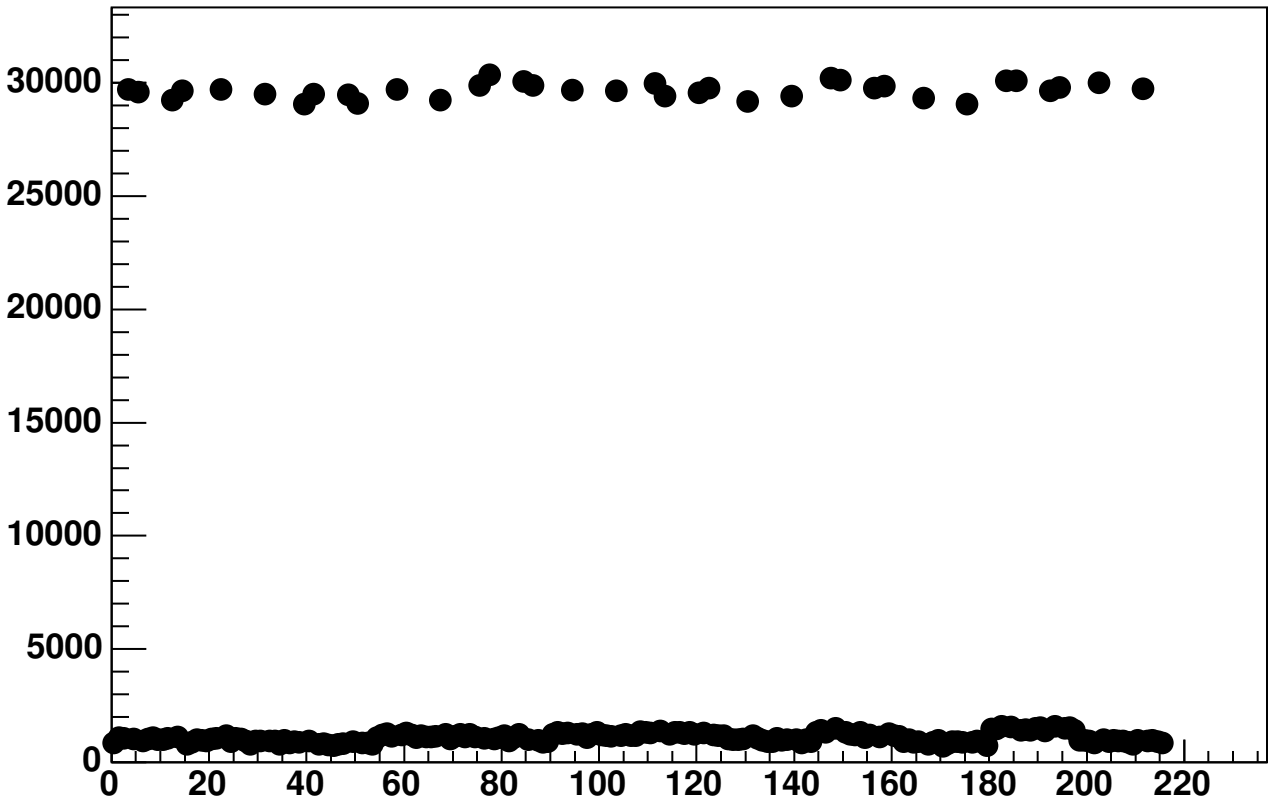
Enable 1, Hold=30, DAC=49152, ADC Mean vs 18*Chip+Chan



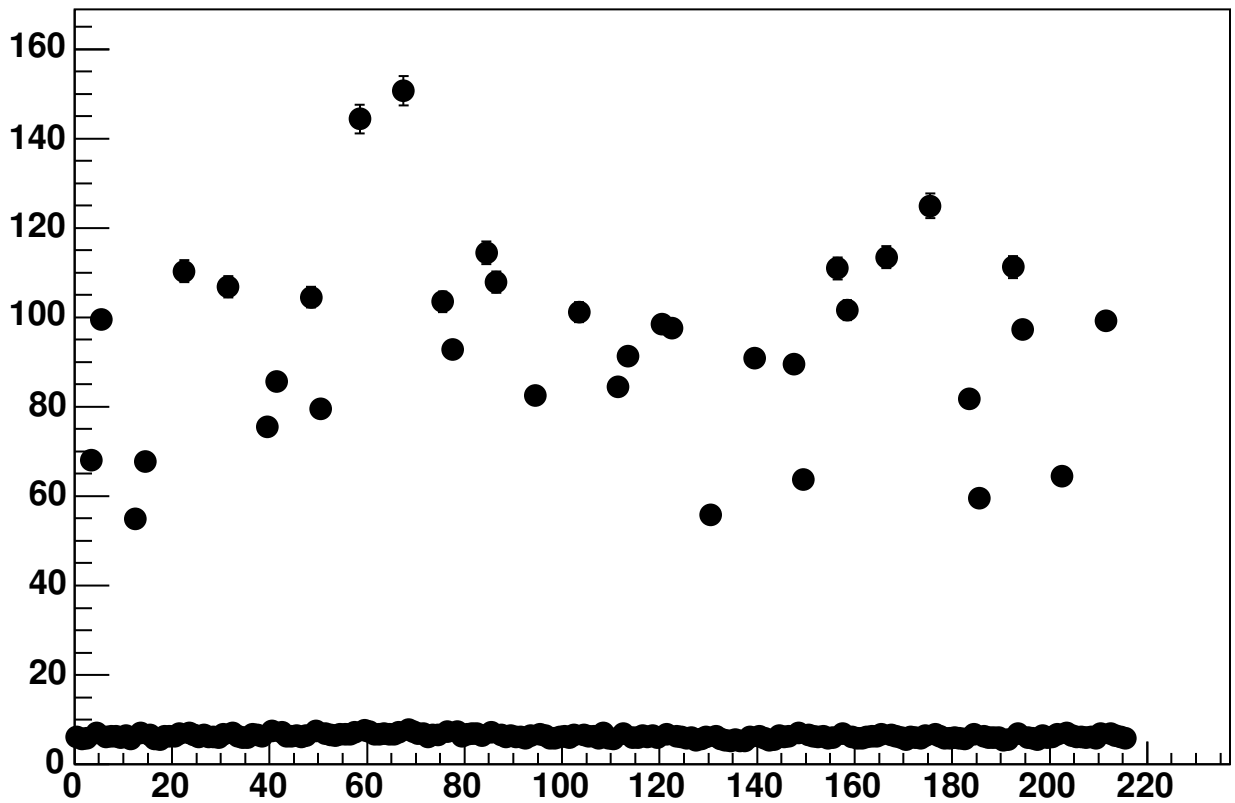
Enable 1, Hold=30, DAC=49152, ADC Noise vs 18*Chip+Chan



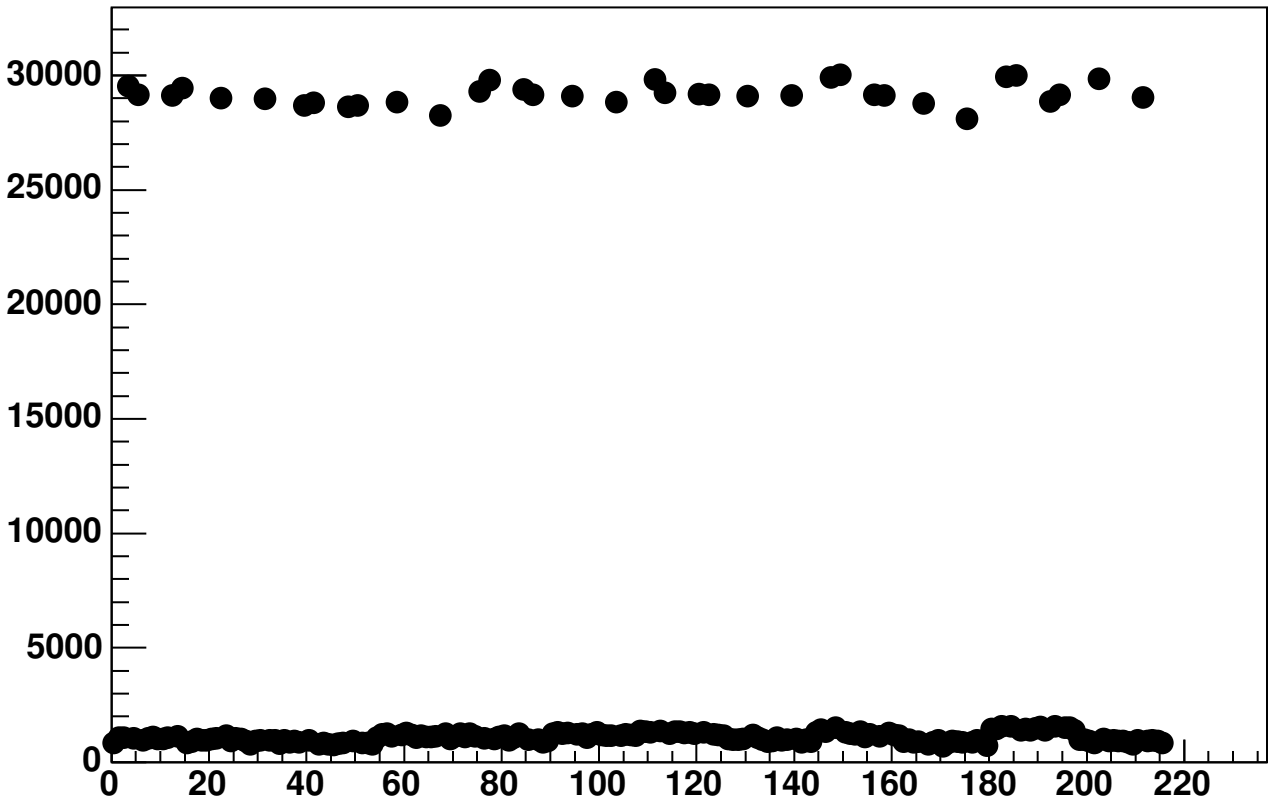
Enable 1, Hold=30, DAC=47104, ADC Mean vs 18*Chip+Chan



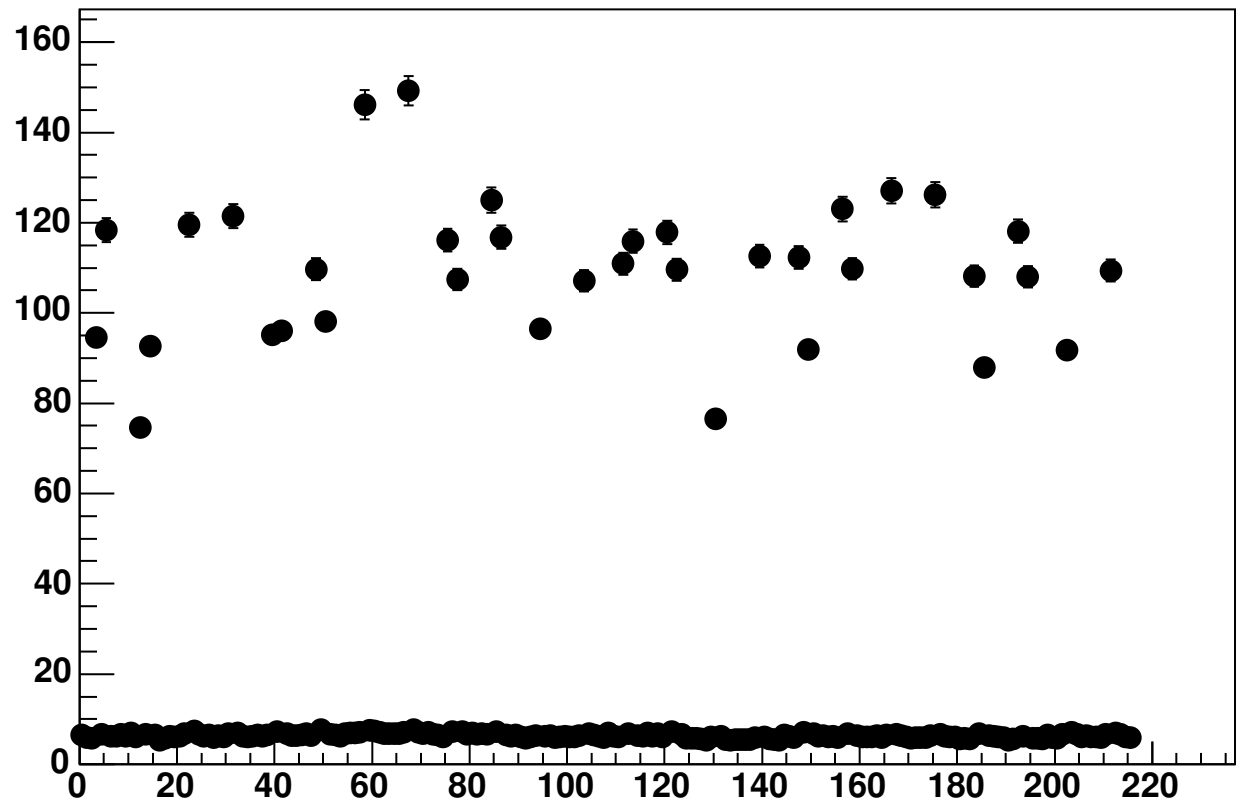
Enable 1, Hold=30, DAC=47104, ADC Noise vs 18*Chip+Chan



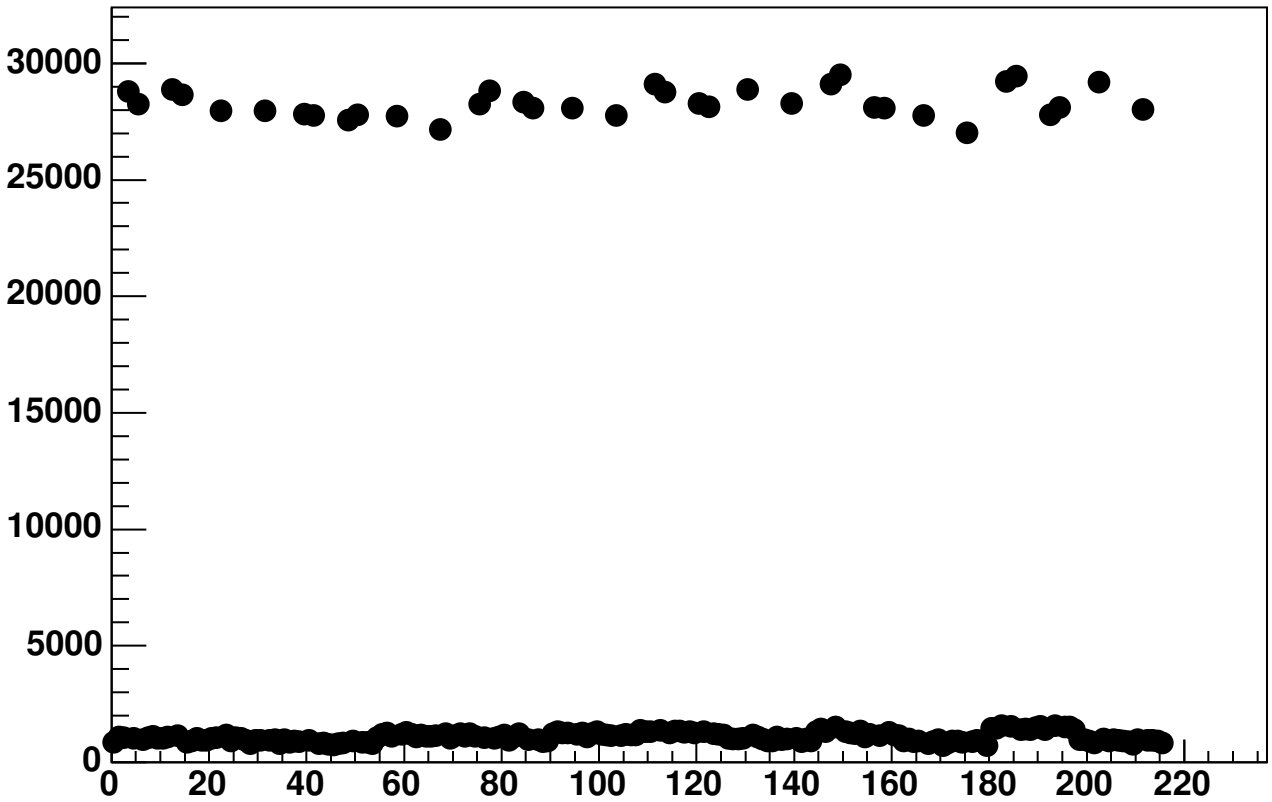
Enable 1, Hold=30, DAC=45056, ADC Mean vs 18*Chip+Chan



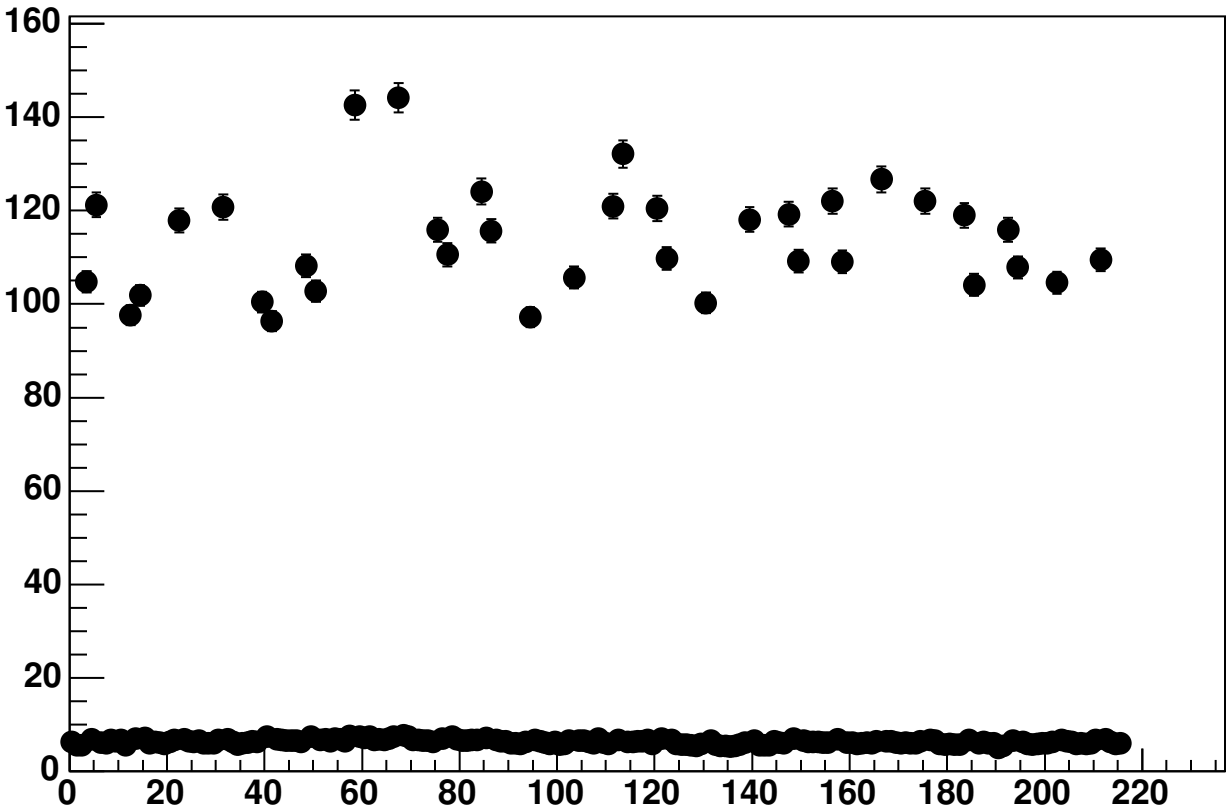
Enable 1, Hold=30, DAC=45056, ADC Noise vs 18*Chip+Chan



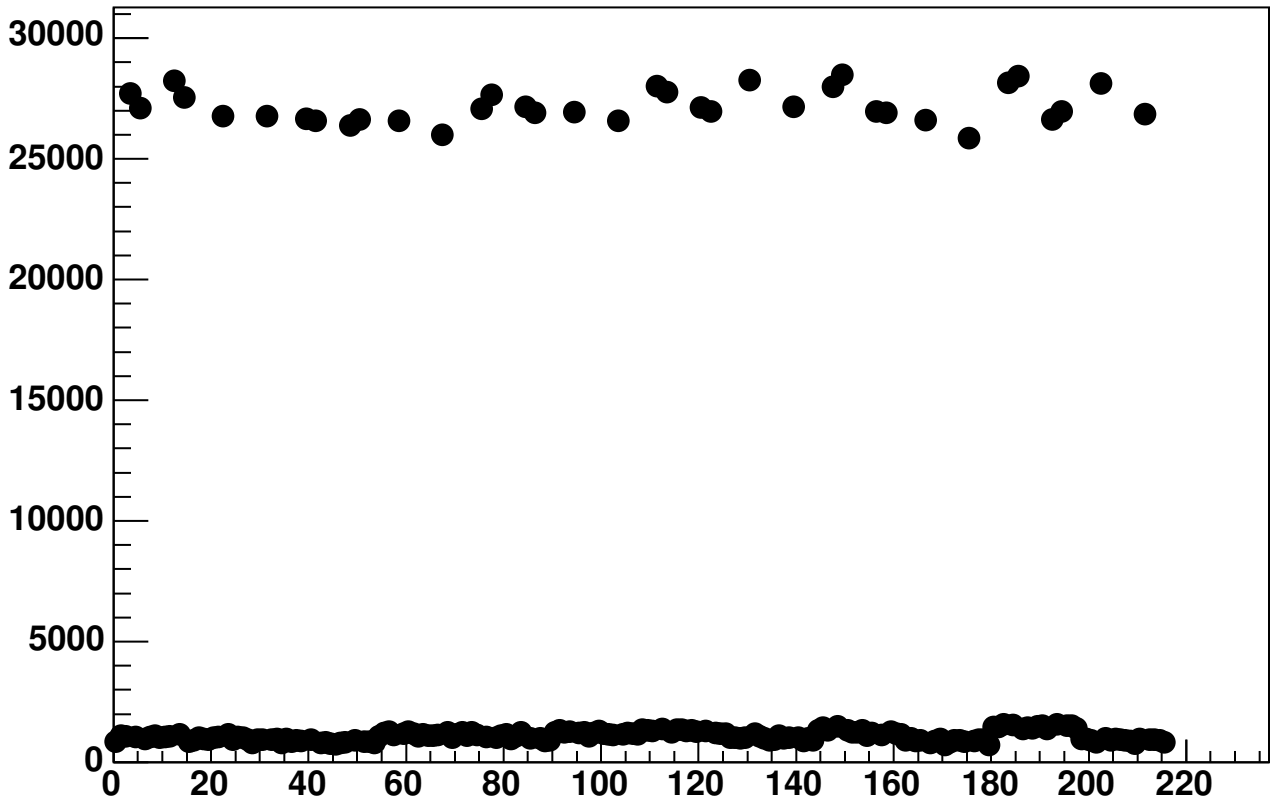
Enable 1, Hold=30, DAC=43008, ADC Mean vs 18*Chip+Chan



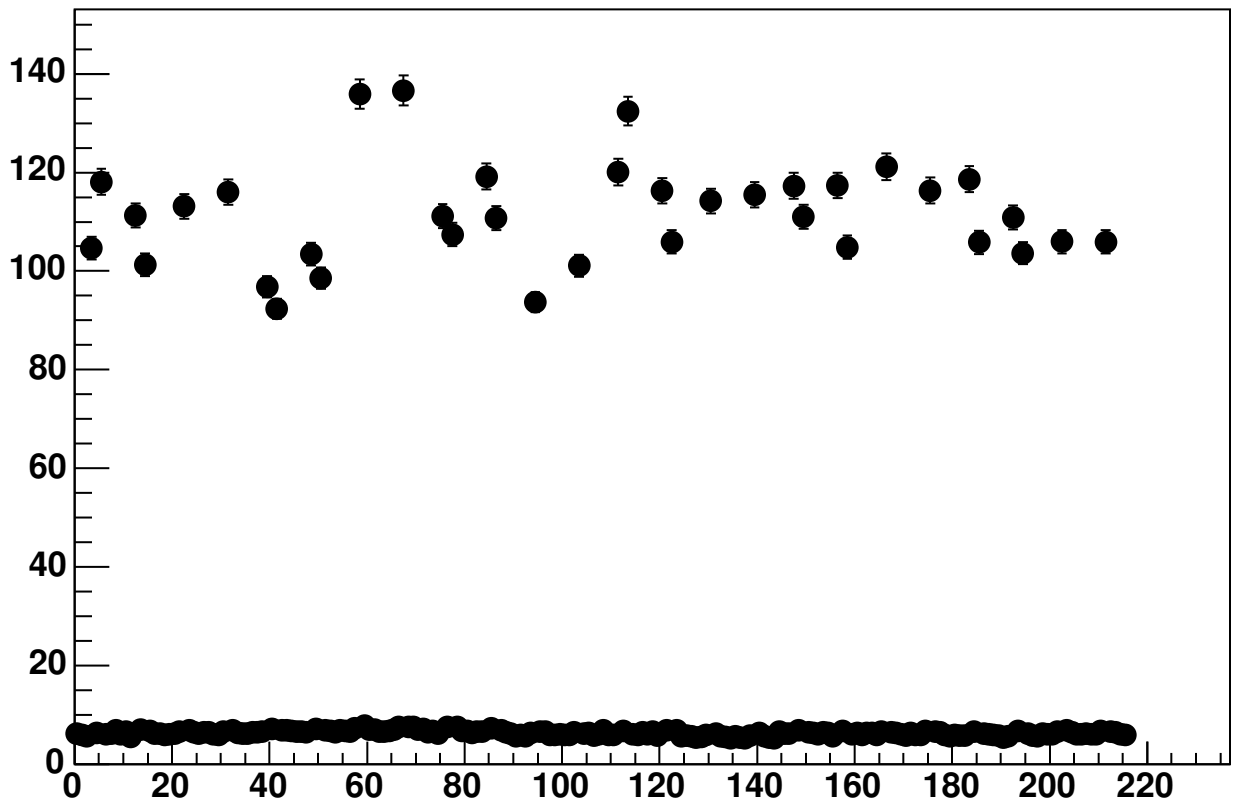
Enable 1, Hold=30, DAC=43008, ADC Noise vs 18*Chip+Chan



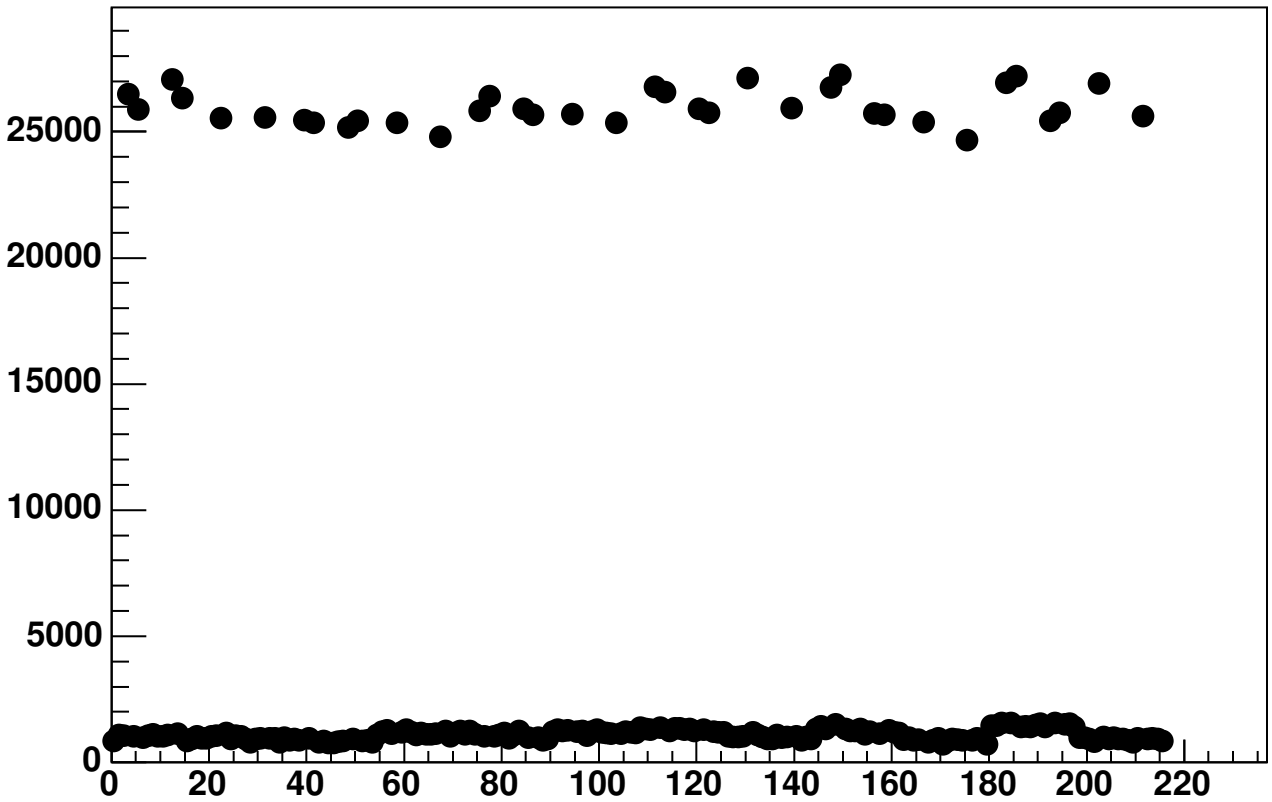
Enable 1, Hold=30, DAC=40960, ADC Mean vs 18*Chip+Chan



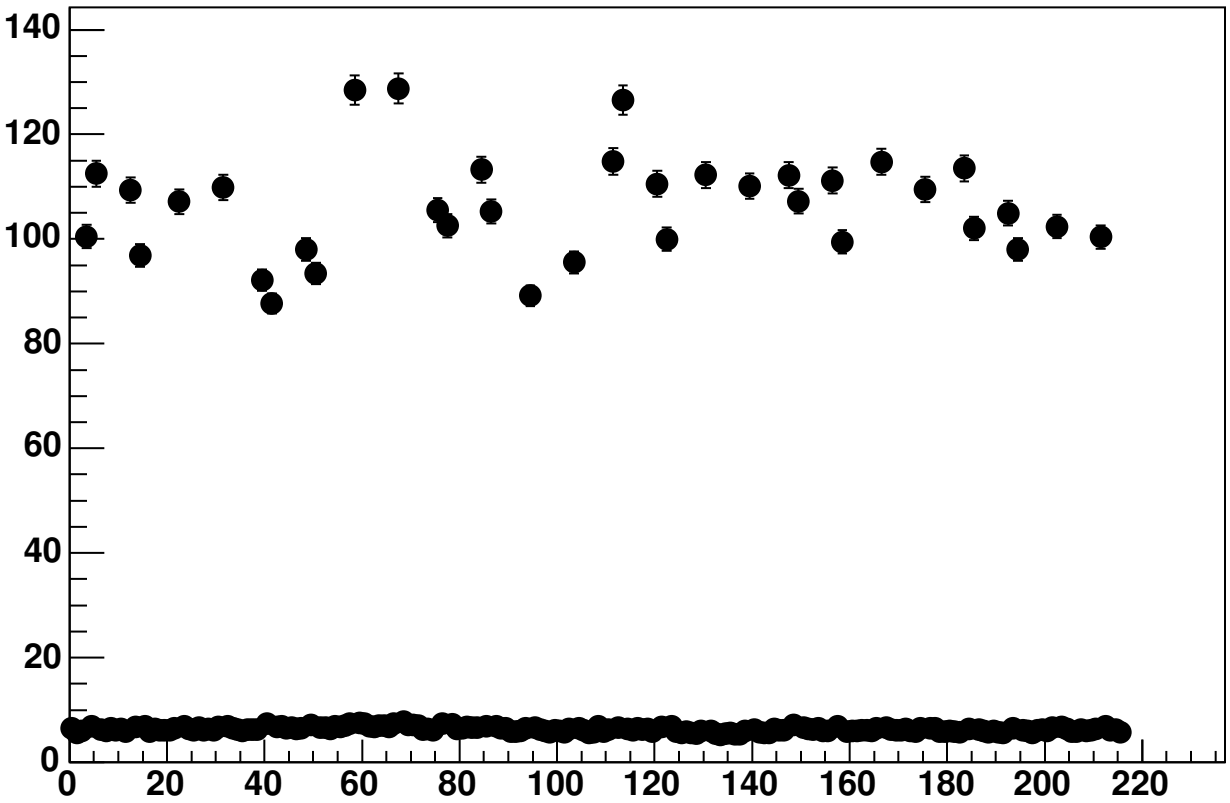
Enable 1, Hold=30, DAC=40960, ADC Noise vs 18*Chip+Chan



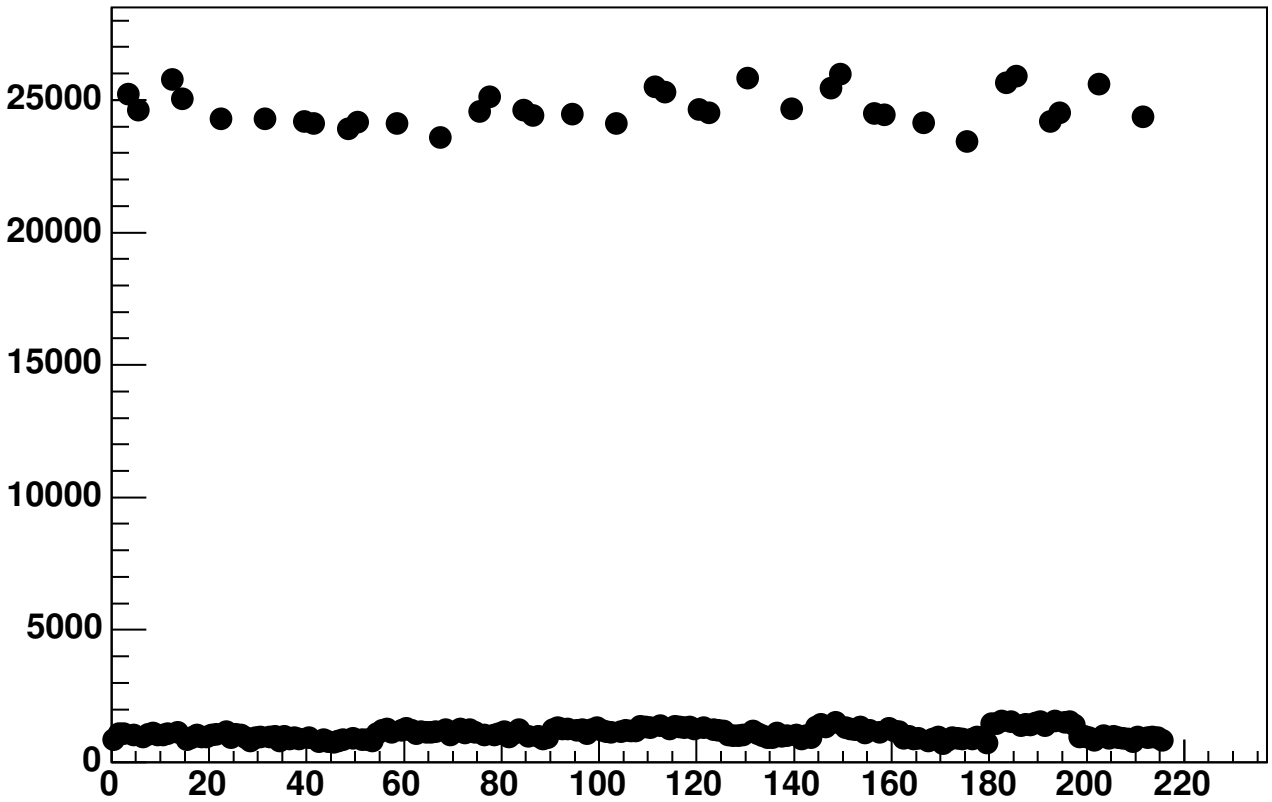
Enable 1, Hold=30, DAC=38912, ADC Mean vs 18*Chip+Chan



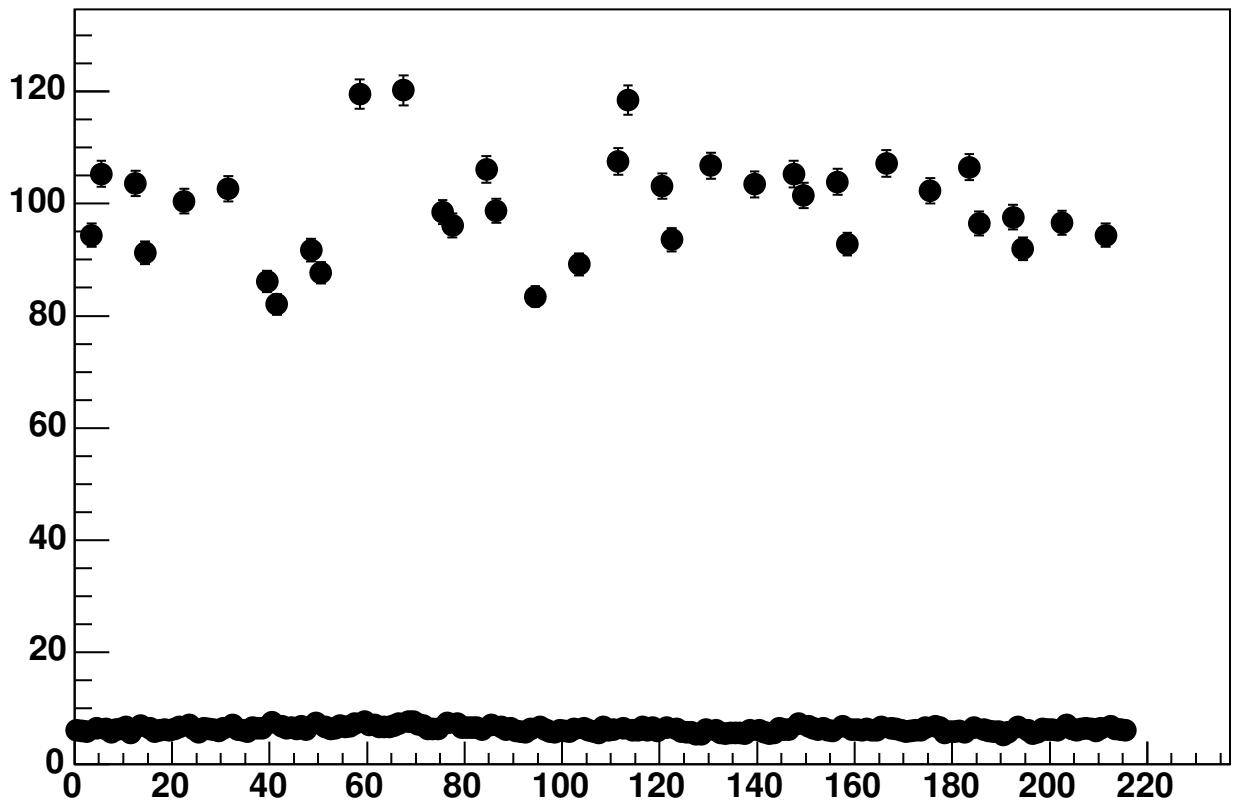
Enable 1, Hold=30, DAC=38912, ADC Noise vs 18*Chip+Chan



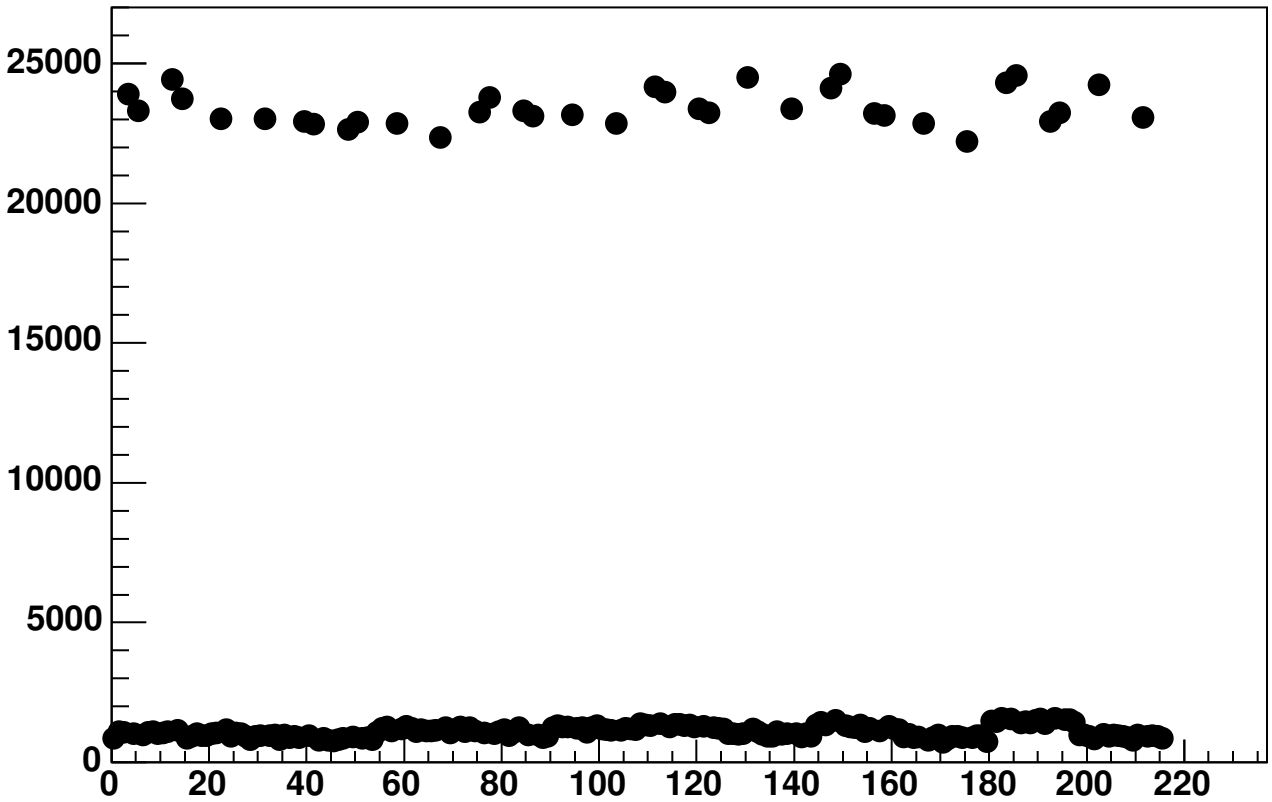
Enable 1, Hold=30, DAC=36864, ADC Mean vs 18*Chip+Chan



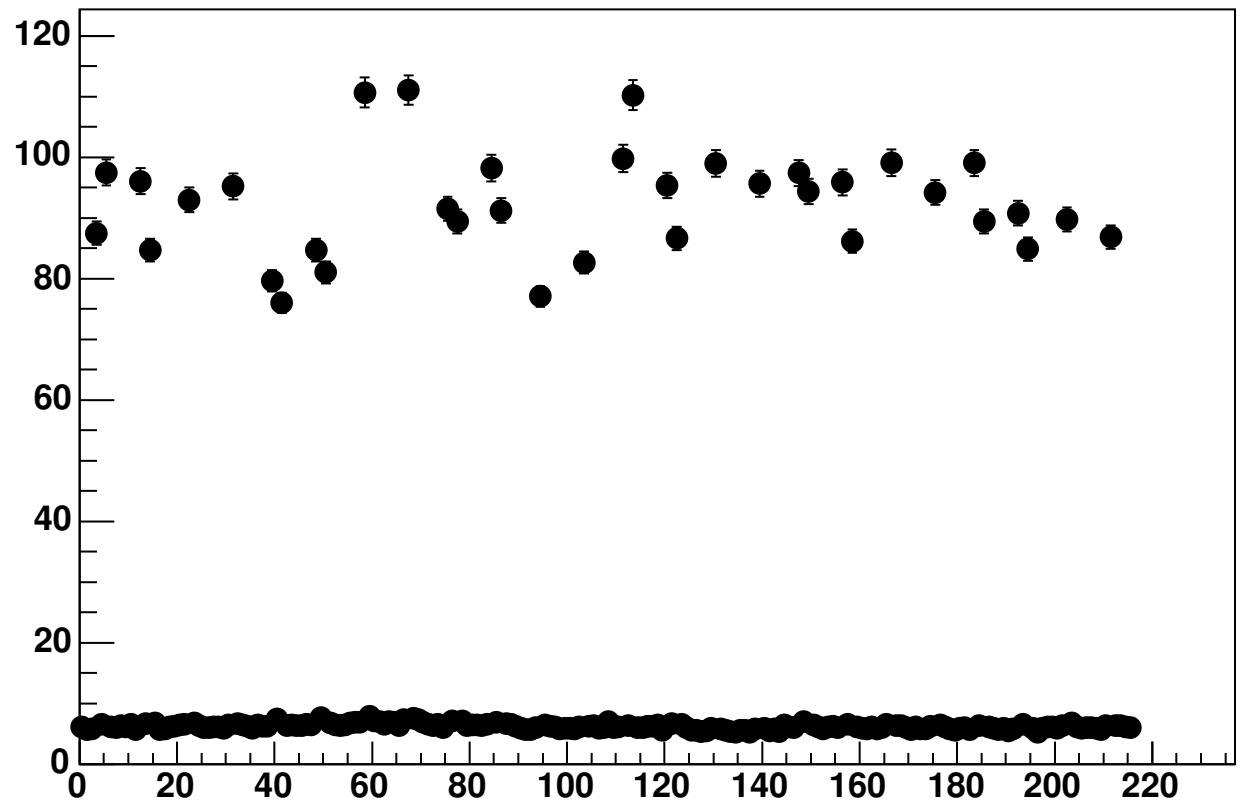
Enable 1, Hold=30, DAC=36864, ADC Noise vs 18*Chip+Chan



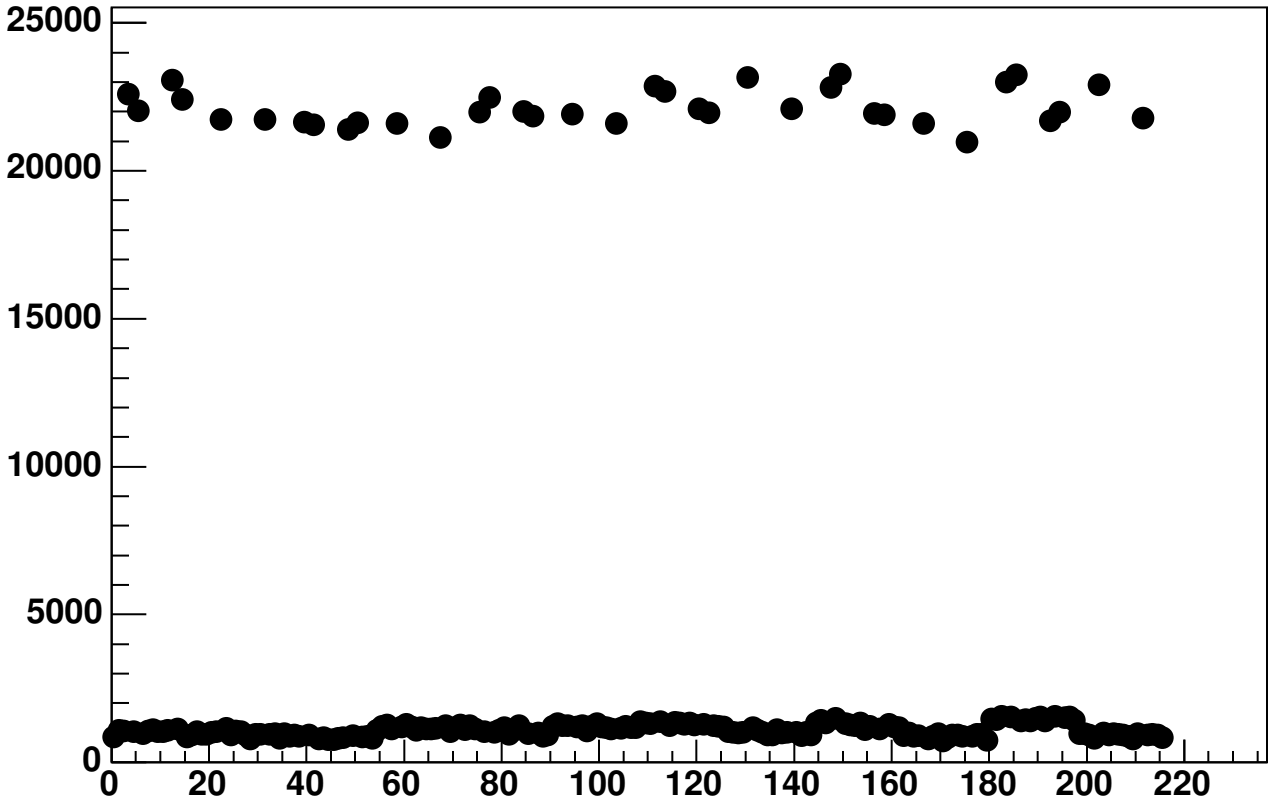
Enable 1, Hold=30, DAC=34816, ADC Mean vs 18*Chip+Chan



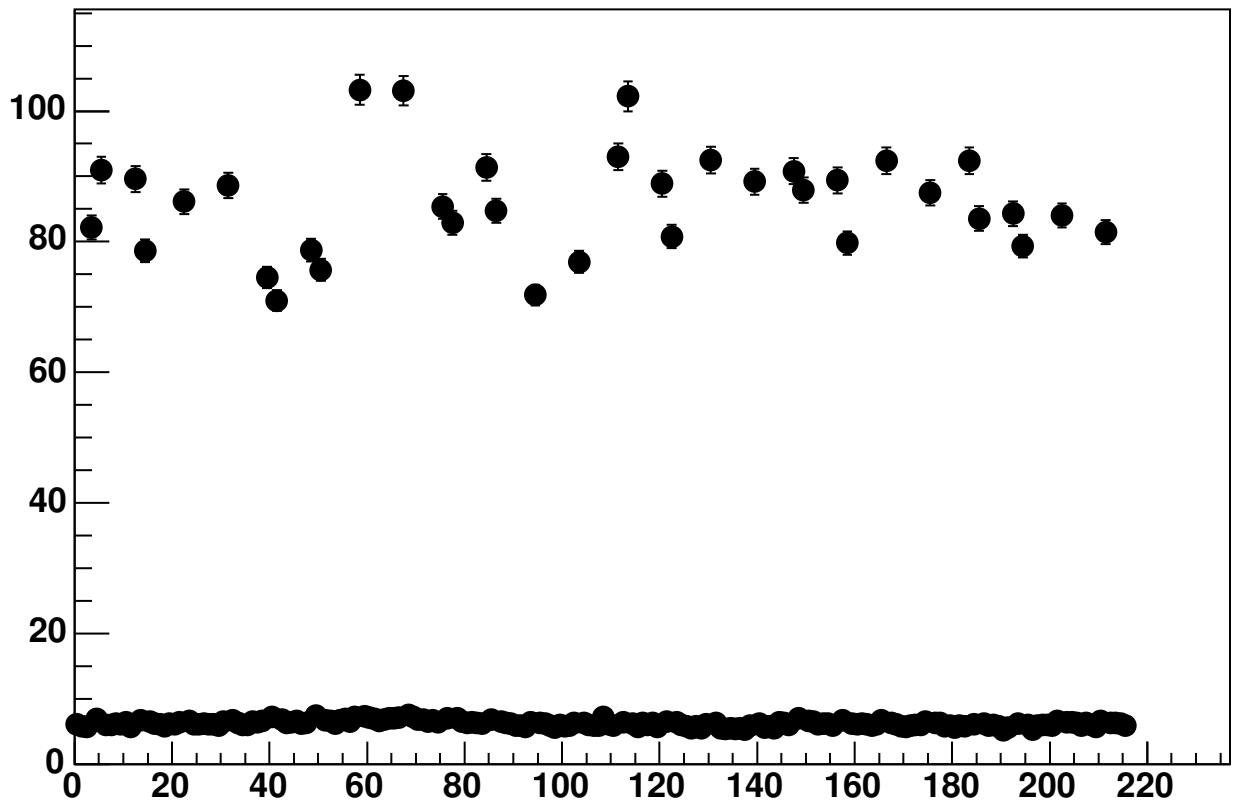
Enable 1, Hold=30, DAC=34816, ADC Noise vs 18*Chip+Chan



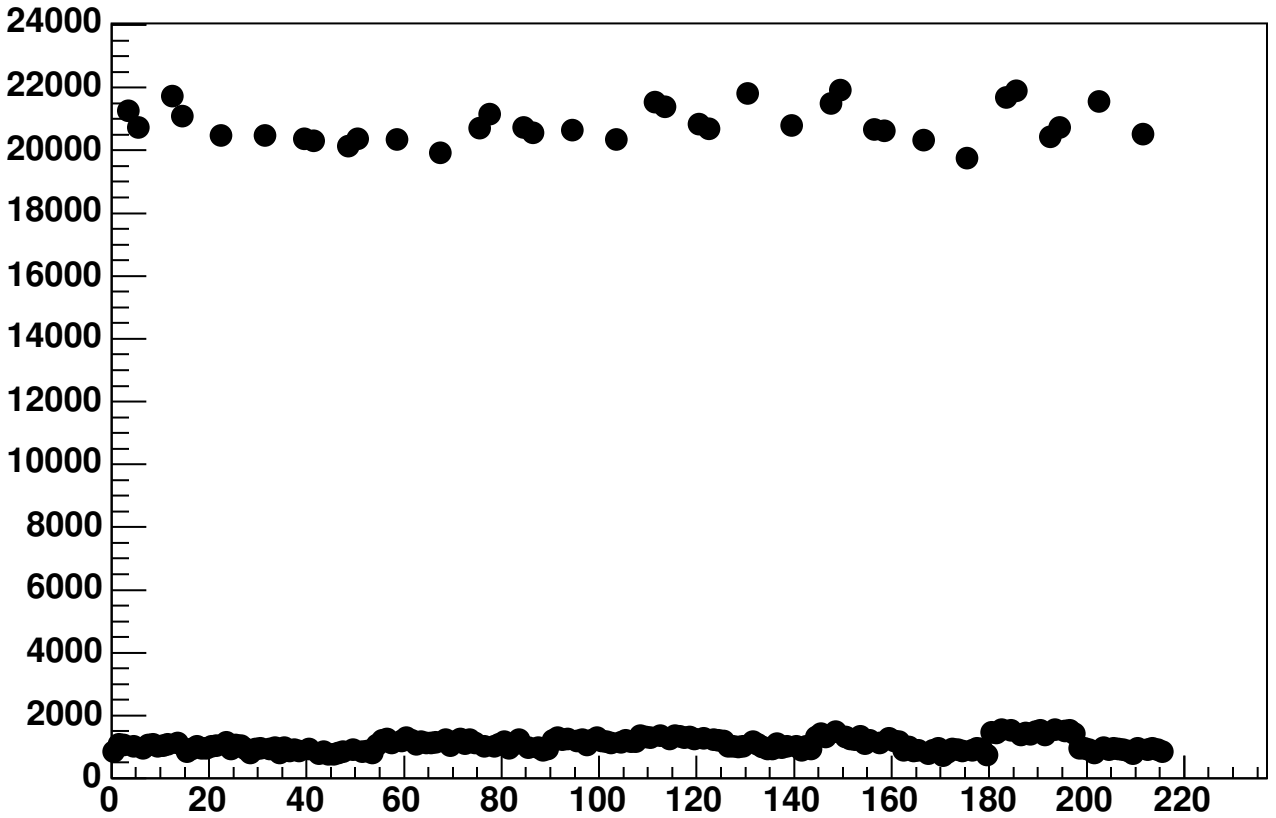
Enable 1, Hold=30, DAC=32768, ADC Mean vs 18*Chip+Chan



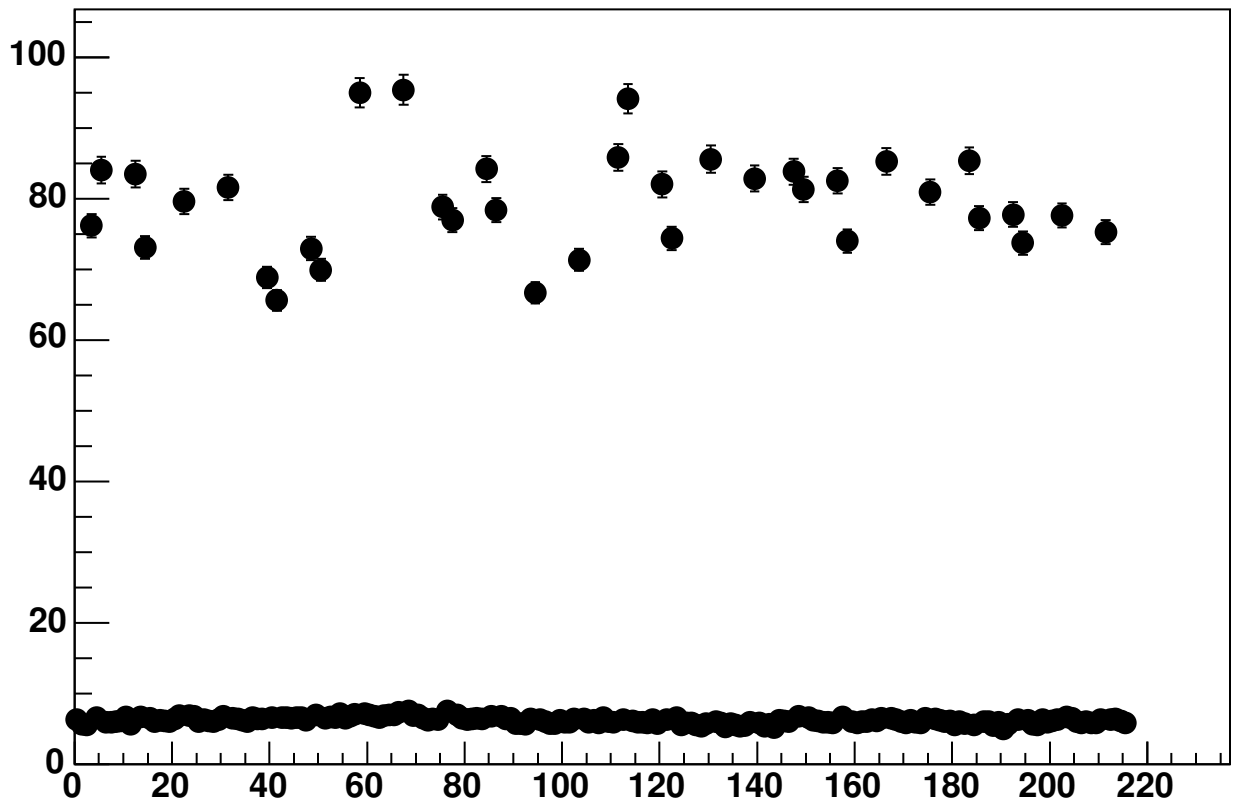
Enable 1, Hold=30, DAC=32768, ADC Noise vs 18*Chip+Chan



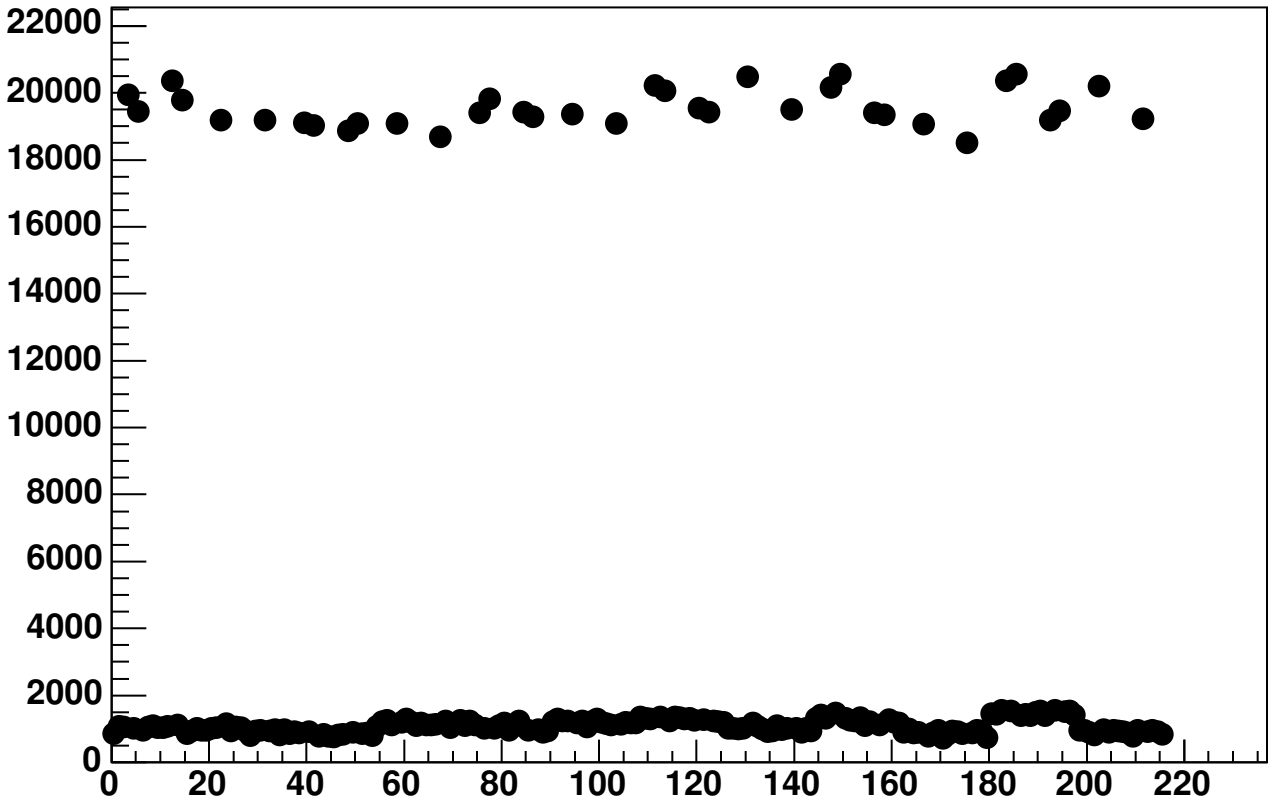
Enable 1, Hold=30, DAC=30720, ADC Mean vs 18*Chip+Chan



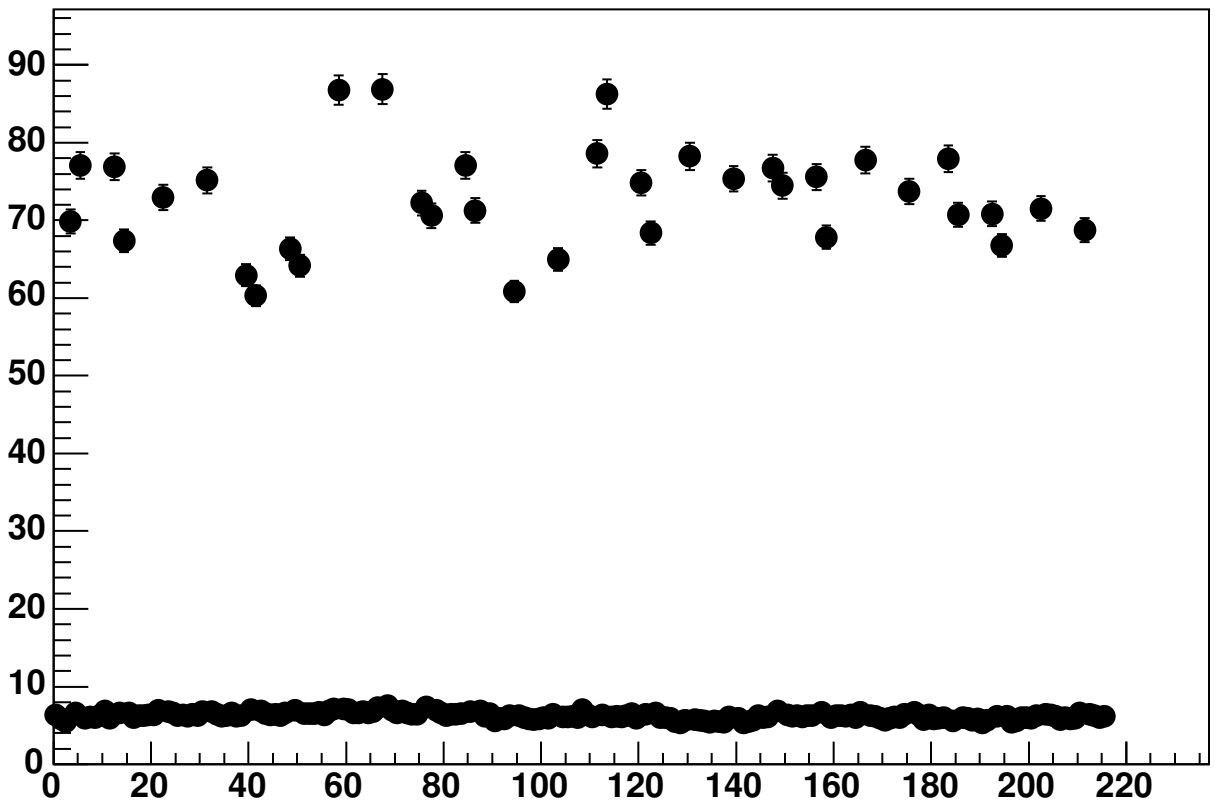
Enable 1, Hold=30, DAC=30720, ADC Noise vs 18*Chip+Chan



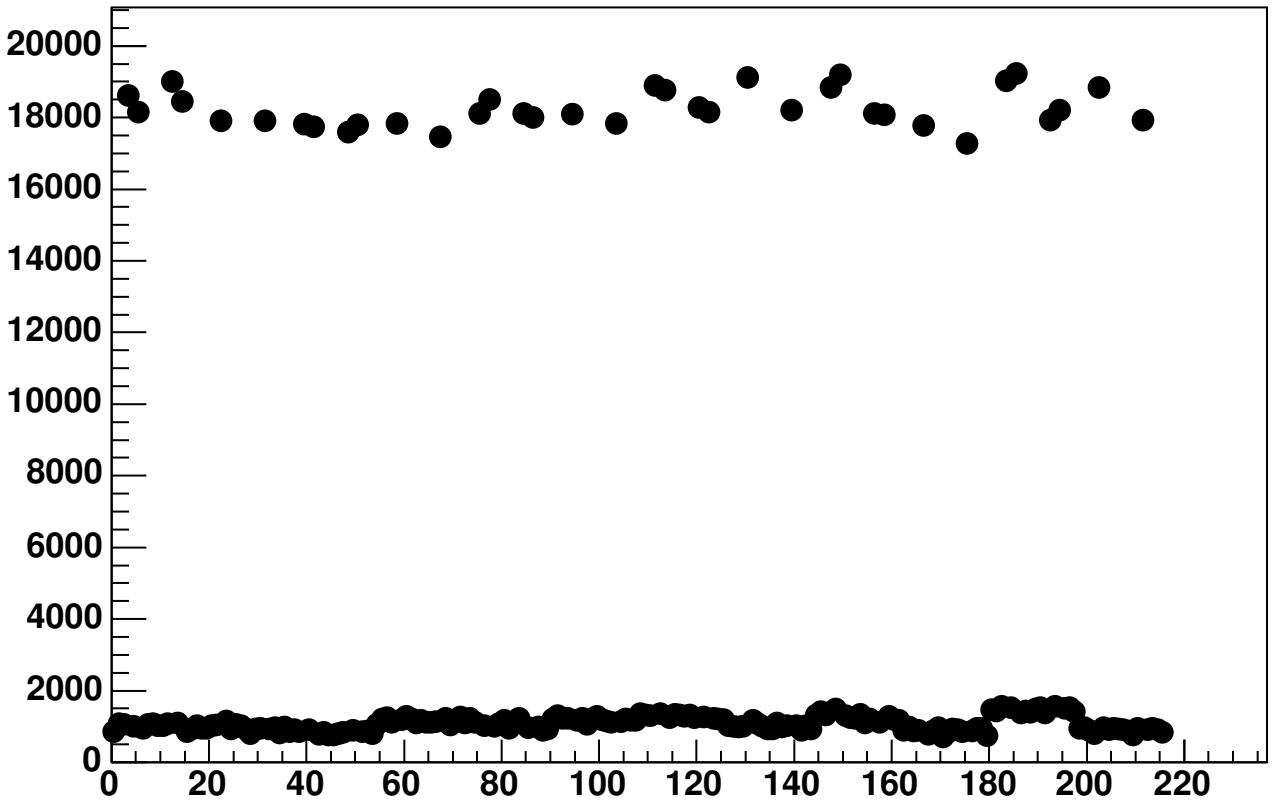
Enable 1, Hold=30, DAC=28672, ADC Mean vs 18*Chip+Chan



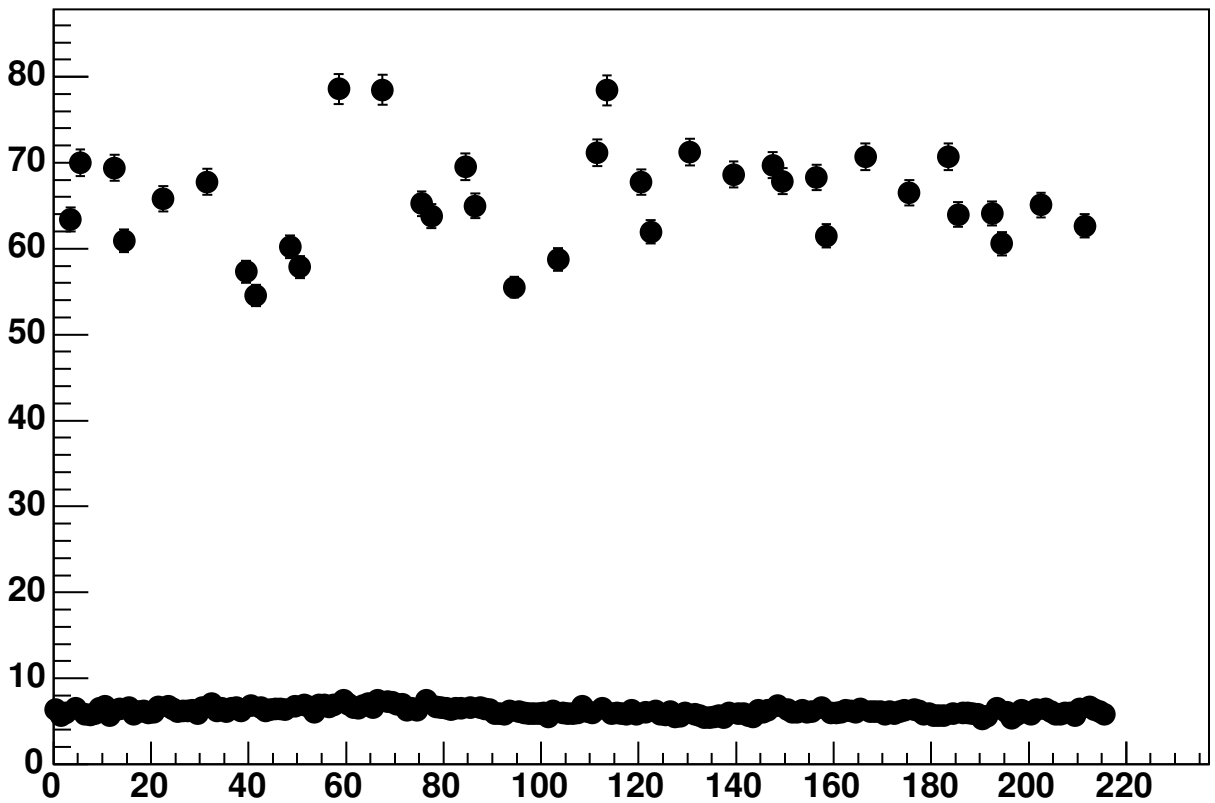
Enable 1, Hold=30, DAC=28672, ADC Noise vs 18*Chip+Chan



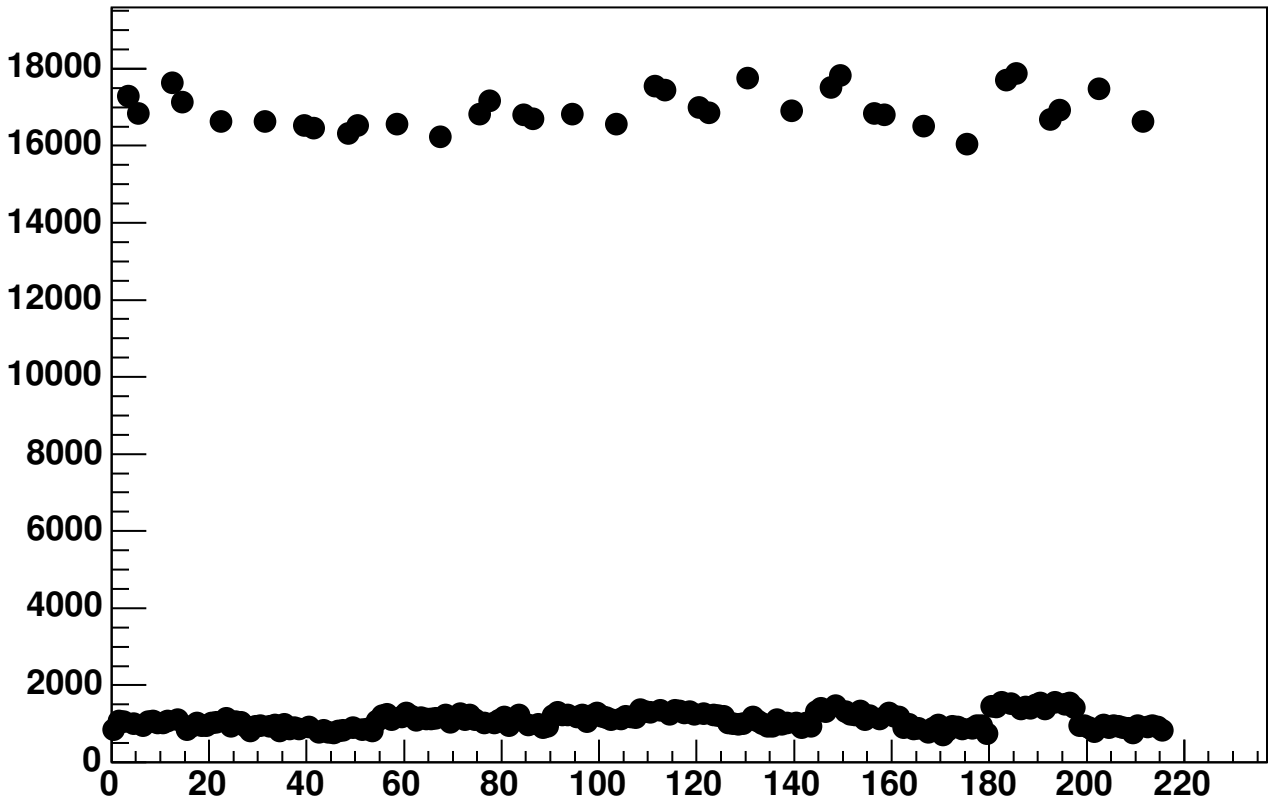
Enable 1, Hold=30, DAC=26624, ADC Mean vs 18*Chip+Chan



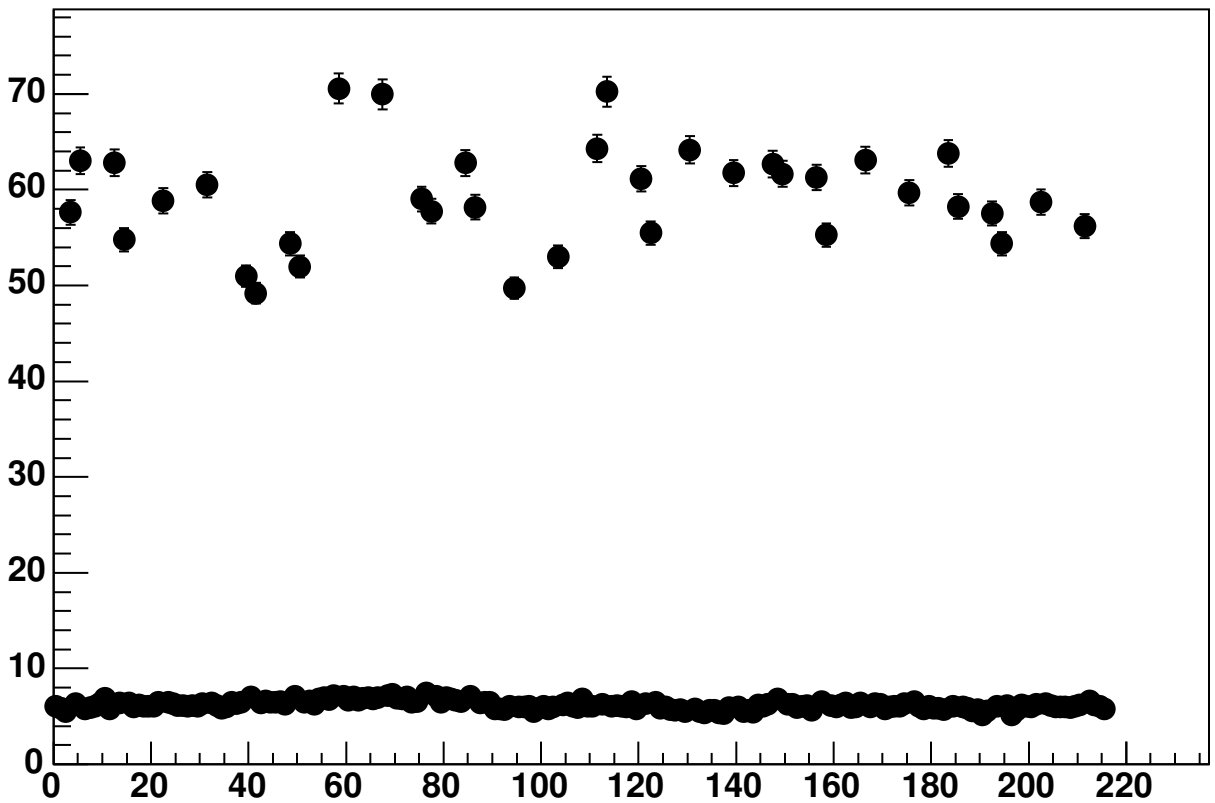
Enable 1, Hold=30, DAC=26624, ADC Noise vs 18*Chip+Chan



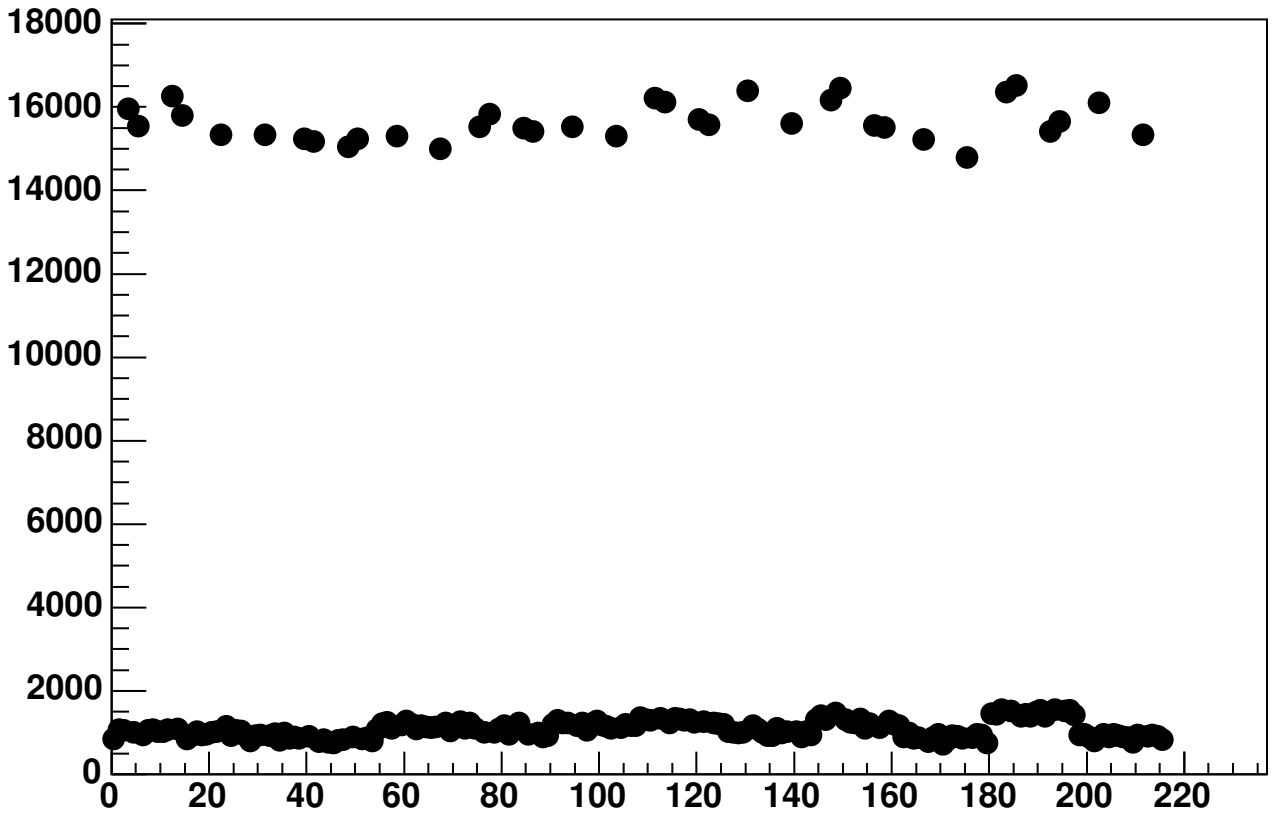
Enable 1, Hold=30, DAC=24576, ADC Mean vs 18*Chip+Chan



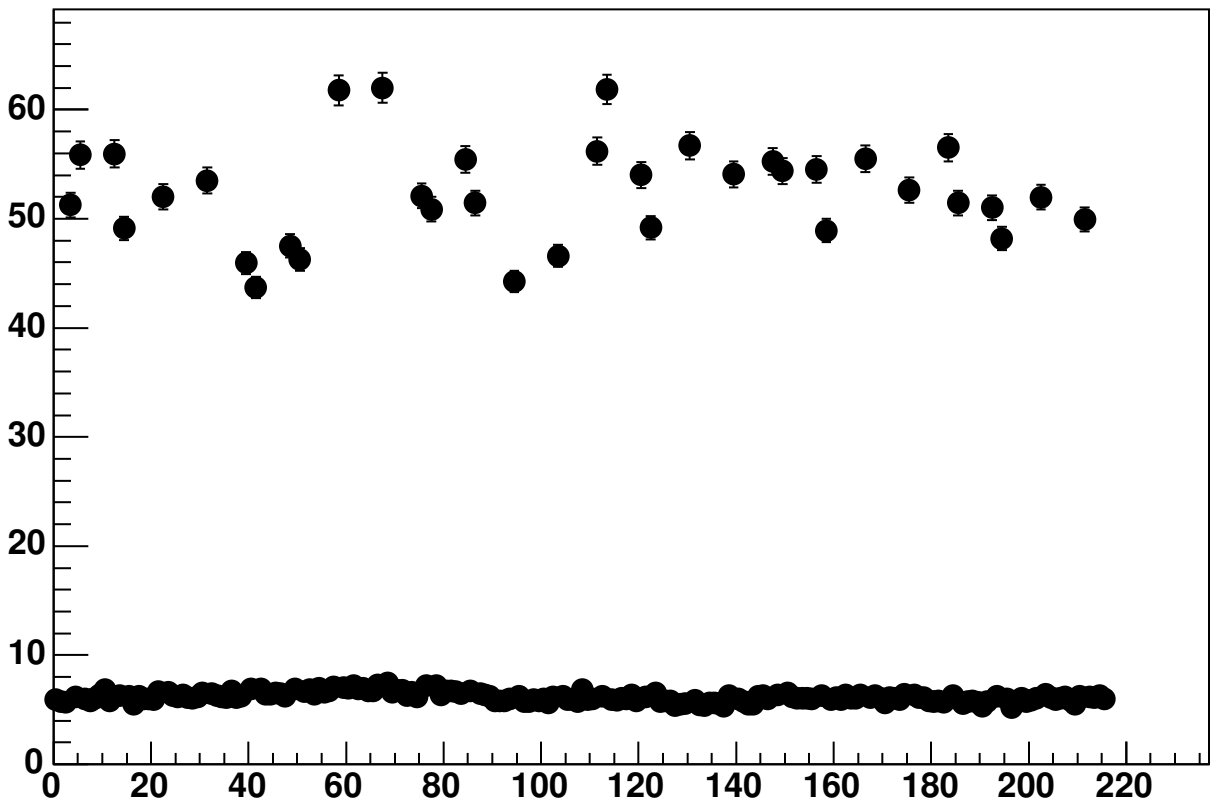
Enable 1, Hold=30, DAC=24576, ADC Noise vs 18*Chip+Chan



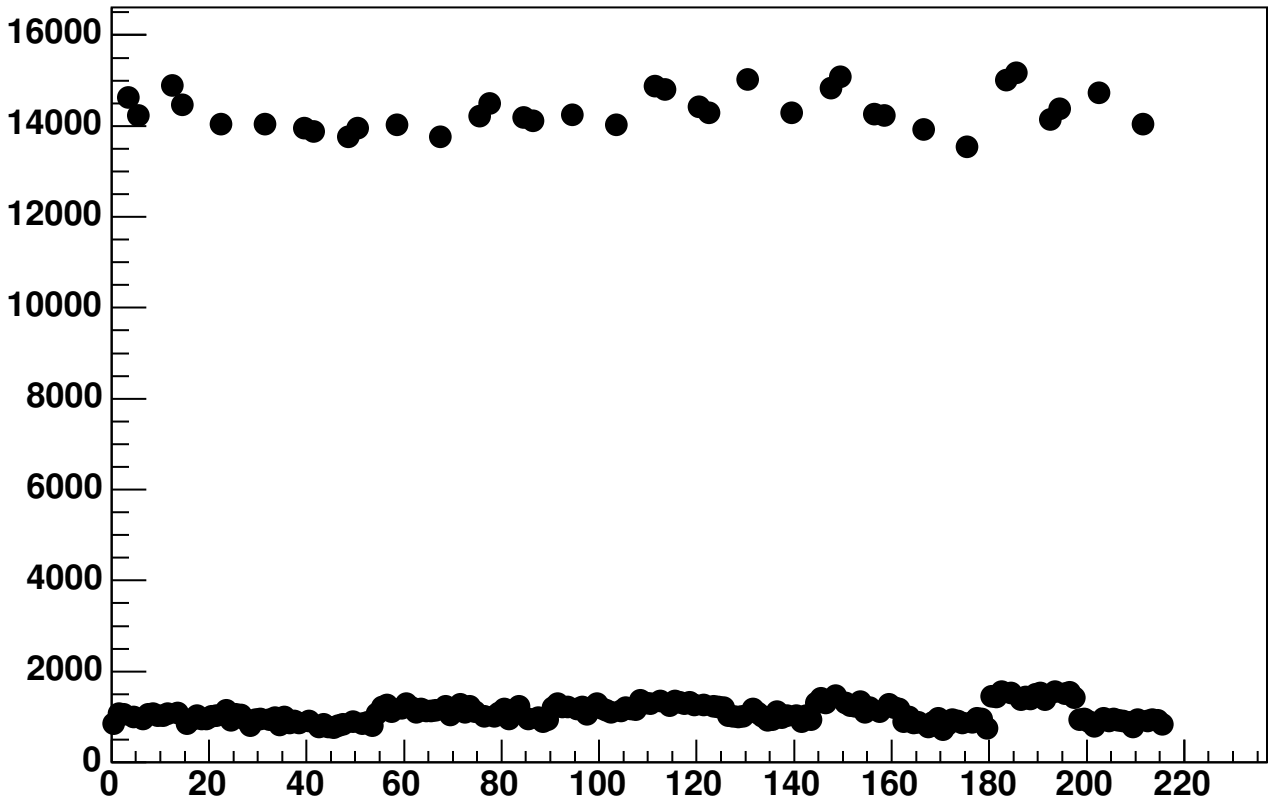
Enable 1, Hold=30, DAC=22528, ADC Mean vs 18*Chip+Chan



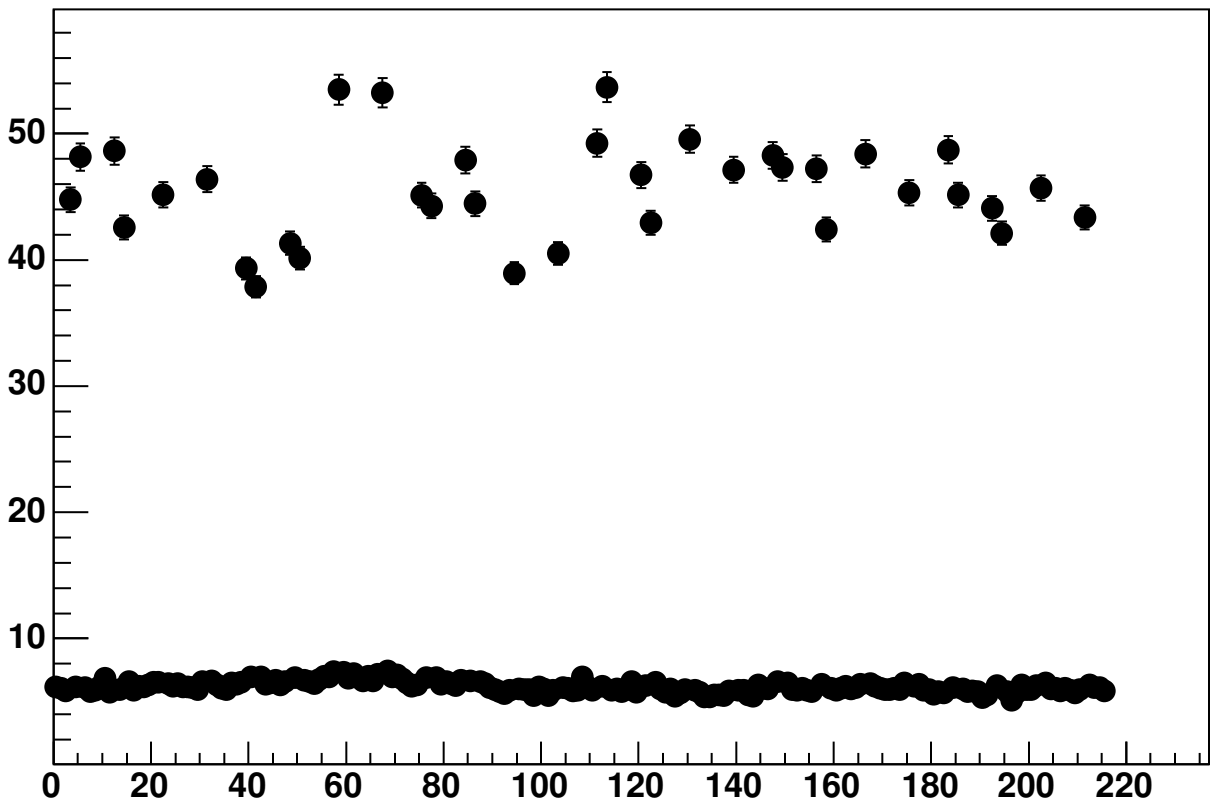
Enable 1, Hold=30, DAC=22528, ADC Noise vs 18*Chip+Chan



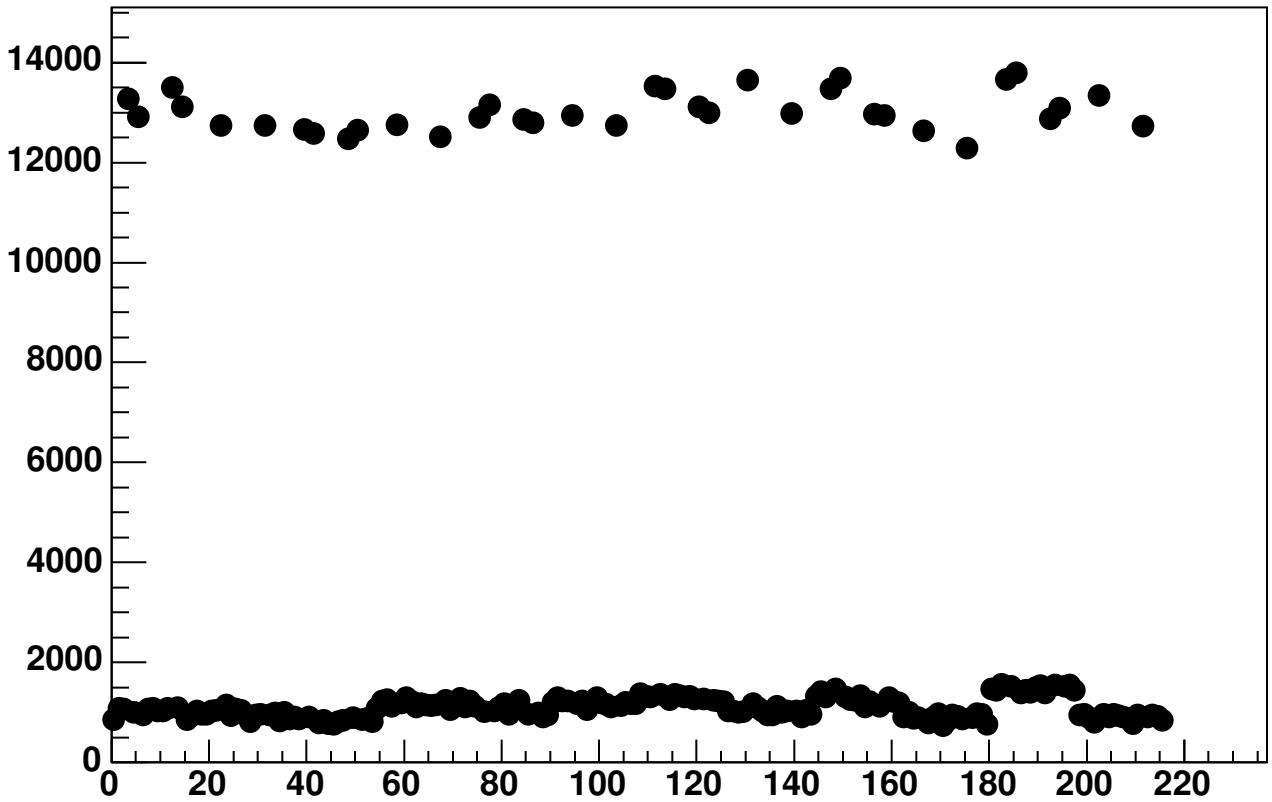
Enable 1, Hold=30, DAC=20480, ADC Mean vs 18*Chip+Chan



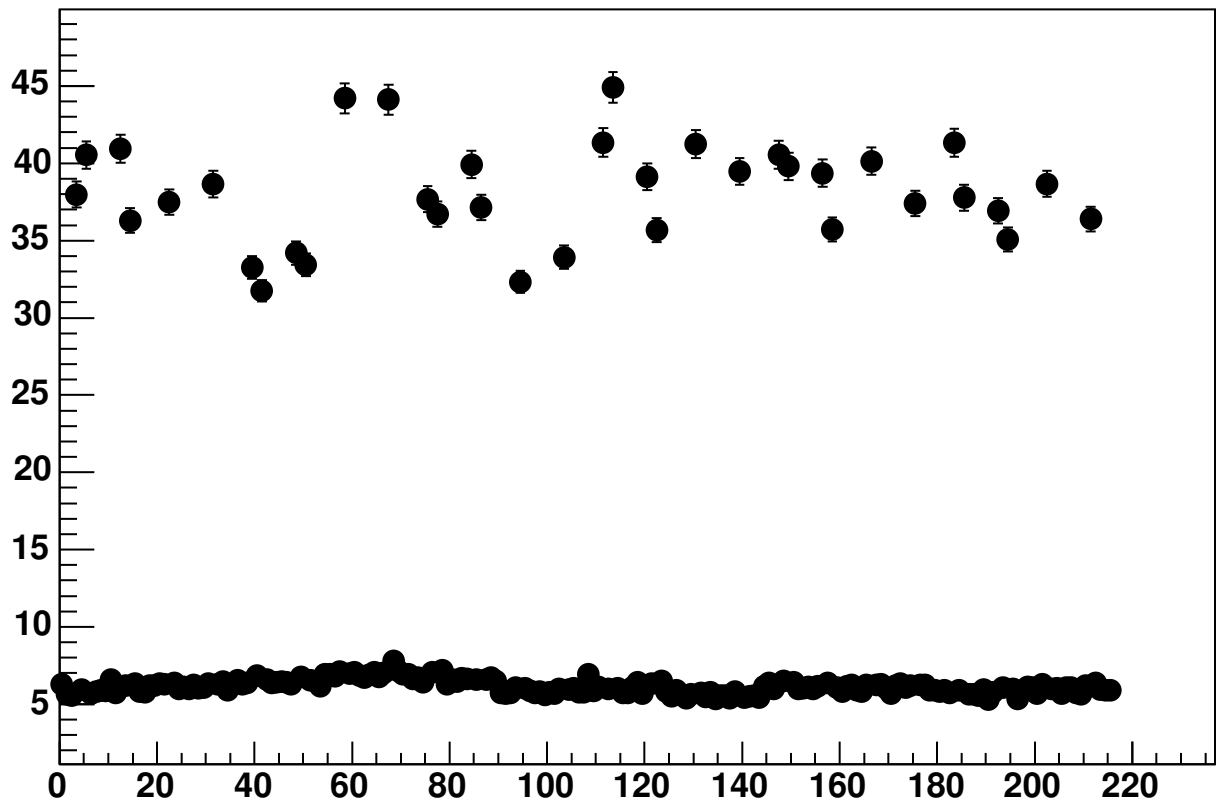
Enable 1, Hold=30, DAC=20480, ADC Noise vs 18*Chip+Chan



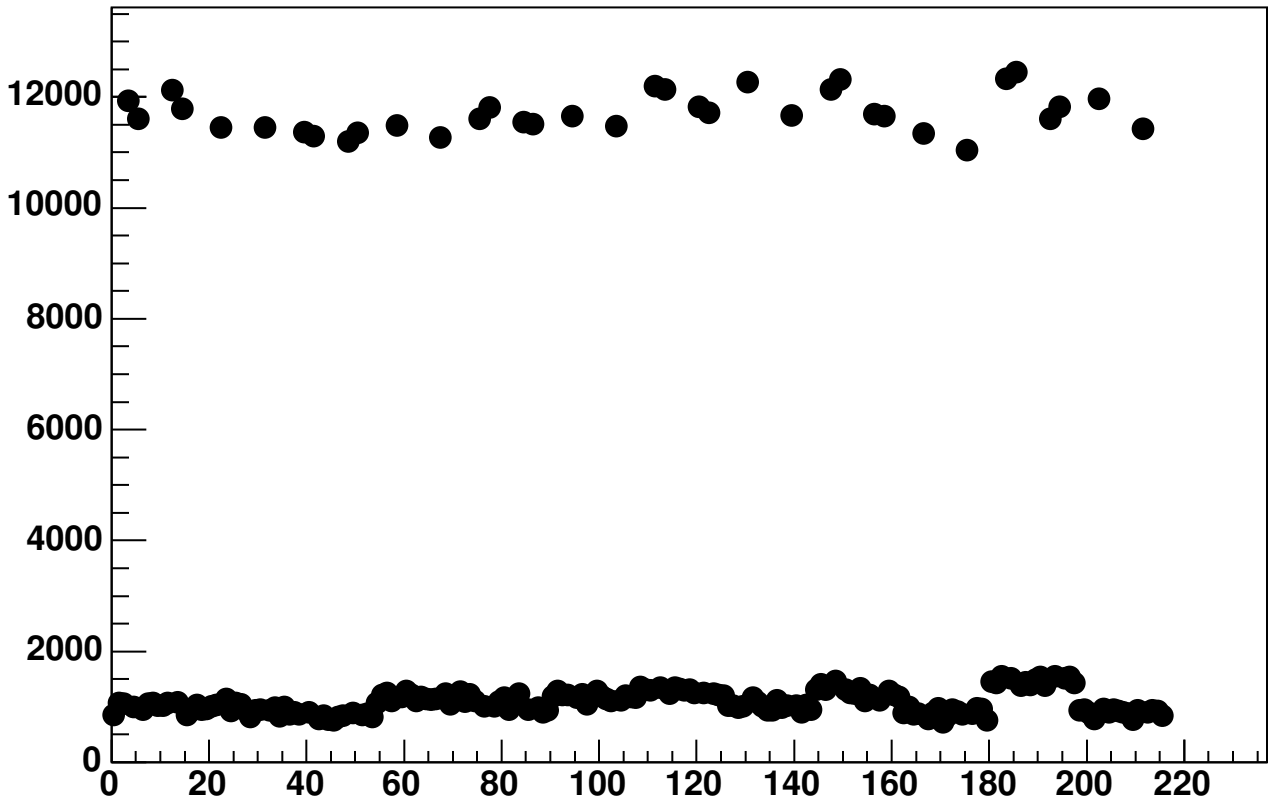
Enable 1, Hold=30, DAC=18432, ADC Mean vs 18*Chip+Chan



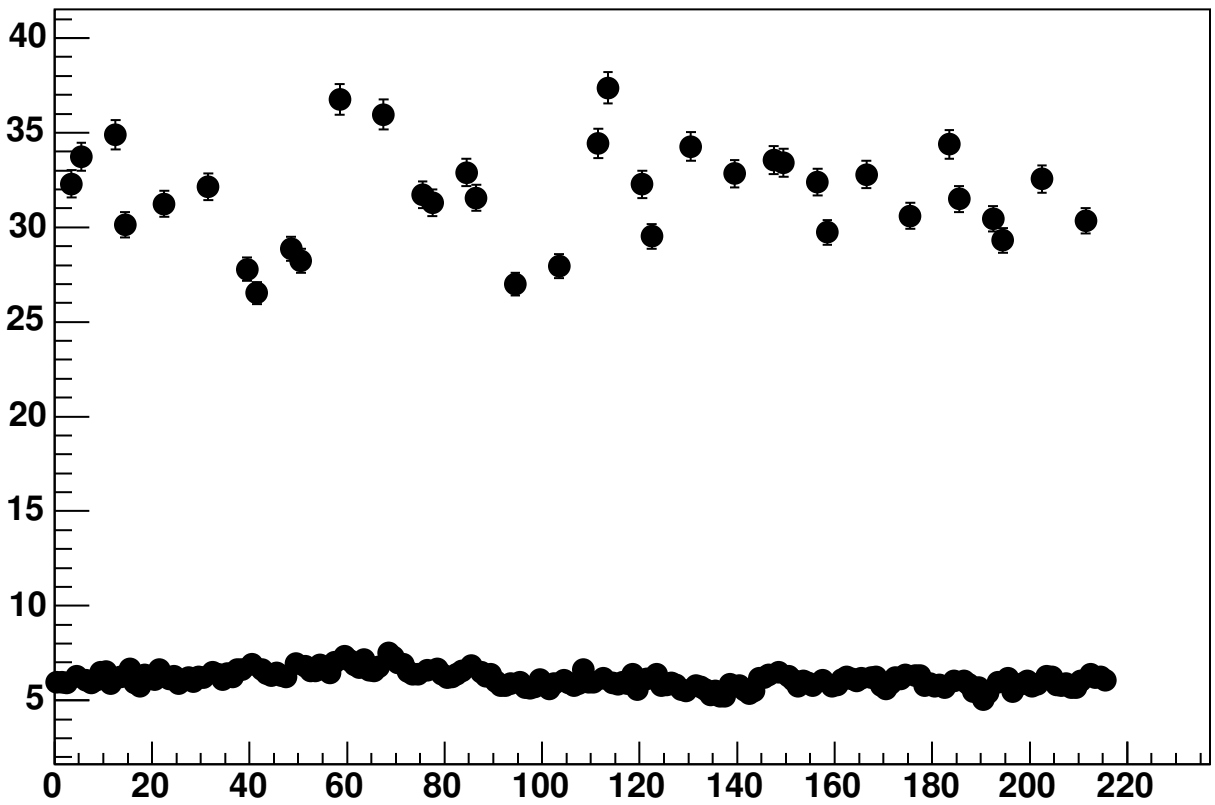
Enable 1, Hold=30, DAC=18432, ADC Noise vs 18*Chip+Chan



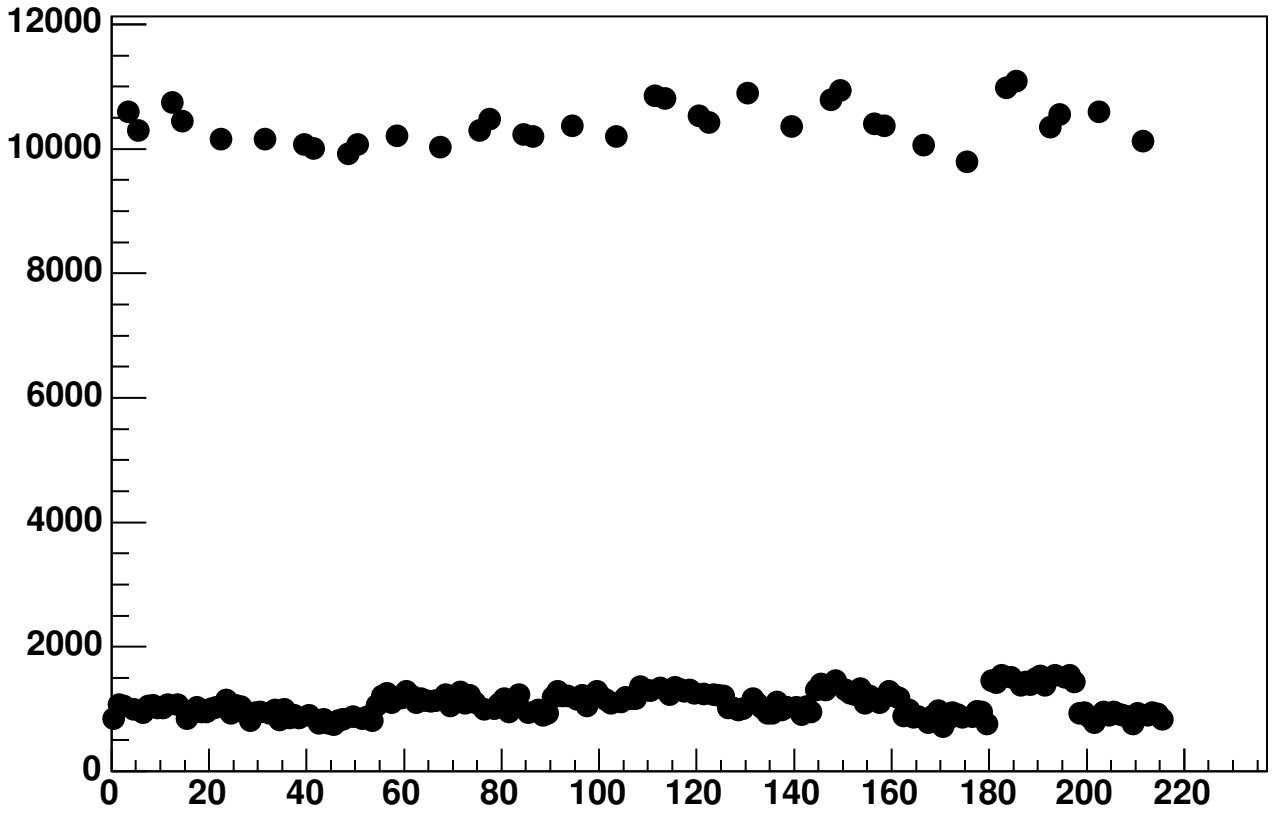
Enable 1, Hold=30, DAC=16384, ADC Mean vs 18*Chip+Chan



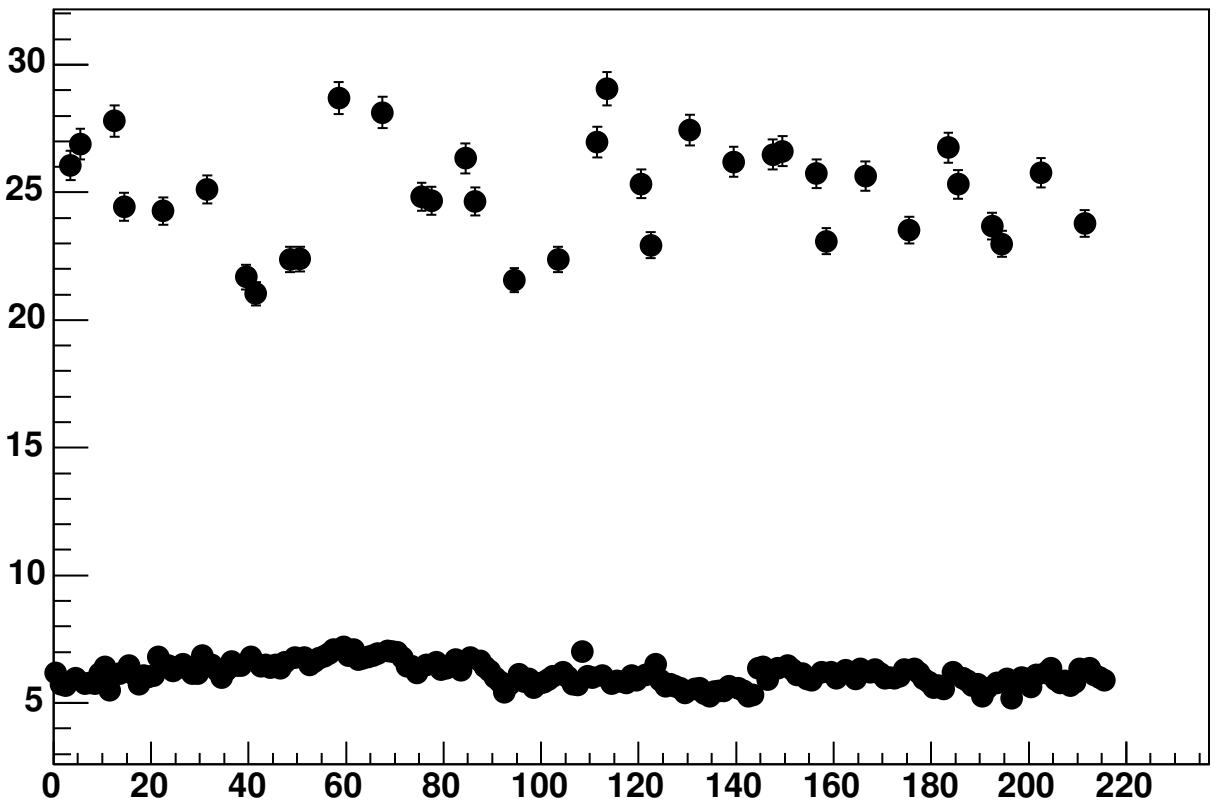
Enable 1, Hold=30, DAC=16384, ADC Noise vs 18*Chip+Chan



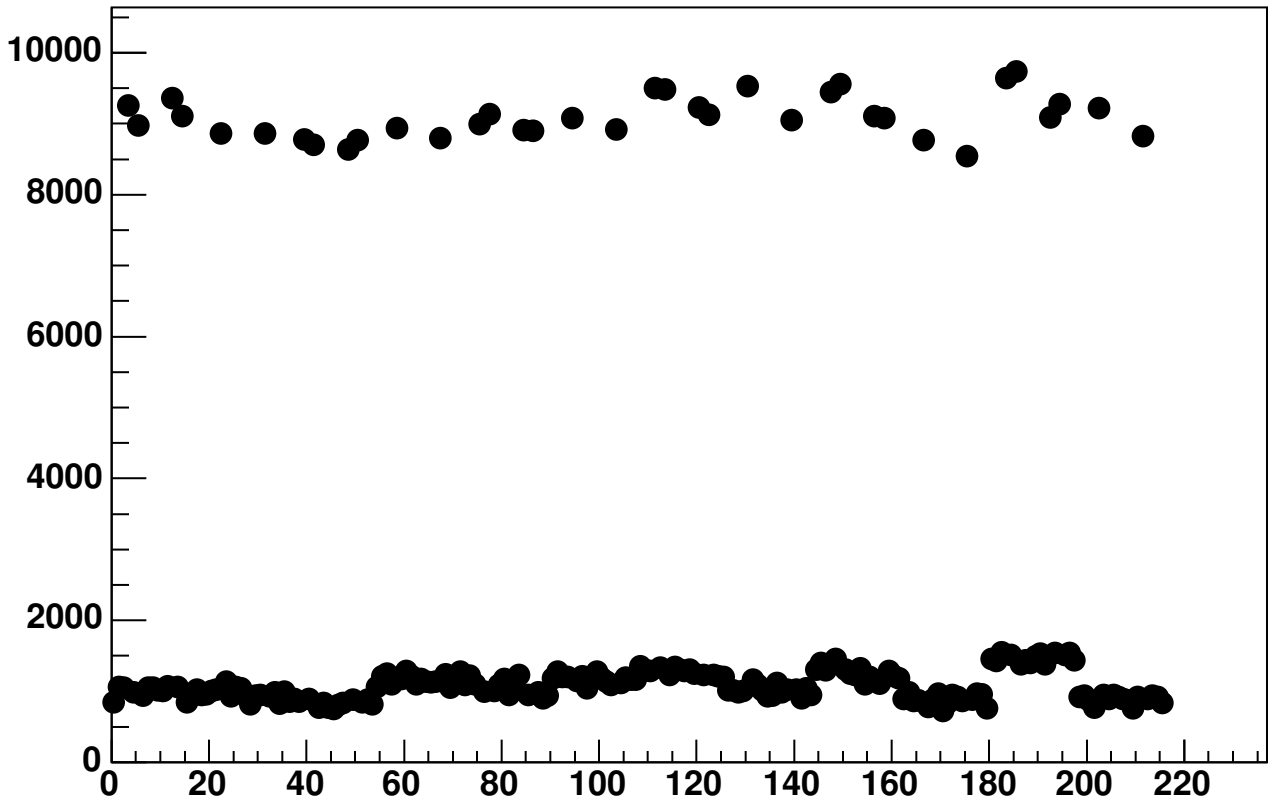
Enable 1, Hold=30, DAC=14336, ADC Mean vs 18*Chip+Chan



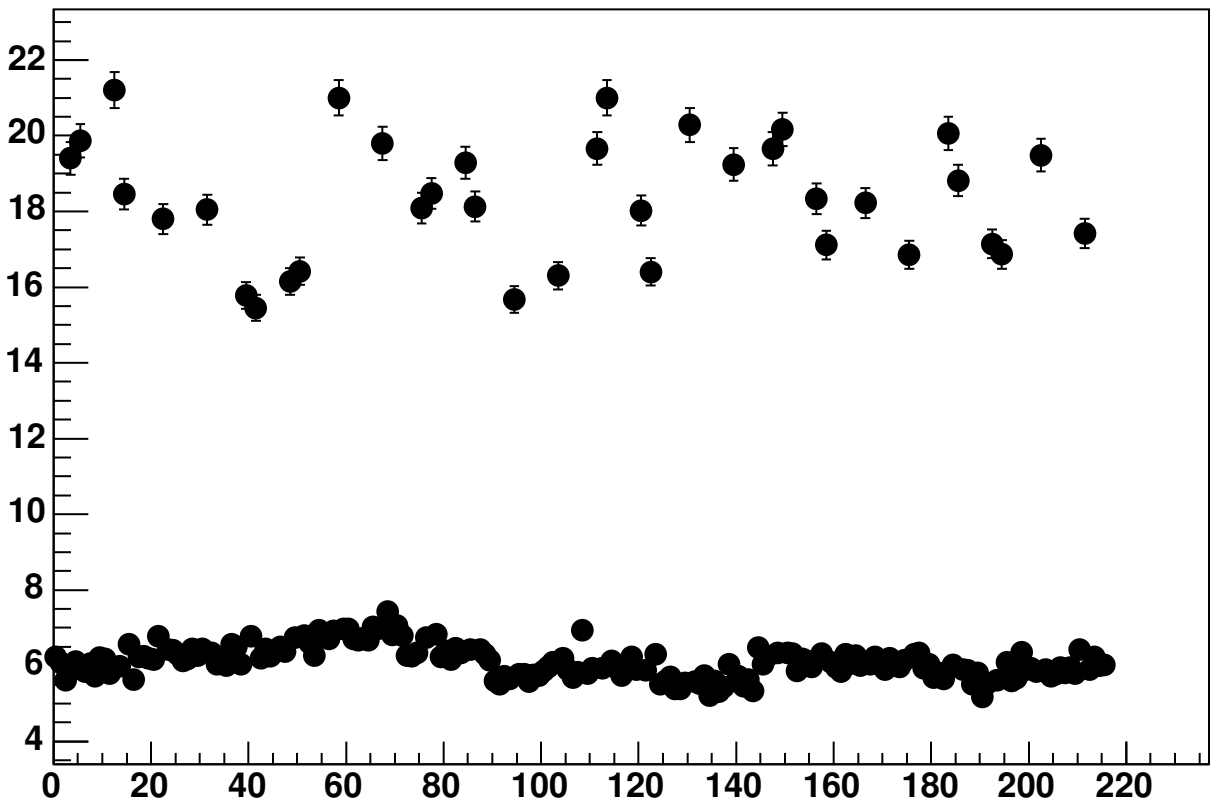
Enable 1, Hold=30, DAC=14336, ADC Noise vs 18*Chip+Chan



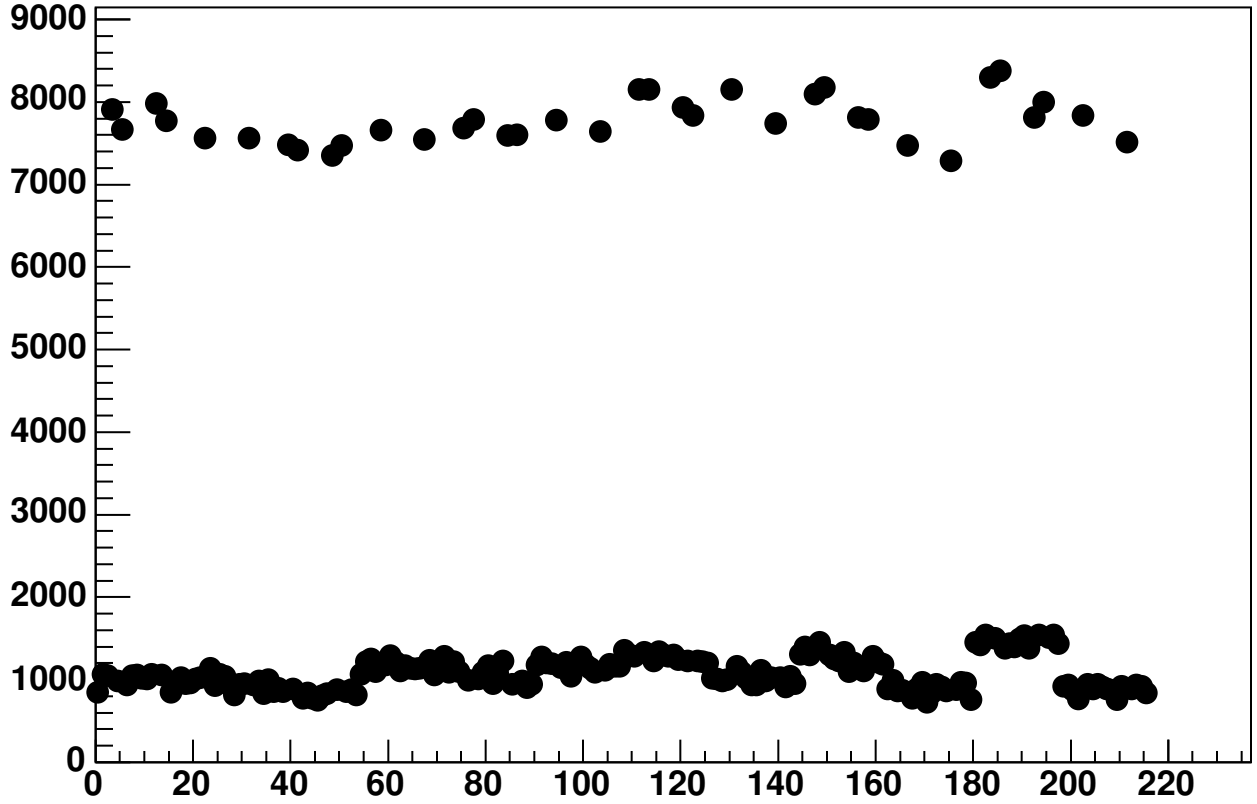
Enable 1, Hold=30, DAC=12288, ADC Mean vs 18*Chip+Chan



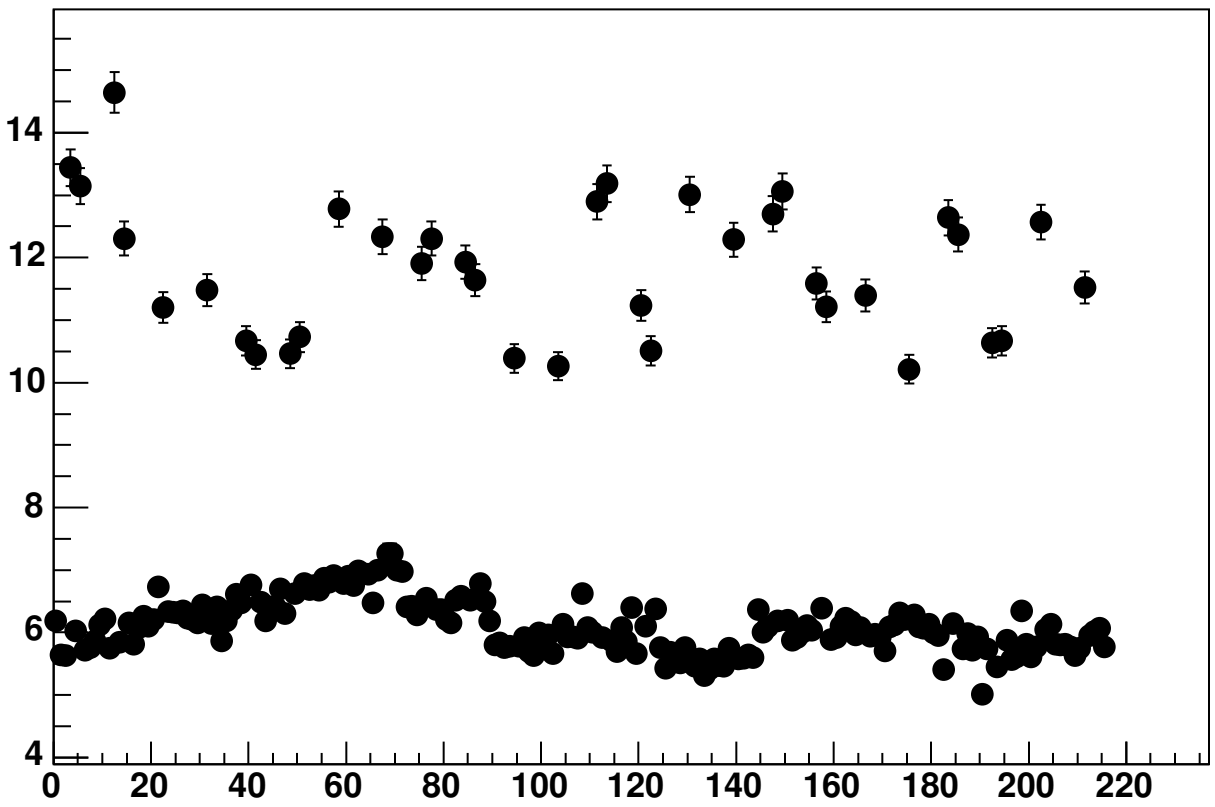
Enable 1, Hold=30, DAC=12288, ADC Noise vs 18*Chip+Chan



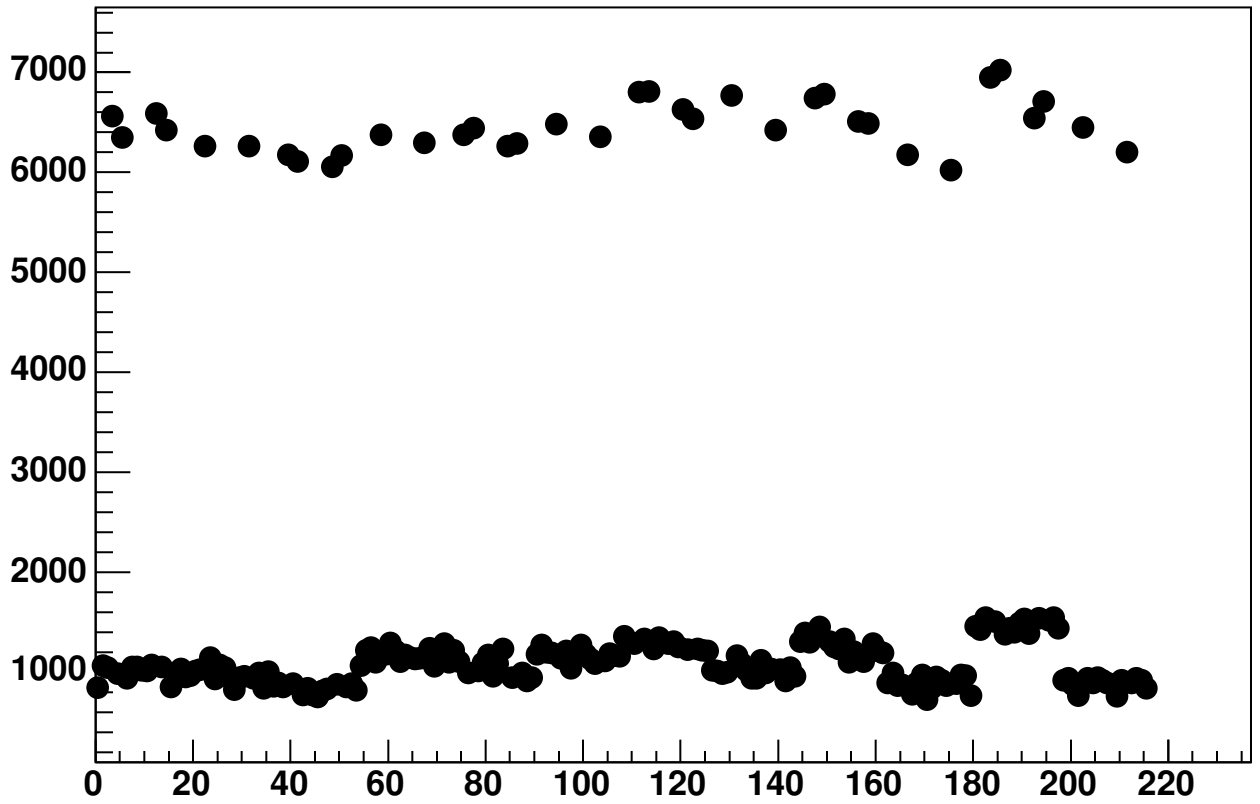
Enable 1, Hold=30, DAC=10240, ADC Mean vs 18*Chip+Chan



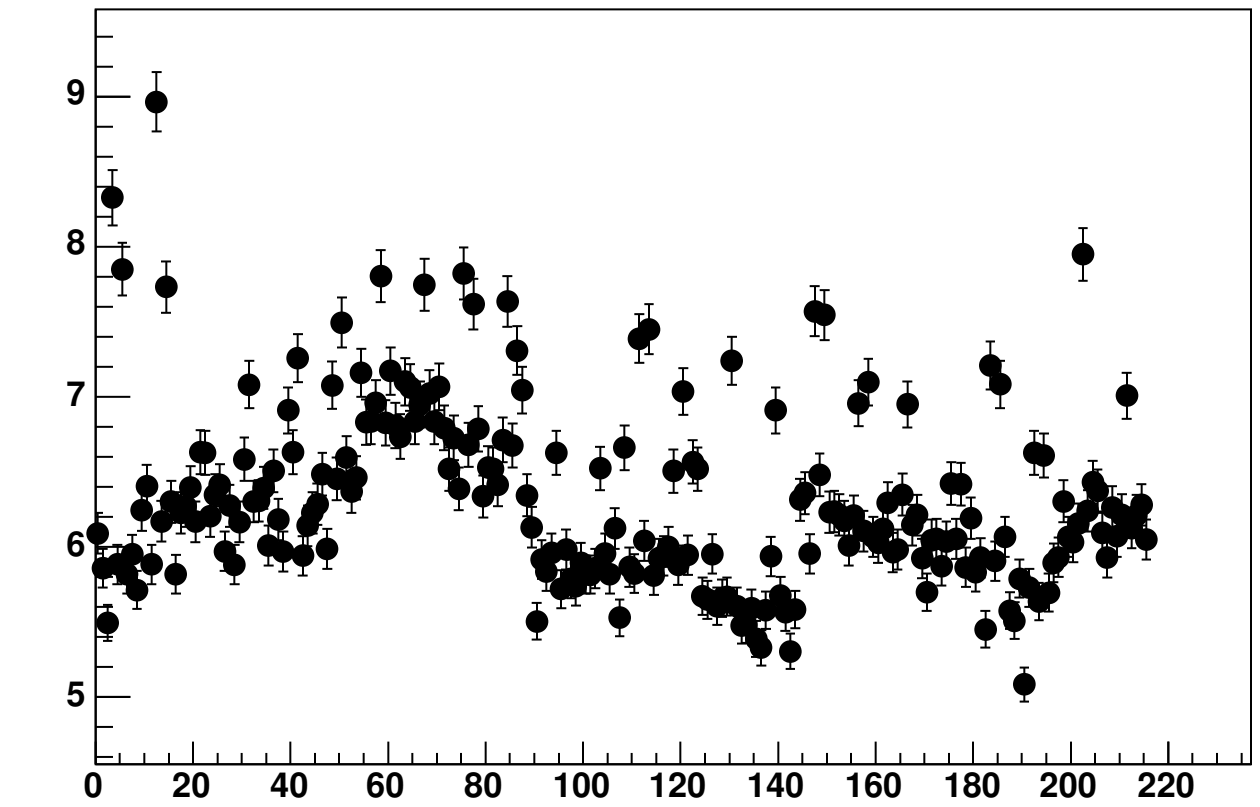
Enable 1, Hold=30, DAC=10240, ADC Noise vs 18*Chip+Chan



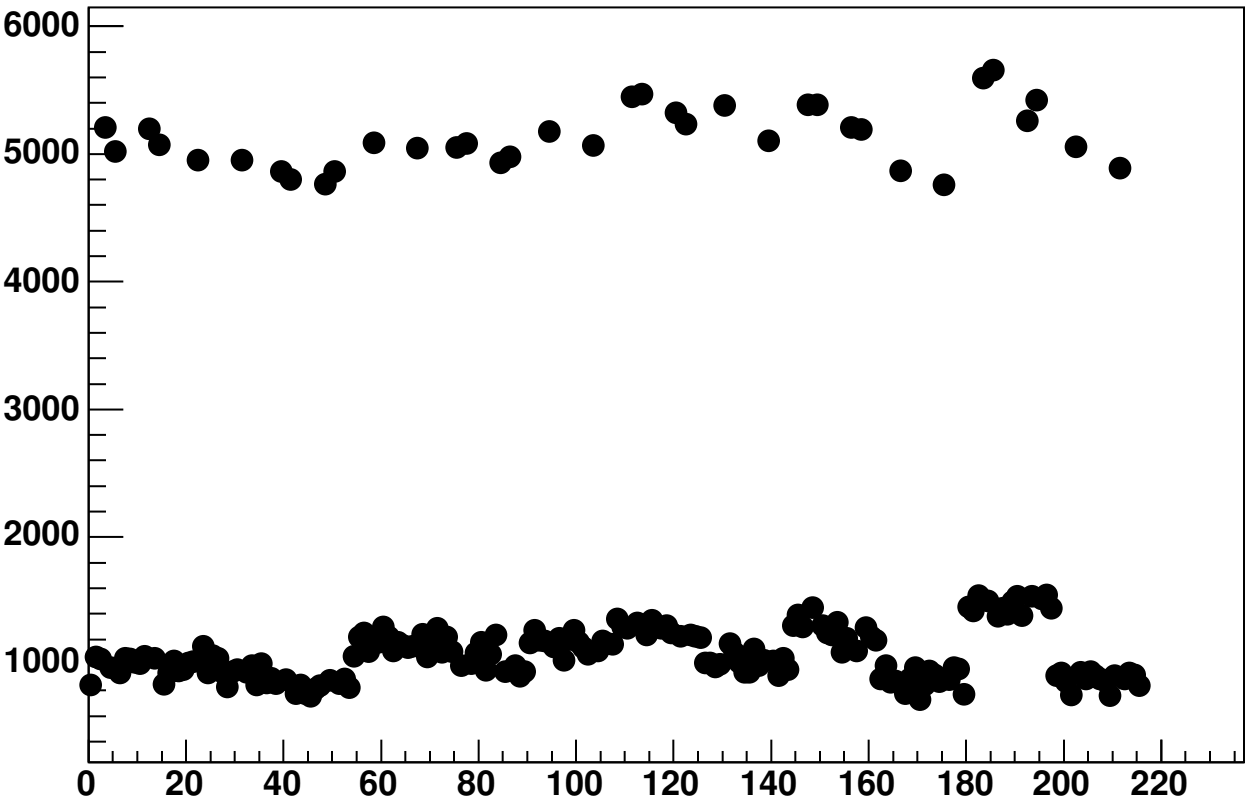
Enable 1, Hold=30, DAC=8192, ADC Mean vs 18*Chip+Chan



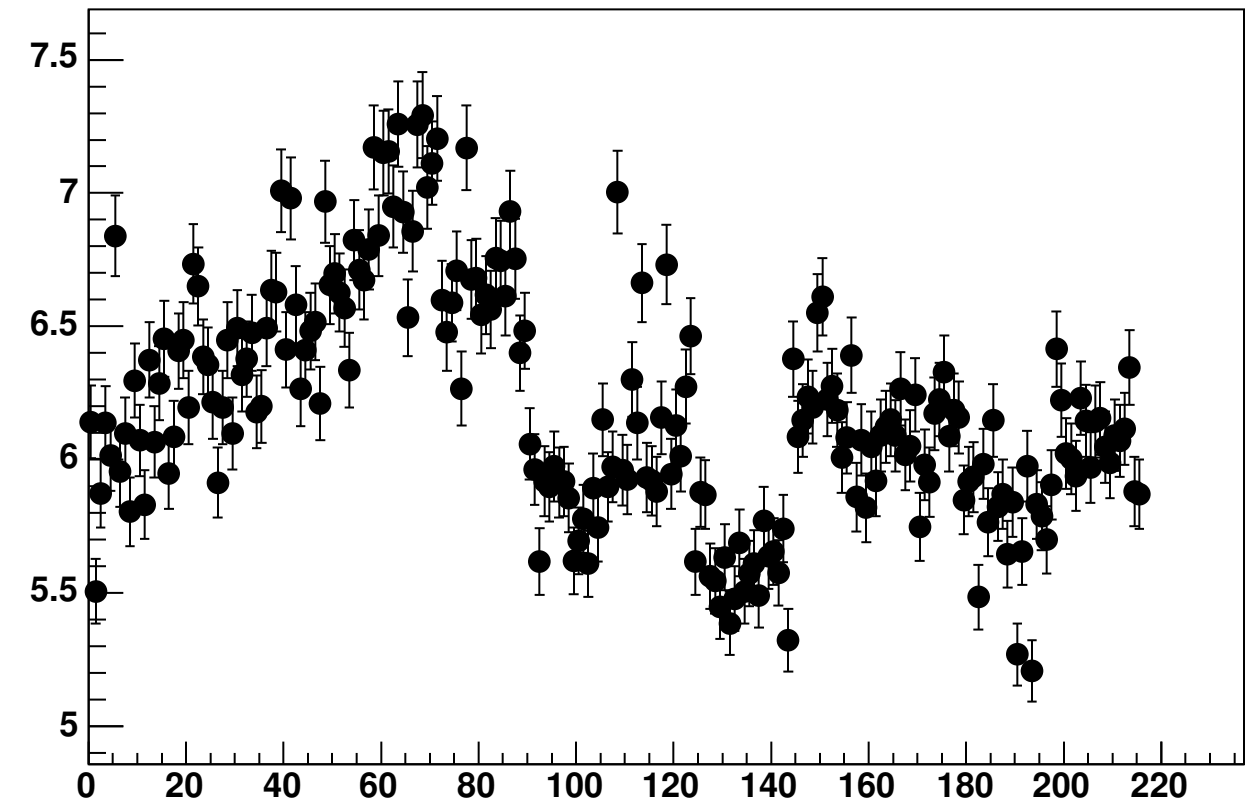
Enable 1, Hold=30, DAC=8192, ADC Noise vs 18*Chip+Chan



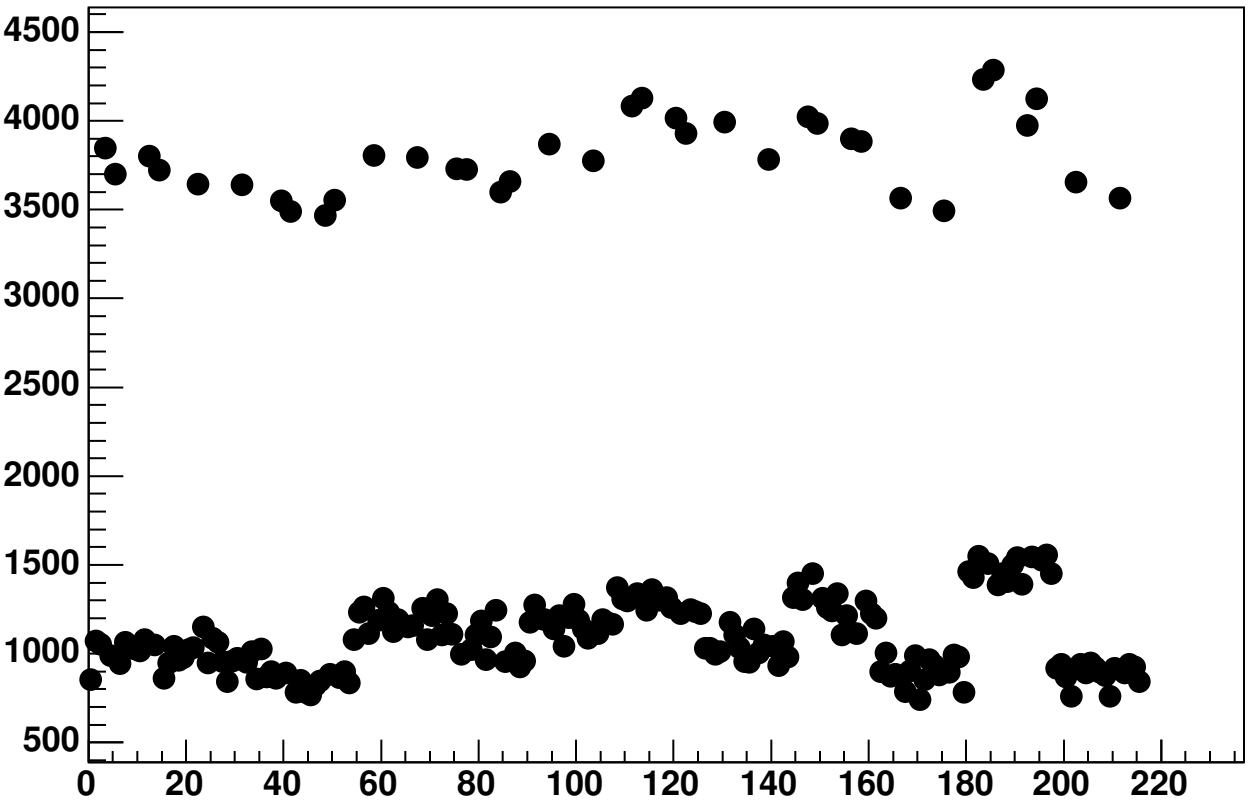
Enable 1, Hold=30, DAC=6144, ADC Mean vs 18*Chip+Chan



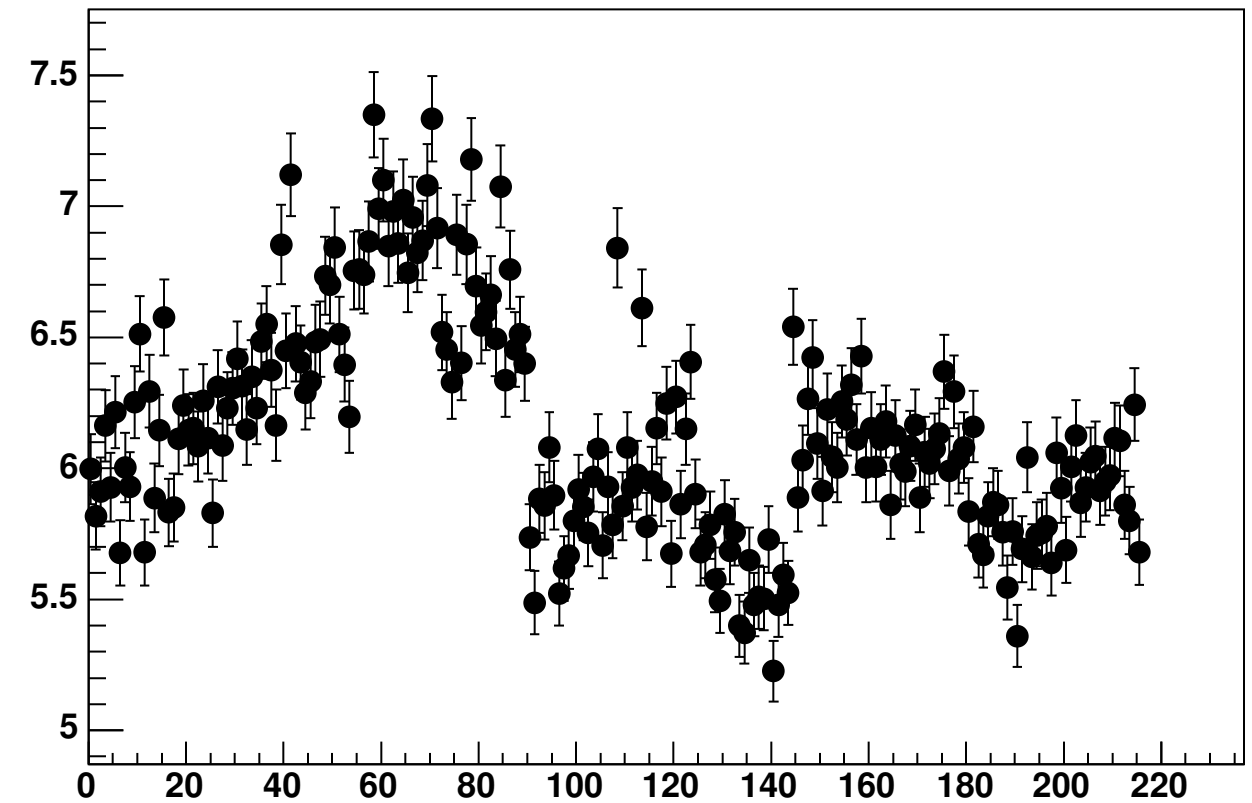
Enable 1, Hold=30, DAC=6144, ADC Noise vs 18*Chip+Chan



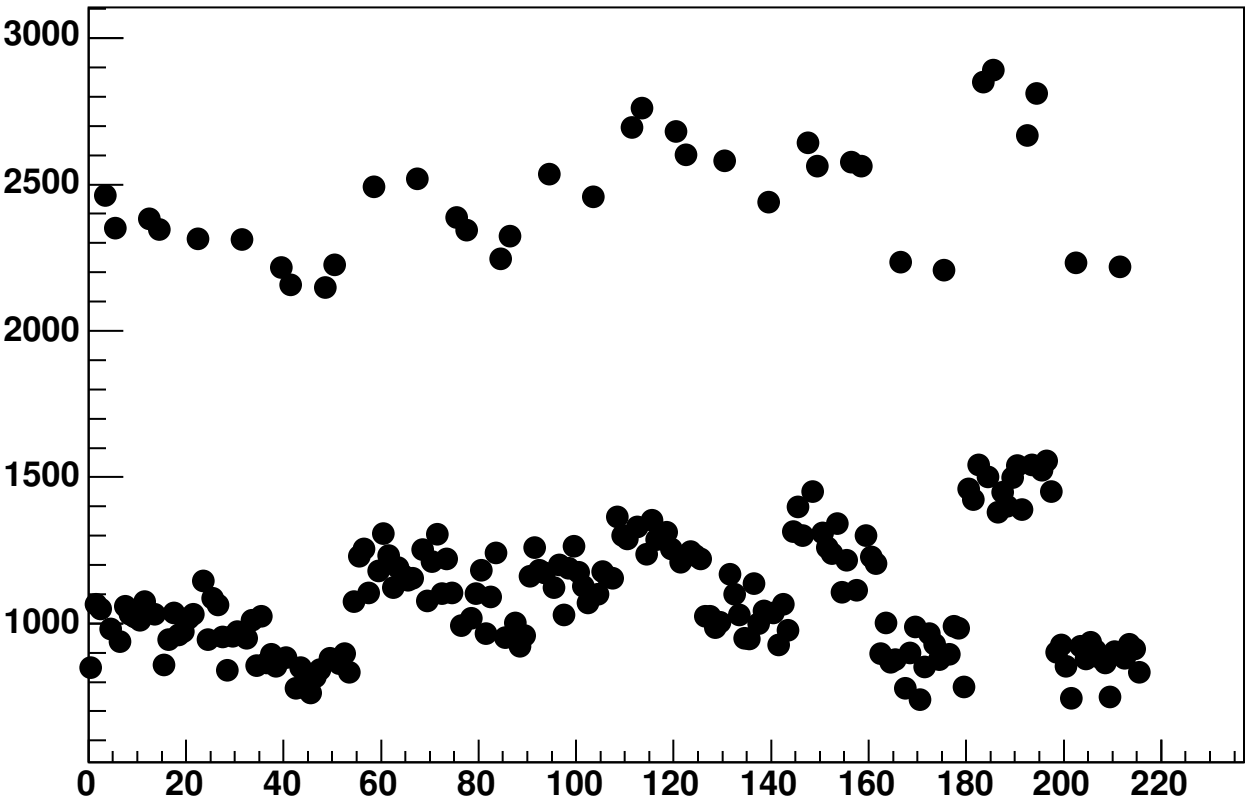
Enable 1, Hold=30, DAC=4096, ADC Mean vs 18*Chip+Chan



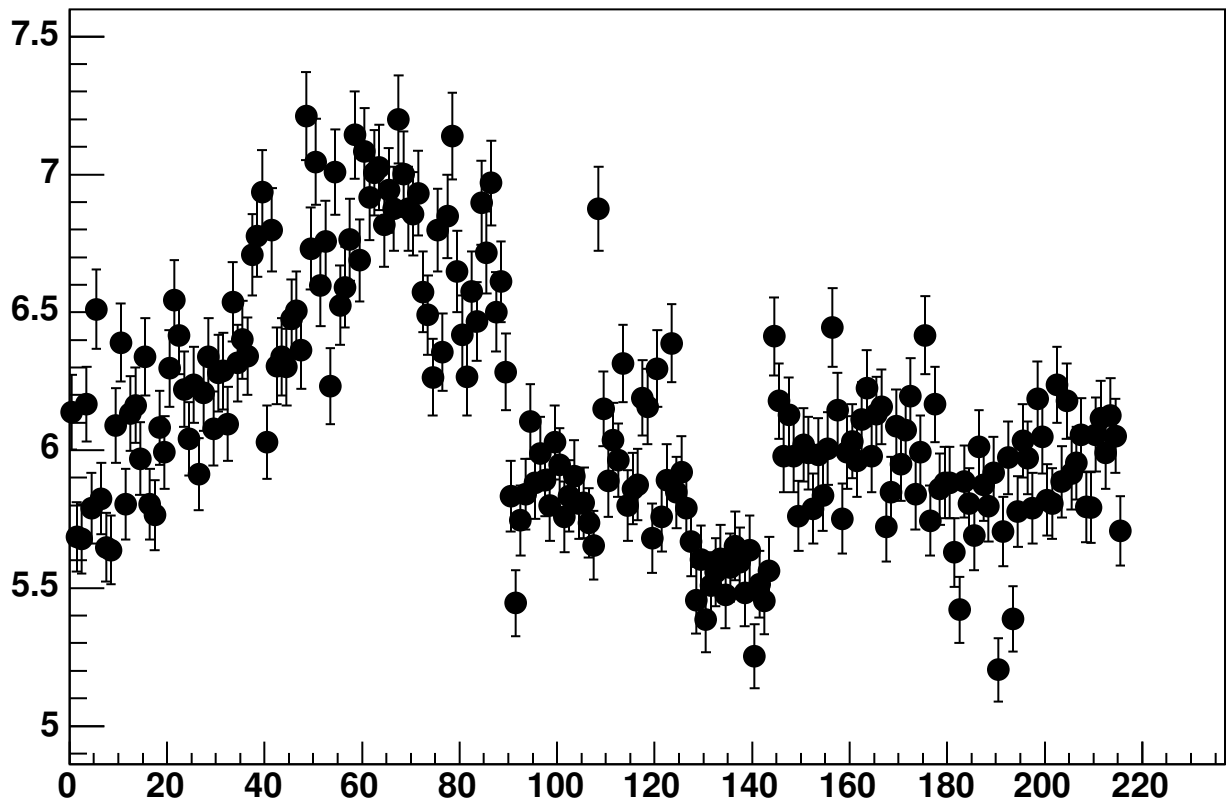
Enable 1, Hold=30, DAC=4096, ADC Noise vs 18*Chip+Chan



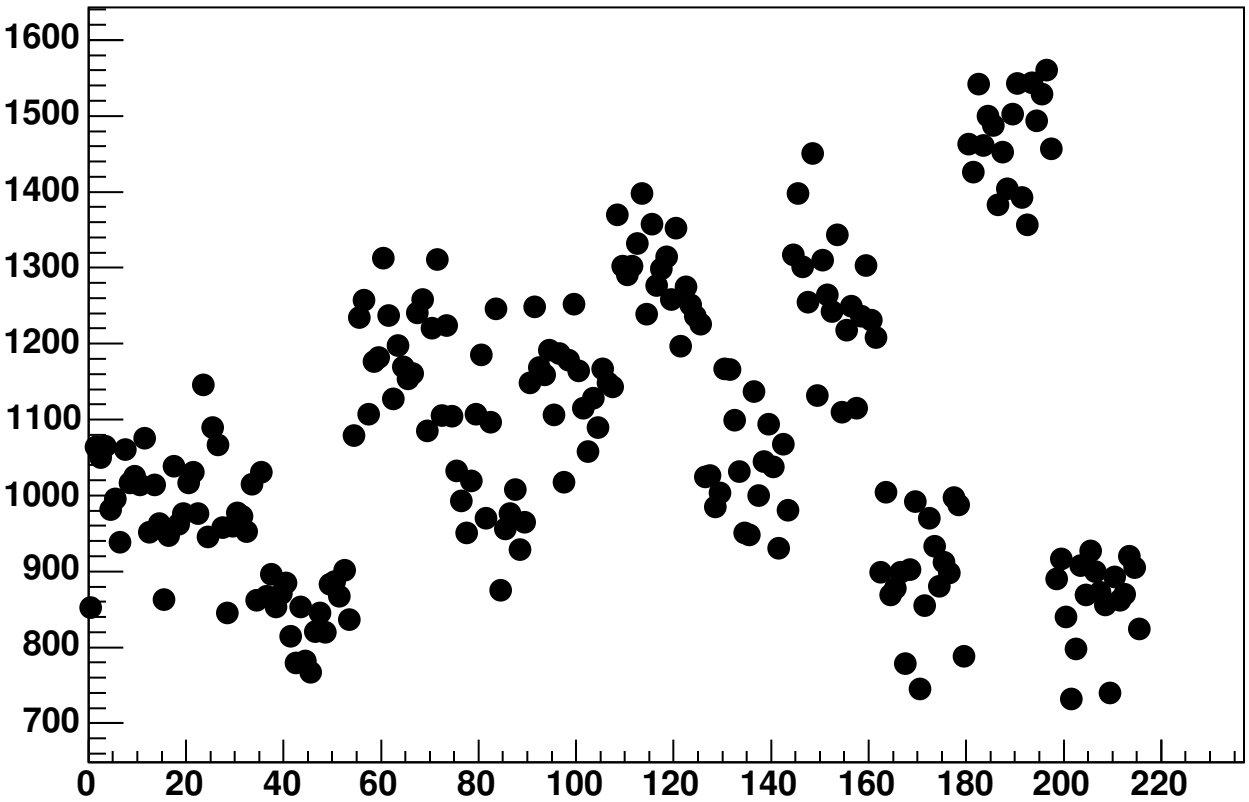
Enable 1, Hold=30, DAC=2048, ADC Mean vs 18*Chip+Chan



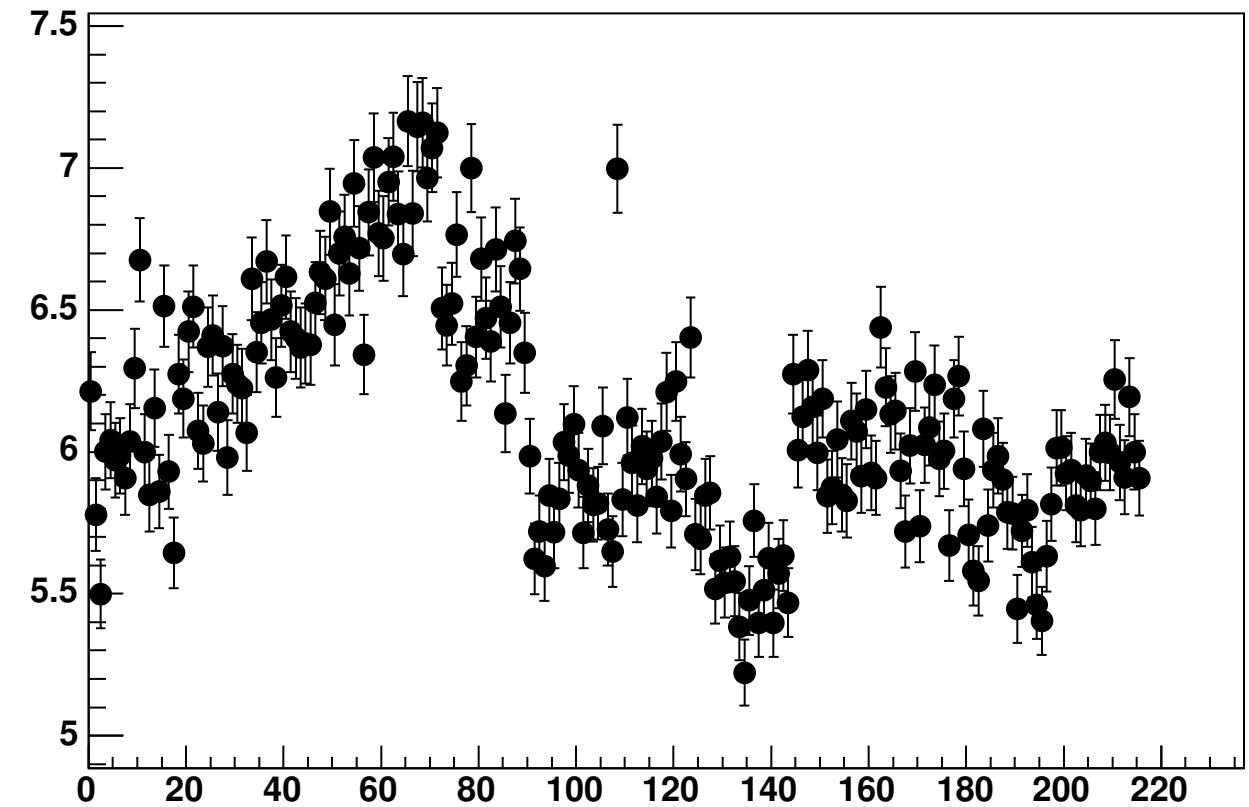
Enable 1, Hold=30, DAC=2048, ADC Noise vs 18*Chip+Chan



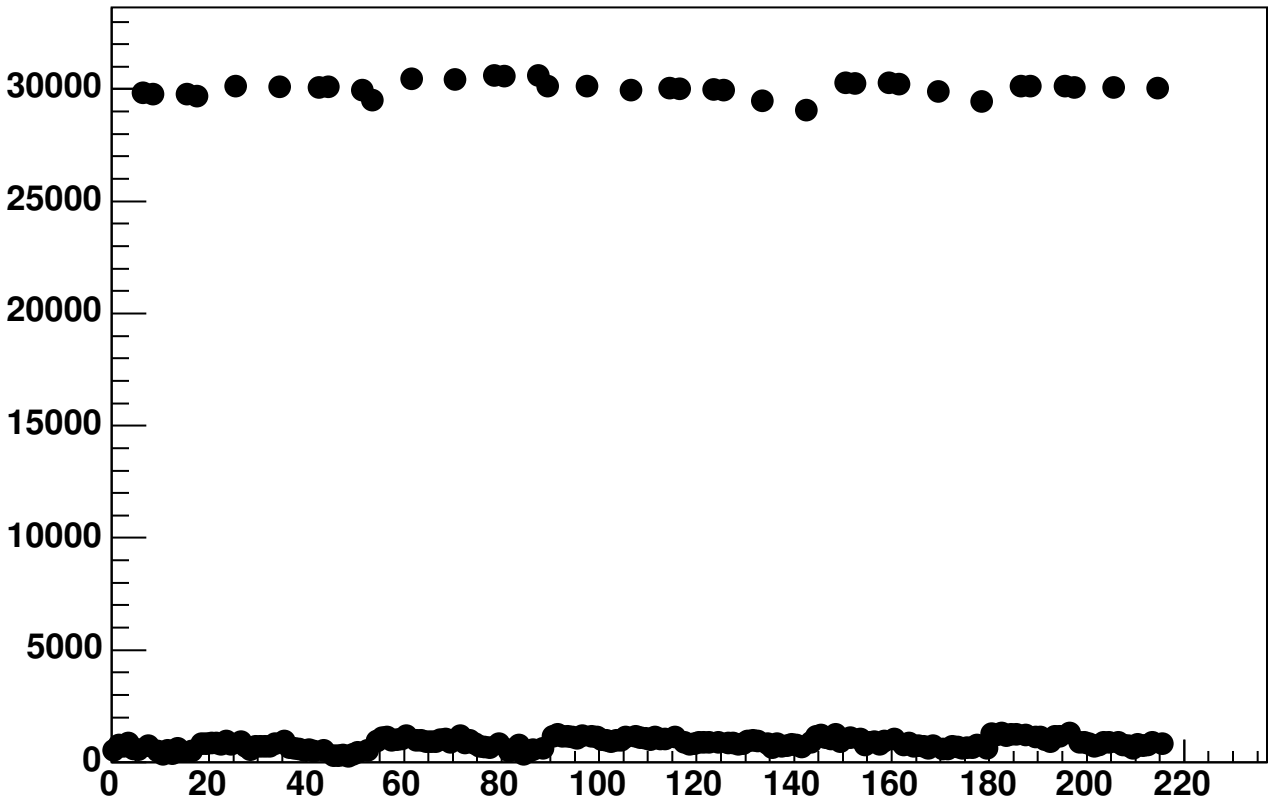
Enable 1, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



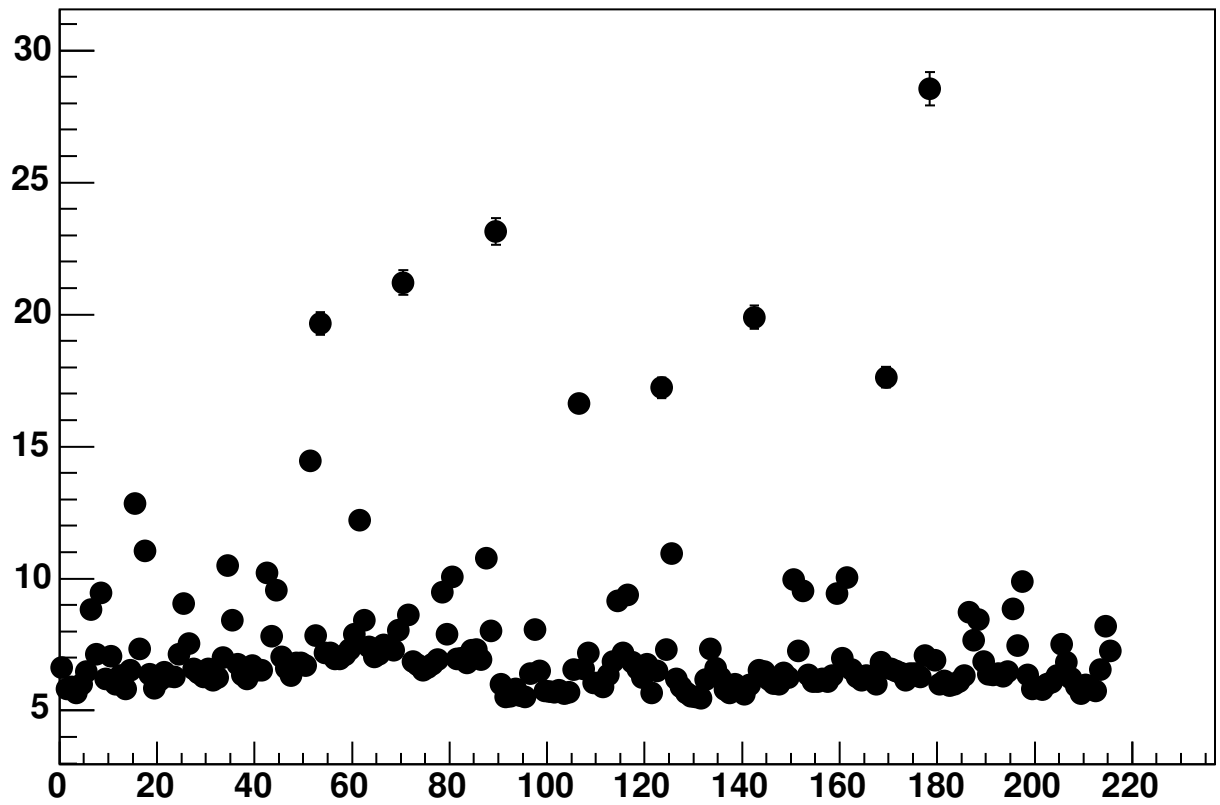
Enable 1, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



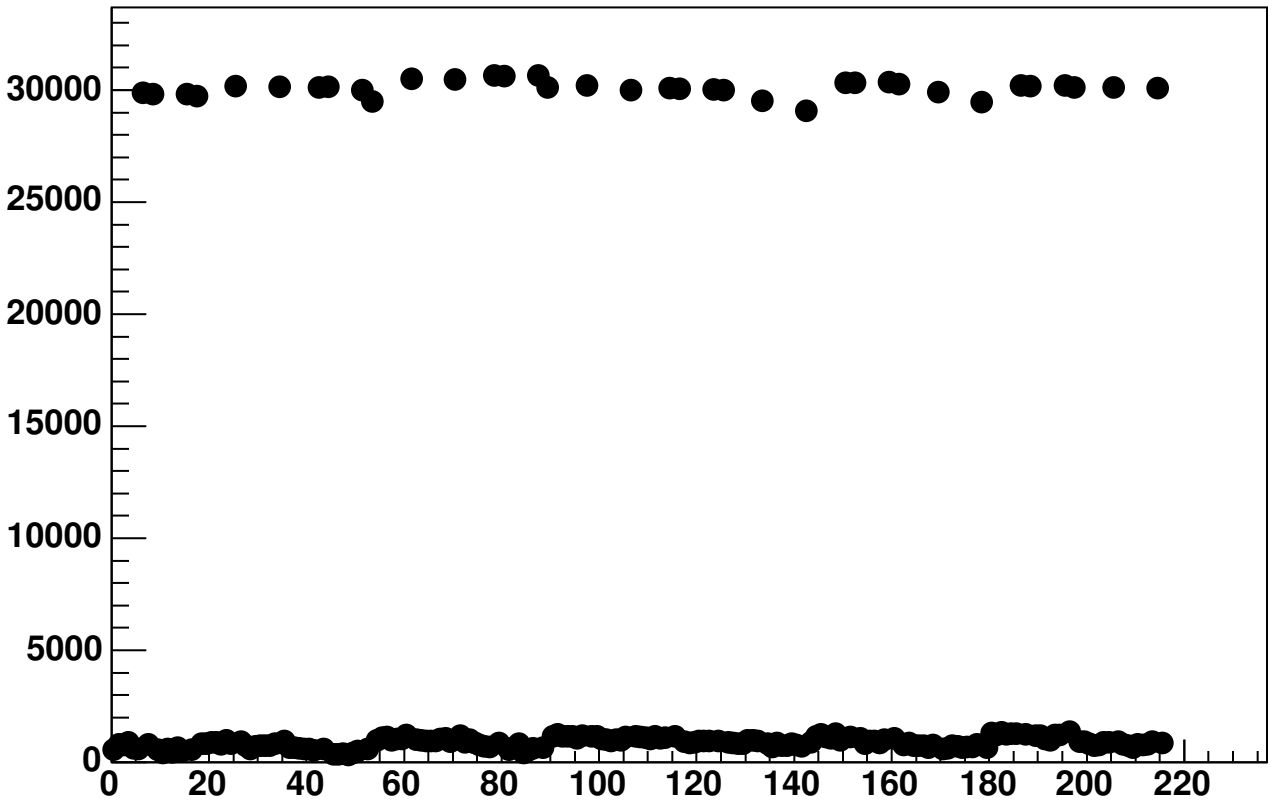
Enable 2, Hold=30, DAC=63488, ADC Mean vs 18*Chip+Chan



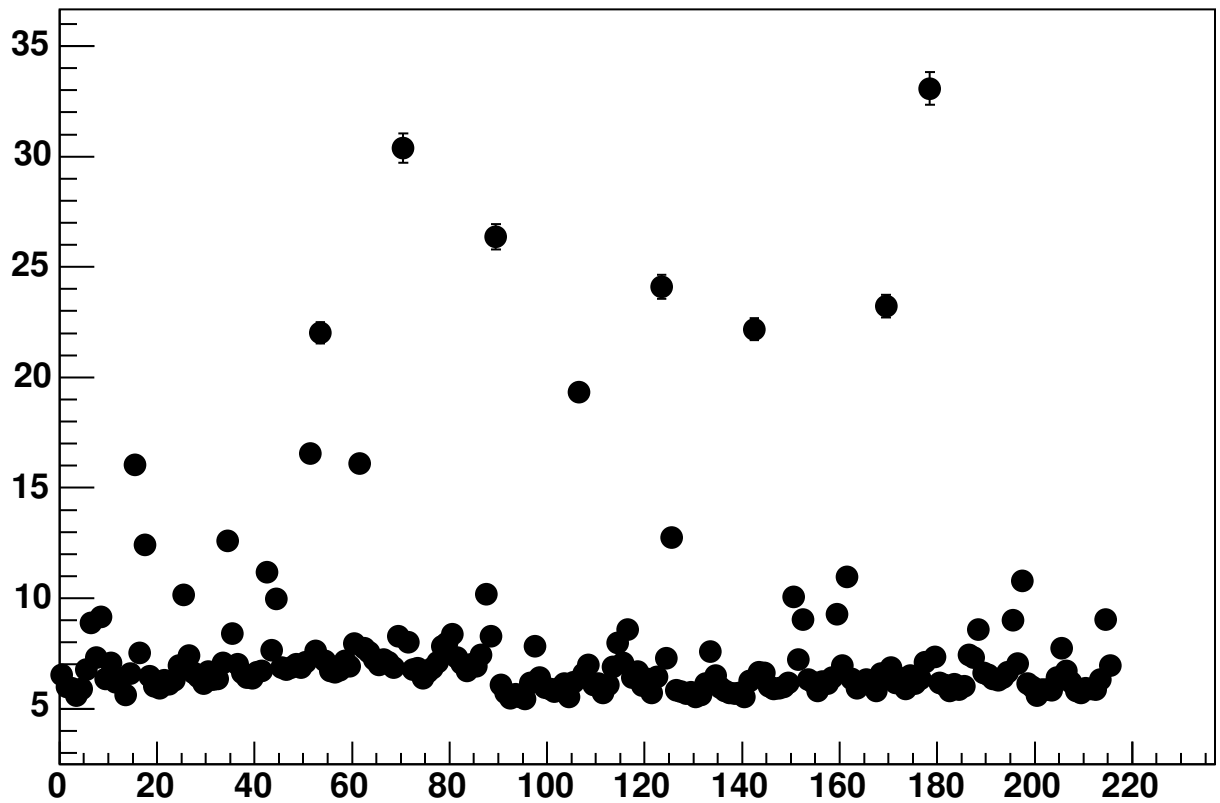
Enable 2, Hold=30, DAC=63488, ADC Noise vs 18*Chip+Chan



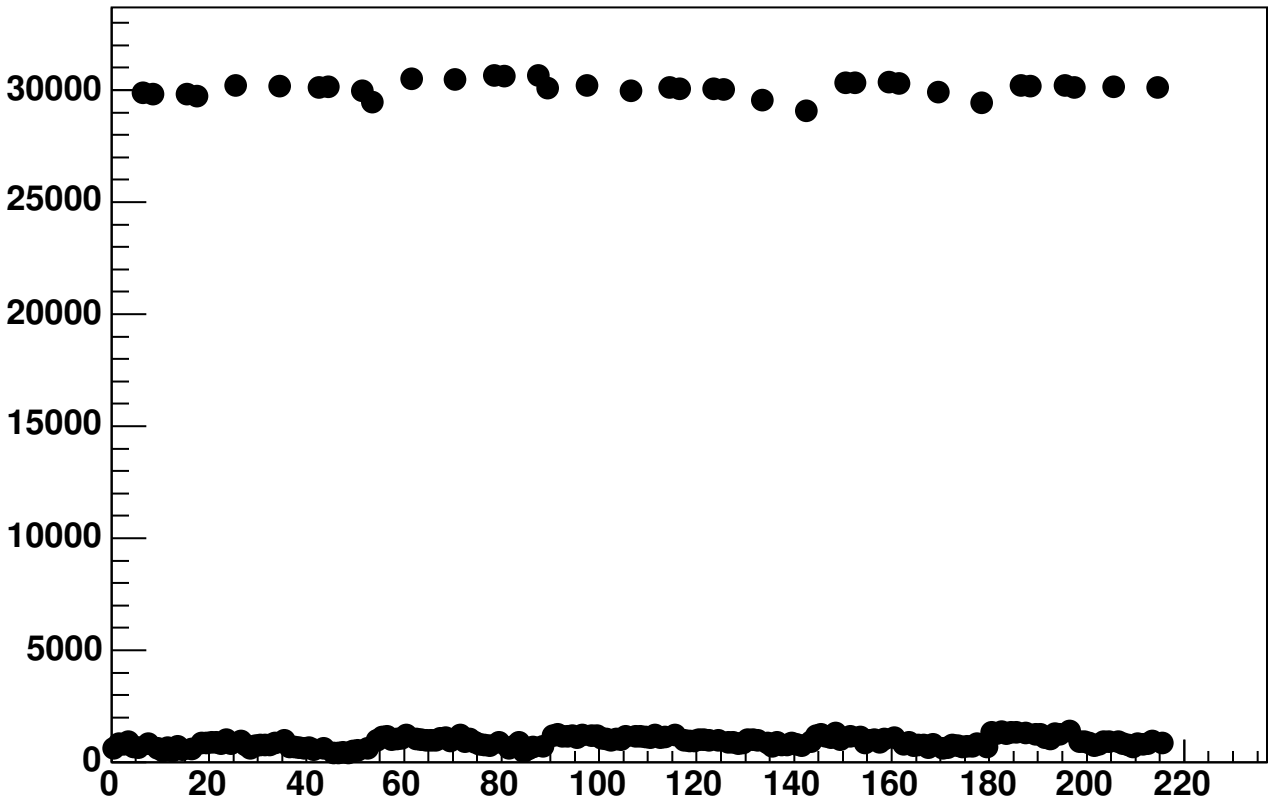
Enable 2, Hold=30, DAC=61440, ADC Mean vs 18*Chip+Chan



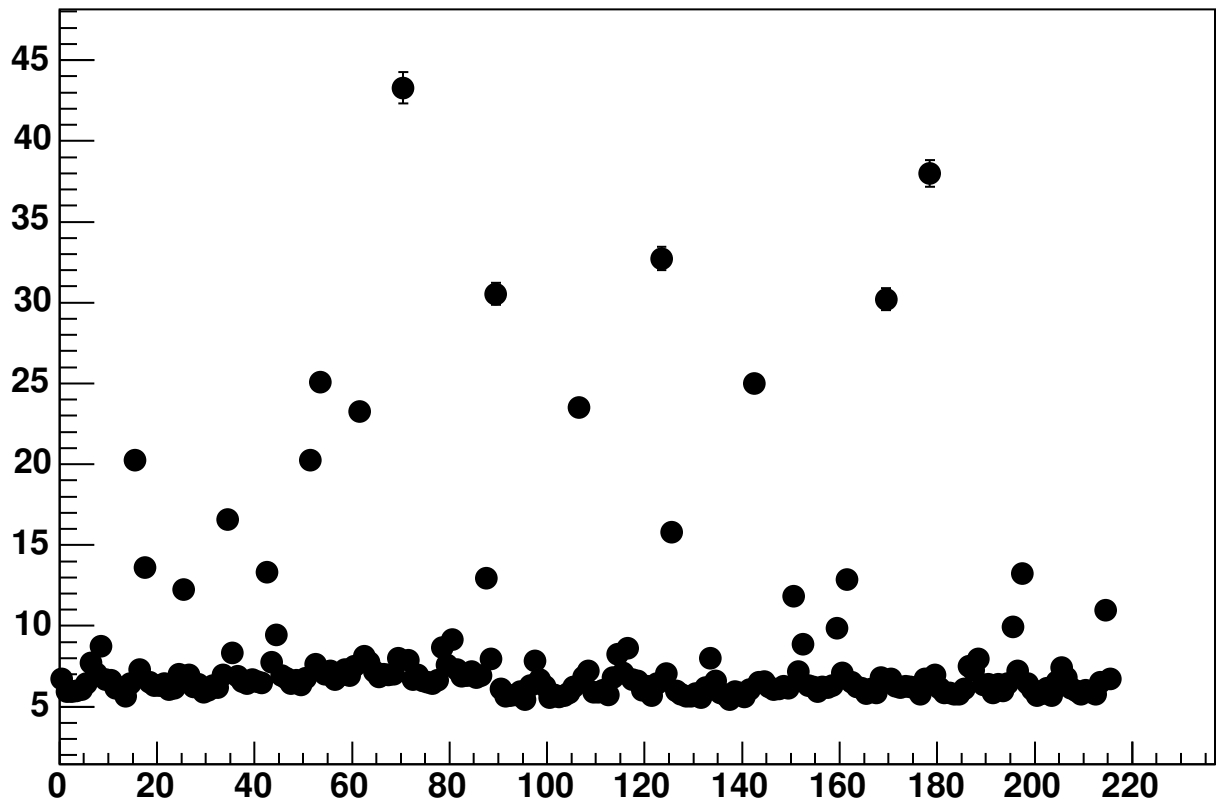
Enable 2, Hold=30, DAC=61440, ADC Noise vs 18*Chip+Chan



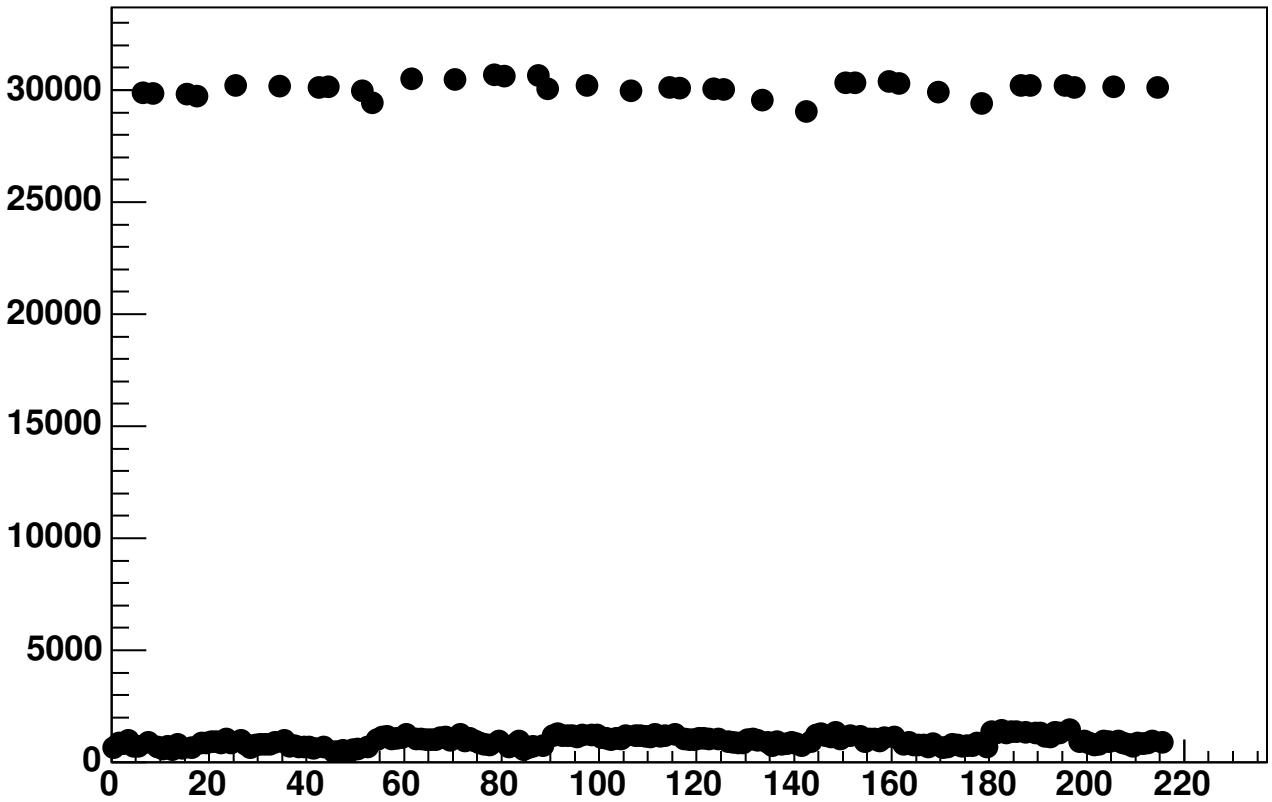
Enable 2, Hold=30, DAC=59392, ADC Mean vs 18*Chip+Chan



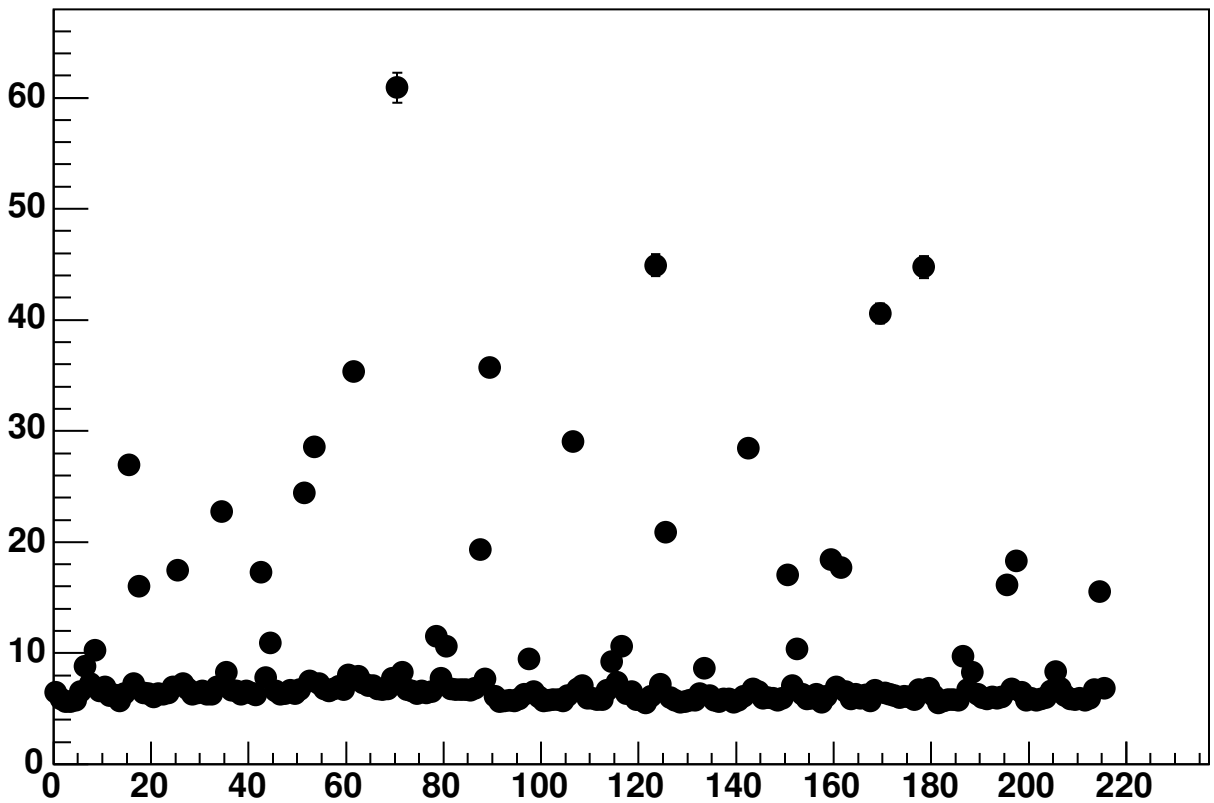
Enable 2, Hold=30, DAC=59392, ADC Noise vs 18*Chip+Chan



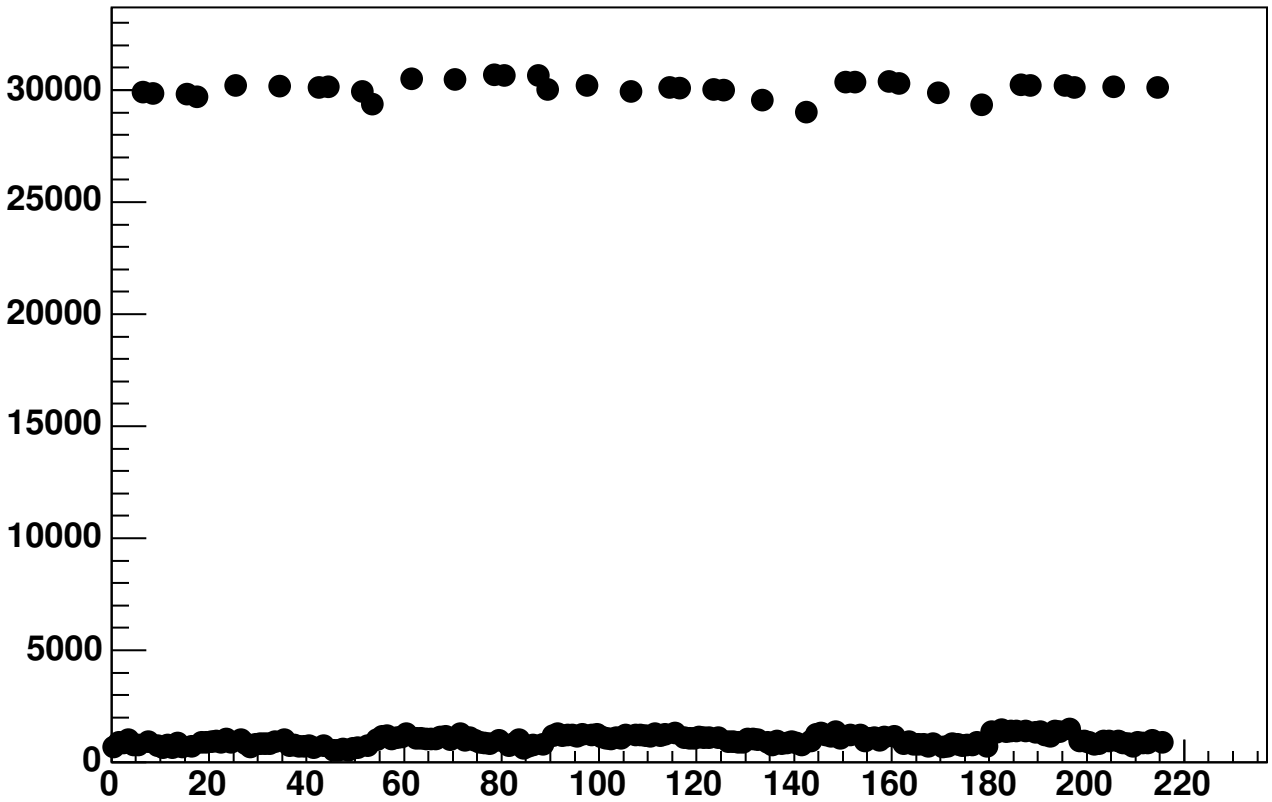
Enable 2, Hold=30, DAC=57344, ADC Mean vs 18*Chip+Chan



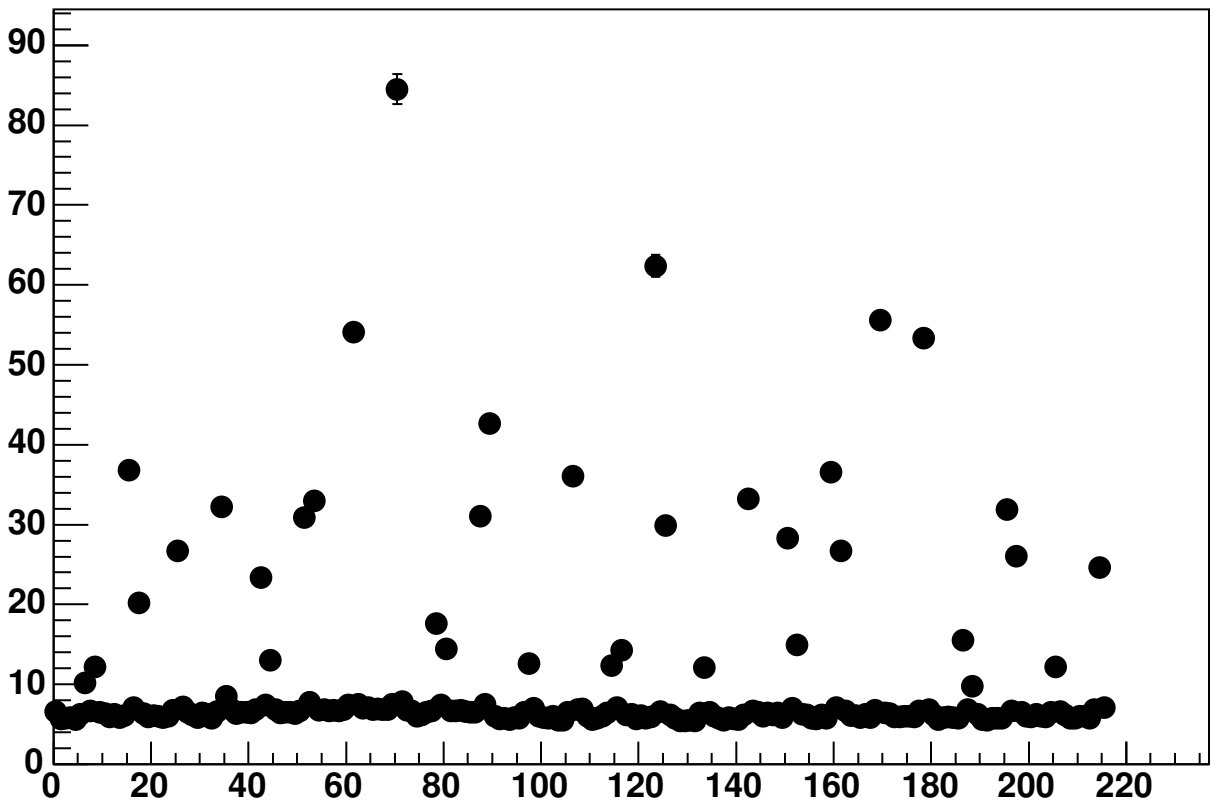
Enable 2, Hold=30, DAC=57344, ADC Noise vs 18*Chip+Chan



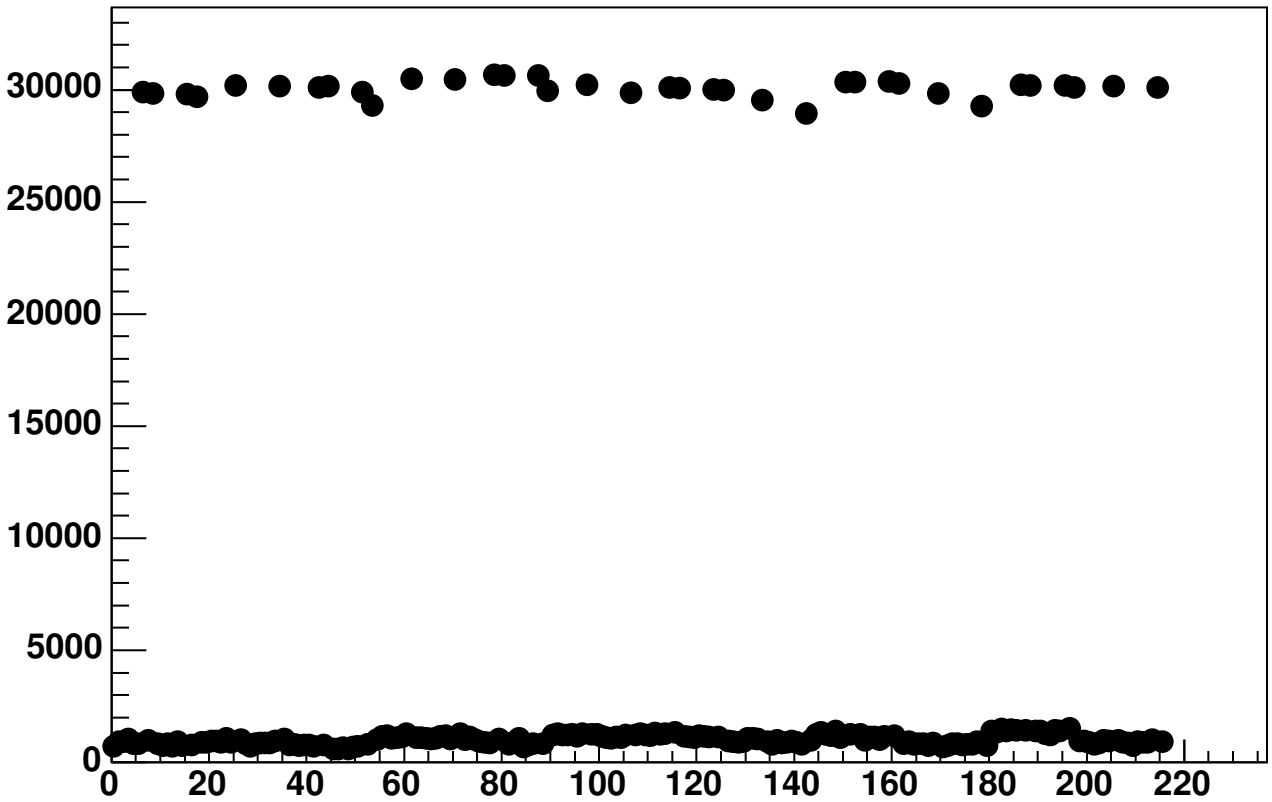
Enable 2, Hold=30, DAC=55296, ADC Mean vs 18*Chip+Chan



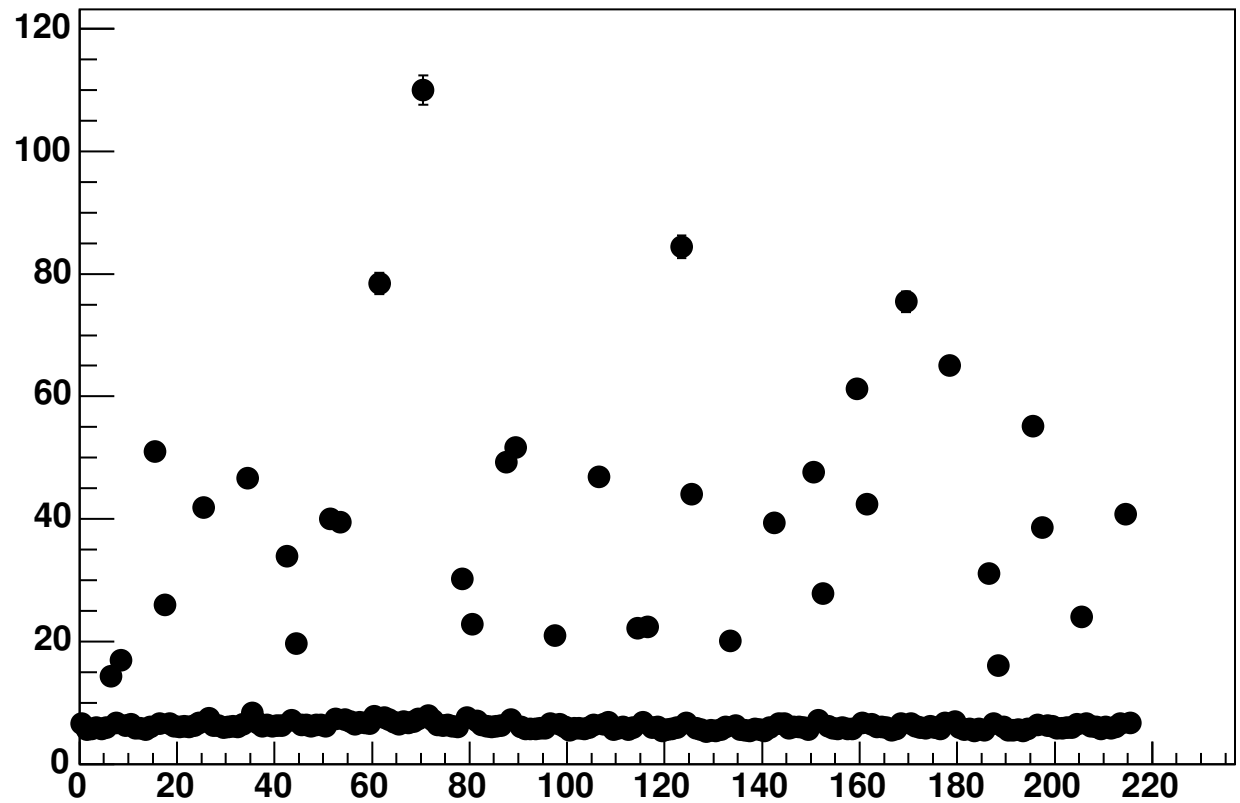
Enable 2, Hold=30, DAC=55296, ADC Noise vs 18*Chip+Chan



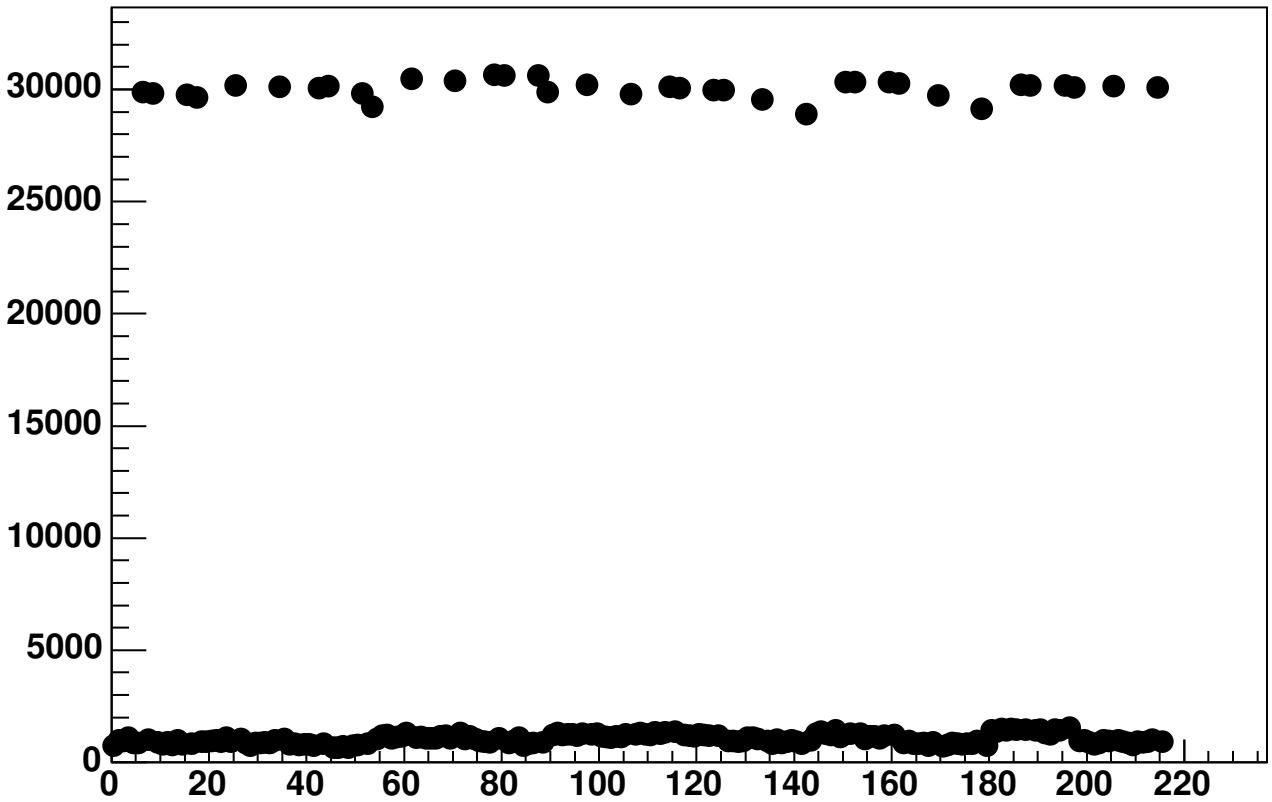
Enable 2, Hold=30, DAC=53248, ADC Mean vs 18*Chip+Chan



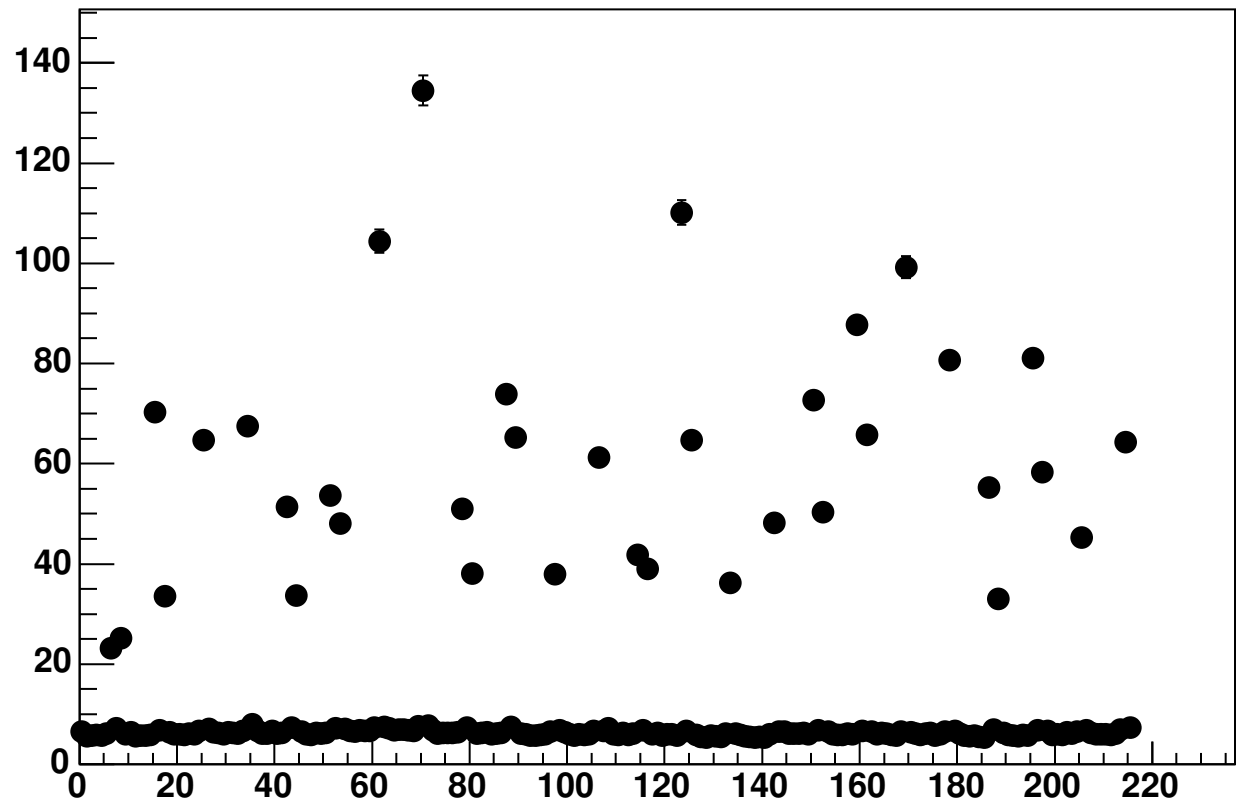
Enable 2, Hold=30, DAC=53248, ADC Noise vs 18*Chip+Chan



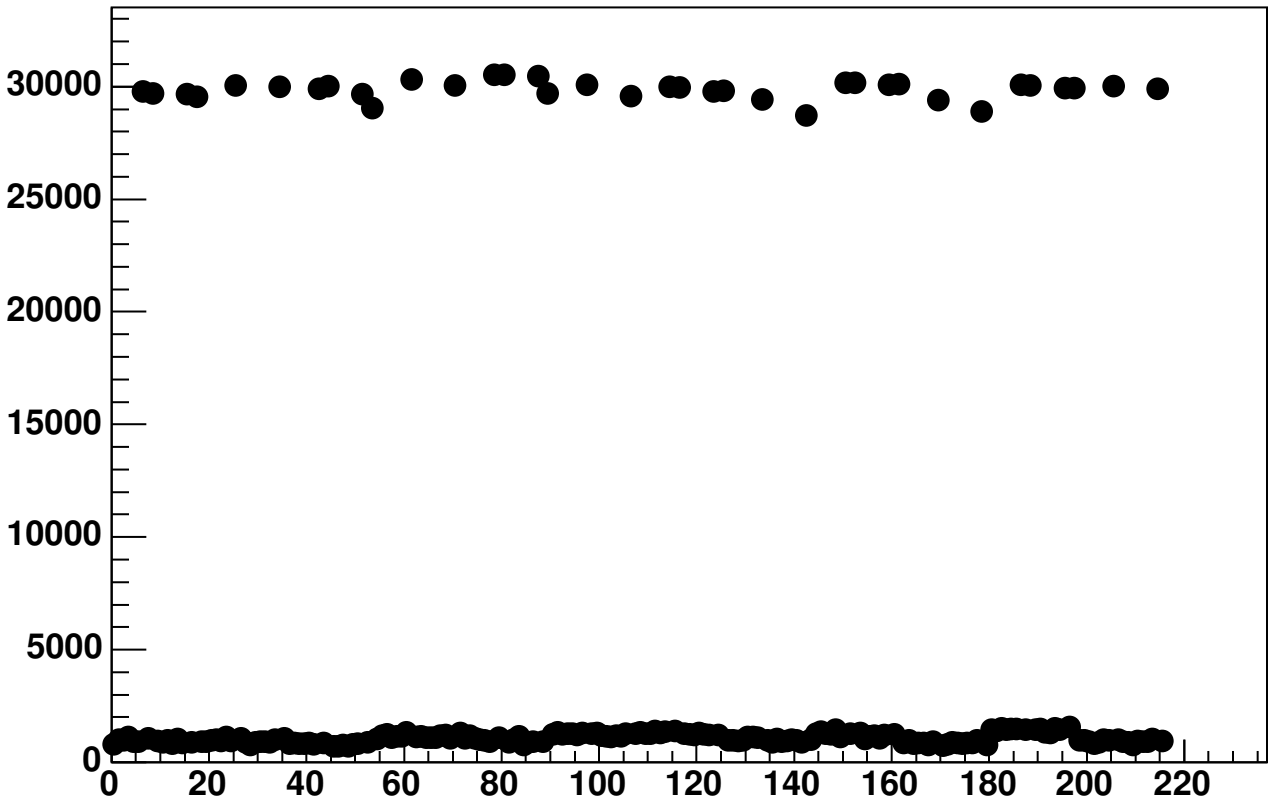
Enable 2, Hold=30, DAC=51200, ADC Mean vs 18*Chip+Chan



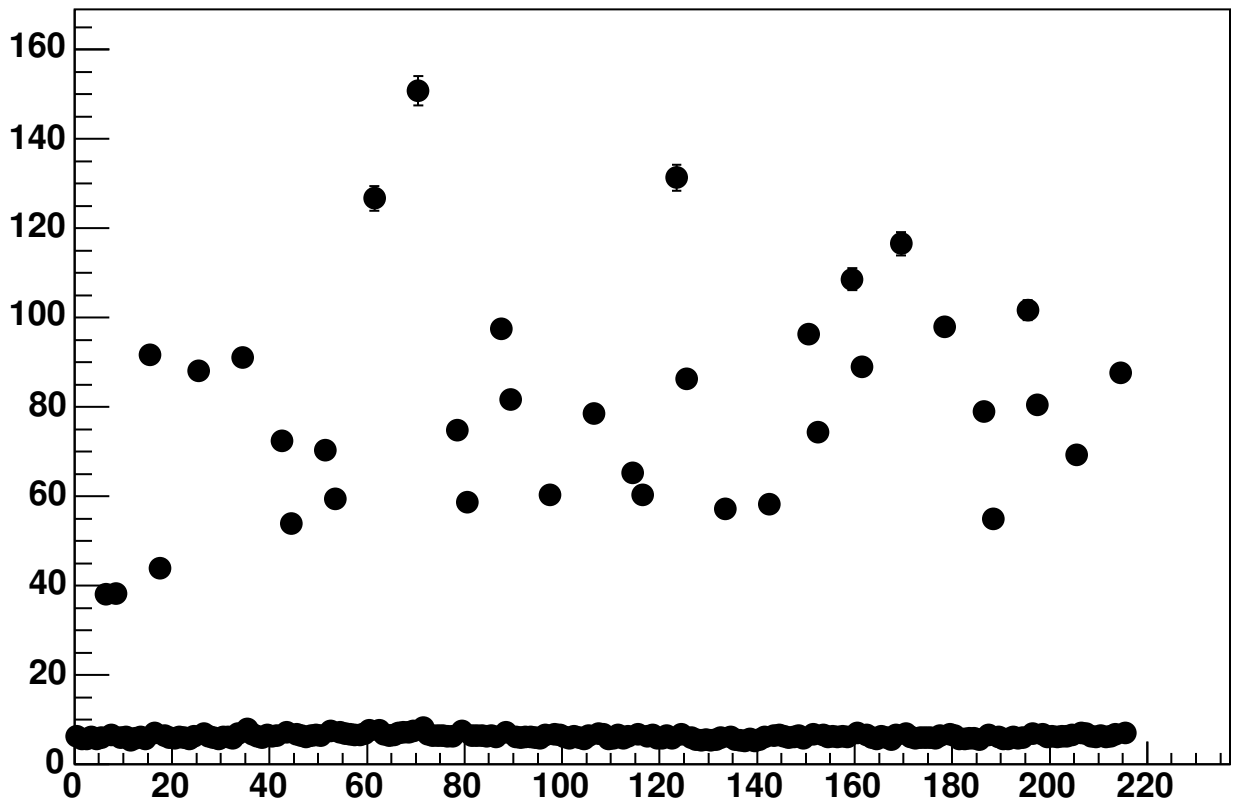
Enable 2, Hold=30, DAC=51200, ADC Noise vs 18*Chip+Chan



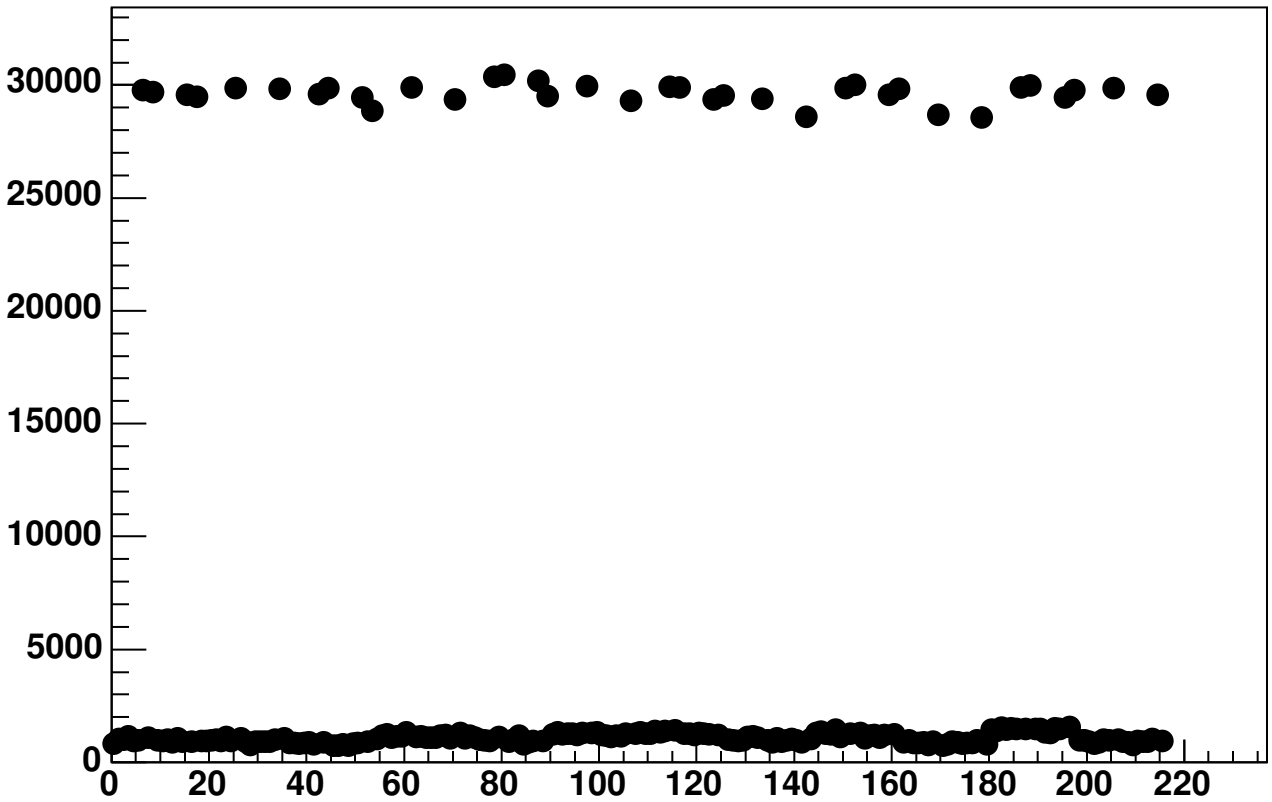
Enable 2, Hold=30, DAC=49152, ADC Mean vs 18*Chip+Chan



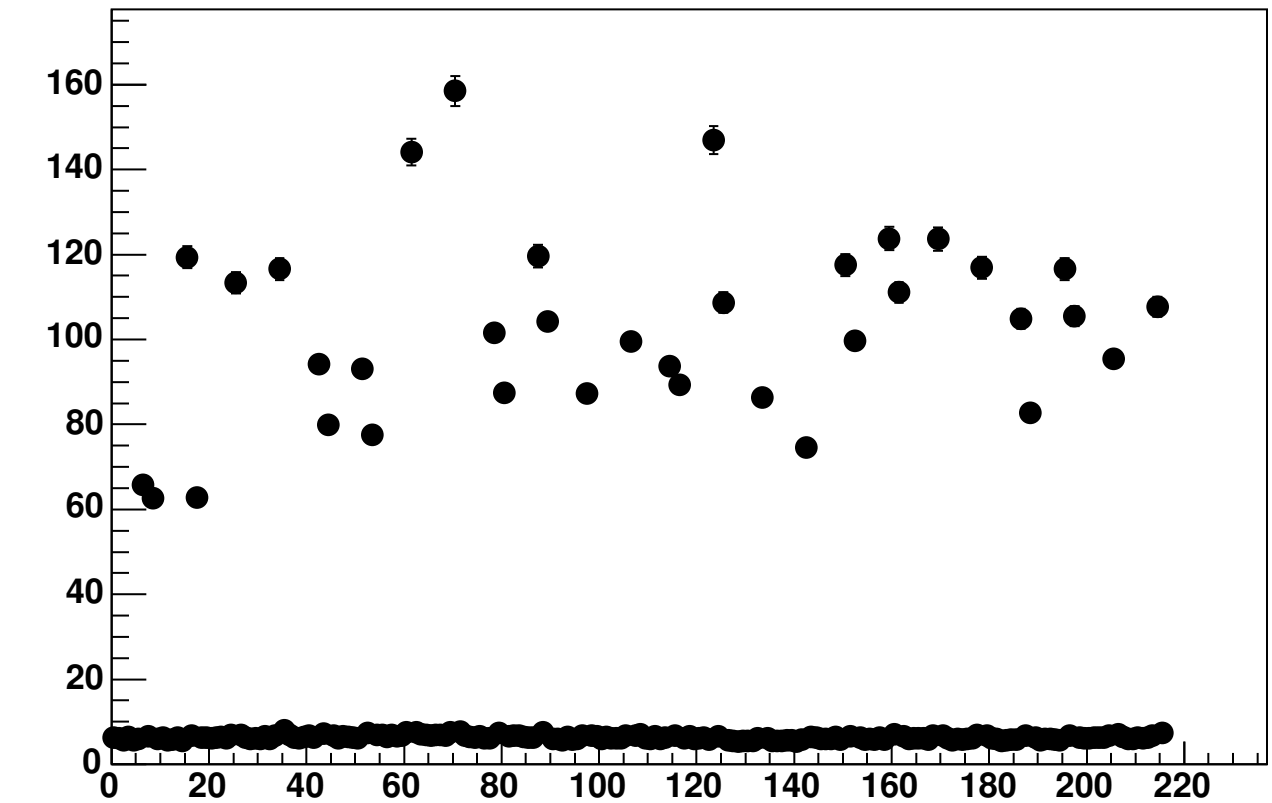
Enable 2, Hold=30, DAC=49152, ADC Noise vs 18*Chip+Chan



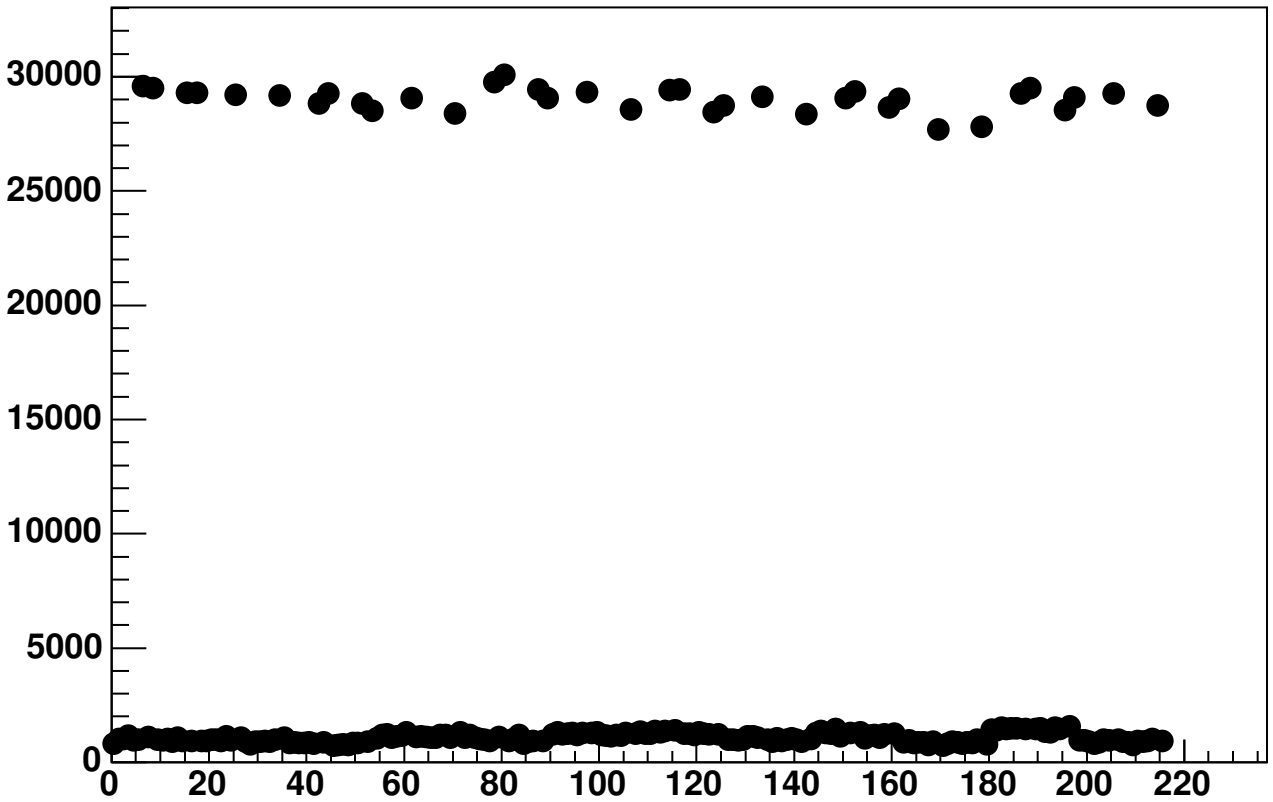
Enable 2, Hold=30, DAC=47104, ADC Mean vs 18*Chip+Chan



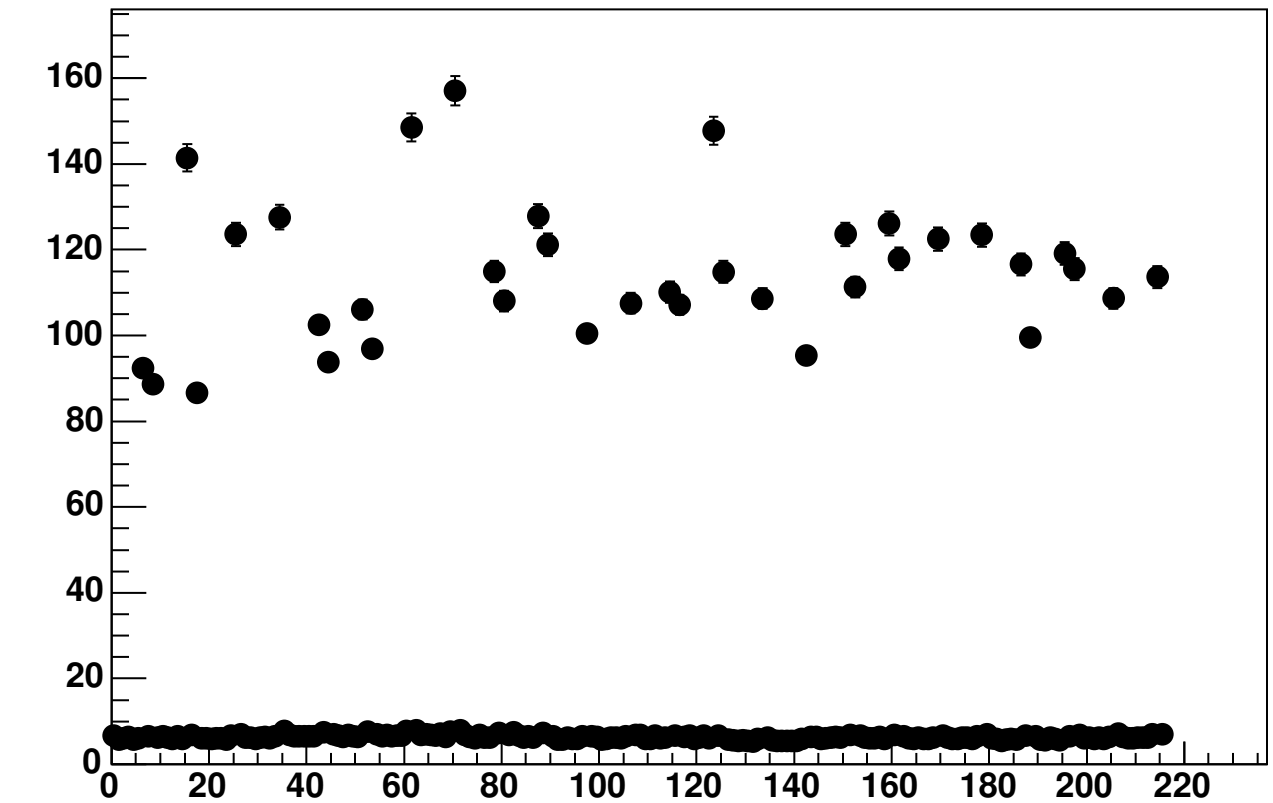
Enable 2, Hold=30, DAC=47104, ADC Noise vs 18*Chip+Chan



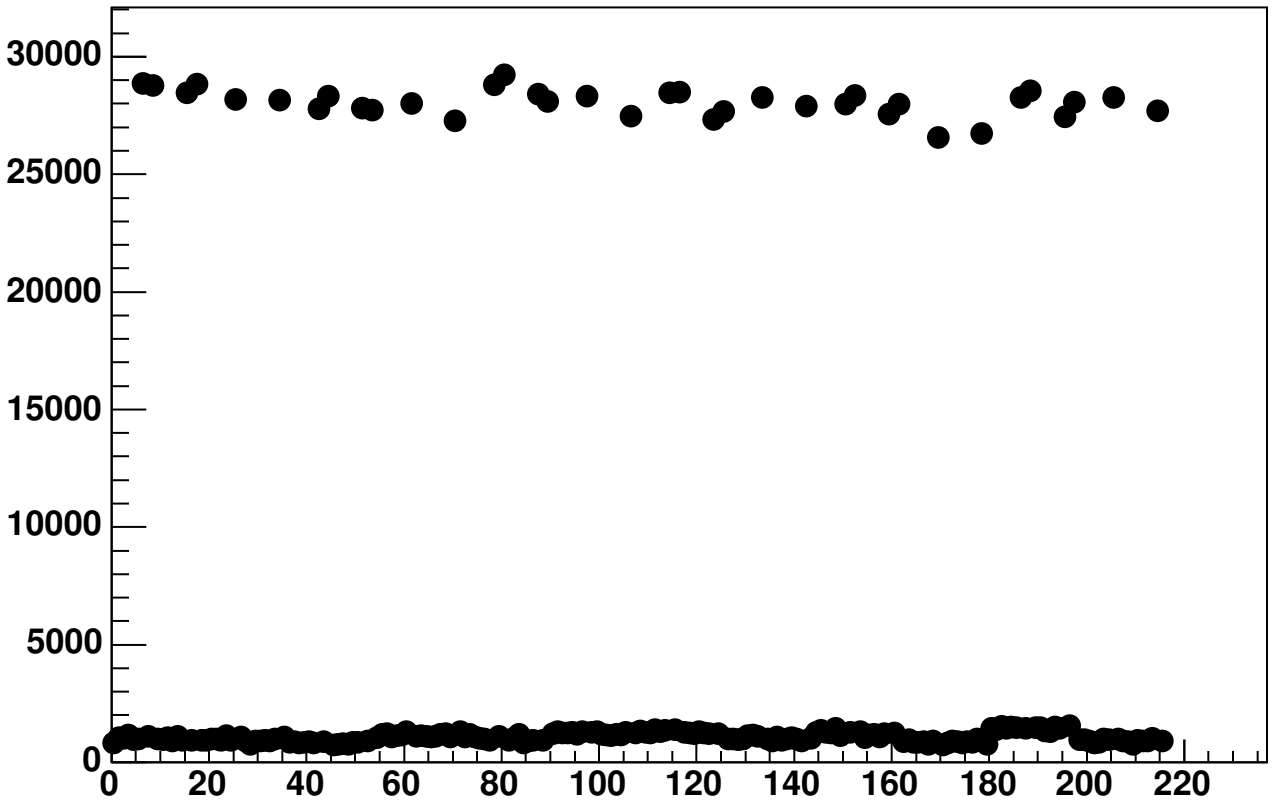
Enable 2, Hold=30, DAC=45056, ADC Mean vs 18*Chip+Chan



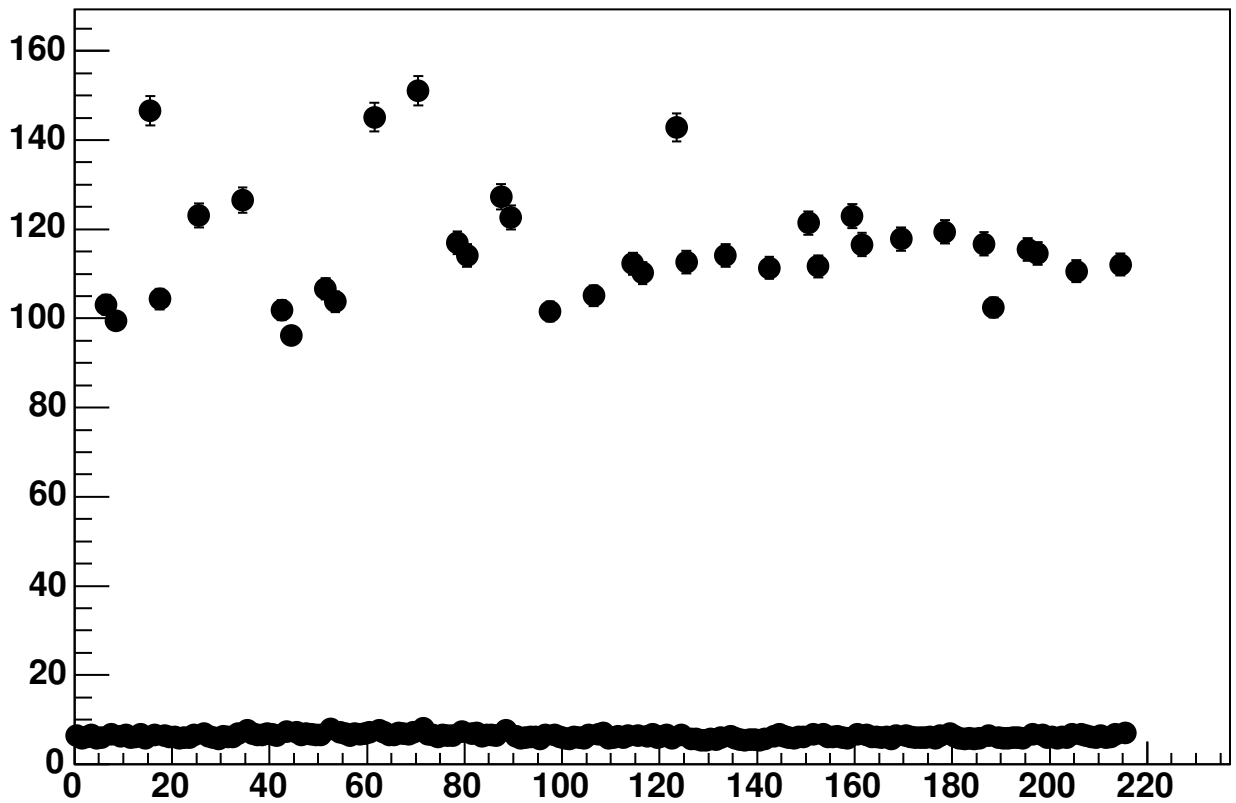
Enable 2, Hold=30, DAC=45056, ADC Noise vs 18*Chip+Chan



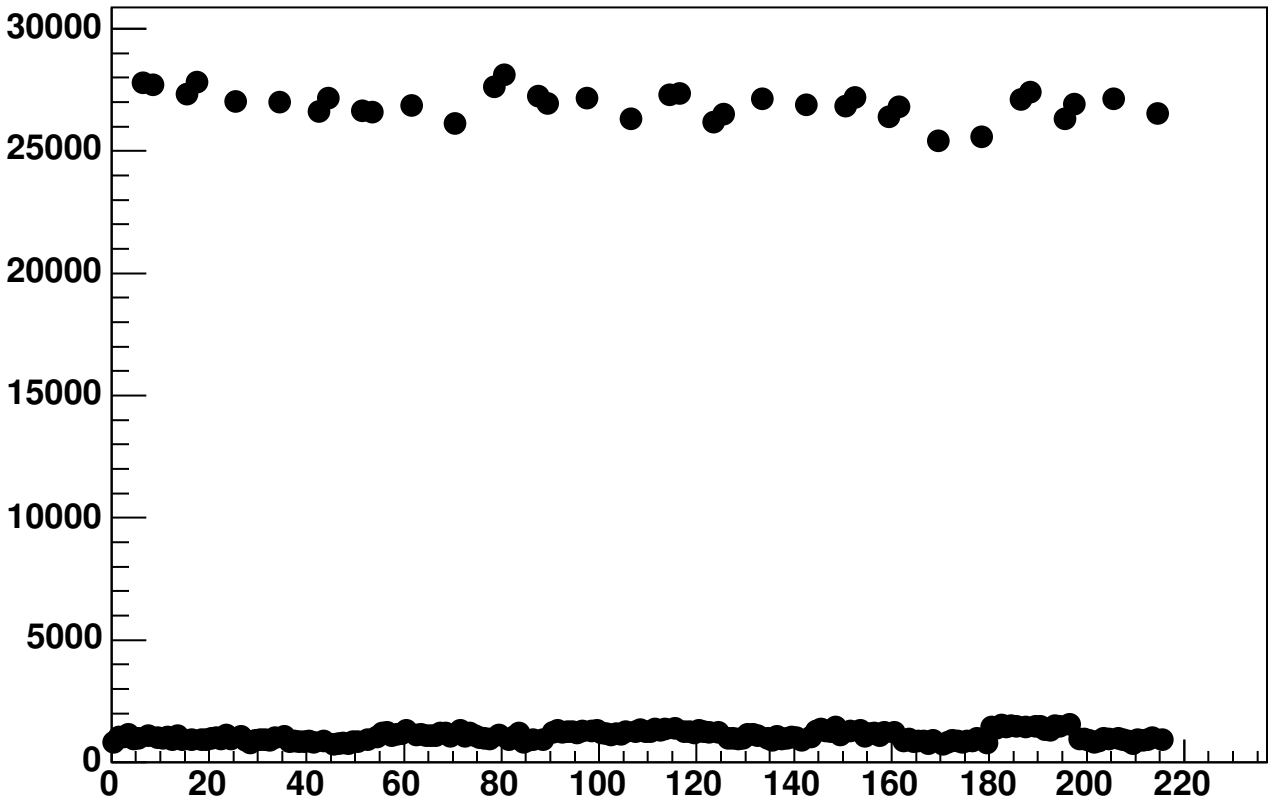
Enable 2, Hold=30, DAC=43008, ADC Mean vs 18*Chip+Chan



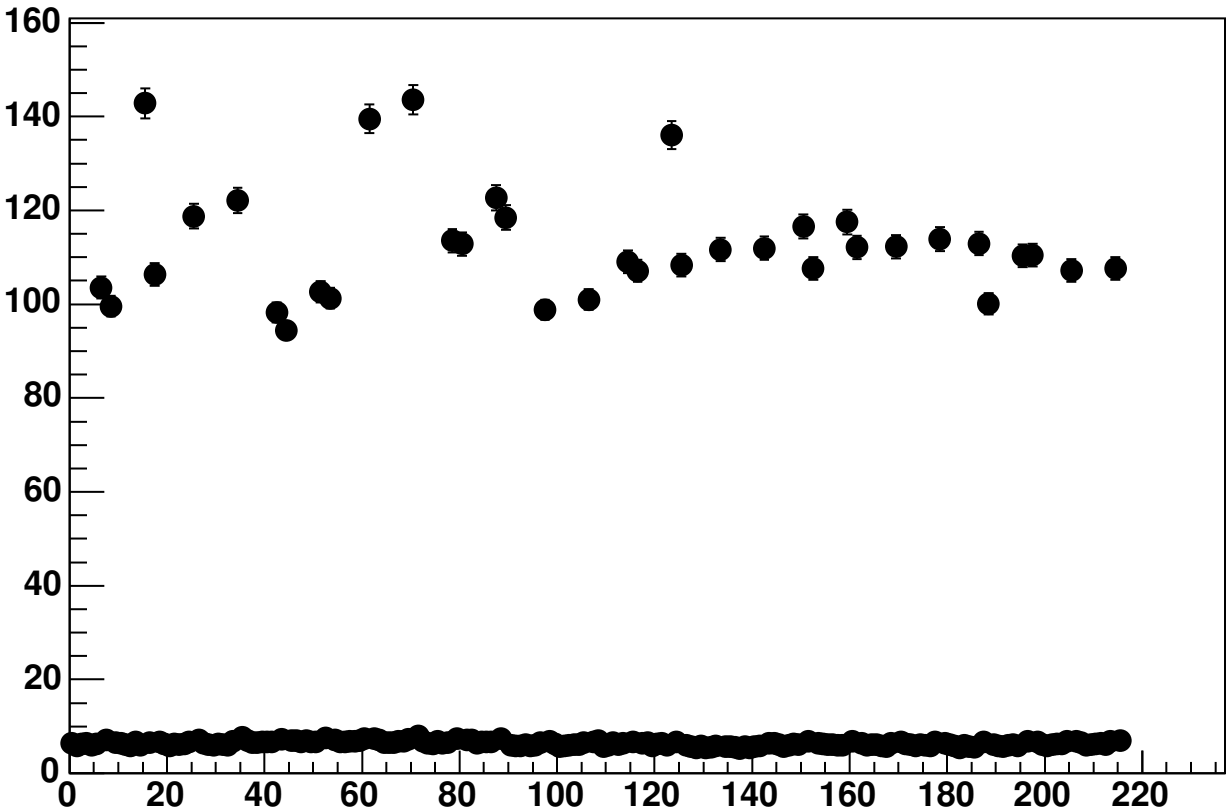
Enable 2, Hold=30, DAC=43008, ADC Noise vs 18*Chip+Chan



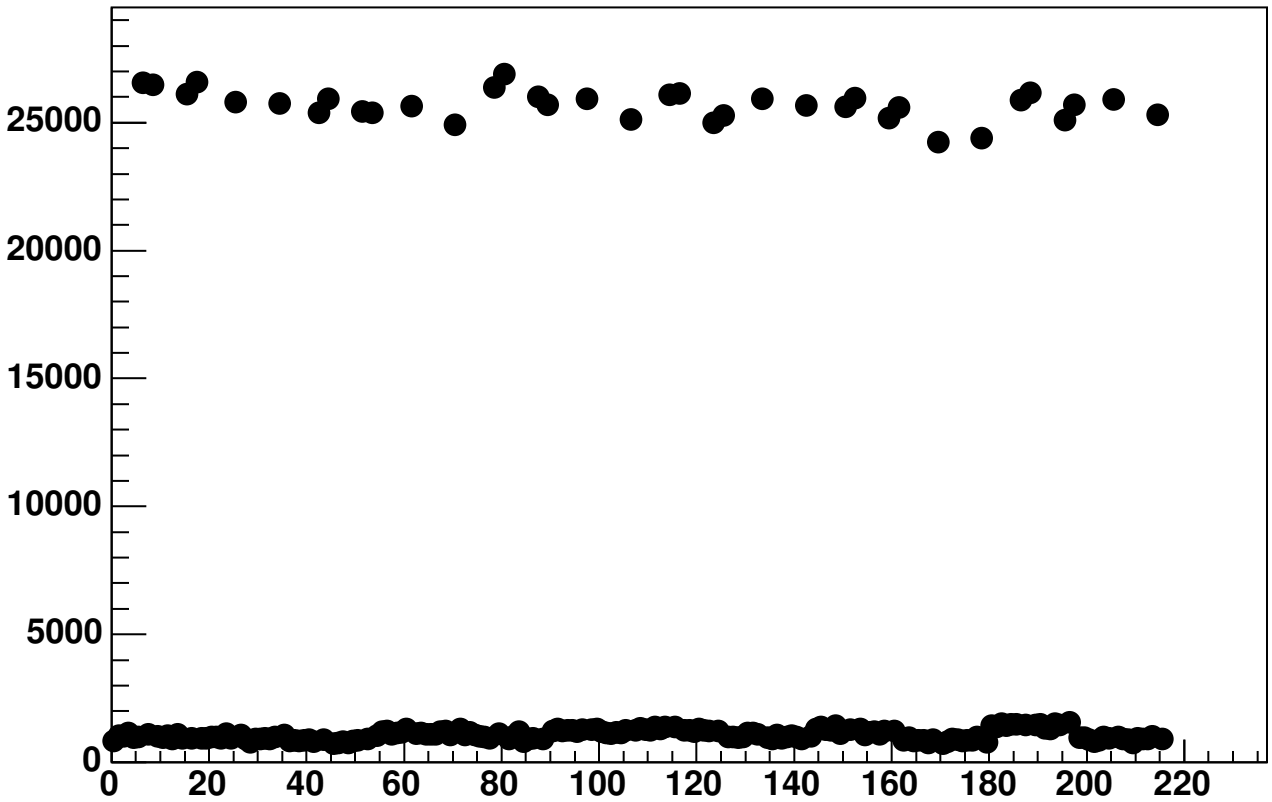
Enable 2, Hold=30, DAC=40960, ADC Mean vs 18*Chip+Chan



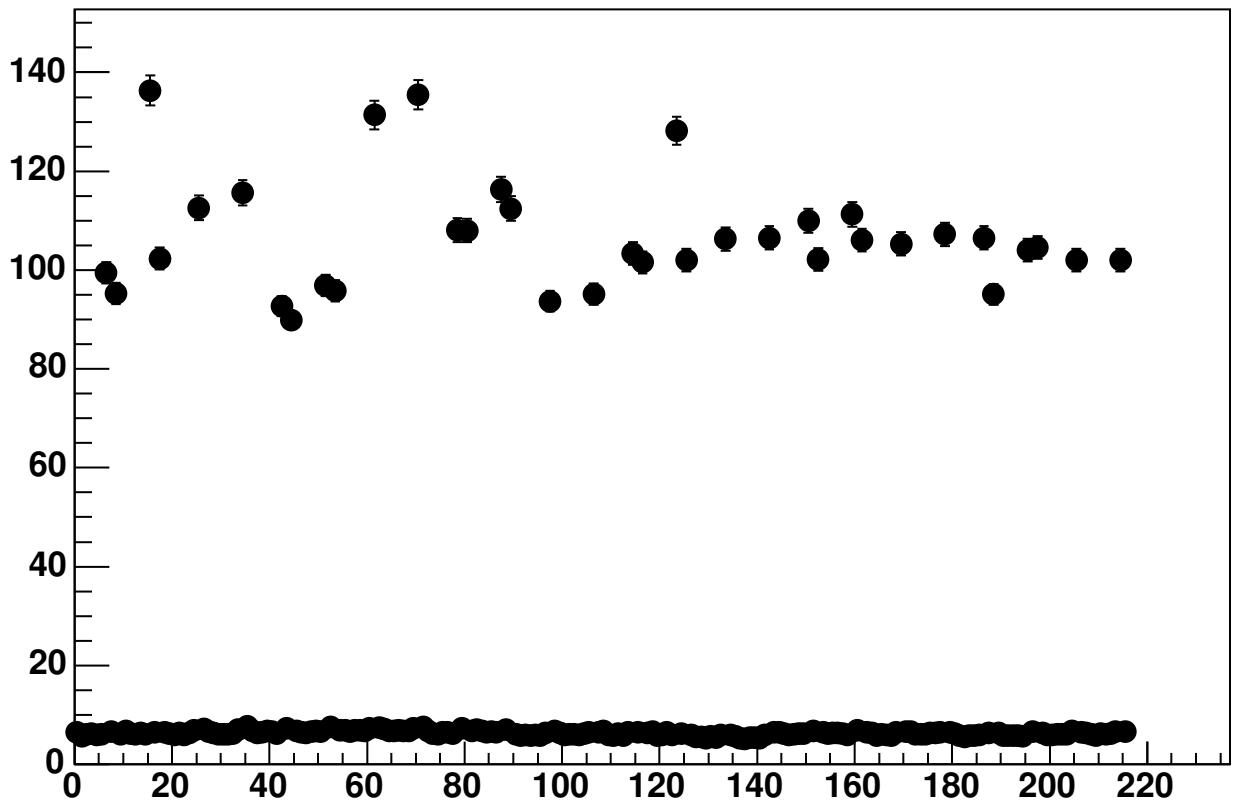
Enable 2, Hold=30, DAC=40960, ADC Noise vs 18*Chip+Chan



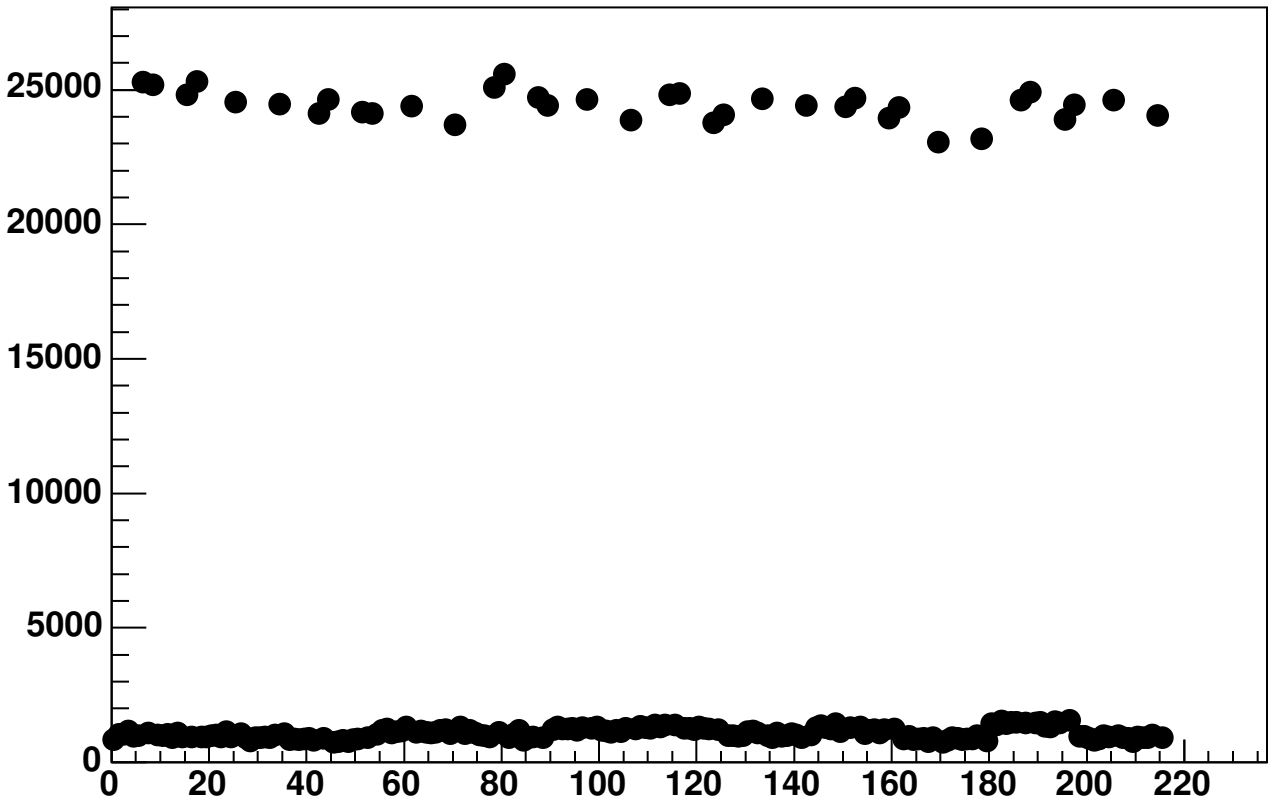
Enable 2, Hold=30, DAC=38912, ADC Mean vs 18*Chip+Chan



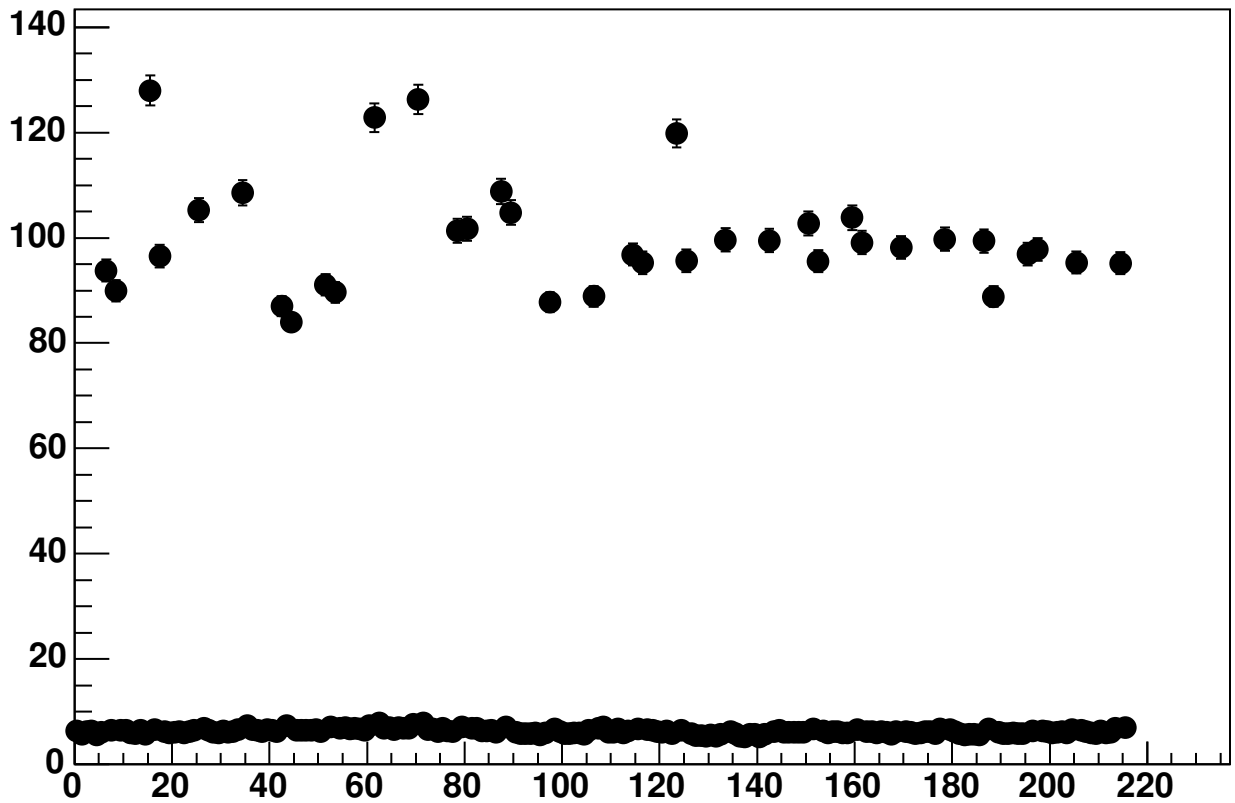
Enable 2, Hold=30, DAC=38912, ADC Noise vs 18*Chip+Chan



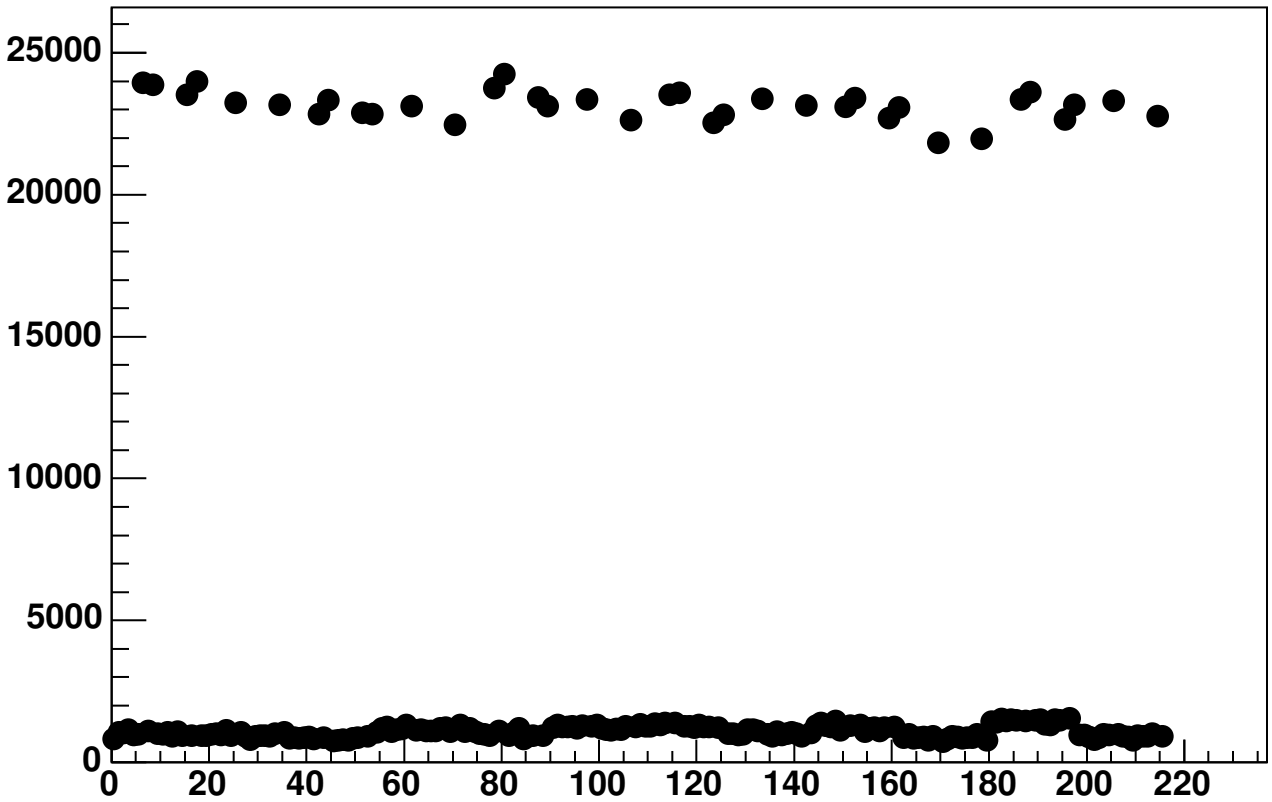
Enable 2, Hold=30, DAC=36864, ADC Mean vs 18*Chip+Chan



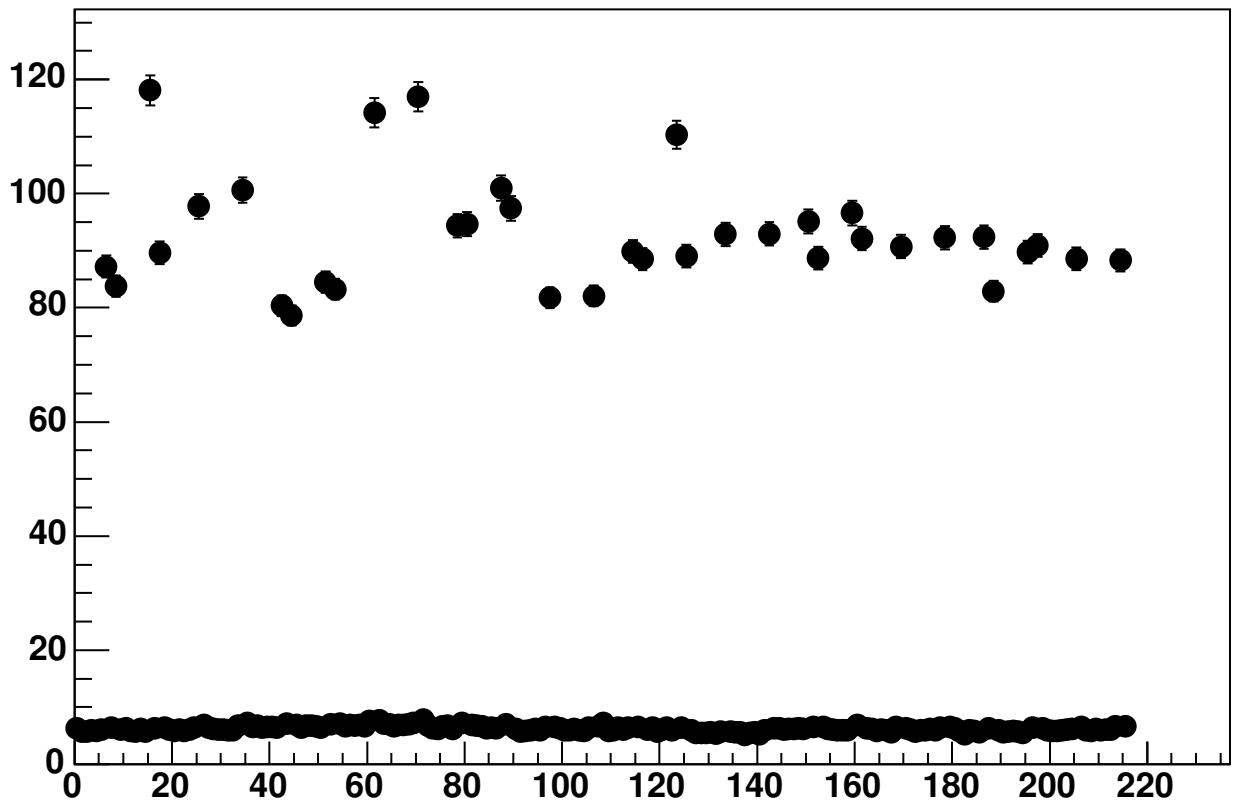
Enable 2, Hold=30, DAC=36864, ADC Noise vs 18*Chip+Chan



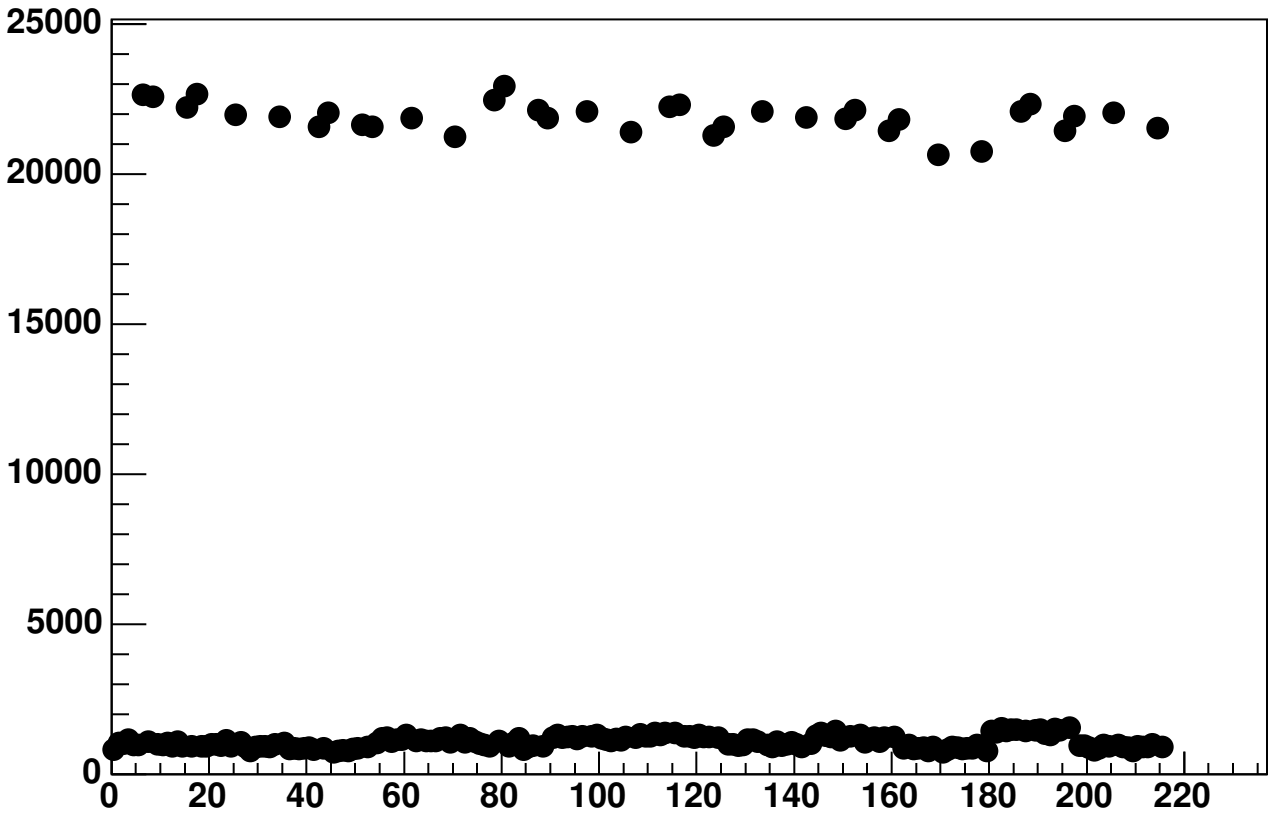
Enable 2, Hold=30, DAC=34816, ADC Mean vs 18*Chip+Chan



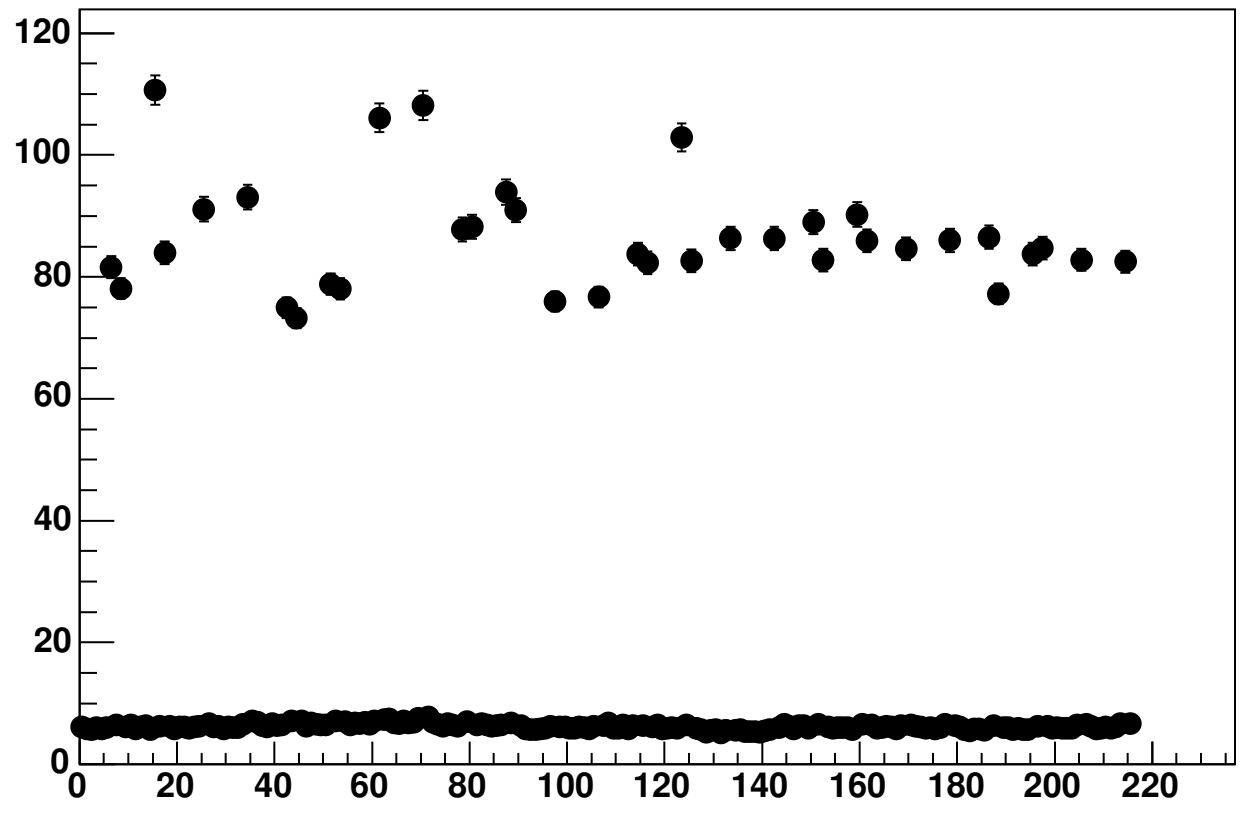
Enable 2, Hold=30, DAC=34816, ADC Noise vs 18*Chip+Chan



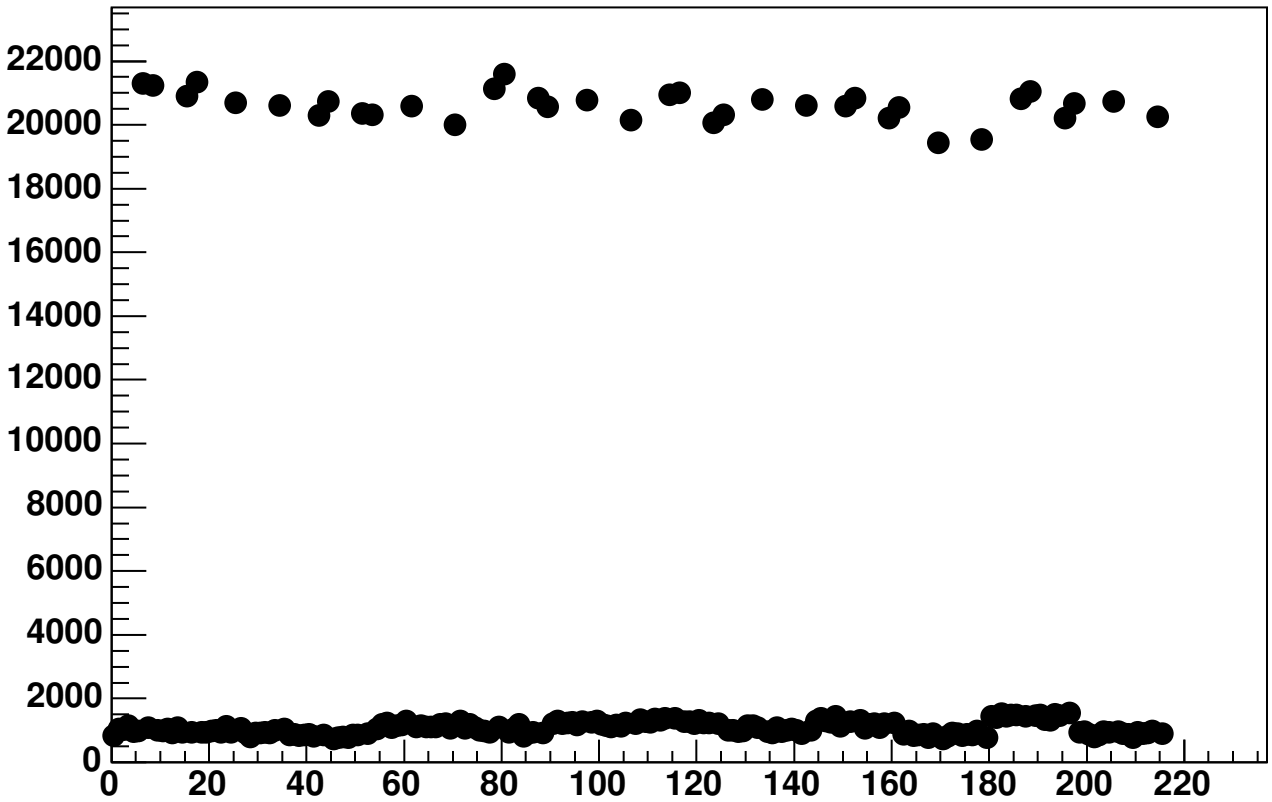
Enable 2, Hold=30, DAC=32768, ADC Mean vs 18*Chip+Chan



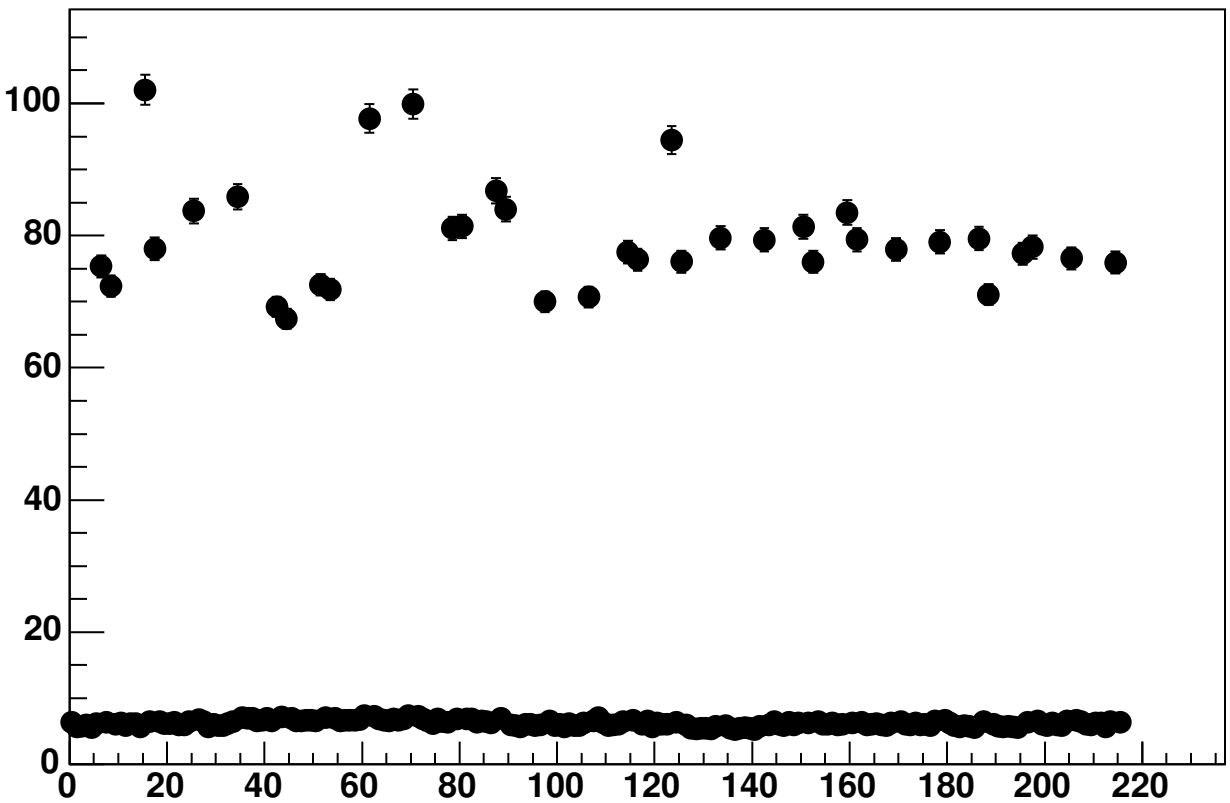
Enable 2, Hold=30, DAC=32768, ADC Noise vs 18*Chip+Chan



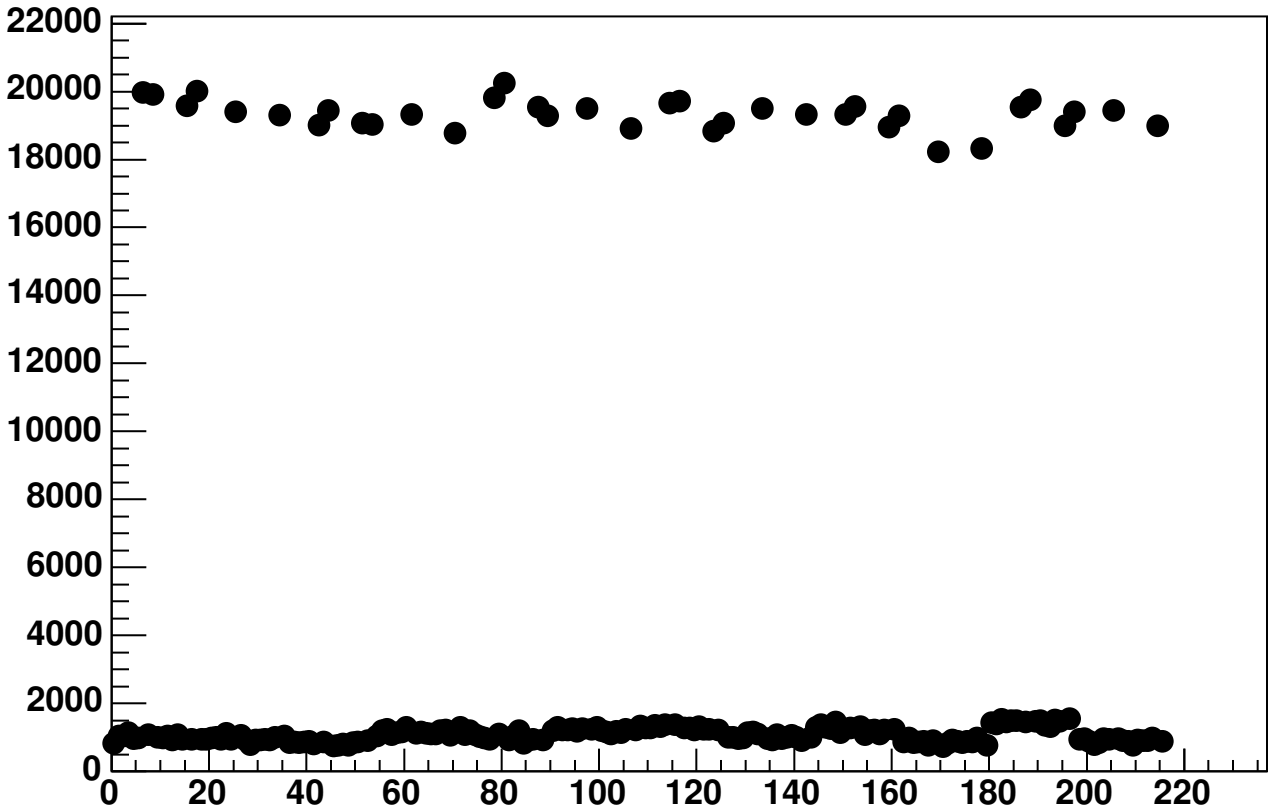
Enable 2, Hold=30, DAC=30720, ADC Mean vs 18*Chip+Chan



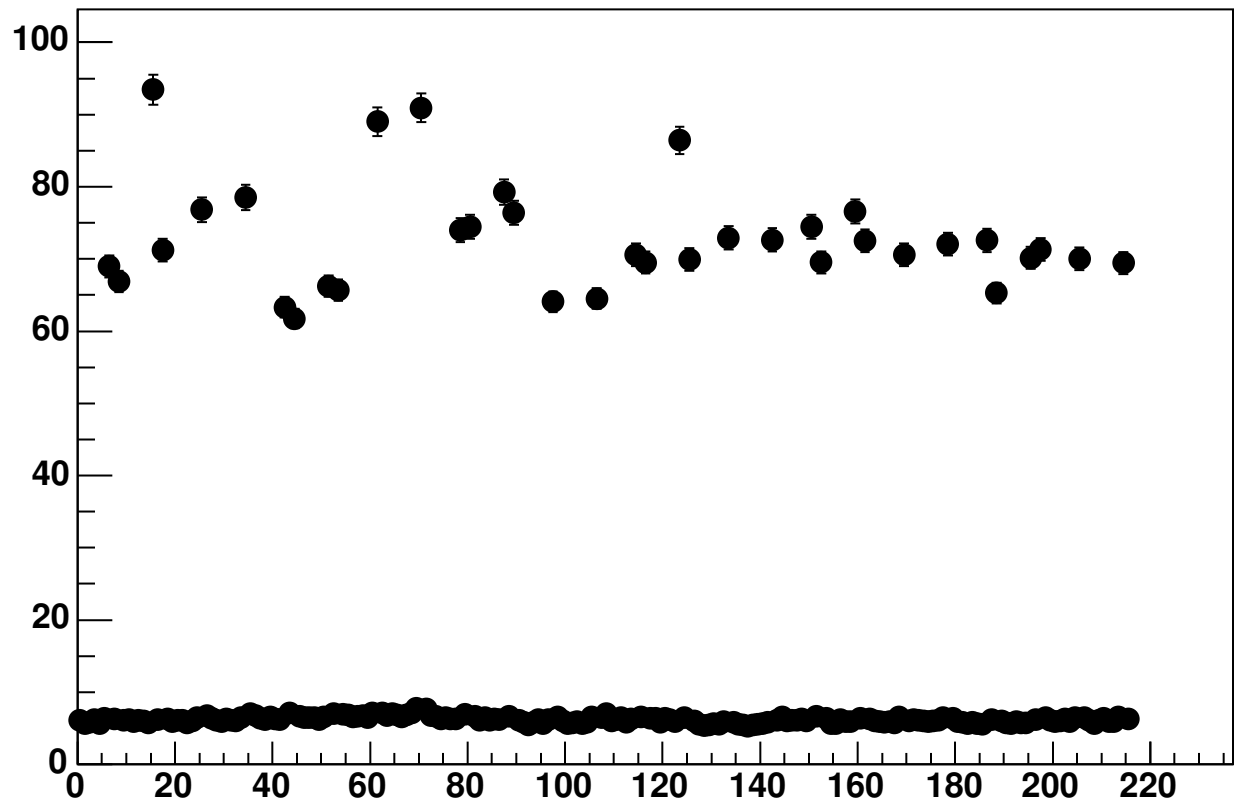
Enable 2, Hold=30, DAC=30720, ADC Noise vs 18*Chip+Chan



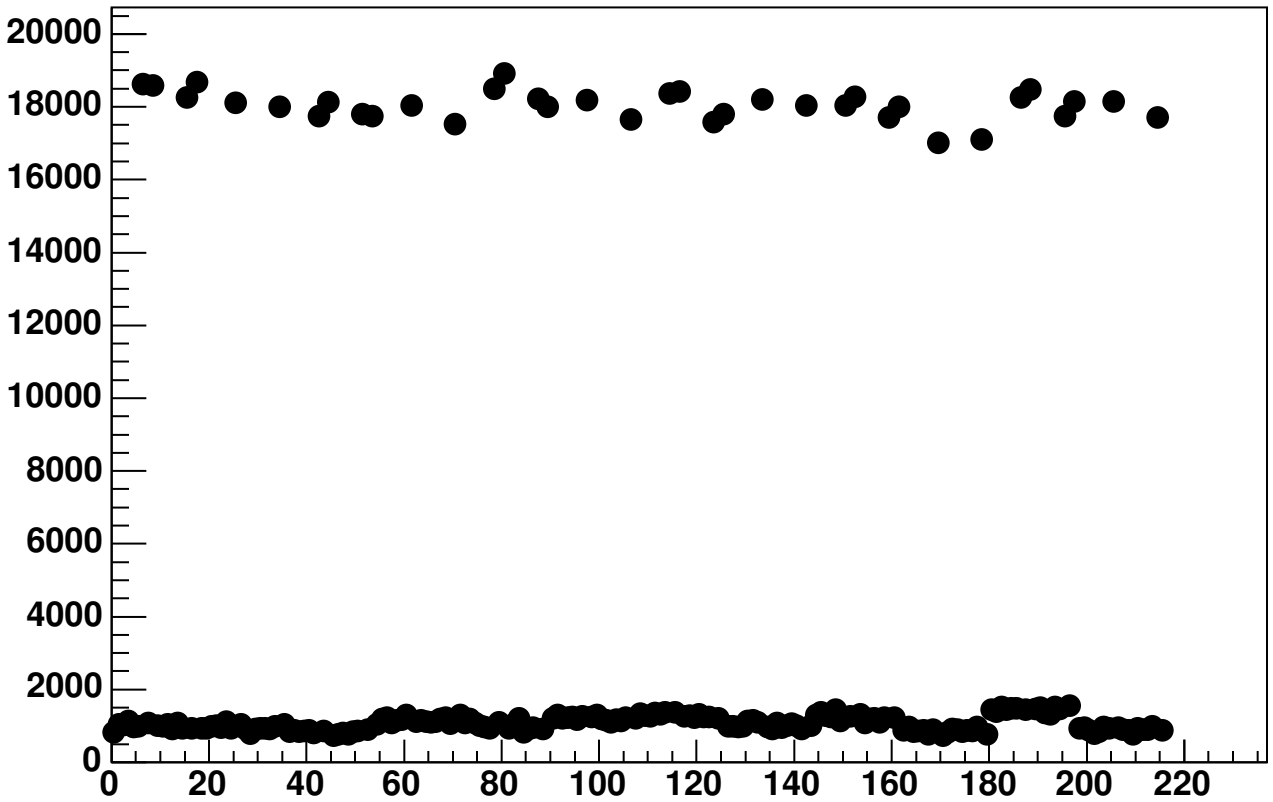
Enable 2, Hold=30, DAC=28672, ADC Mean vs 18*Chip+Chan



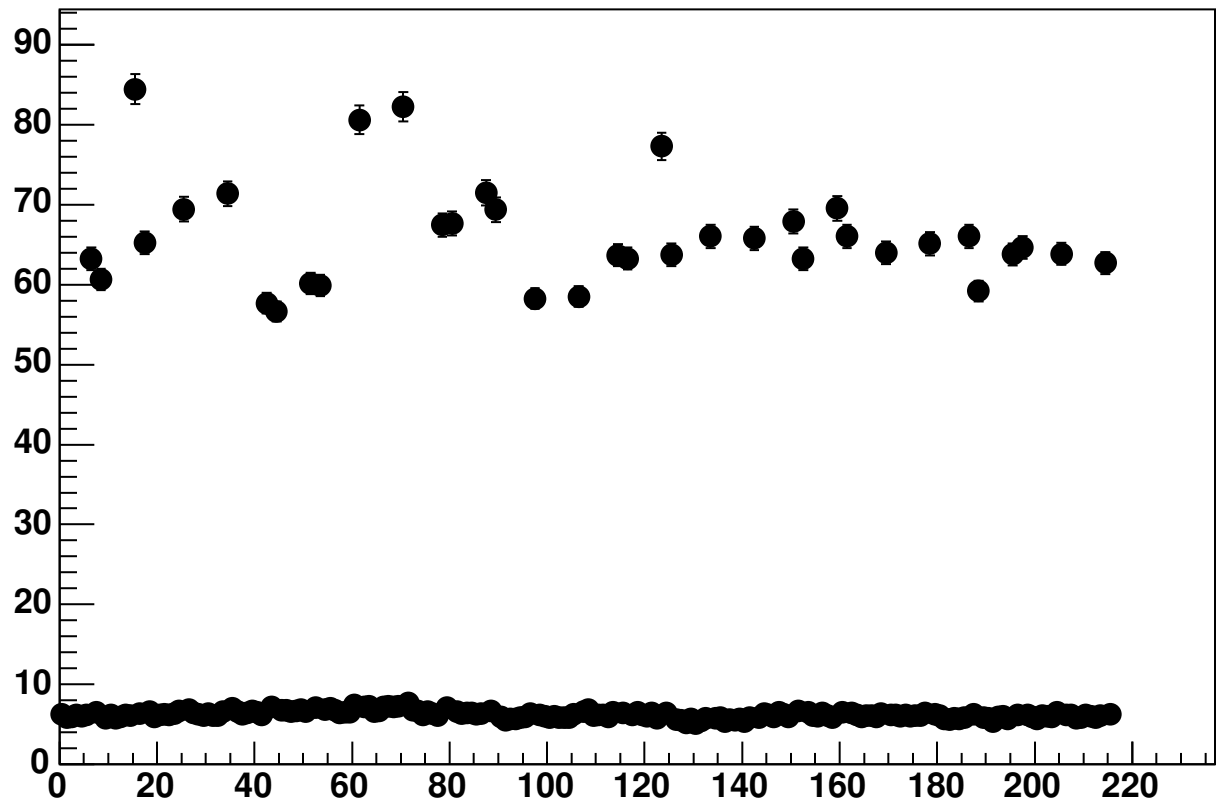
Enable 2, Hold=30, DAC=28672, ADC Noise vs 18*Chip+Chan



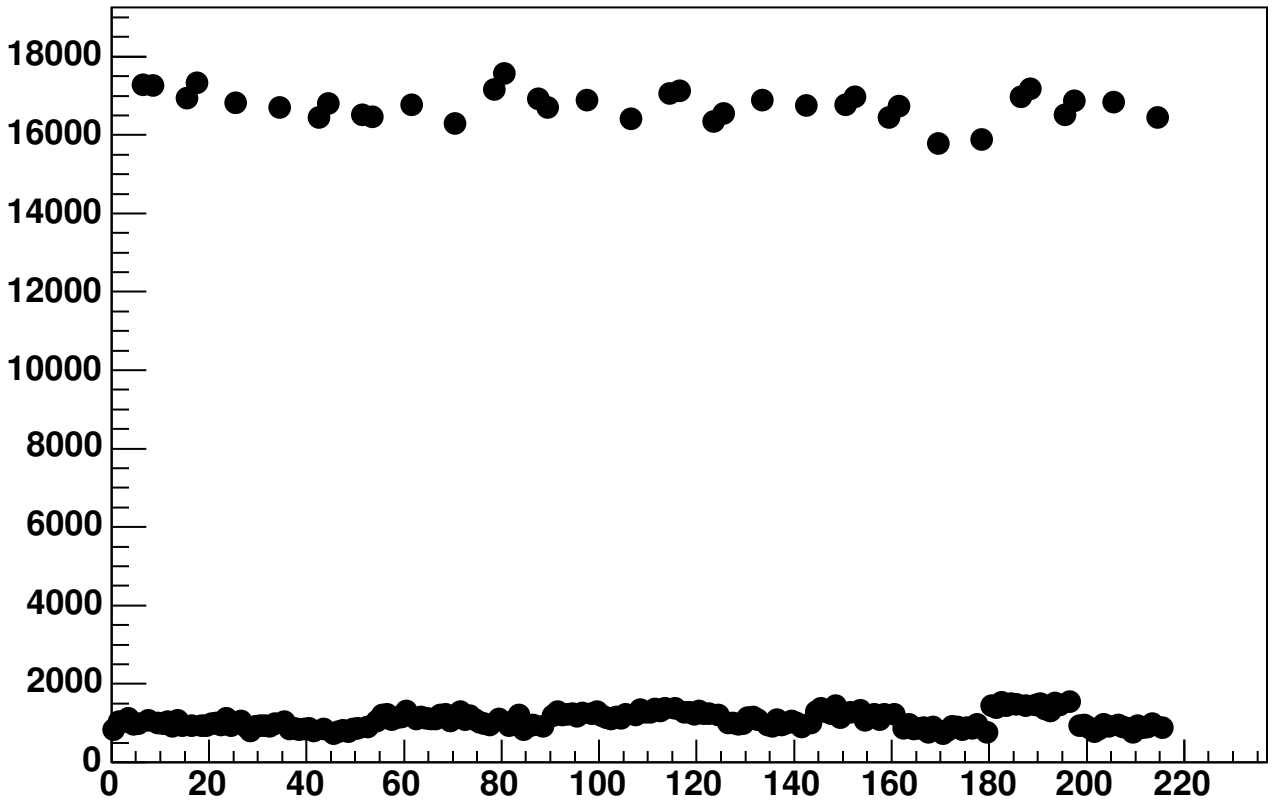
Enable 2, Hold=30, DAC=26624, ADC Mean vs 18*Chip+Chan



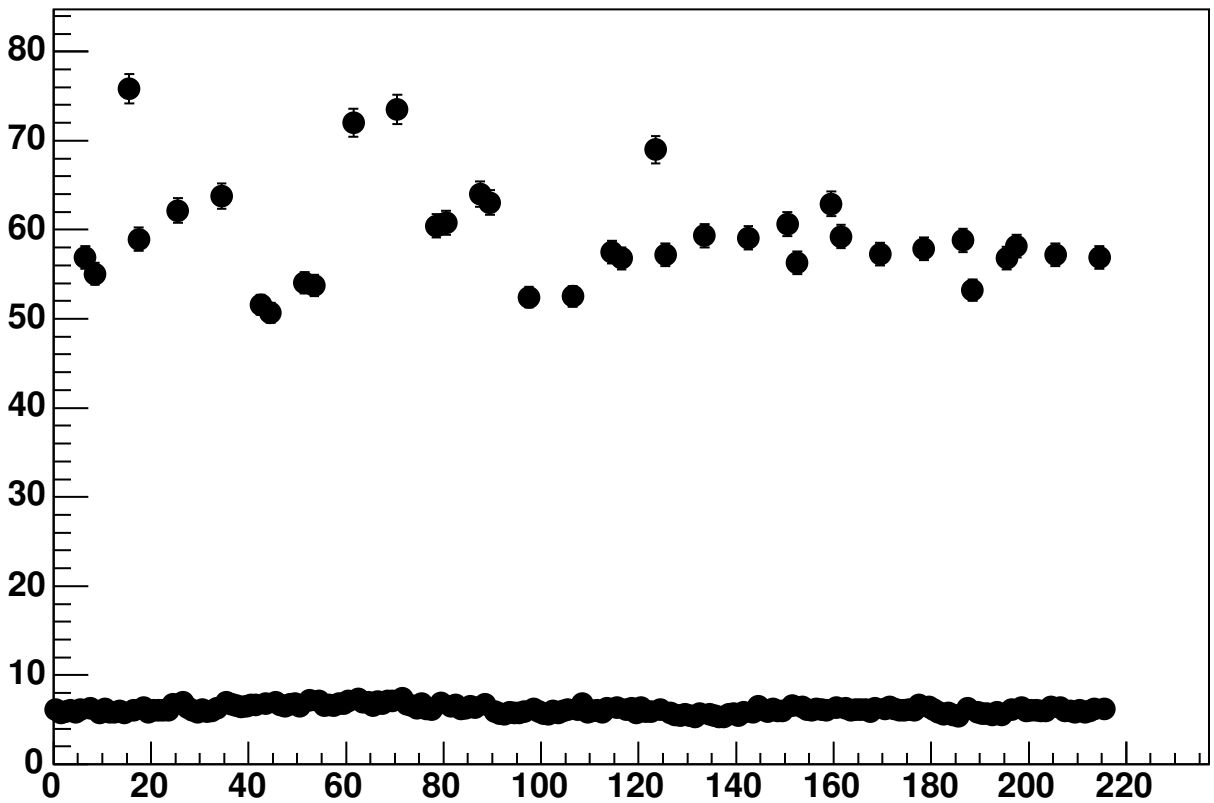
Enable 2, Hold=30, DAC=26624, ADC Noise vs 18*Chip+Chan



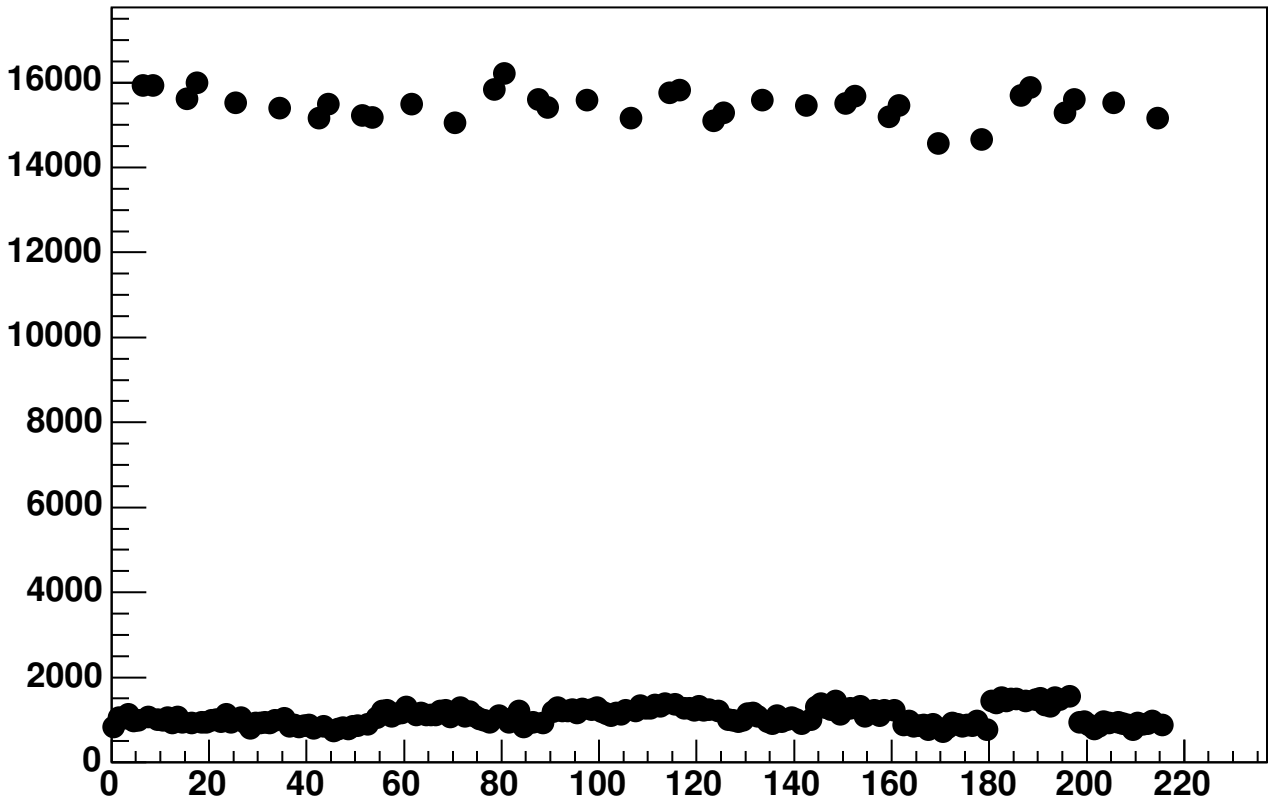
Enable 2, Hold=30, DAC=24576, ADC Mean vs 18*Chip+Chan



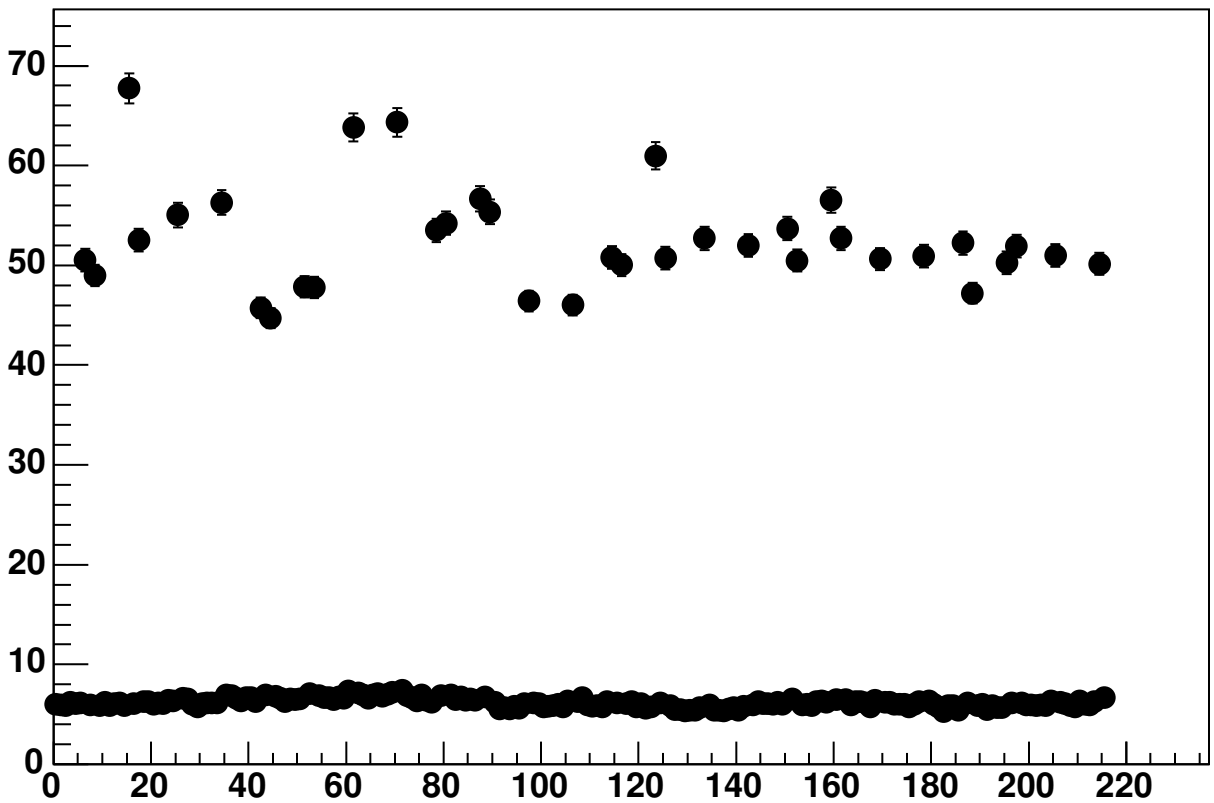
Enable 2, Hold=30, DAC=24576, ADC Noise vs 18*Chip+Chan



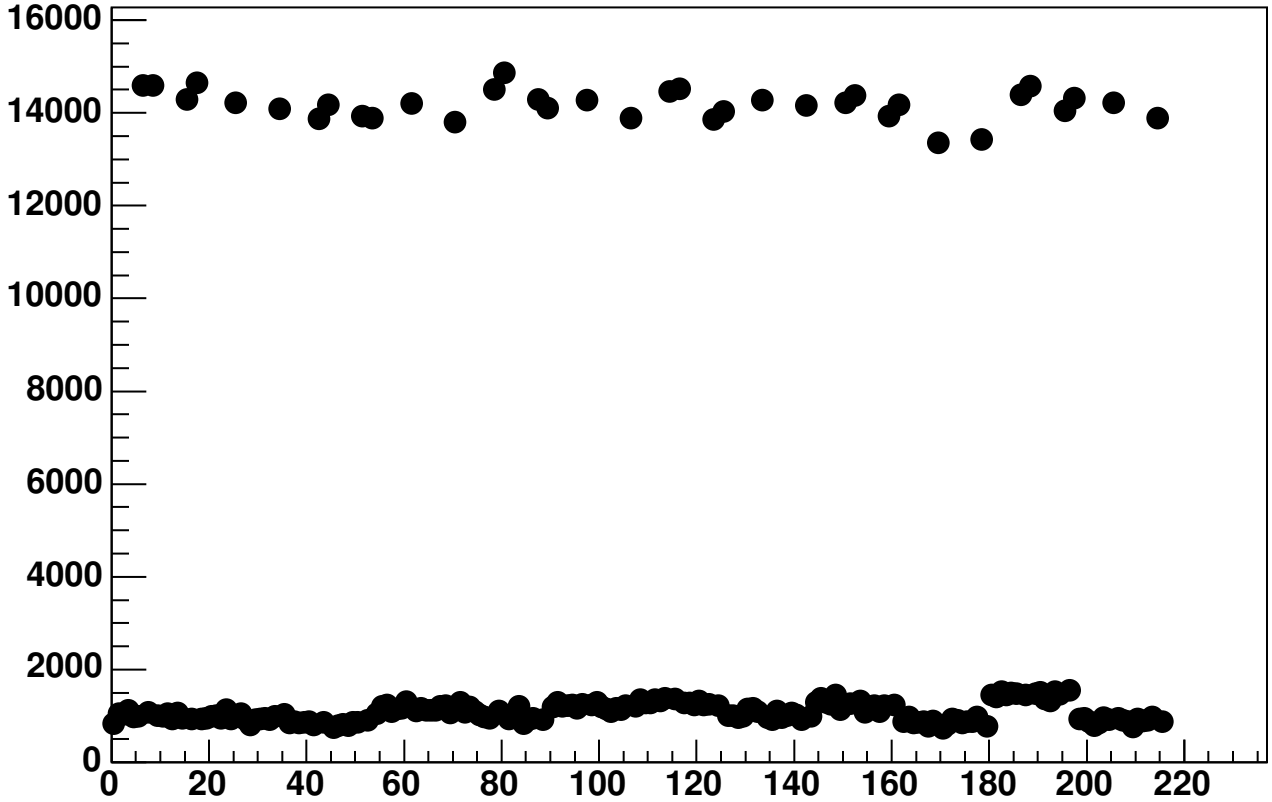
Enable 2, Hold=30, DAC=22528, ADC Mean vs 18*Chip+Chan



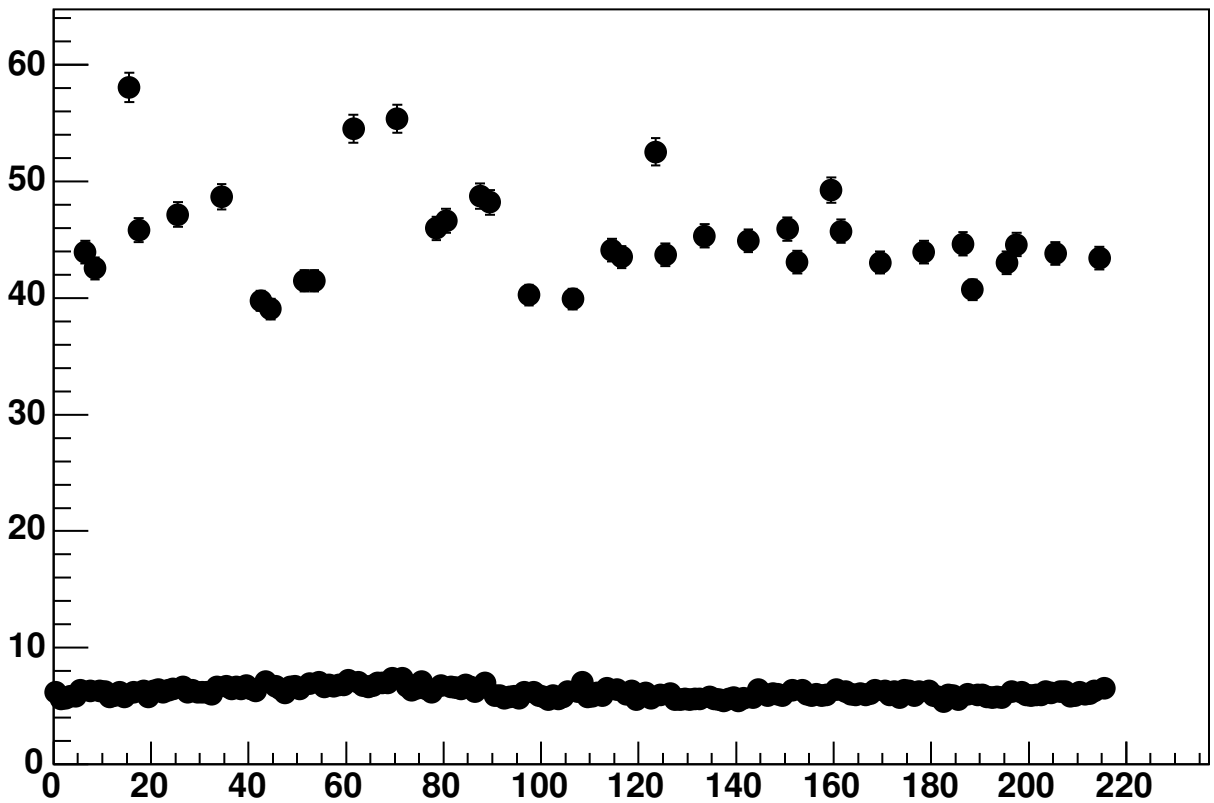
Enable 2, Hold=30, DAC=22528, ADC Noise vs 18*Chip+Chan



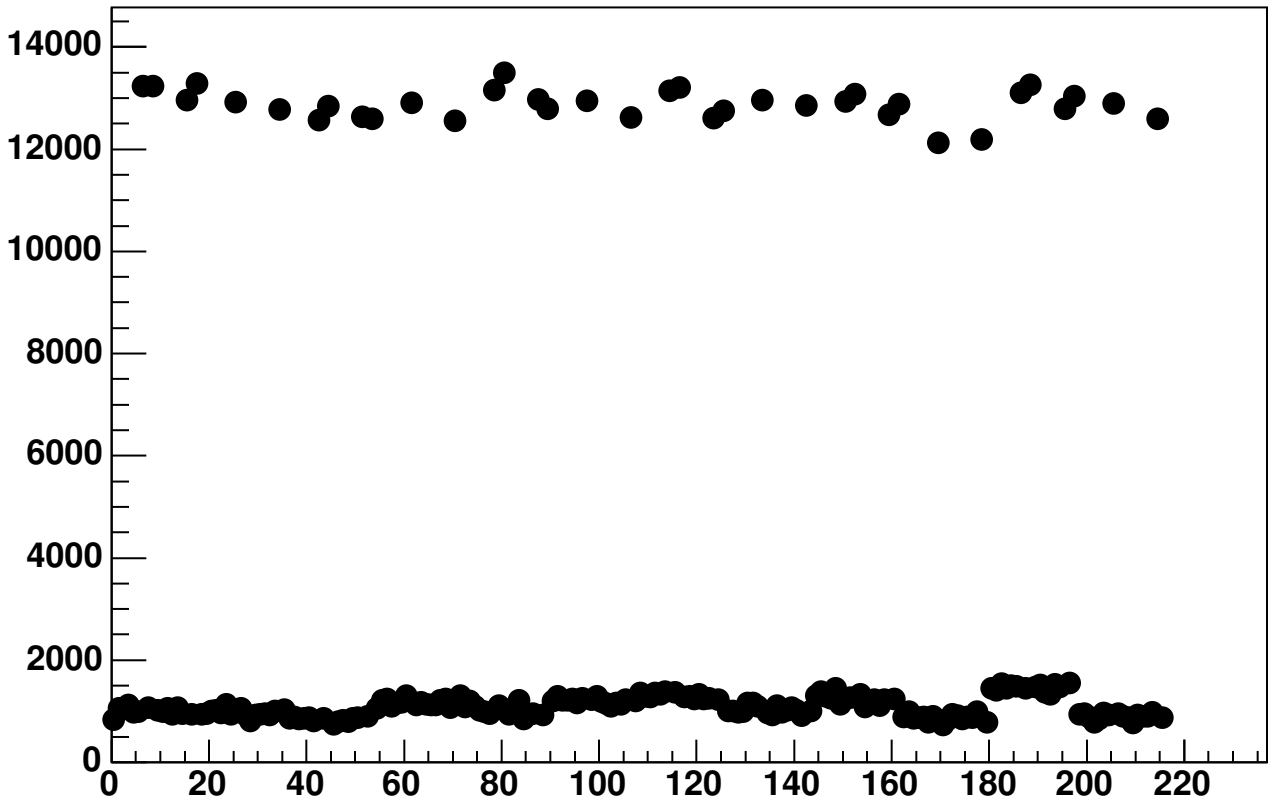
Enable 2, Hold=30, DAC=20480, ADC Mean vs 18*Chip+Chan



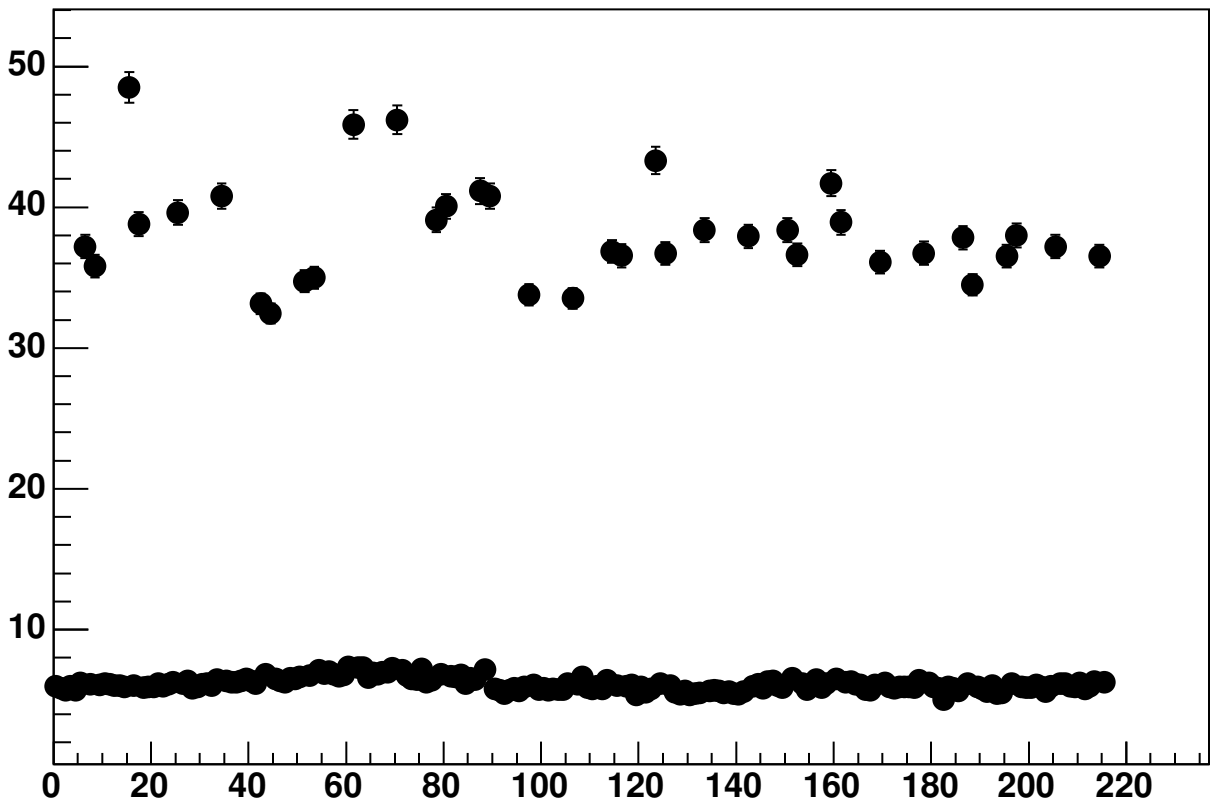
Enable 2, Hold=30, DAC=20480, ADC Noise vs 18*Chip+Chan



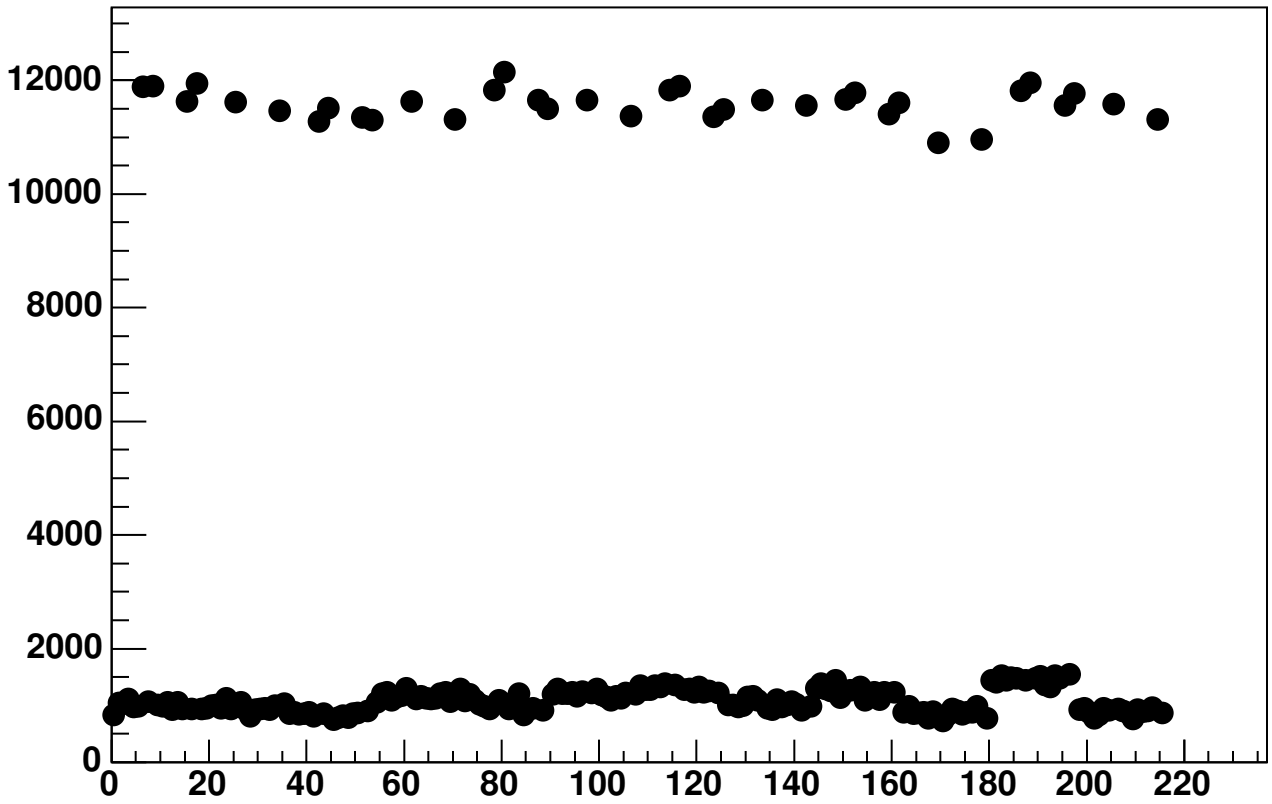
Enable 2, Hold=30, DAC=18432, ADC Mean vs 18*Chip+Chan



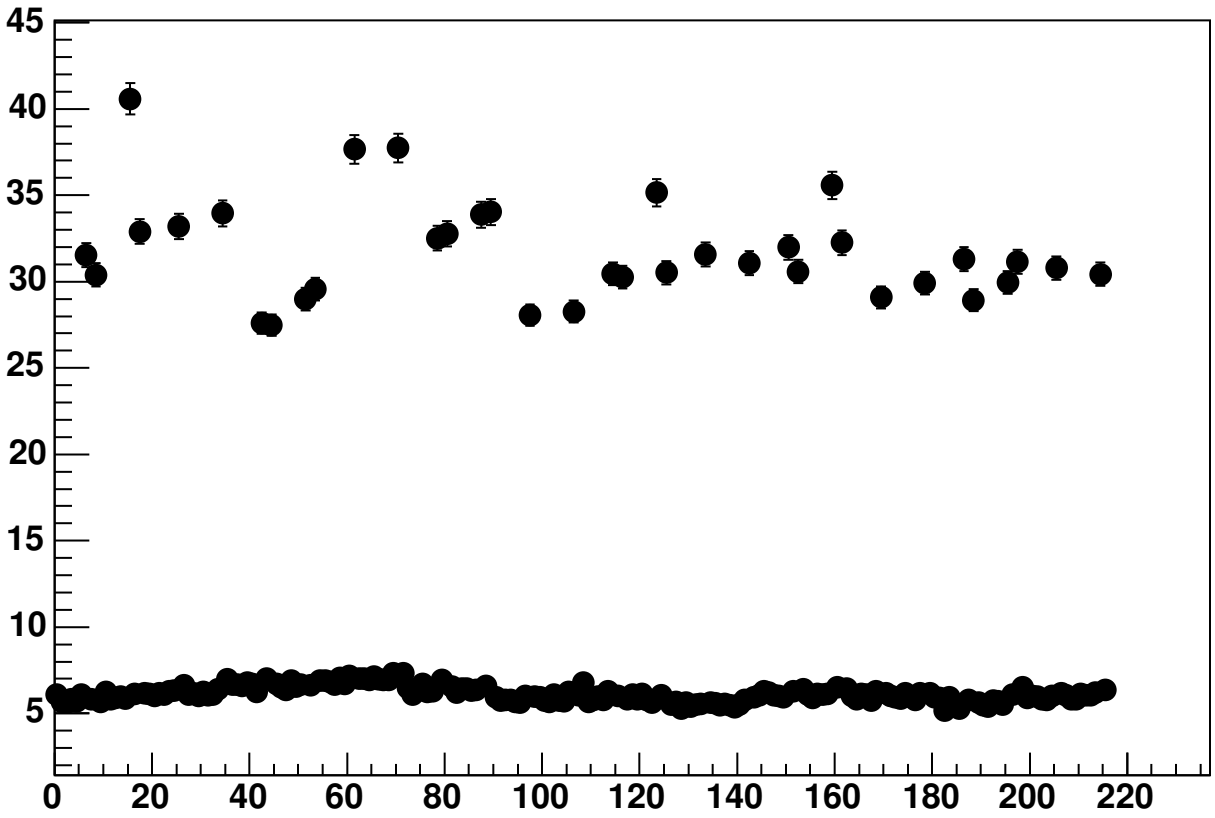
Enable 2, Hold=30, DAC=18432, ADC Noise vs 18*Chip+Chan



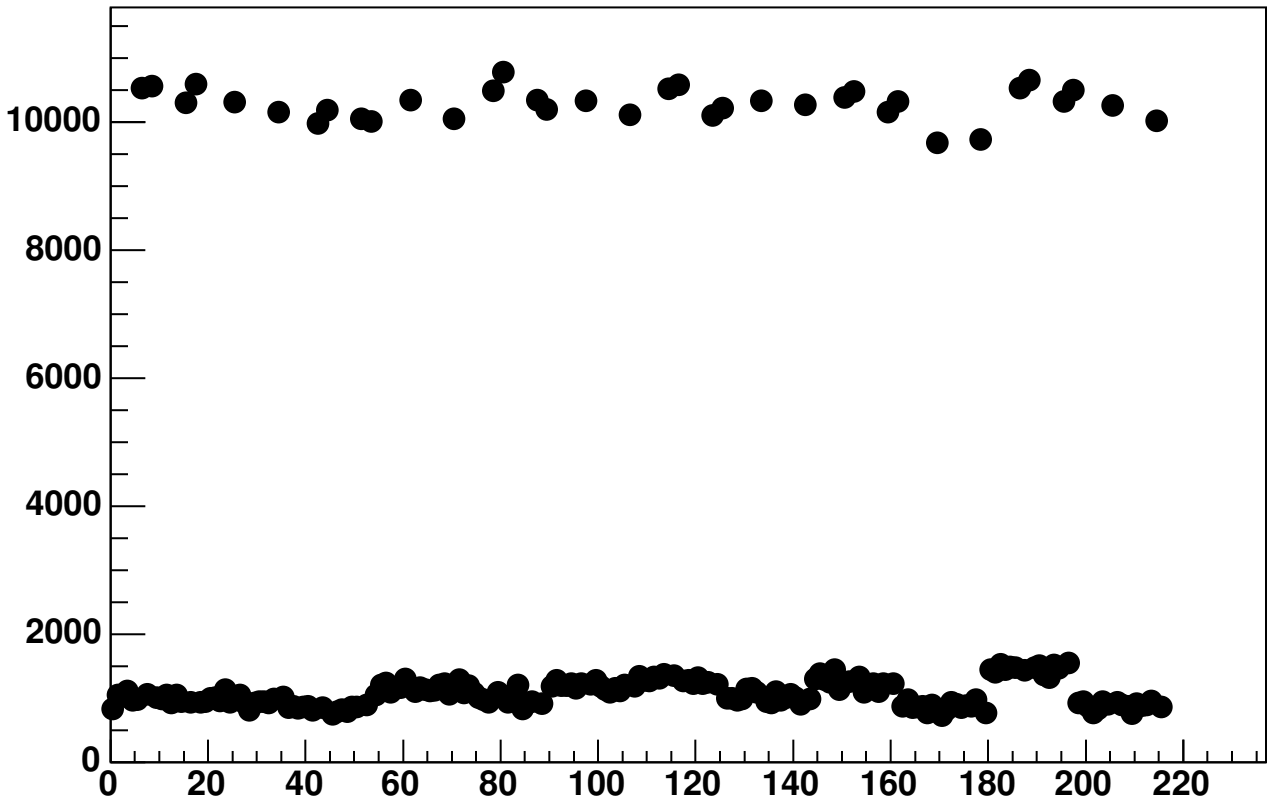
Enable 2, Hold=30, DAC=16384, ADC Mean vs 18*Chip+Chan



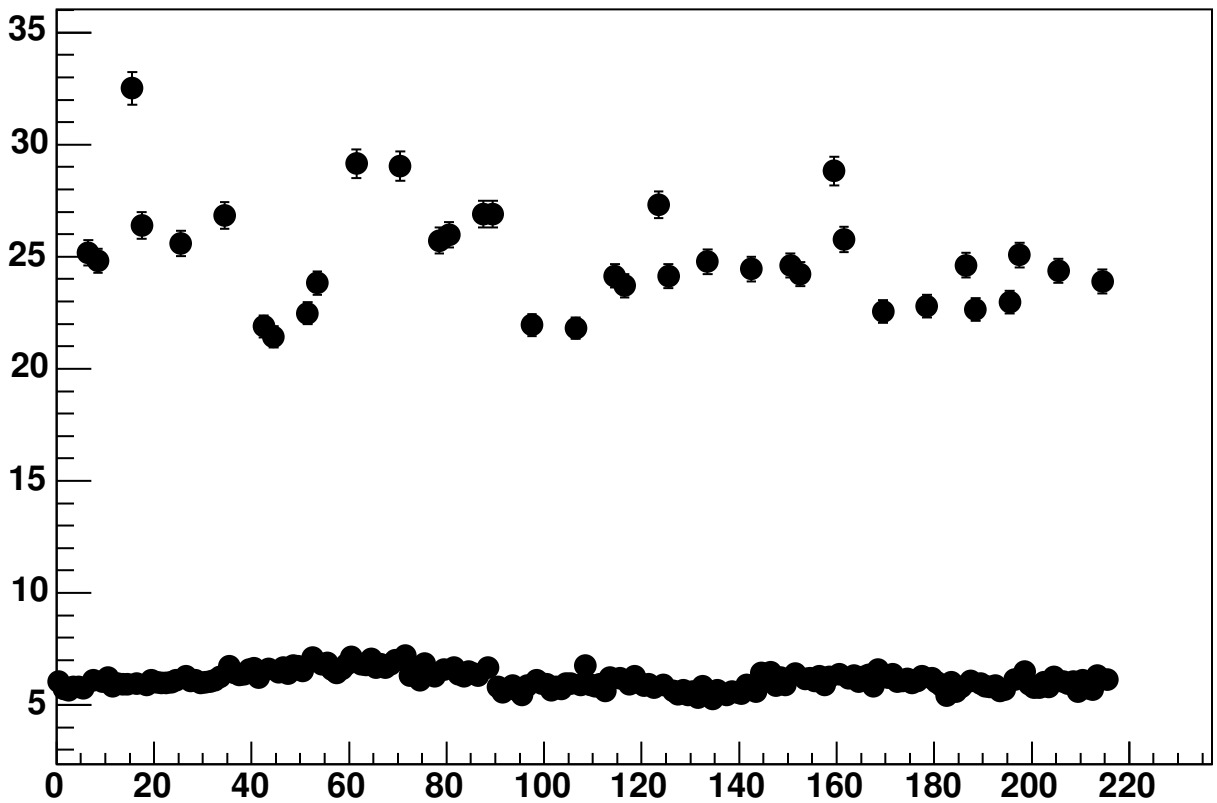
Enable 2, Hold=30, DAC=16384, ADC Noise vs 18*Chip+Chan



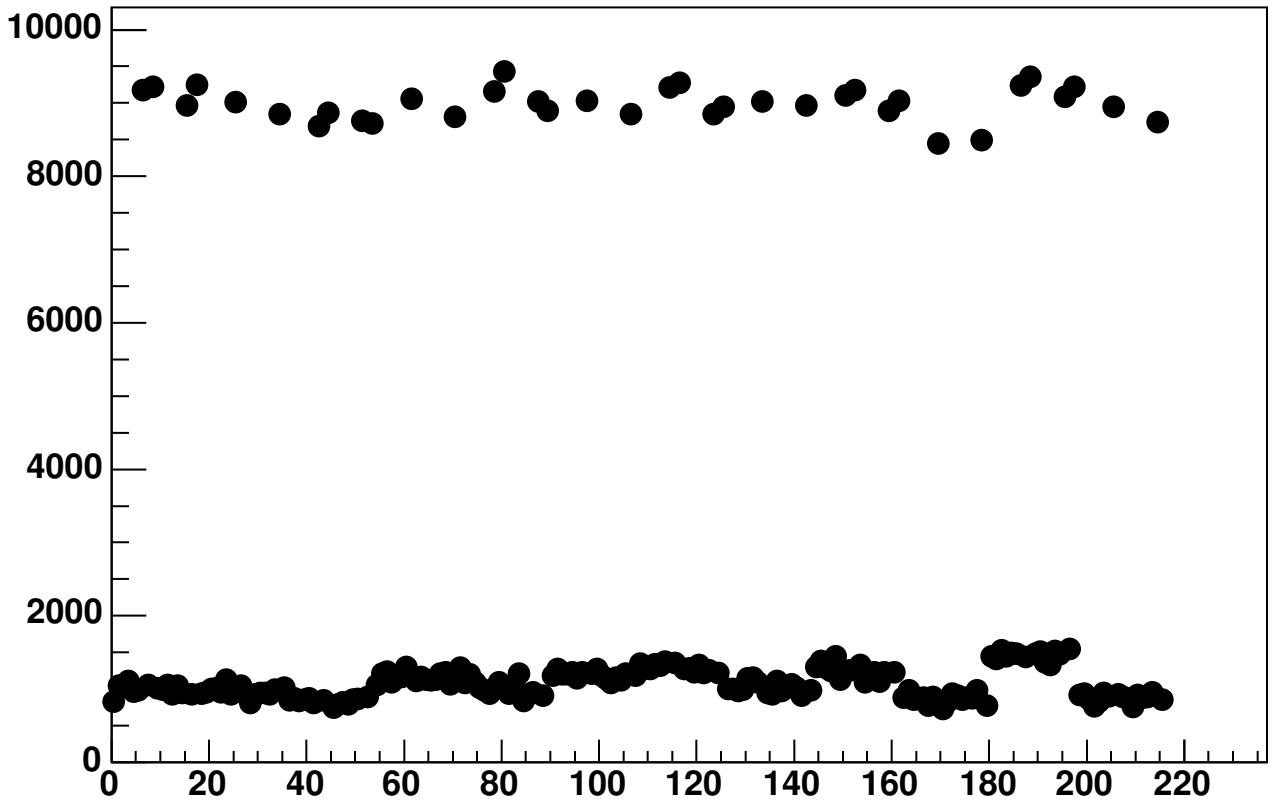
Enable 2, Hold=30, DAC=14336, ADC Mean vs 18*Chip+Chan



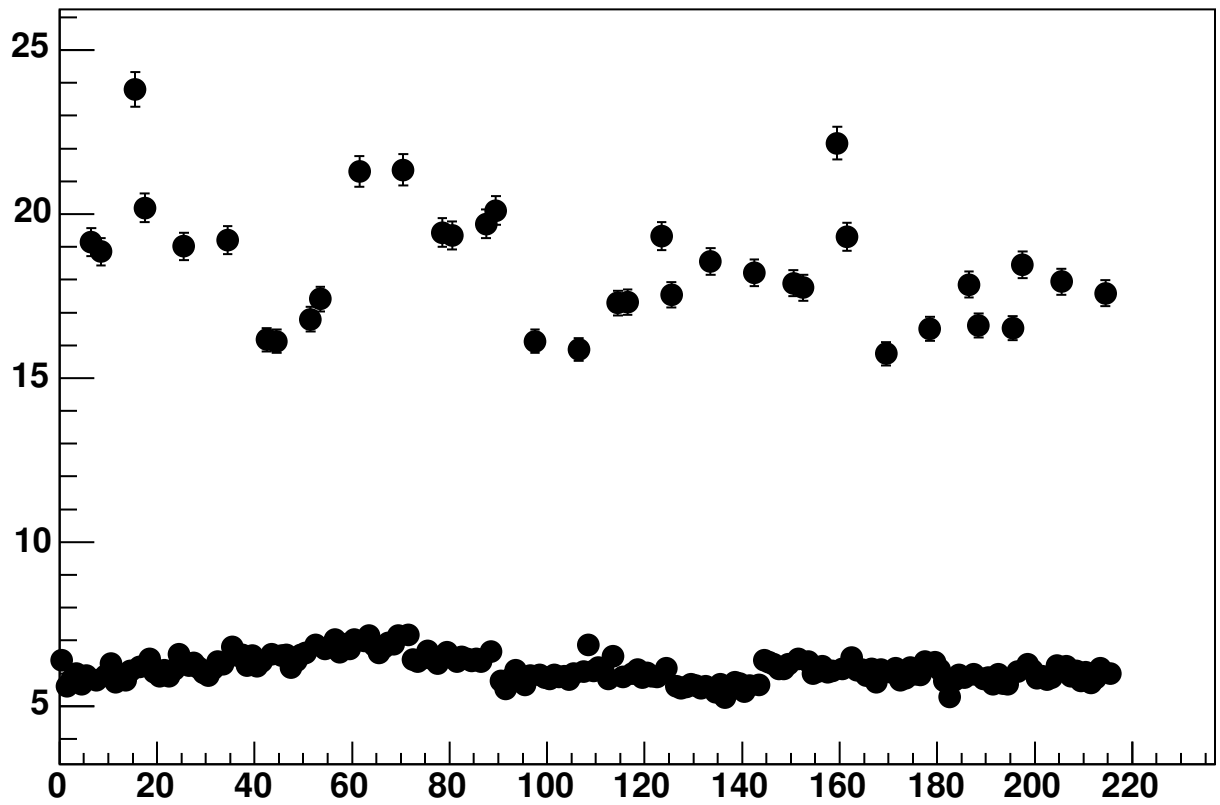
Enable 2, Hold=30, DAC=14336, ADC Noise vs 18*Chip+Chan



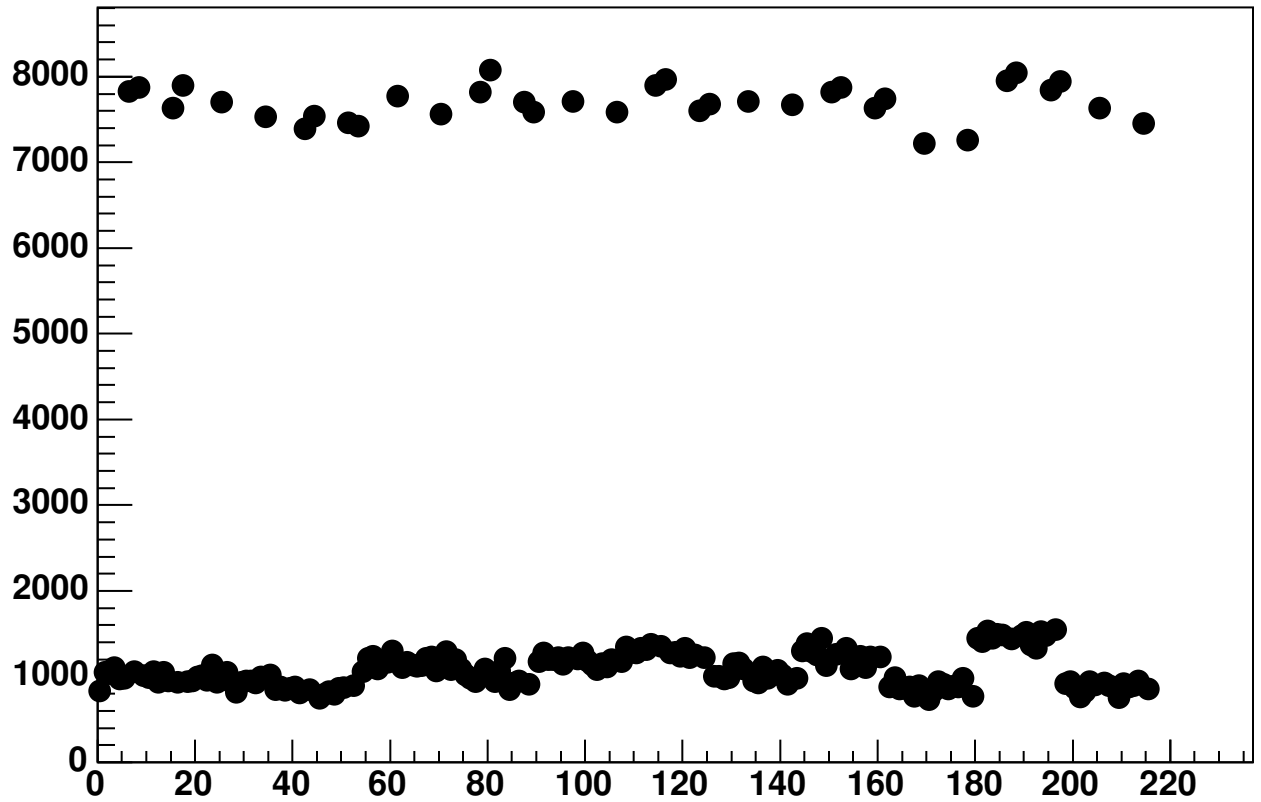
Enable 2, Hold=30, DAC=12288, ADC Mean vs 18*Chip+Chan



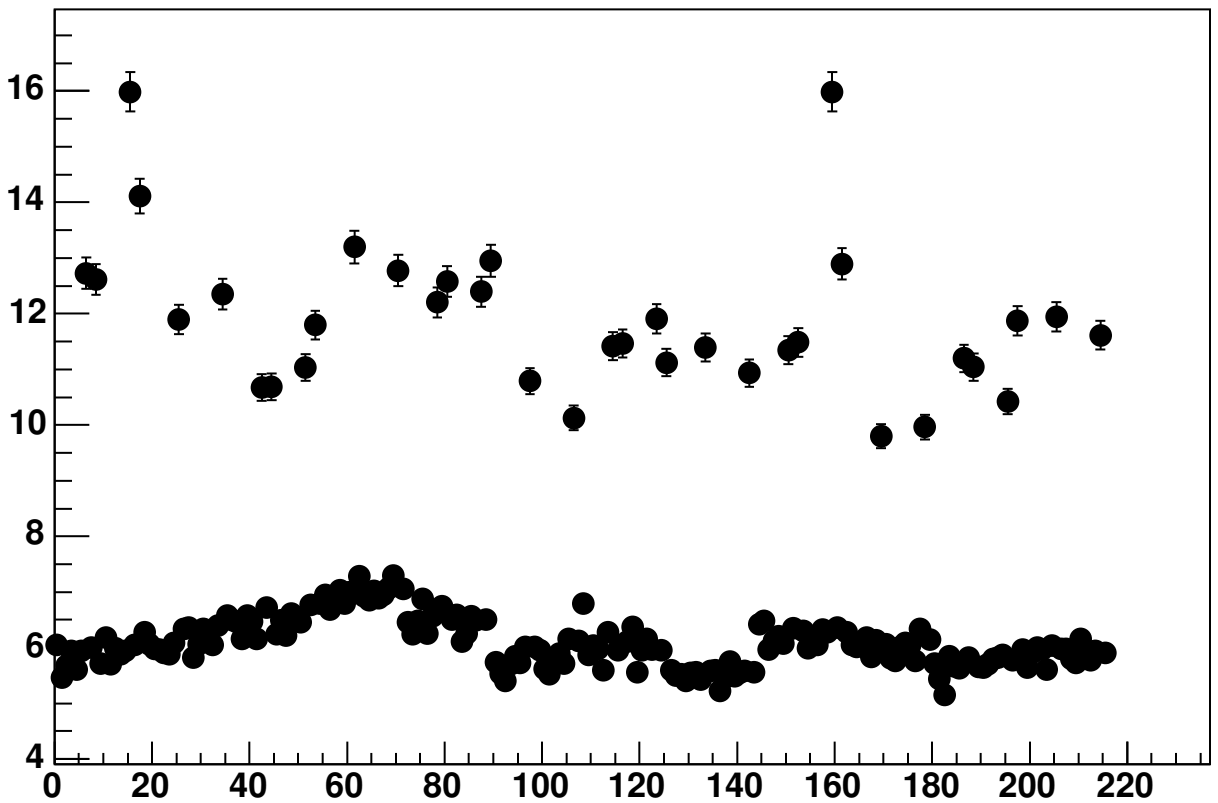
Enable 2, Hold=30, DAC=12288, ADC Noise vs 18*Chip+Chan



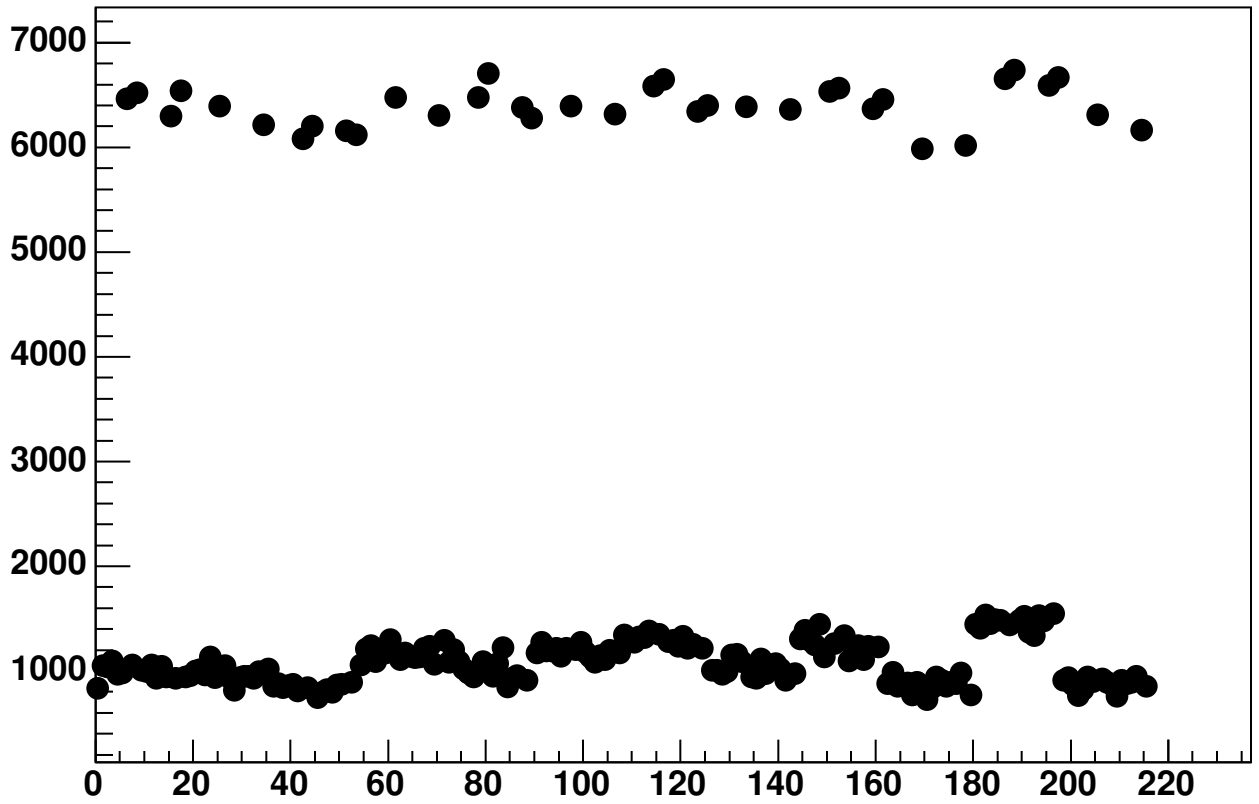
Enable 2, Hold=30, DAC=10240, ADC Mean vs 18*Chip+Chan



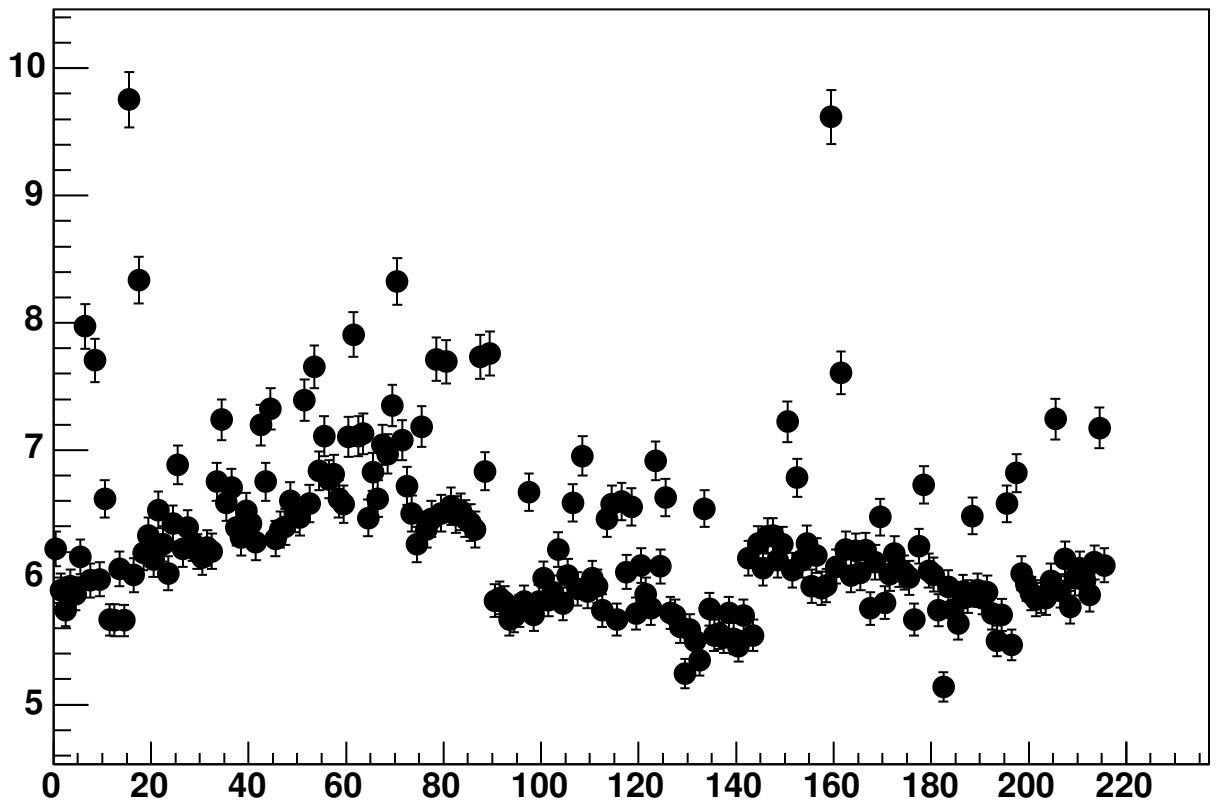
Enable 2, Hold=30, DAC=10240, ADC Noise vs 18*Chip+Chan



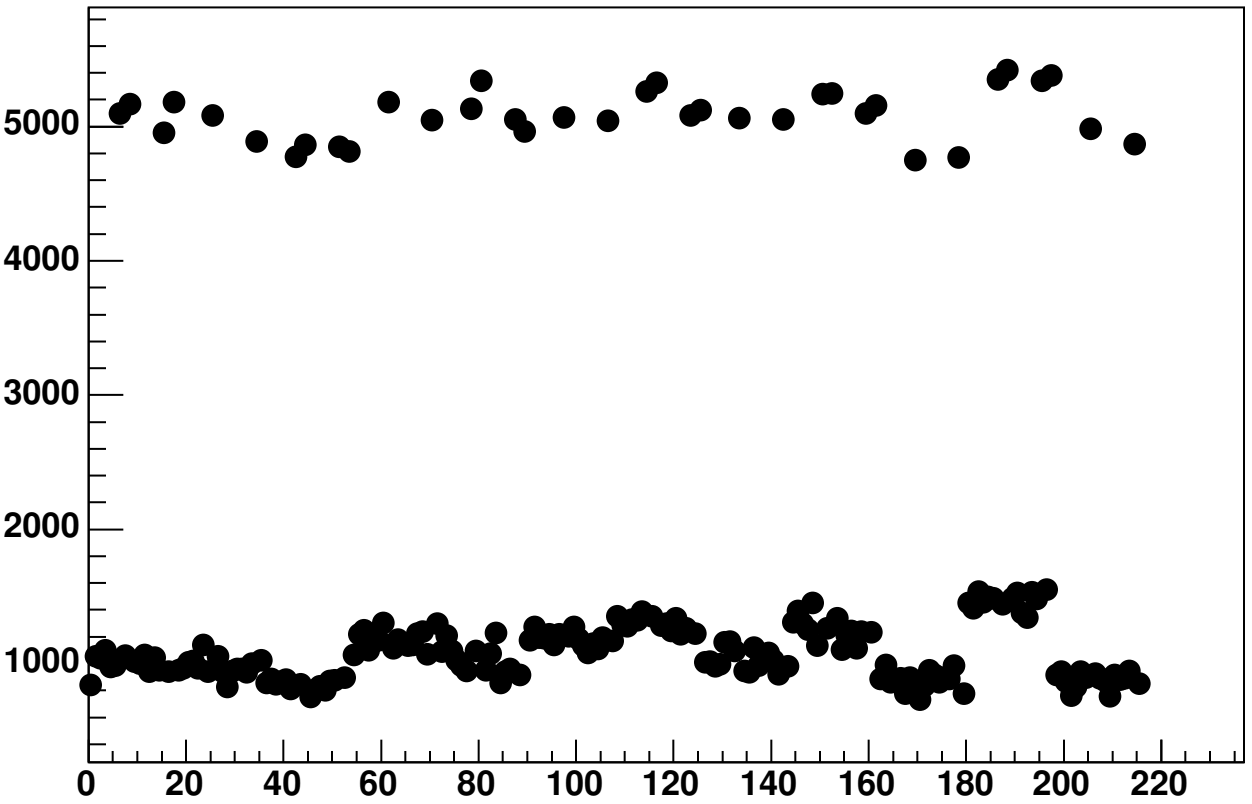
Enable 2, Hold=30, DAC=8192, ADC Mean vs 18*Chip+Chan



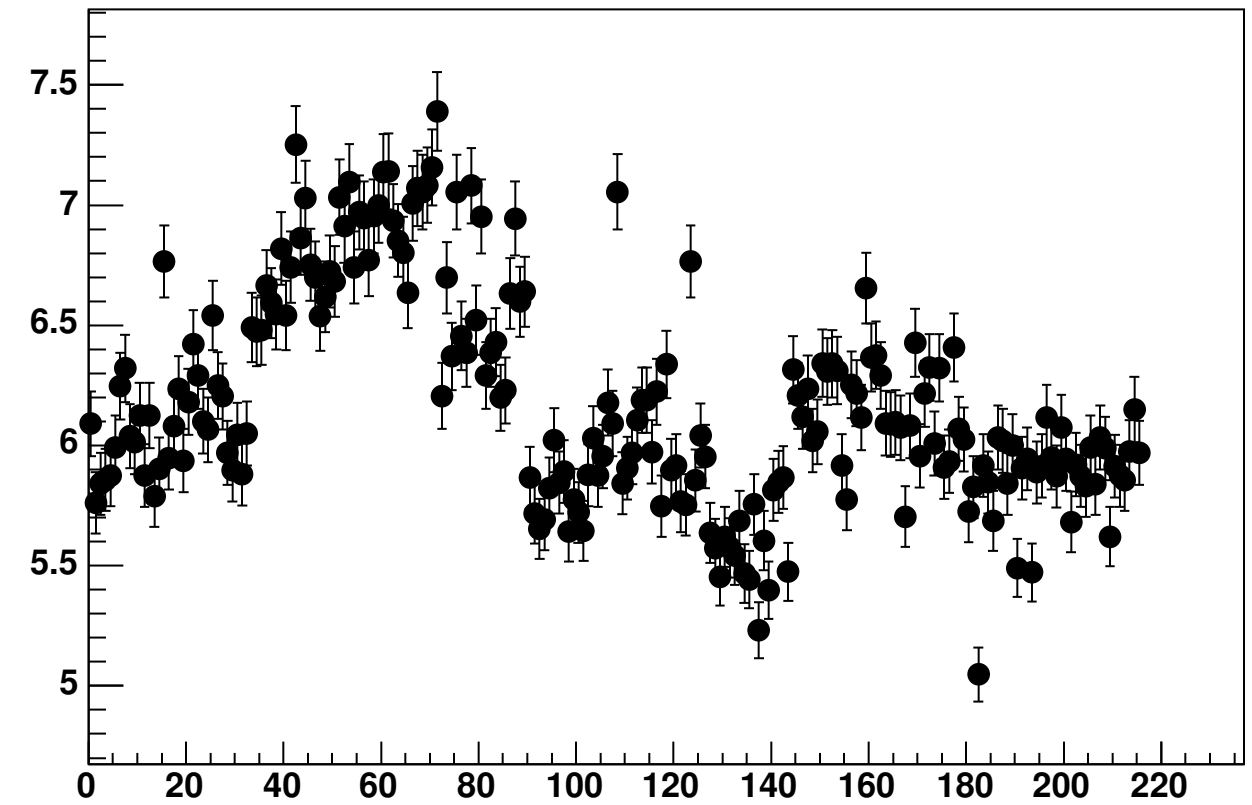
Enable 2, Hold=30, DAC=8192, ADC Noise vs 18*Chip+Chan



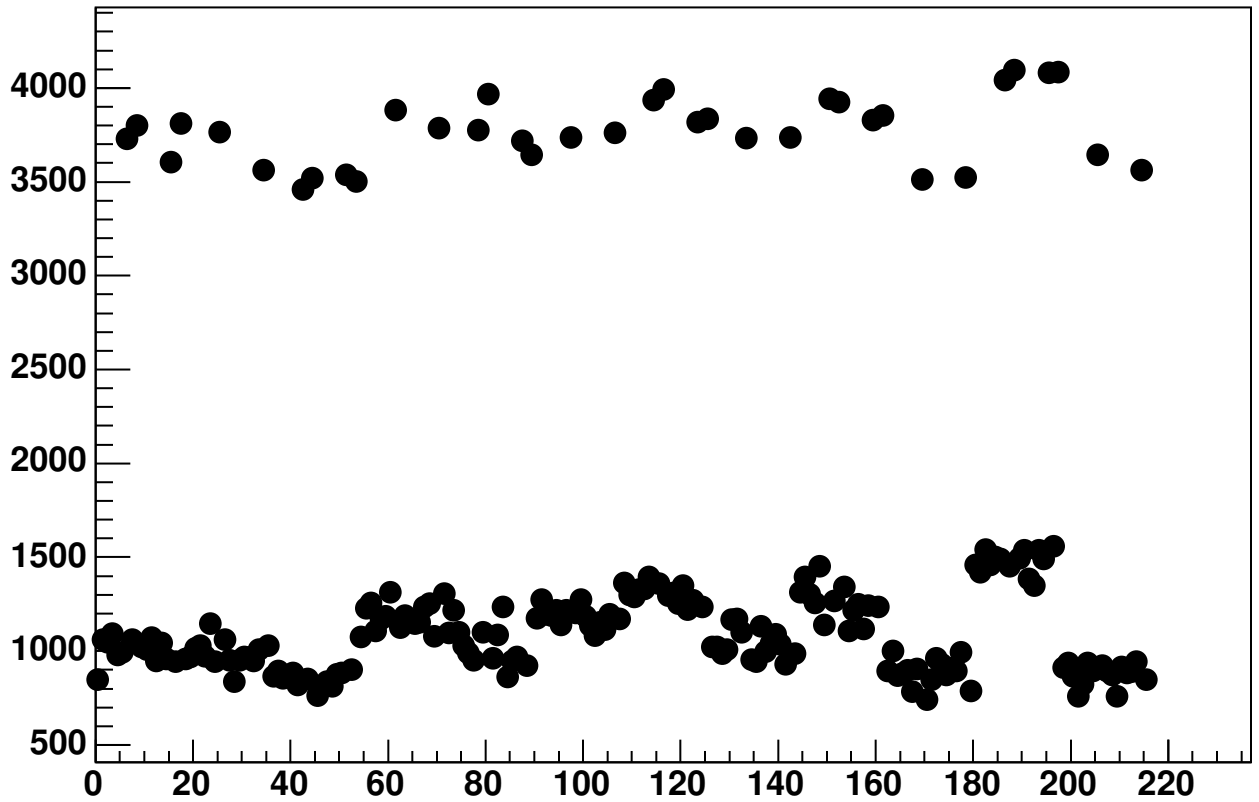
Enable 2, Hold=30, DAC=6144, ADC Mean vs 18*Chip+Chan



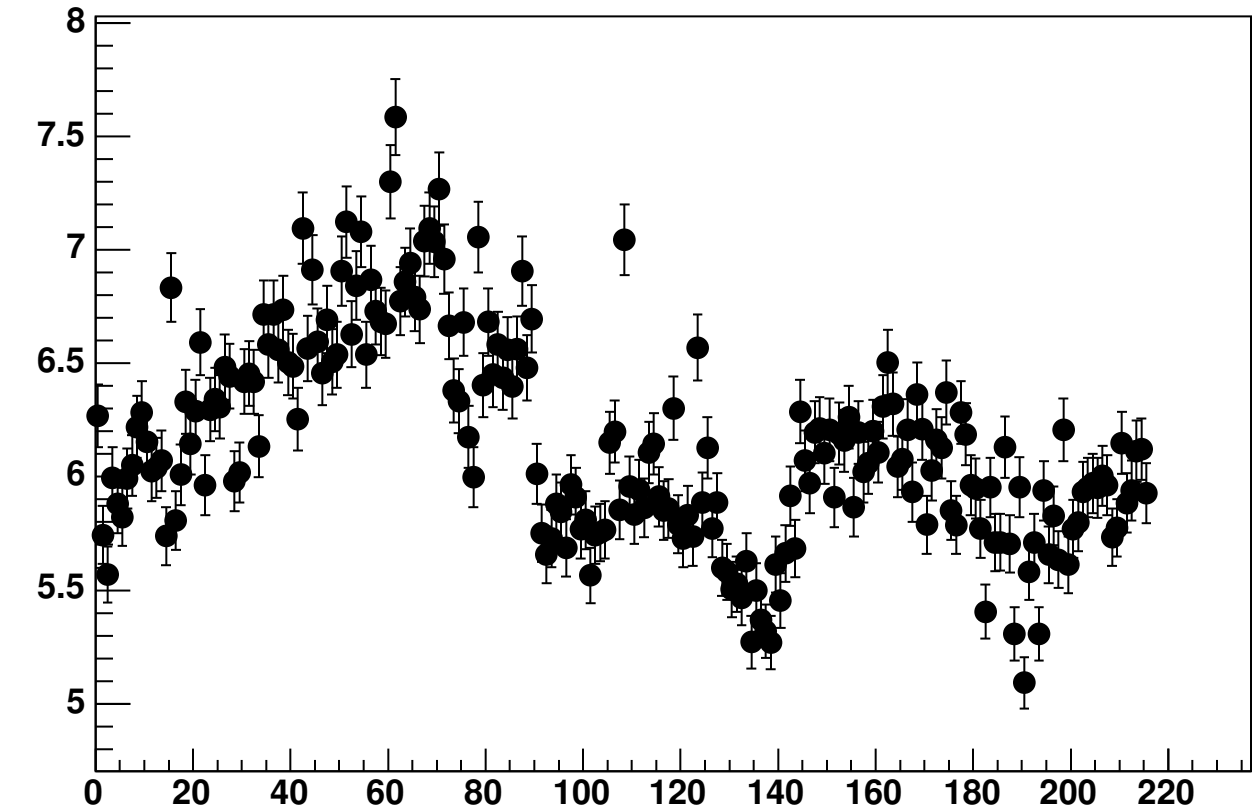
Enable 2, Hold=30, DAC=6144, ADC Noise vs 18*Chip+Chan



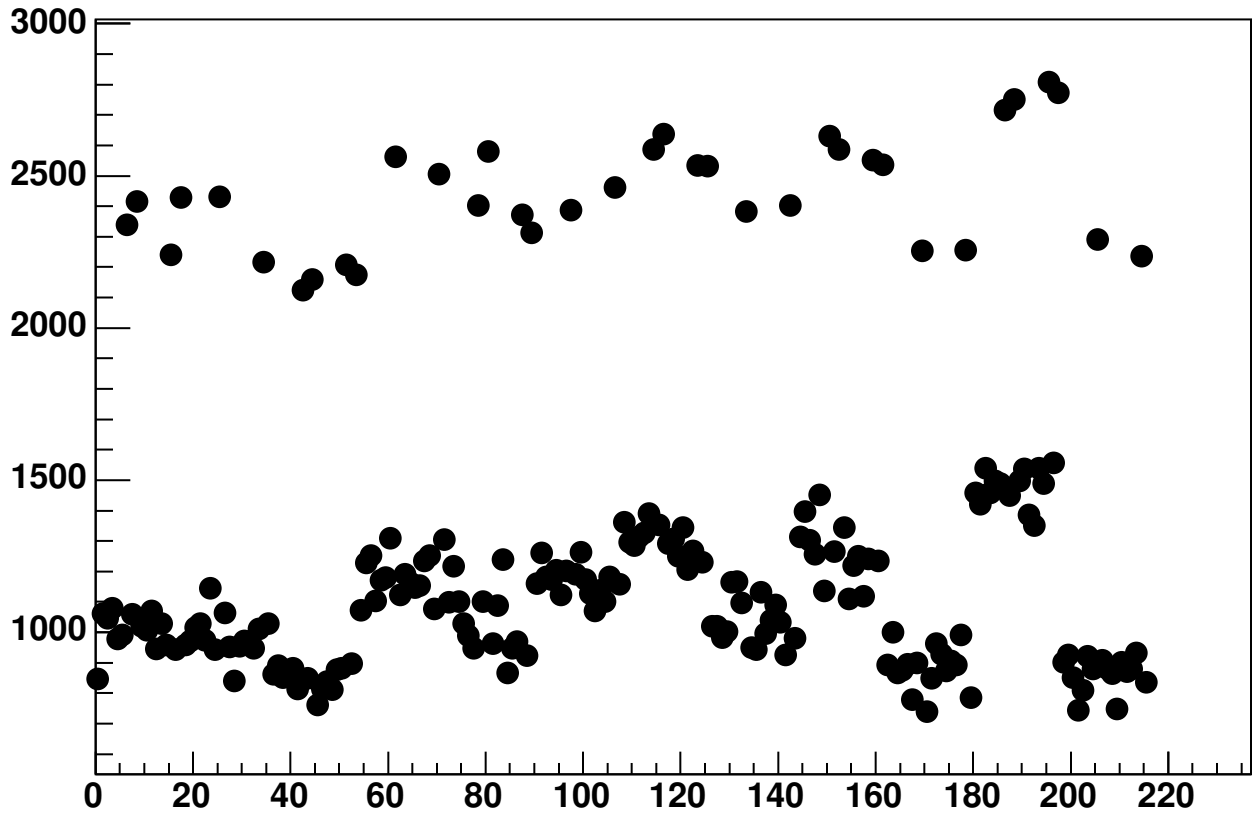
Enable 2, Hold=30, DAC=4096, ADC Mean vs 18*Chip+Chan



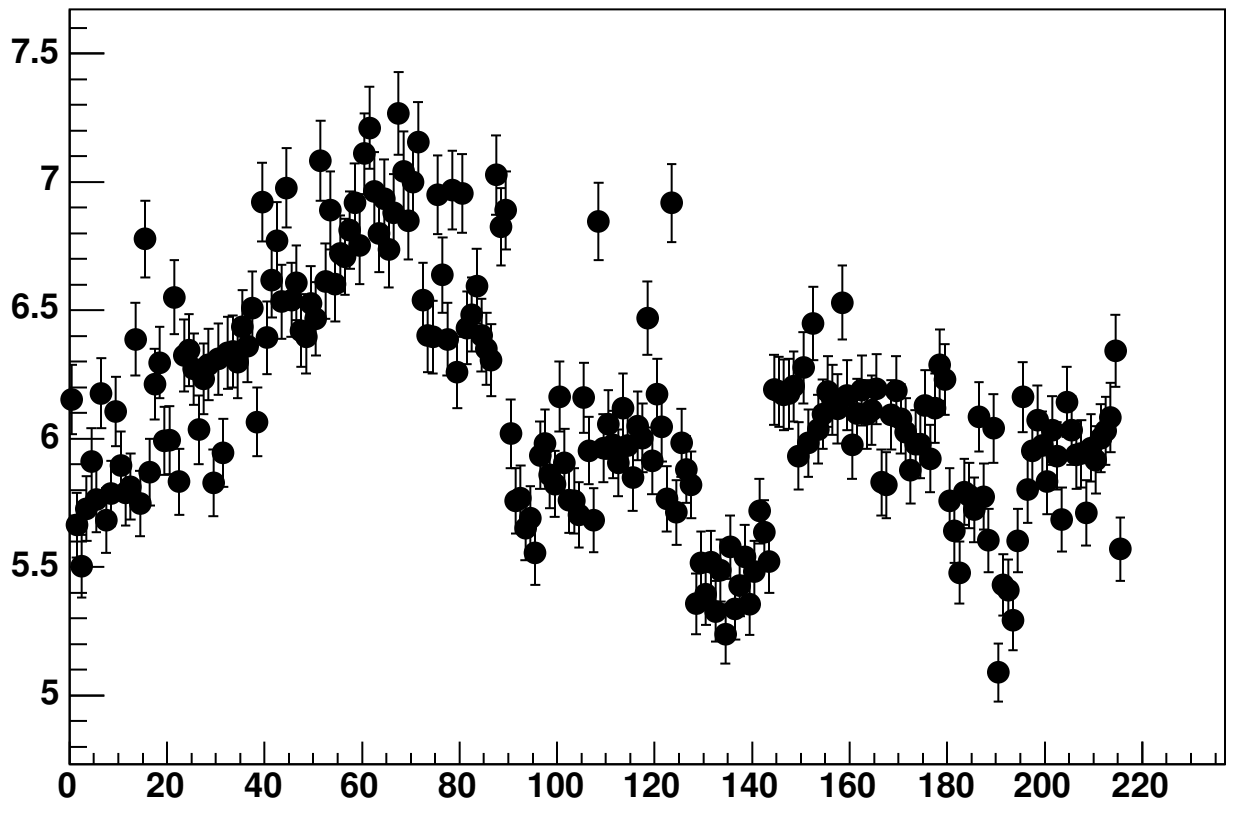
Enable 2, Hold=30, DAC=4096, ADC Noise vs 18*Chip+Chan



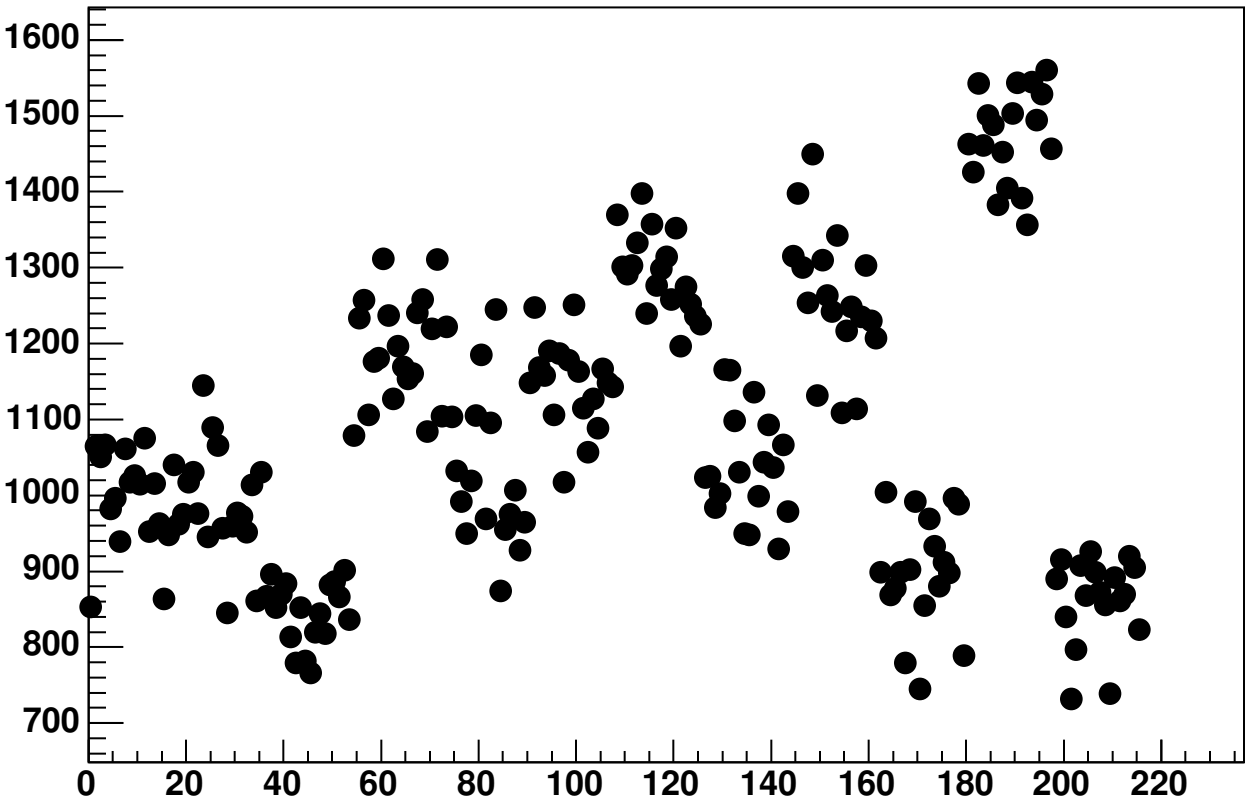
Enable 2, Hold=30, DAC=2048, ADC Mean vs 18*Chip+Chan



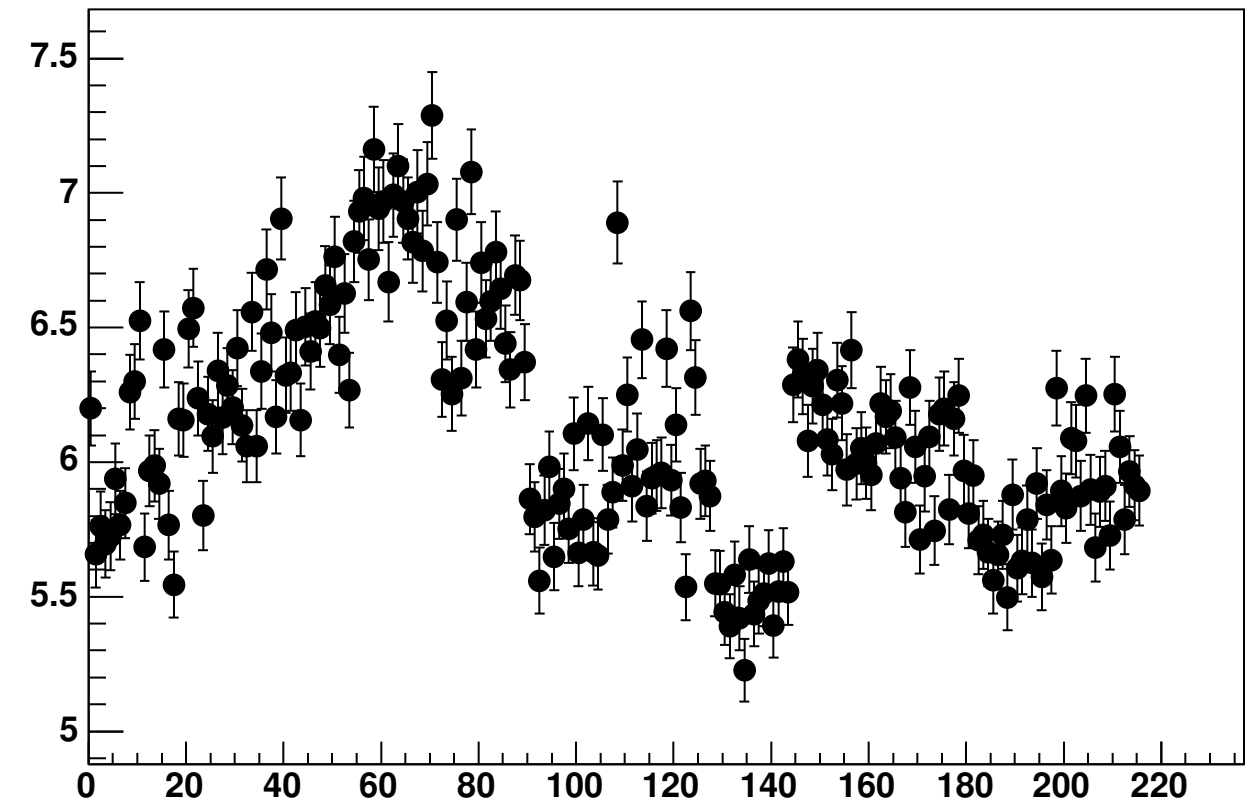
Enable 2, Hold=30, DAC=2048, ADC Noise vs 18*Chip+Chan



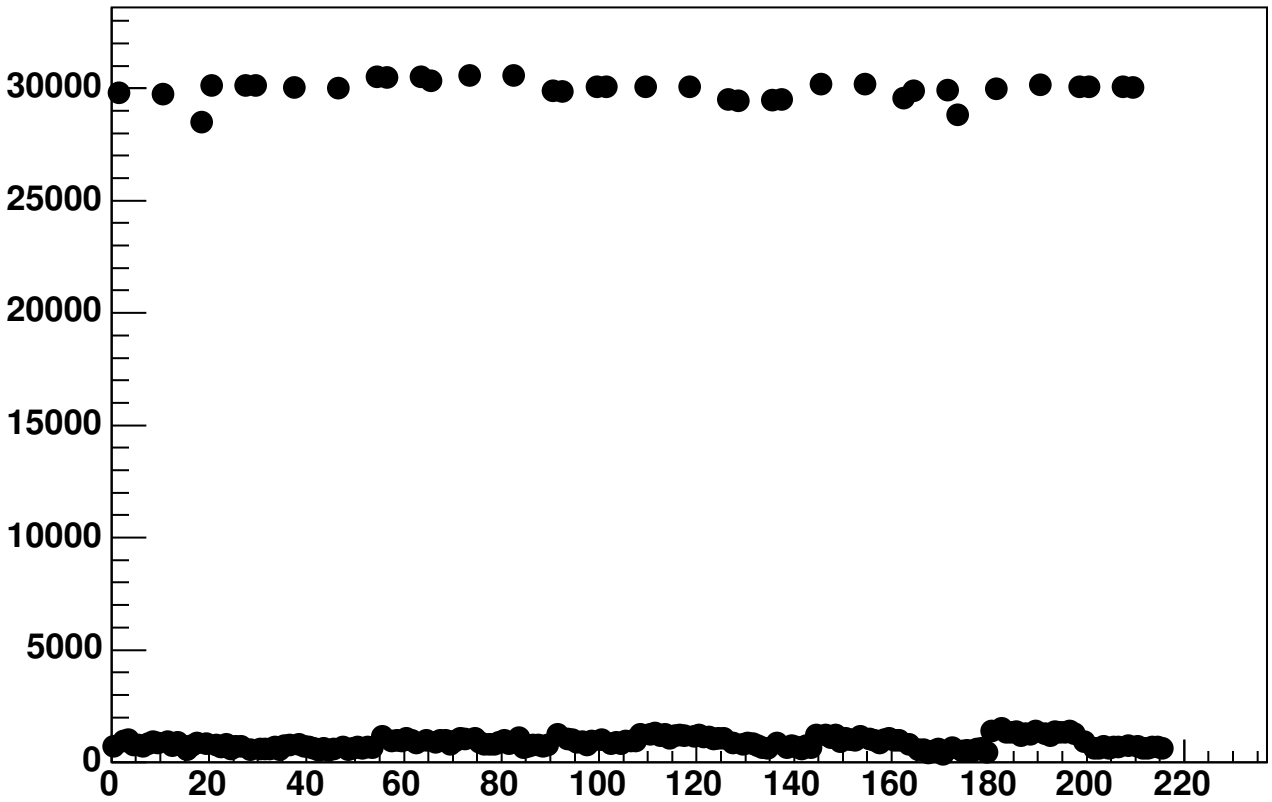
Enable 2, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



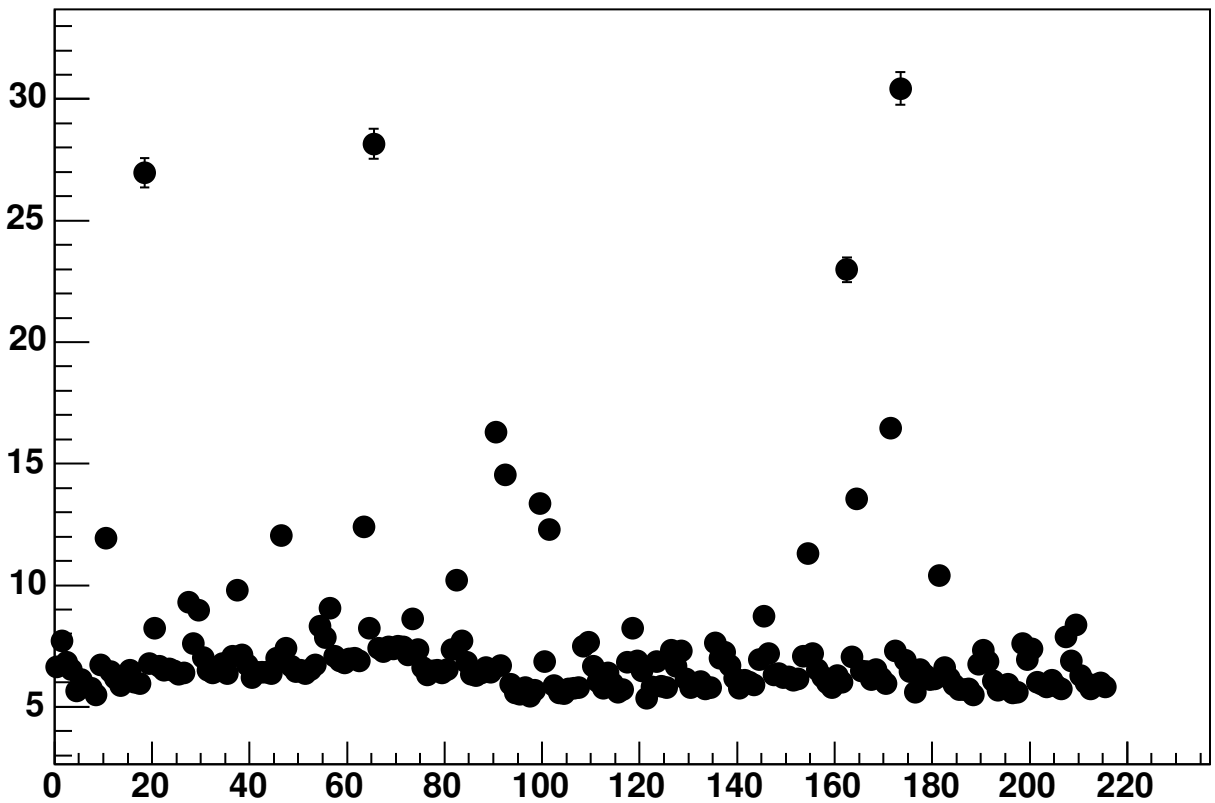
Enable 2, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



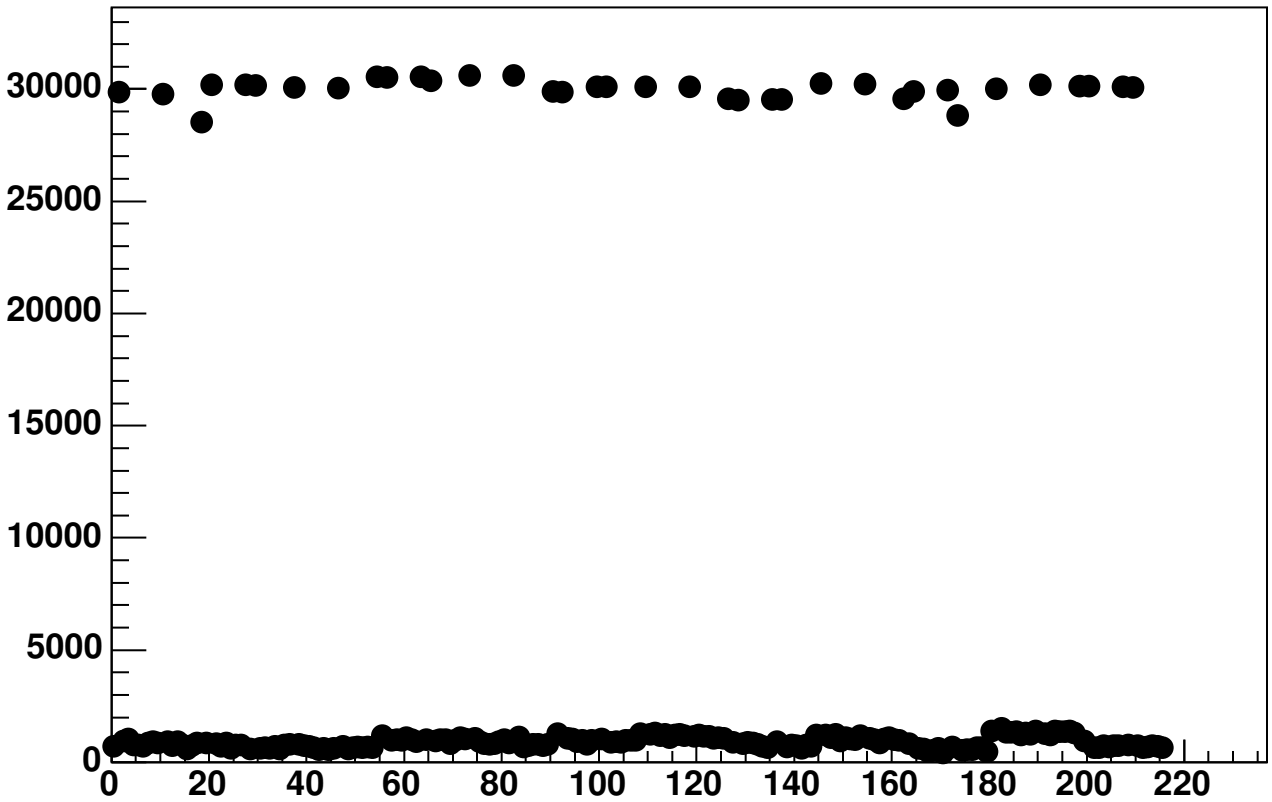
Enable 3, Hold=30, DAC=63488, ADC Mean vs 18*Chip+Chan



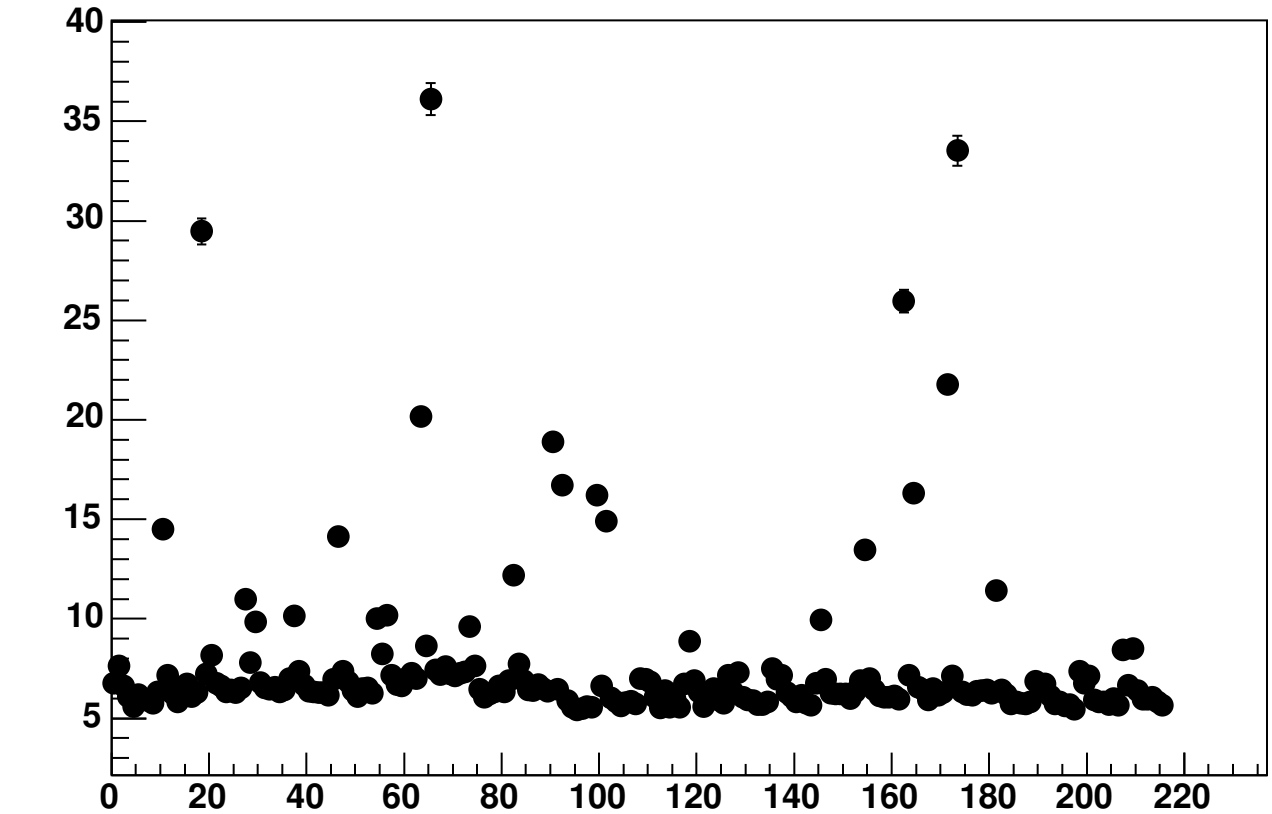
Enable 3, Hold=30, DAC=63488, ADC Noise vs 18*Chip+Chan



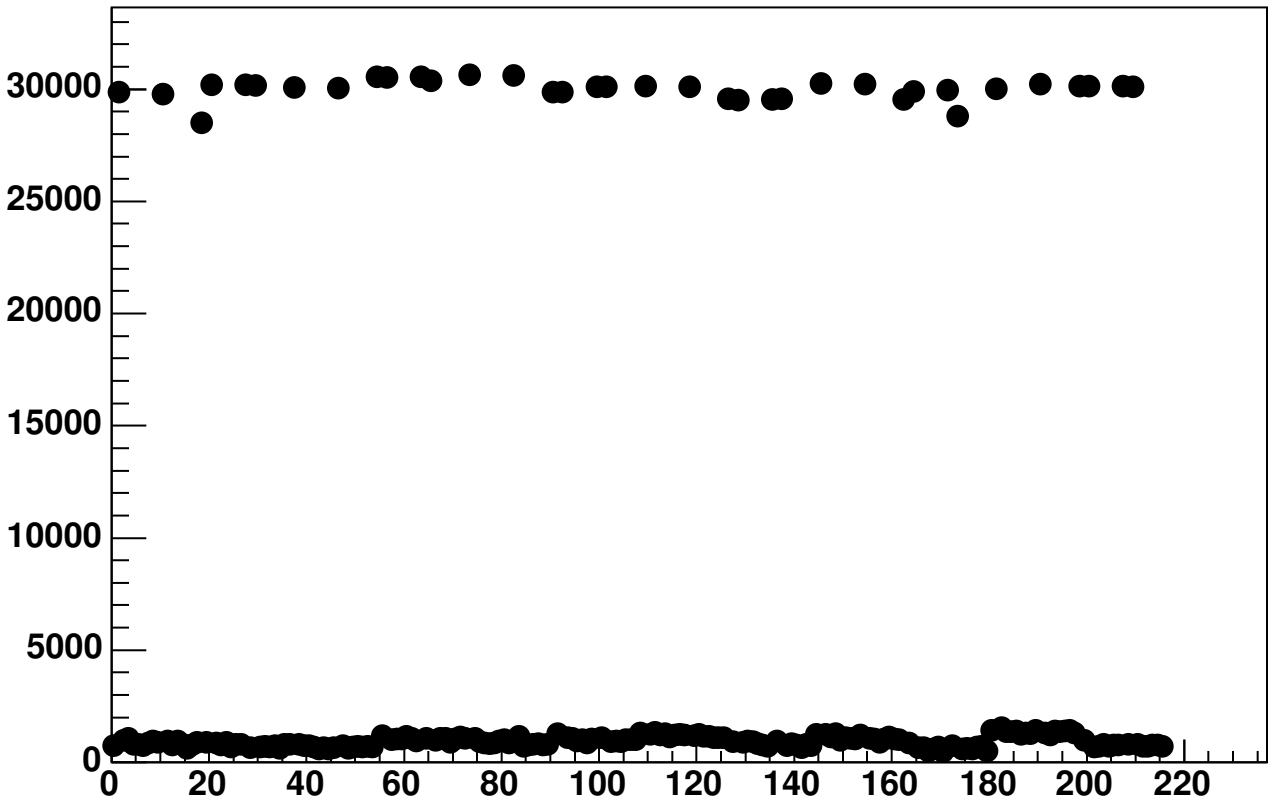
Enable 3, Hold=30, DAC=61440, ADC Mean vs 18*Chip+Chan



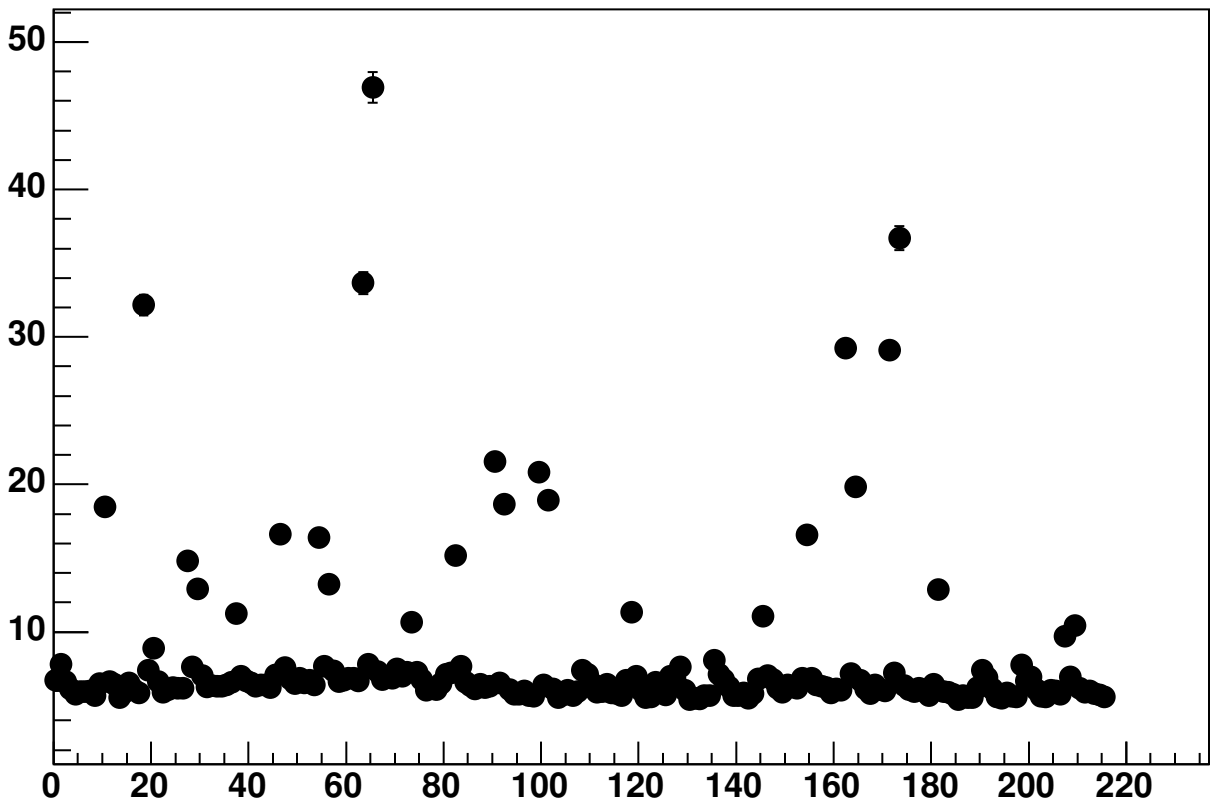
Enable 3, Hold=30, DAC=61440, ADC Noise vs 18*Chip+Chan



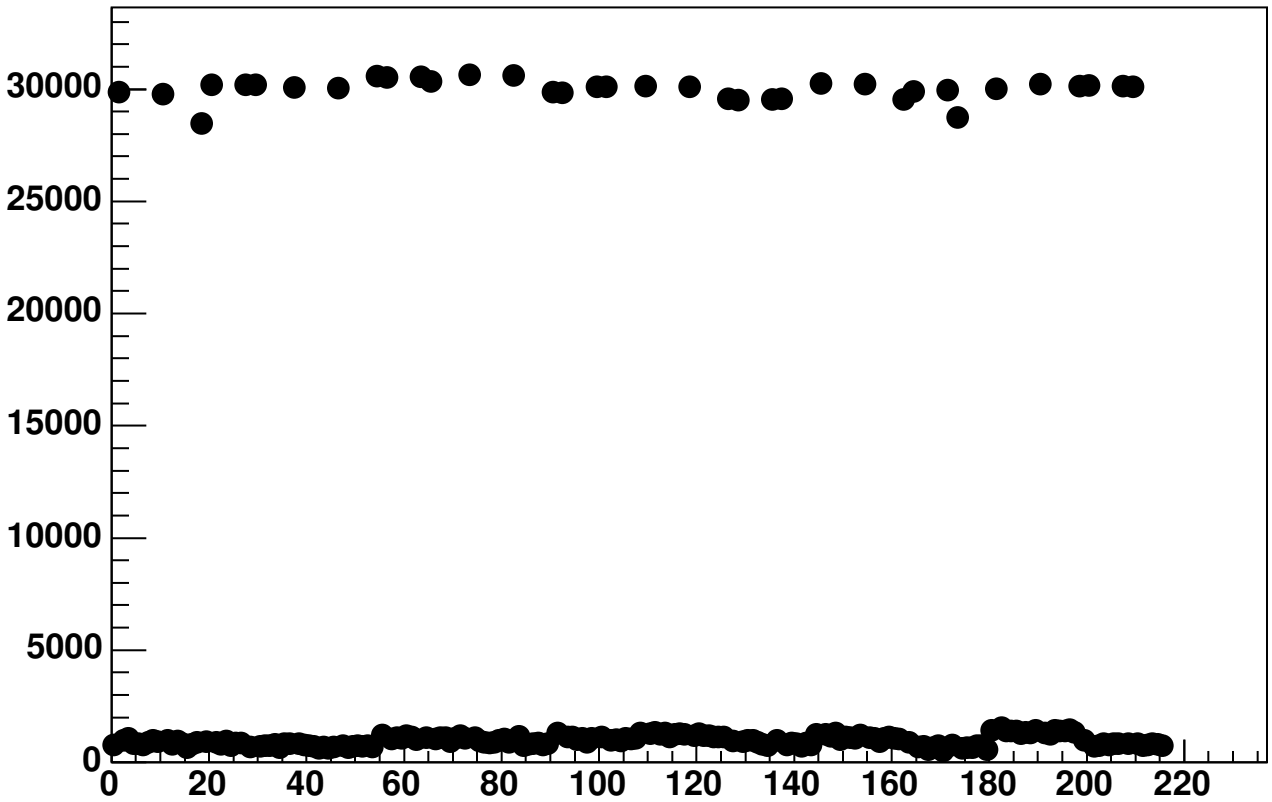
Enable 3, Hold=30, DAC=59392, ADC Mean vs 18*Chip+Chan



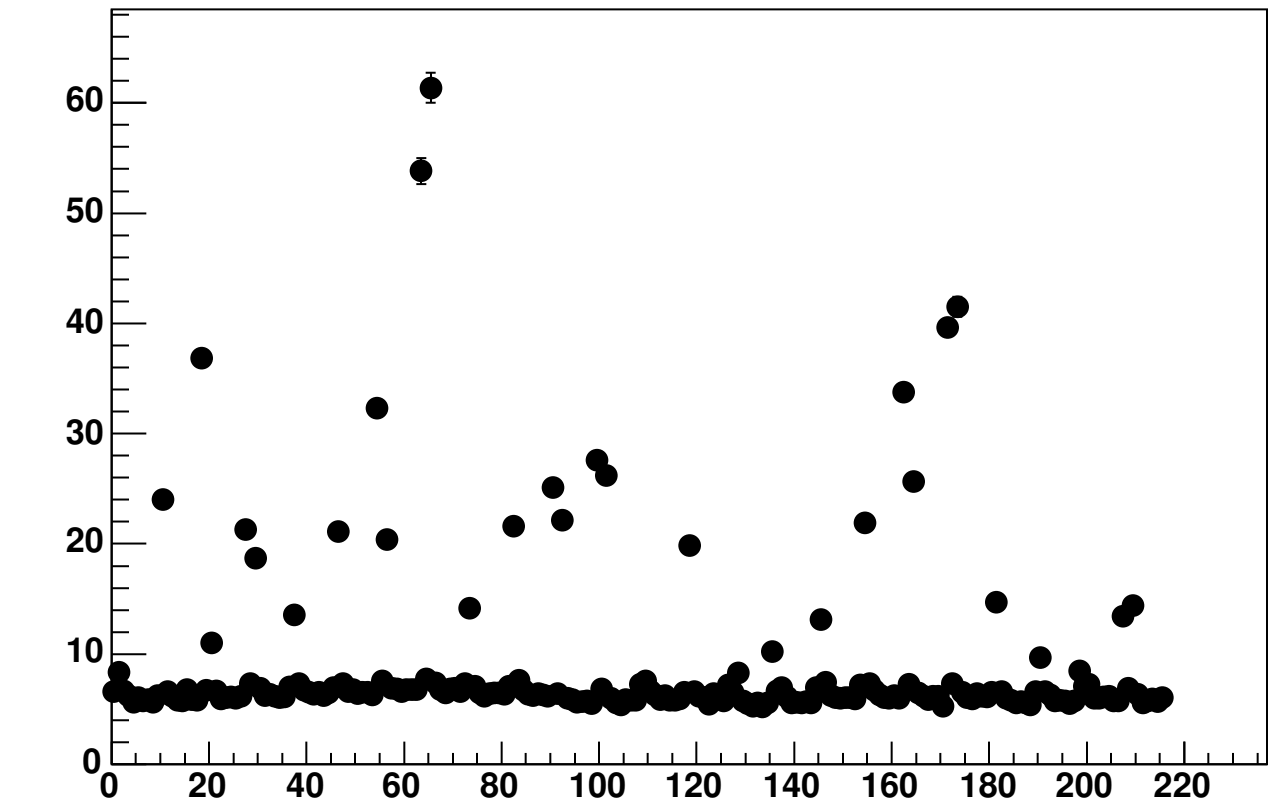
Enable 3, Hold=30, DAC=59392, ADC Noise vs 18*Chip+Chan



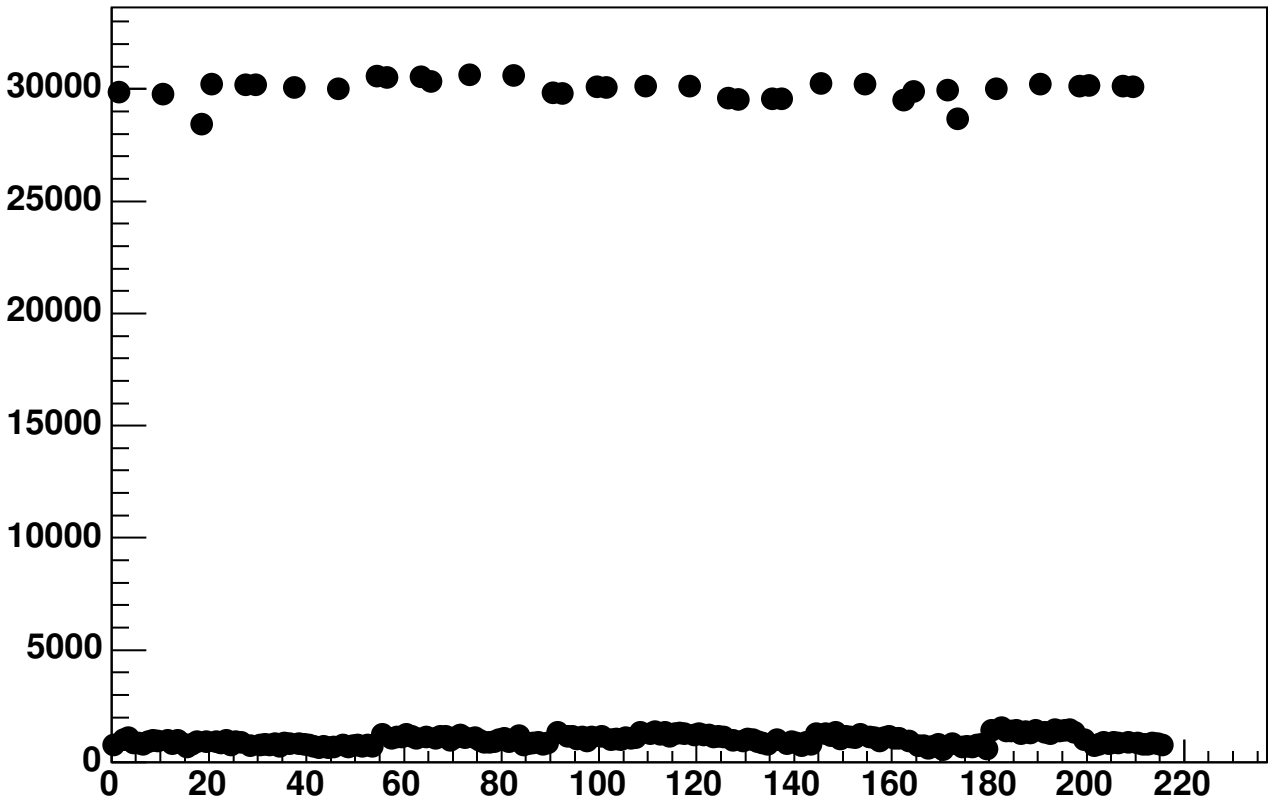
Enable 3, Hold=30, DAC=57344, ADC Mean vs 18*Chip+Chan



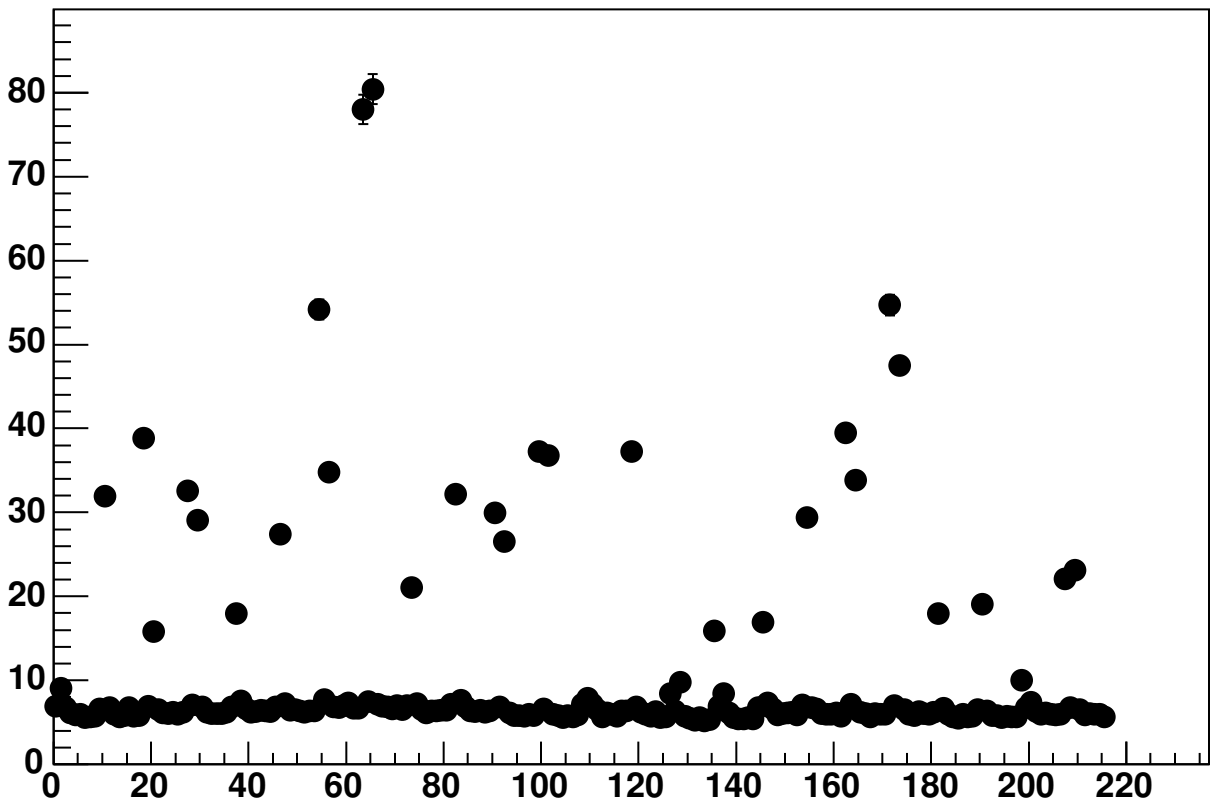
Enable 3, Hold=30, DAC=57344, ADC Noise vs 18*Chip+Chan



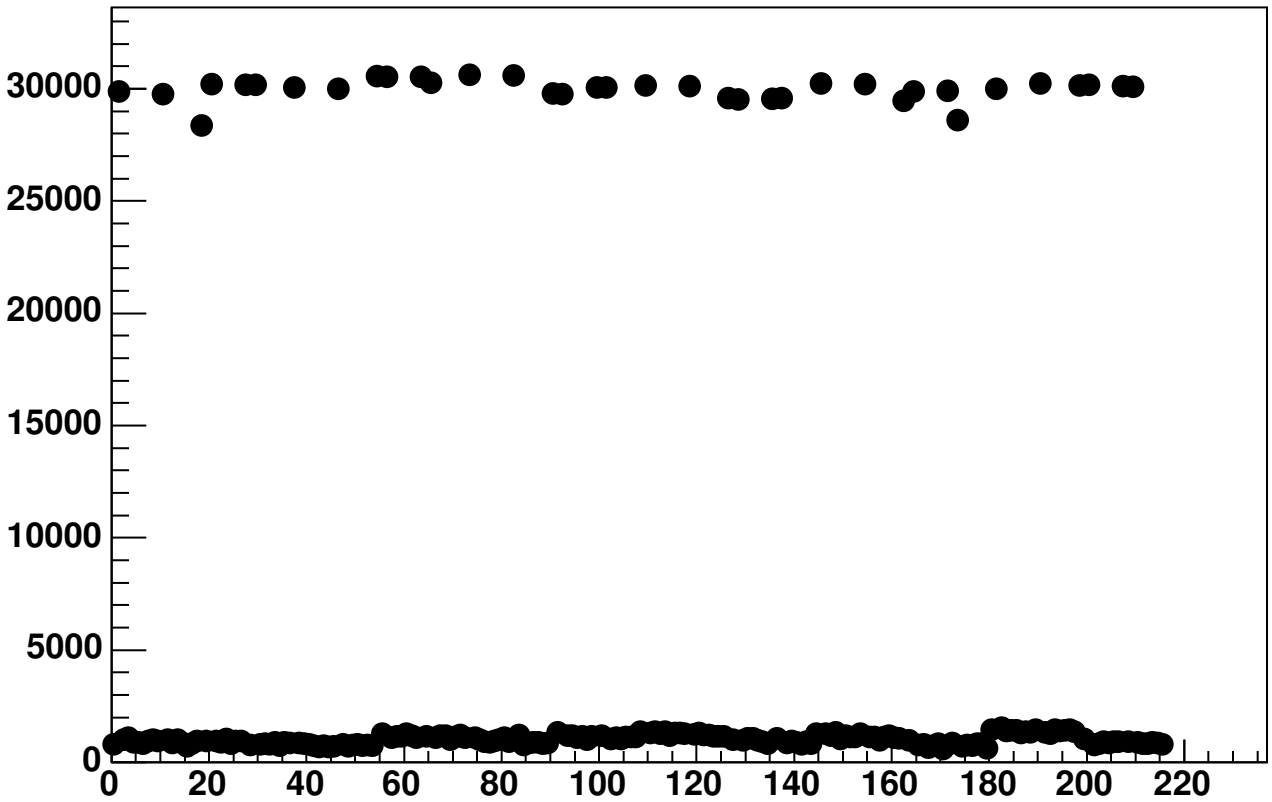
Enable 3, Hold=30, DAC=55296, ADC Mean vs 18*Chip+Chan



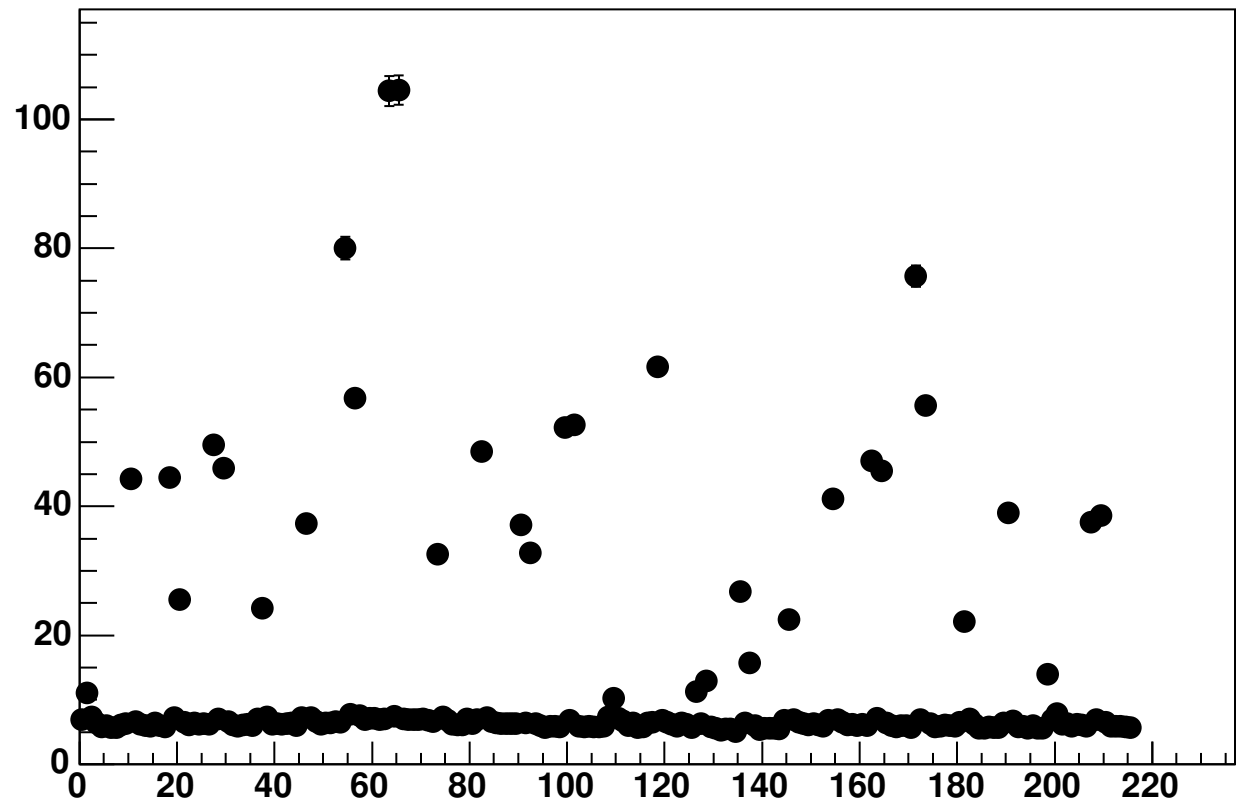
Enable 3, Hold=30, DAC=55296, ADC Noise vs 18*Chip+Chan



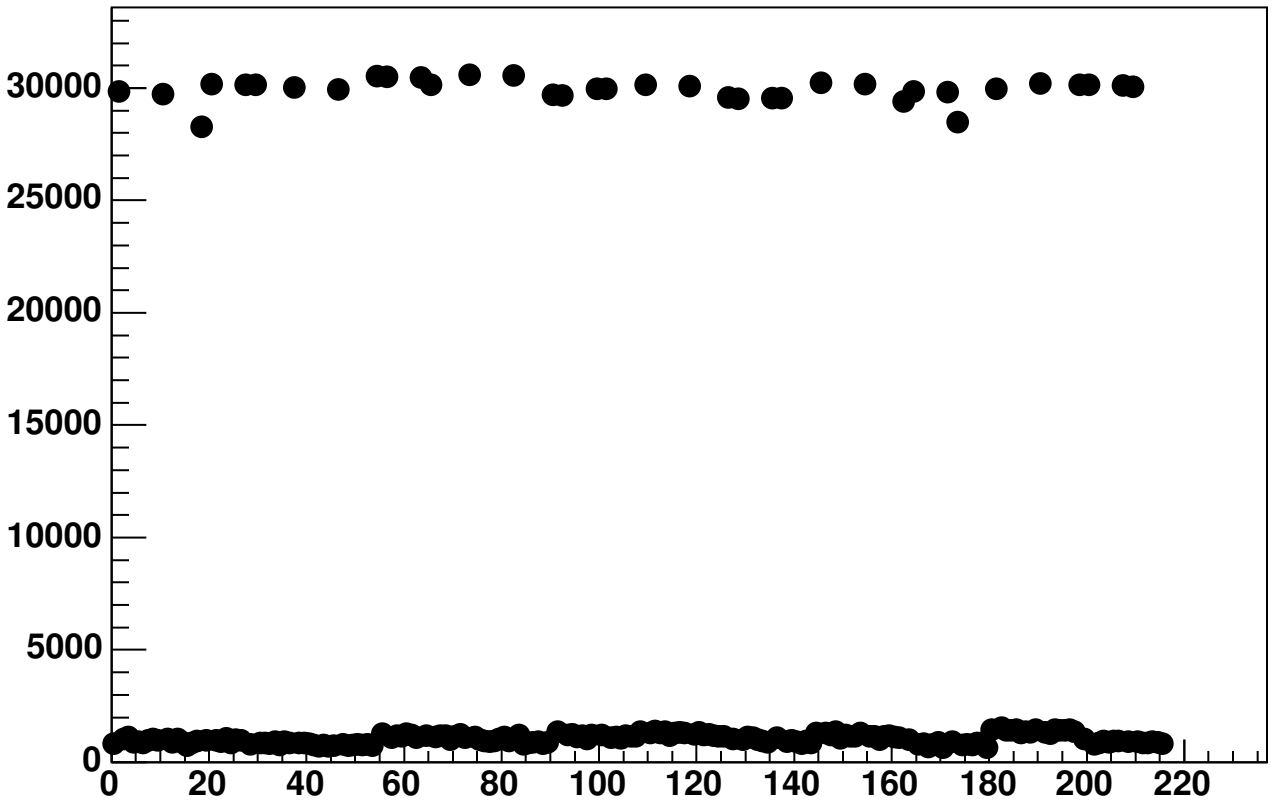
Enable 3, Hold=30, DAC=53248, ADC Mean vs 18*Chip+Chan



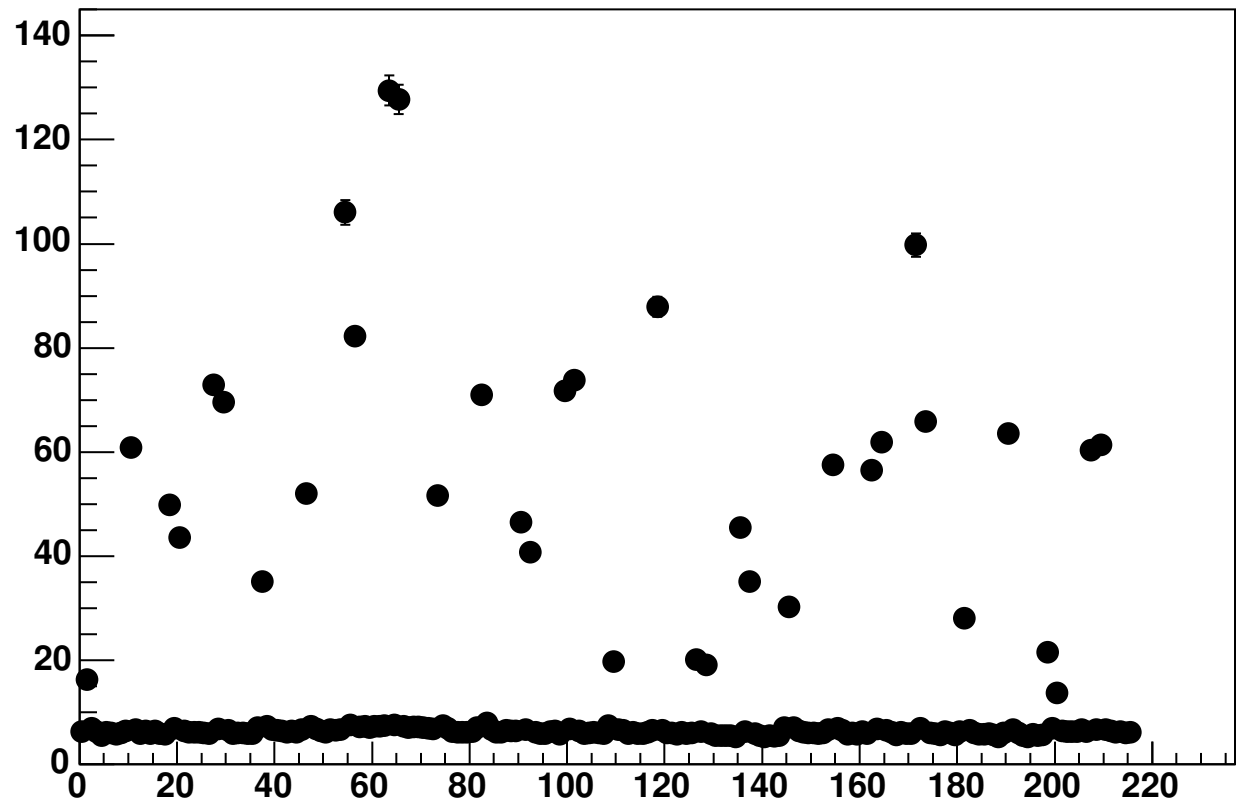
Enable 3, Hold=30, DAC=53248, ADC Noise vs 18*Chip+Chan



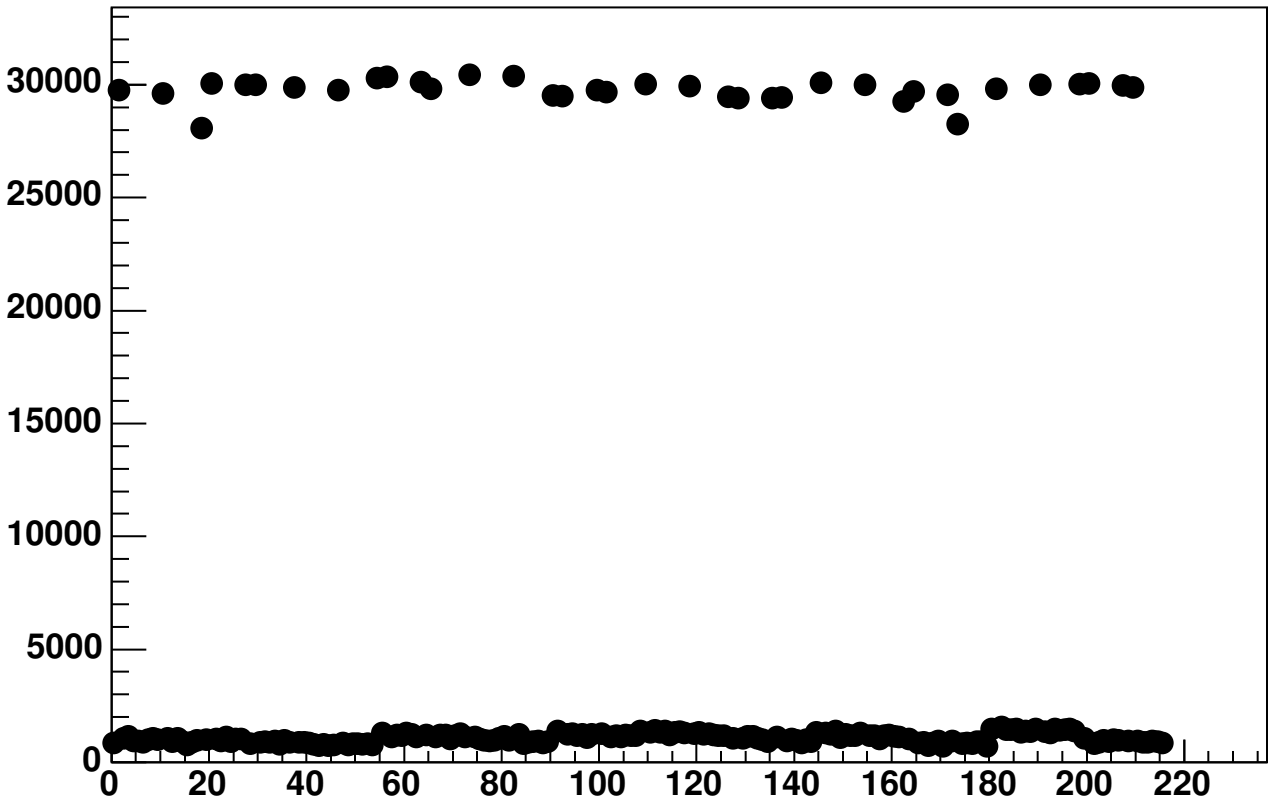
Enable 3, Hold=30, DAC=51200, ADC Mean vs 18*Chip+Chan



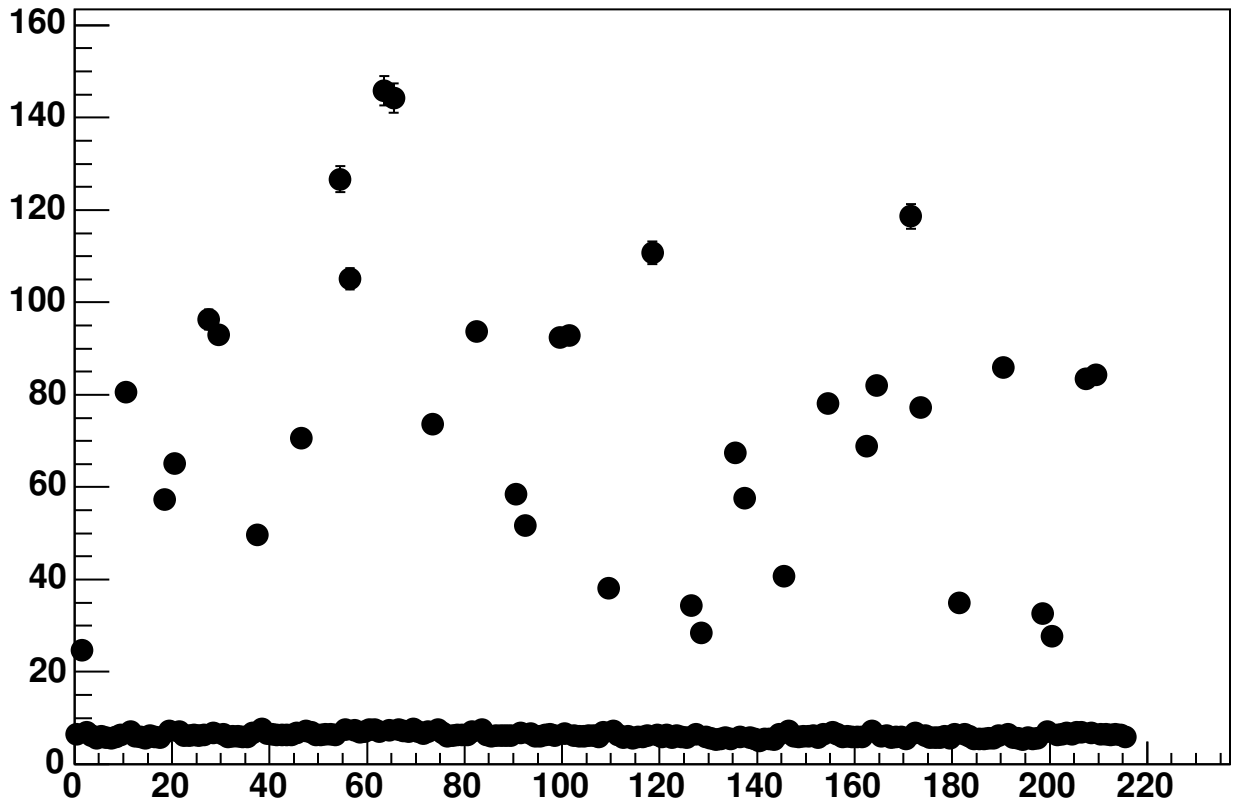
Enable 3, Hold=30, DAC=51200, ADC Noise vs 18*Chip+Chan



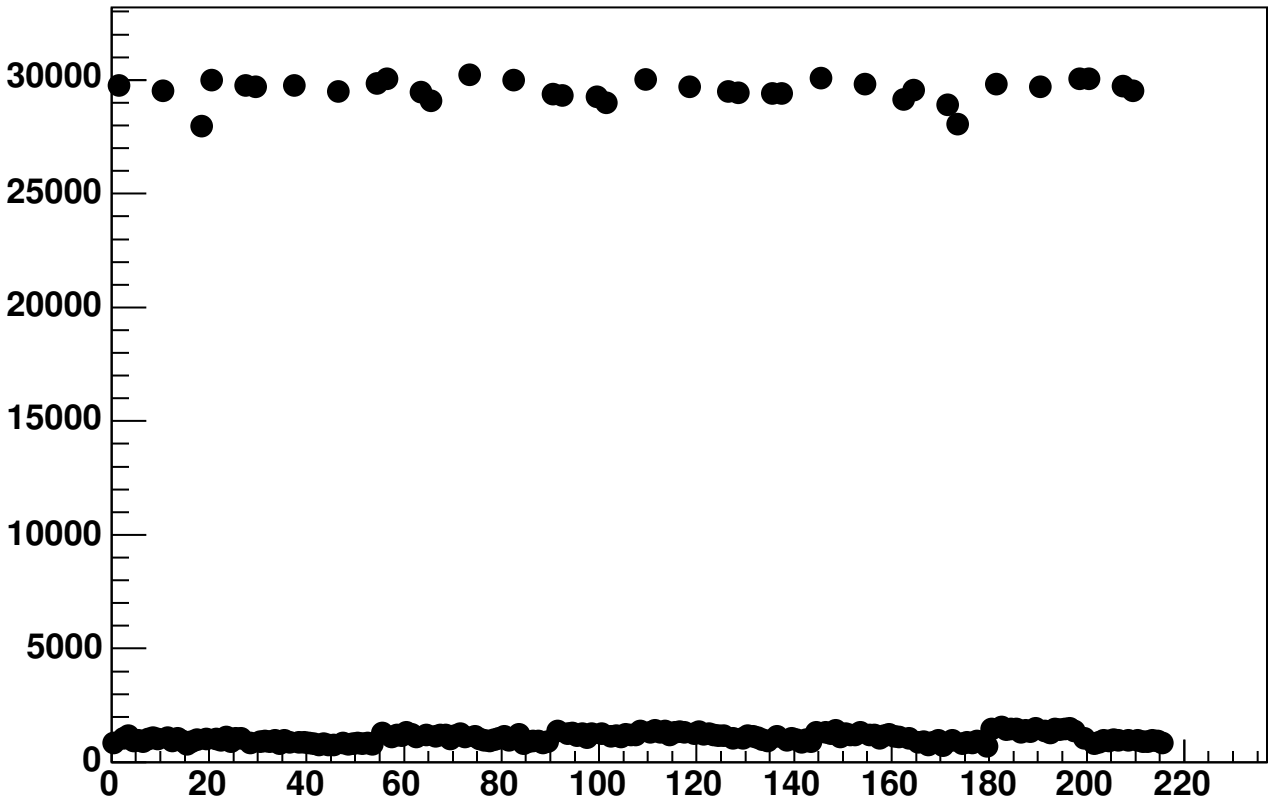
Enable 3, Hold=30, DAC=49152, ADC Mean vs 18*Chip+Chan



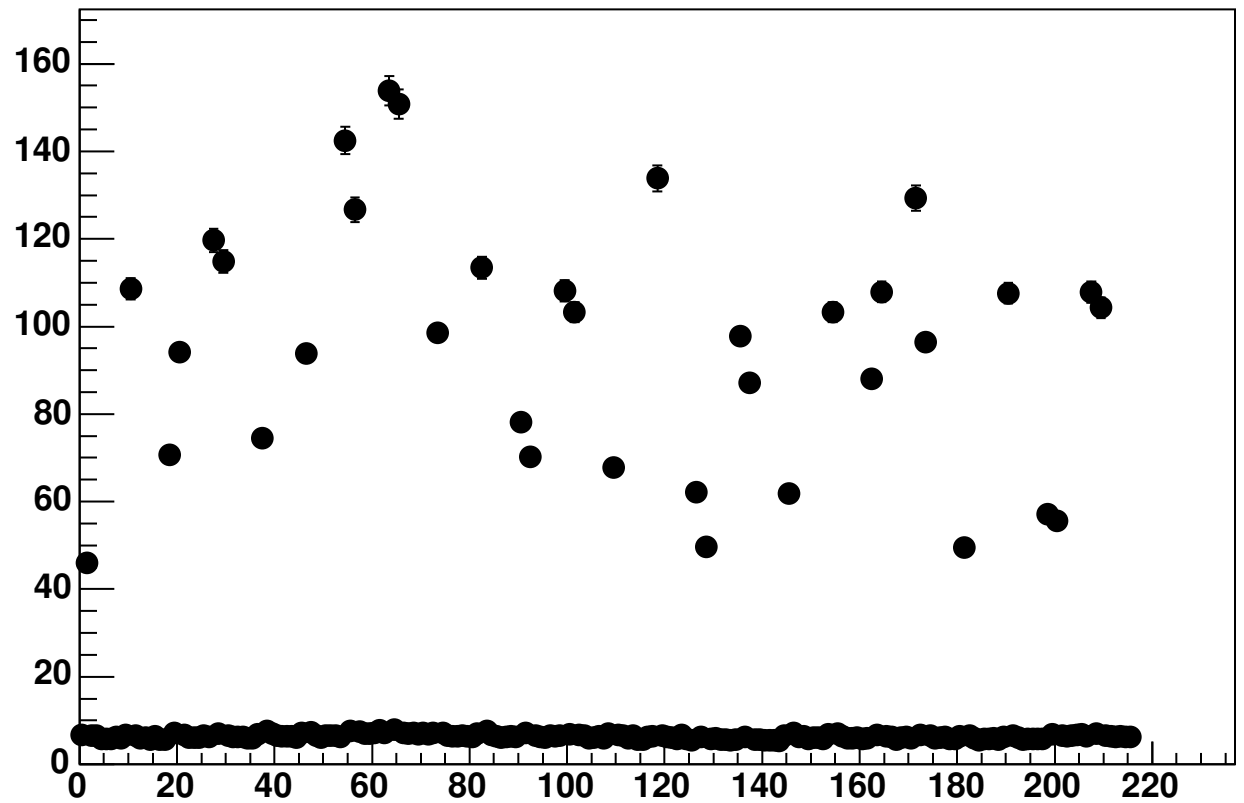
Enable 3, Hold=30, DAC=49152, ADC Noise vs 18*Chip+Chan



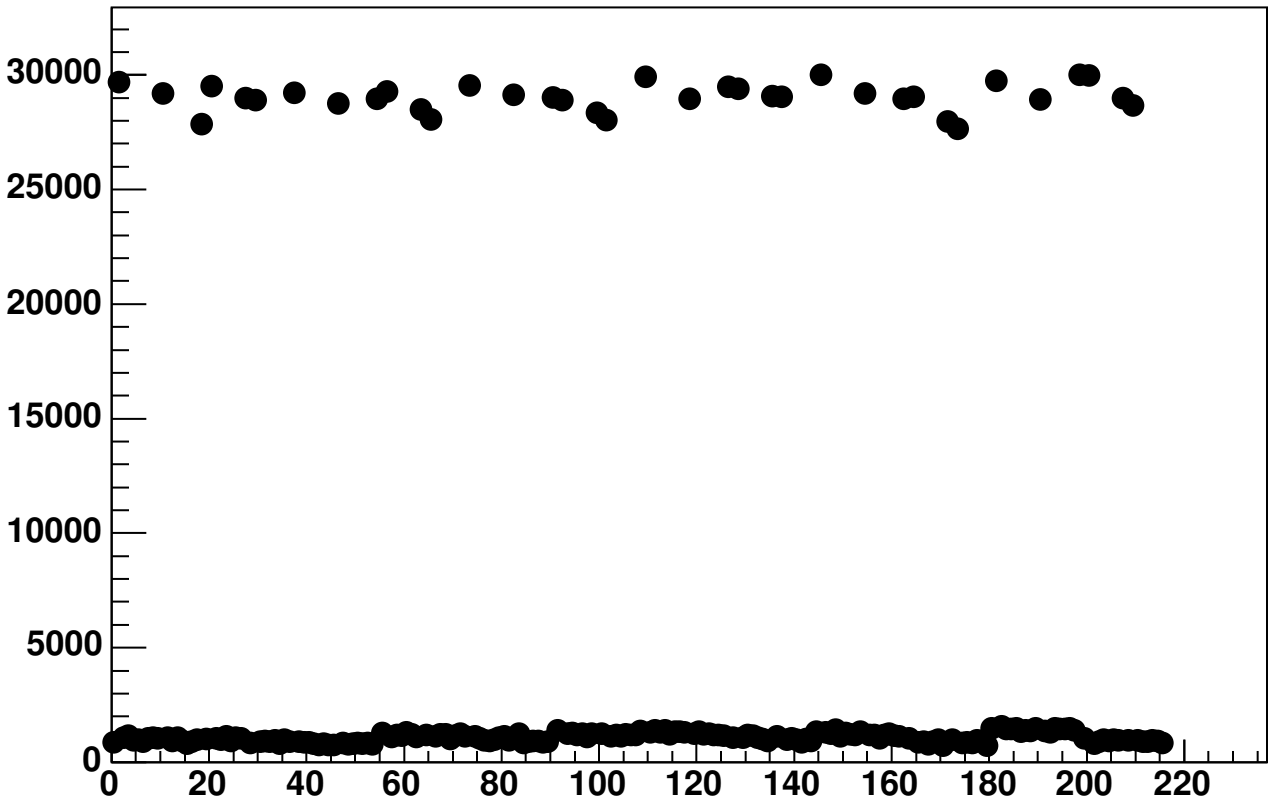
Enable 3, Hold=30, DAC=47104, ADC Mean vs 18*Chip+Chan



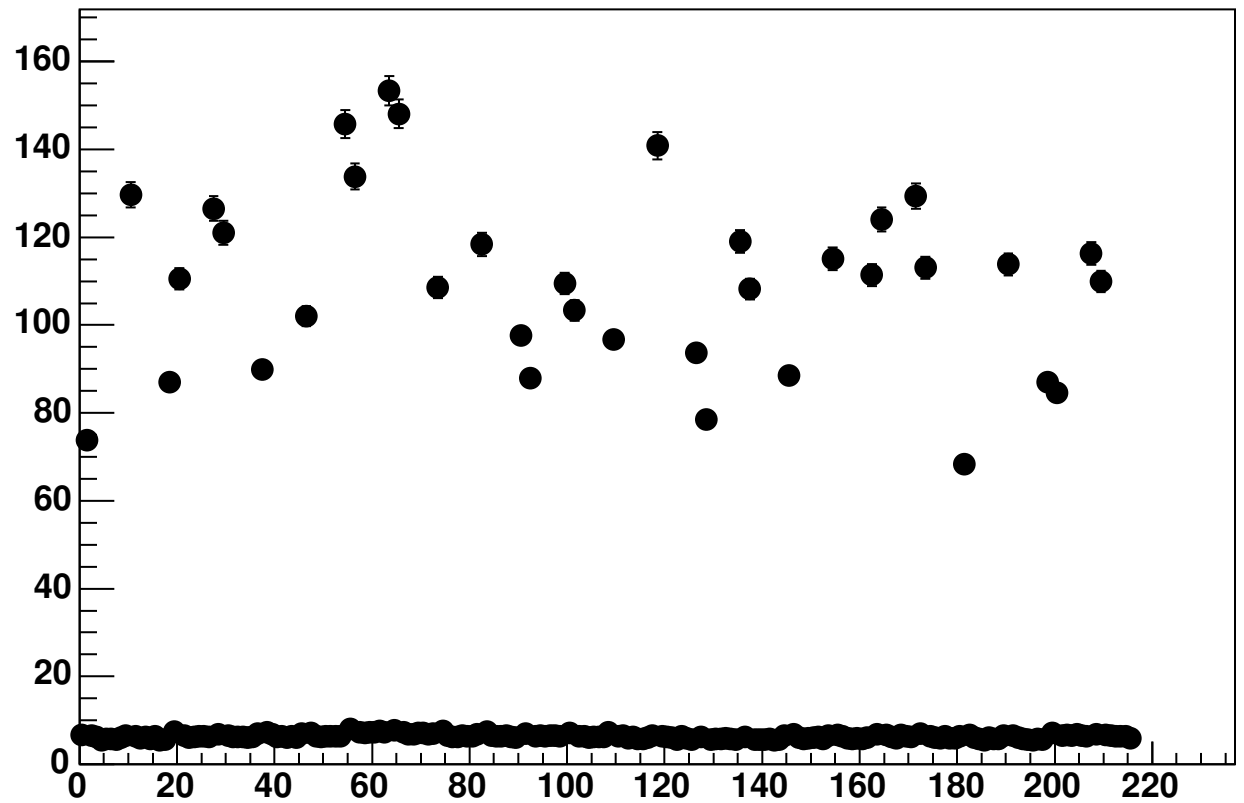
Enable 3, Hold=30, DAC=47104, ADC Noise vs 18*Chip+Chan



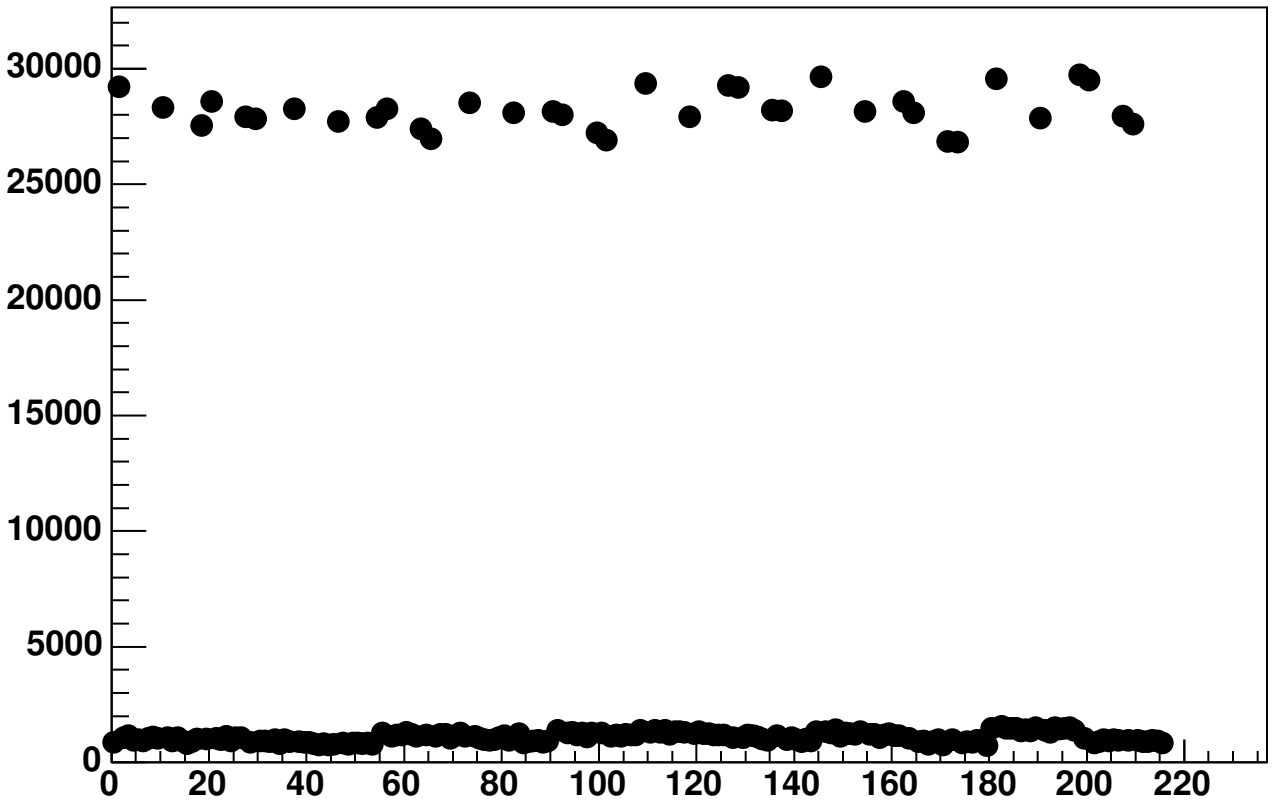
Enable 3, Hold=30, DAC=45056, ADC Mean vs 18*Chip+Chan



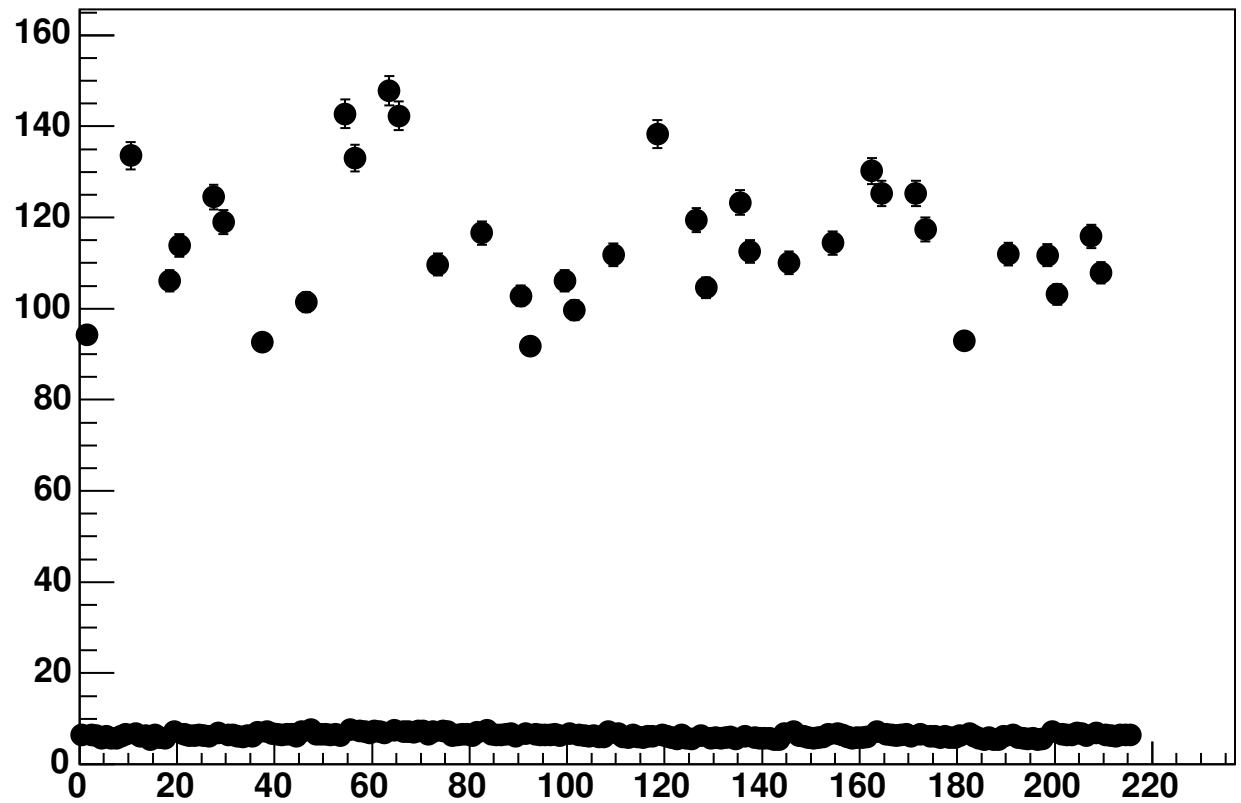
Enable 3, Hold=30, DAC=45056, ADC Noise vs 18*Chip+Chan



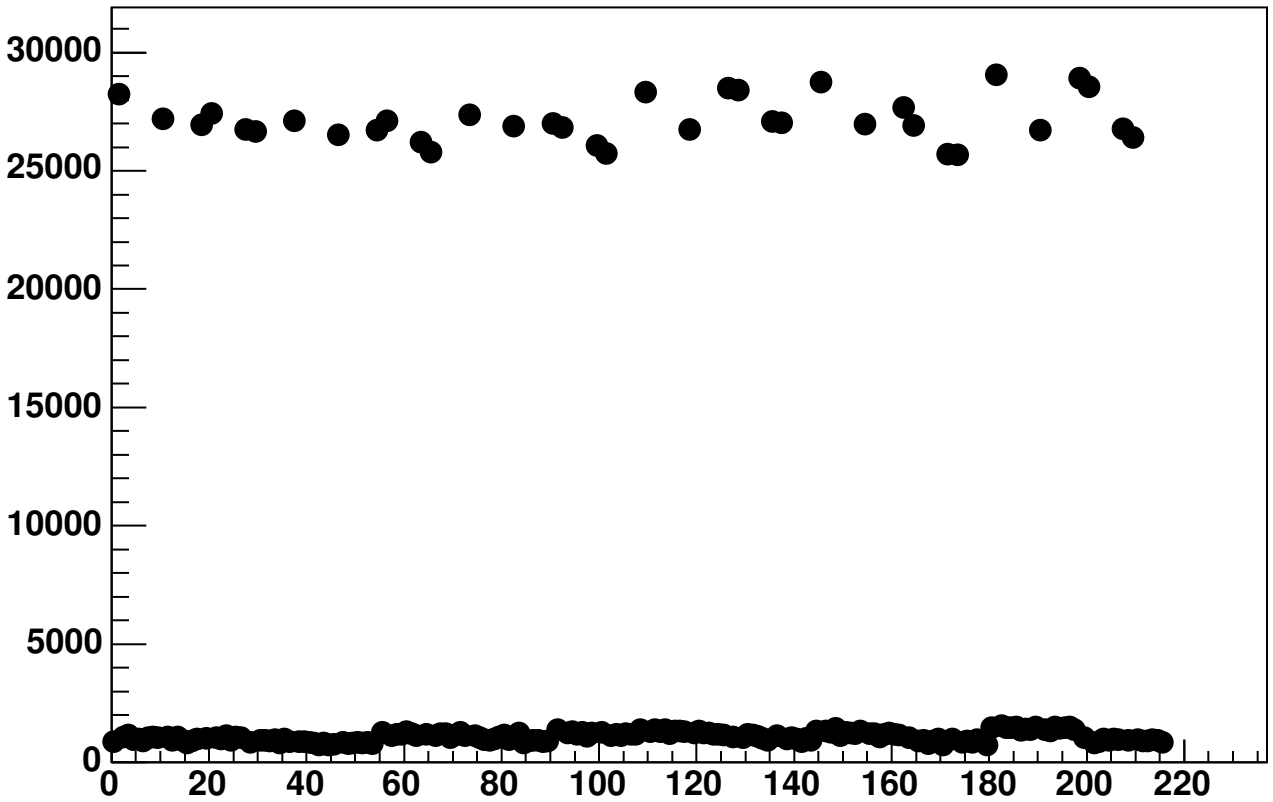
Enable 3, Hold=30, DAC=43008, ADC Mean vs 18*Chip+Chan



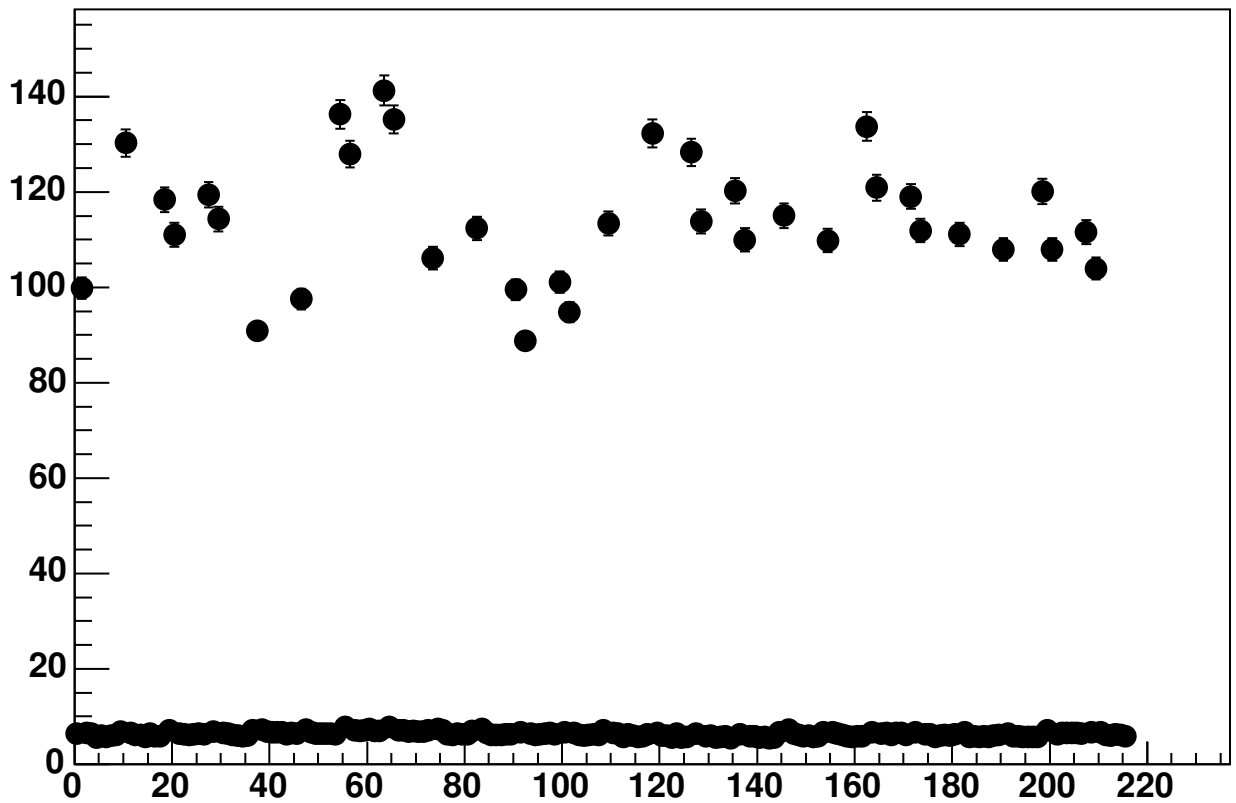
Enable 3, Hold=30, DAC=43008, ADC Noise vs 18*Chip+Chan



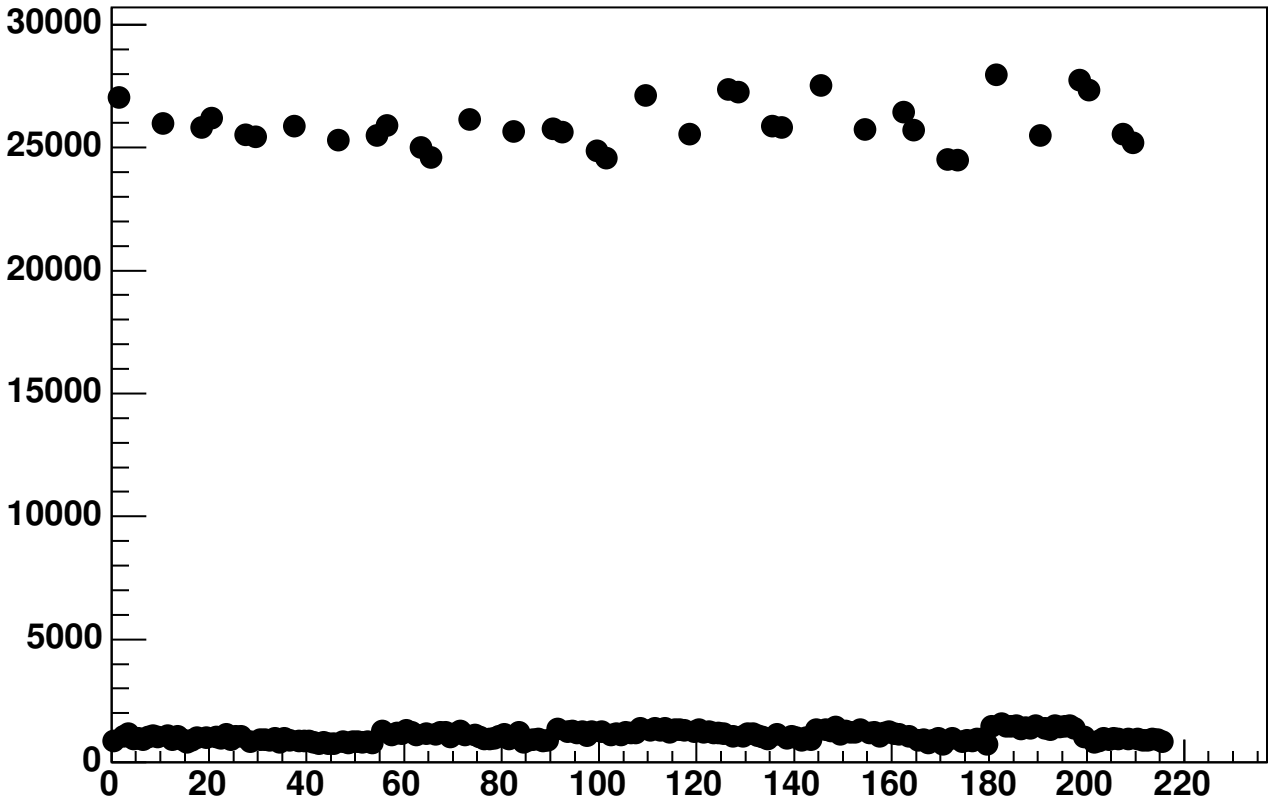
Enable 3, Hold=30, DAC=40960, ADC Mean vs 18*Chip+Chan



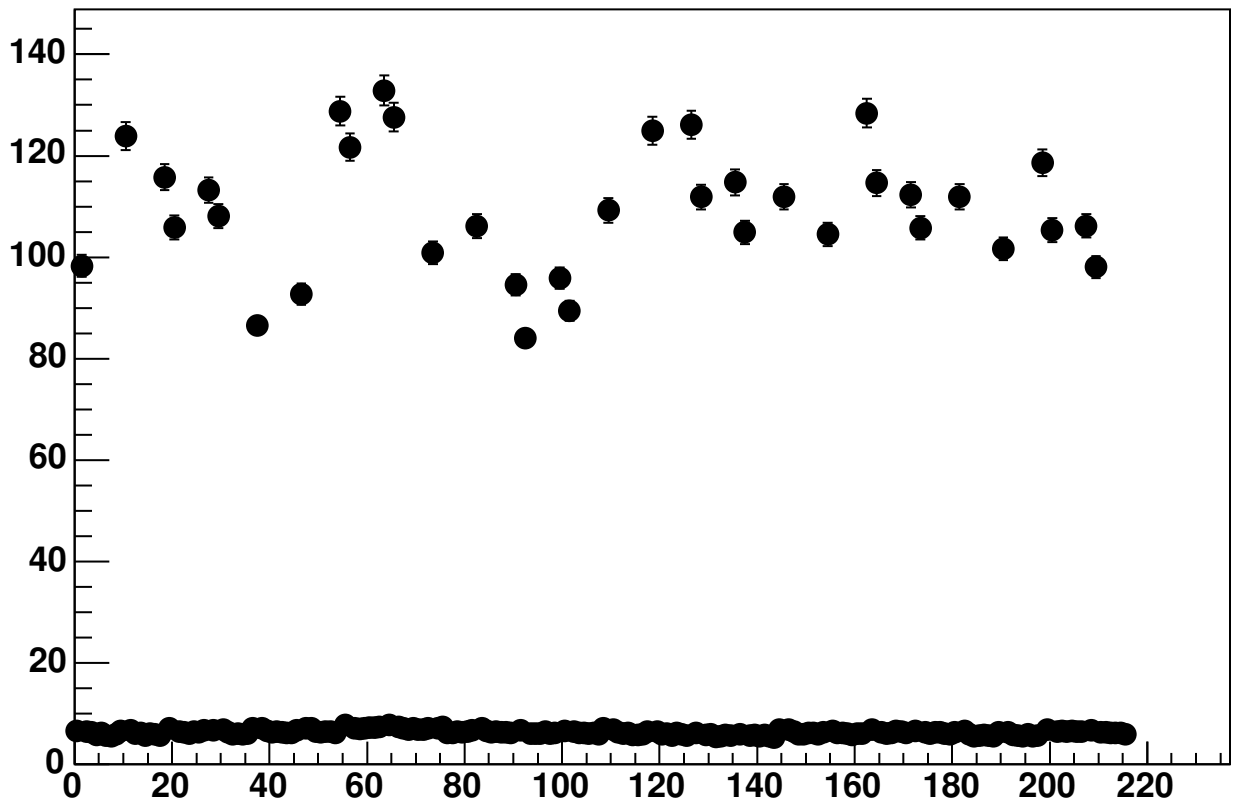
Enable 3, Hold=30, DAC=40960, ADC Noise vs 18*Chip+Chan



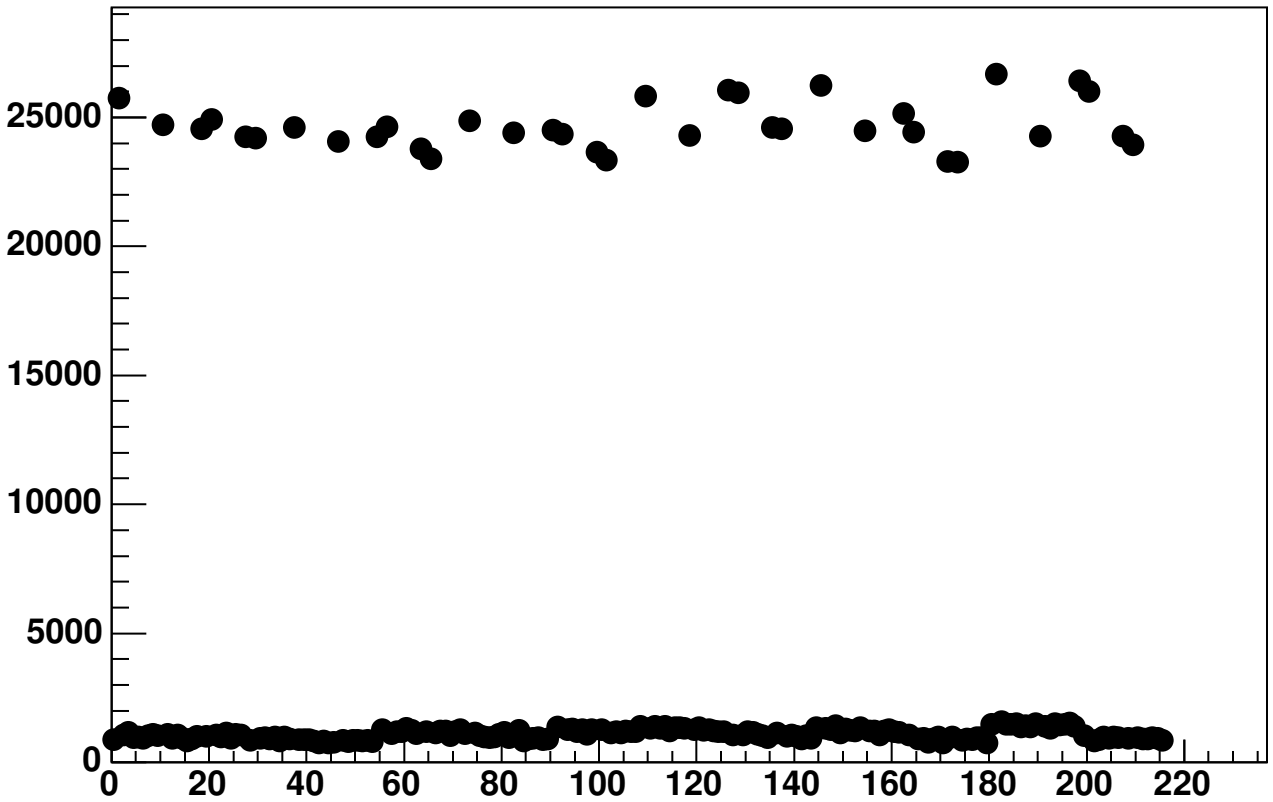
Enable 3, Hold=30, DAC=38912, ADC Mean vs 18*Chip+Chan



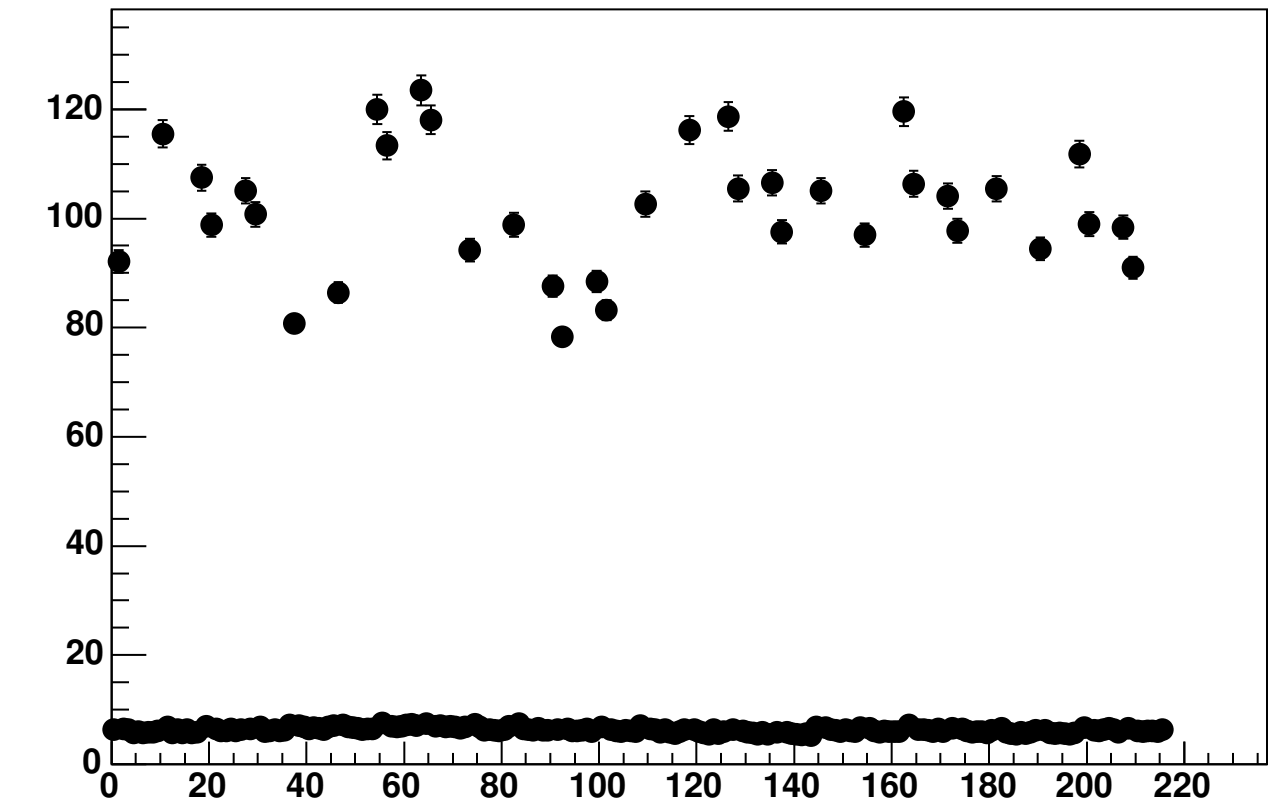
Enable 3, Hold=30, DAC=38912, ADC Noise vs 18*Chip+Chan



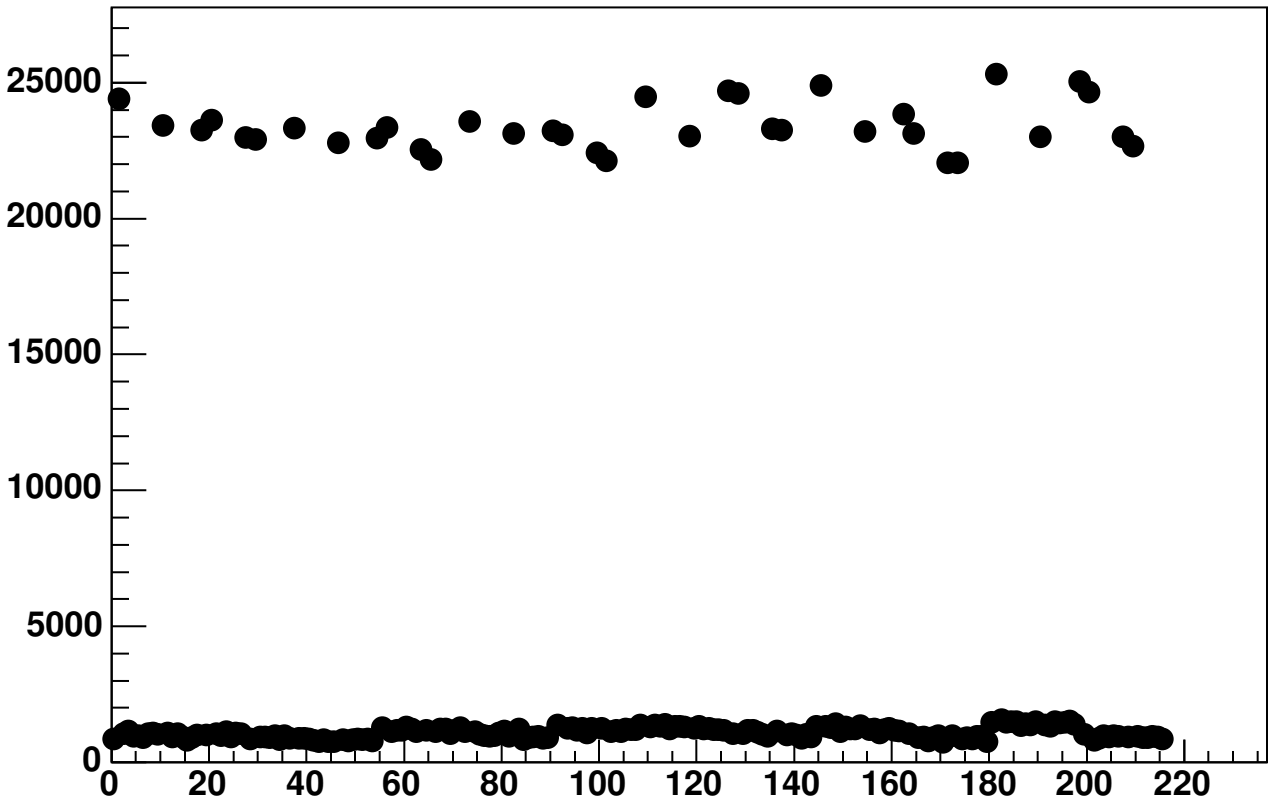
Enable 3, Hold=30, DAC=36864, ADC Mean vs 18*Chip+Chan



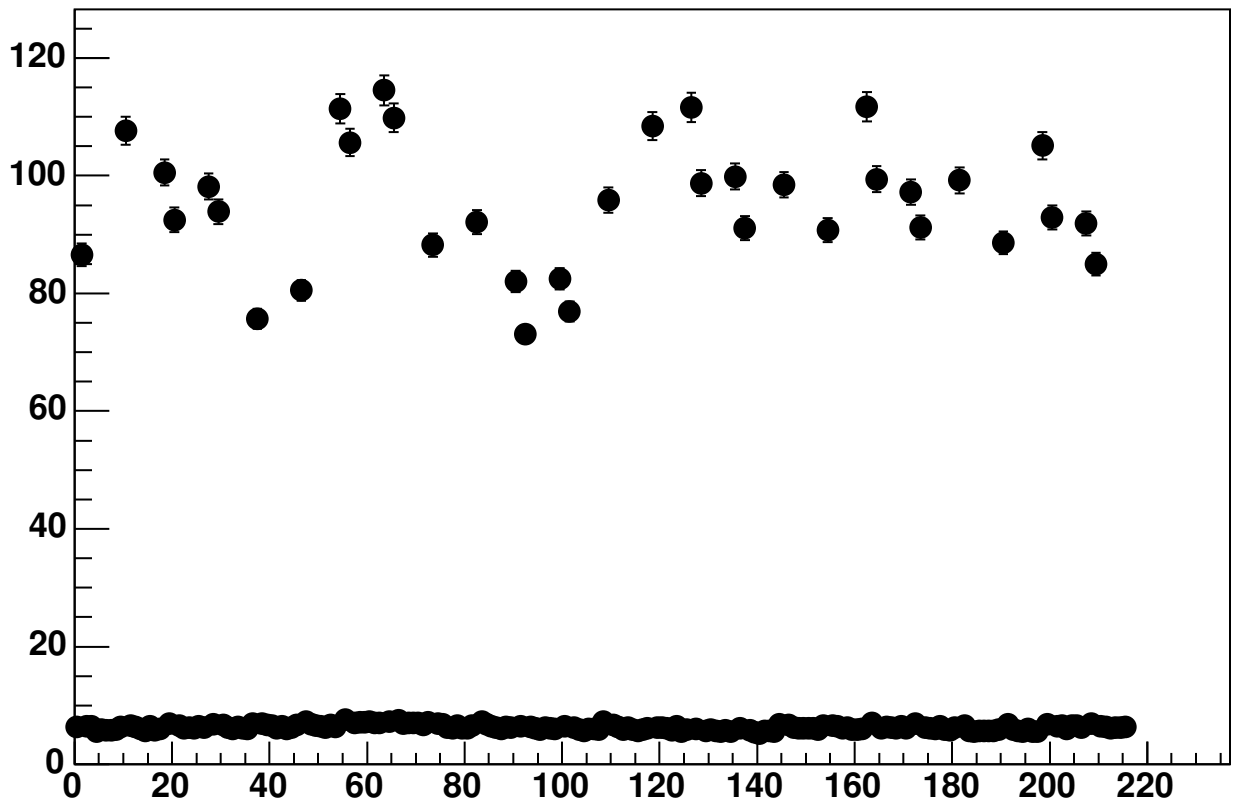
Enable 3, Hold=30, DAC=36864, ADC Noise vs 18*Chip+Chan



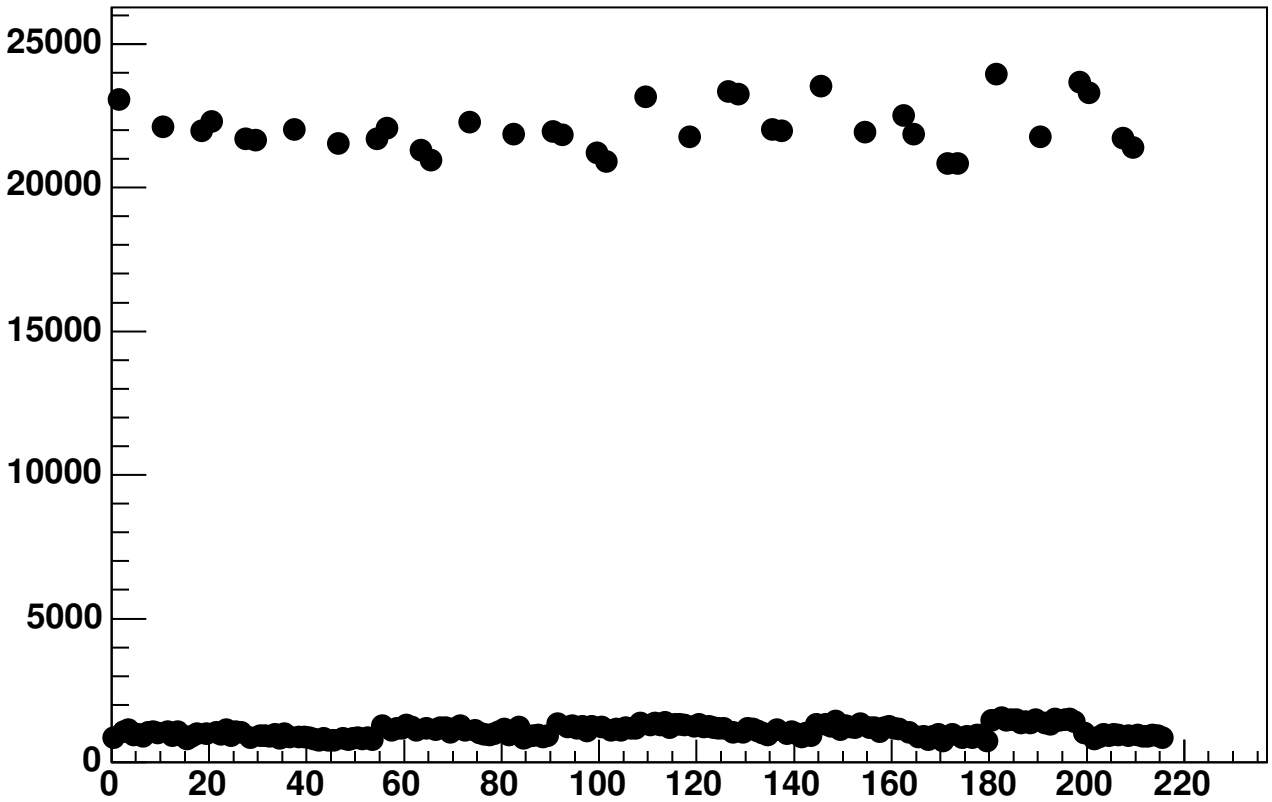
Enable 3, Hold=30, DAC=34816, ADC Mean vs 18*Chip+Chan



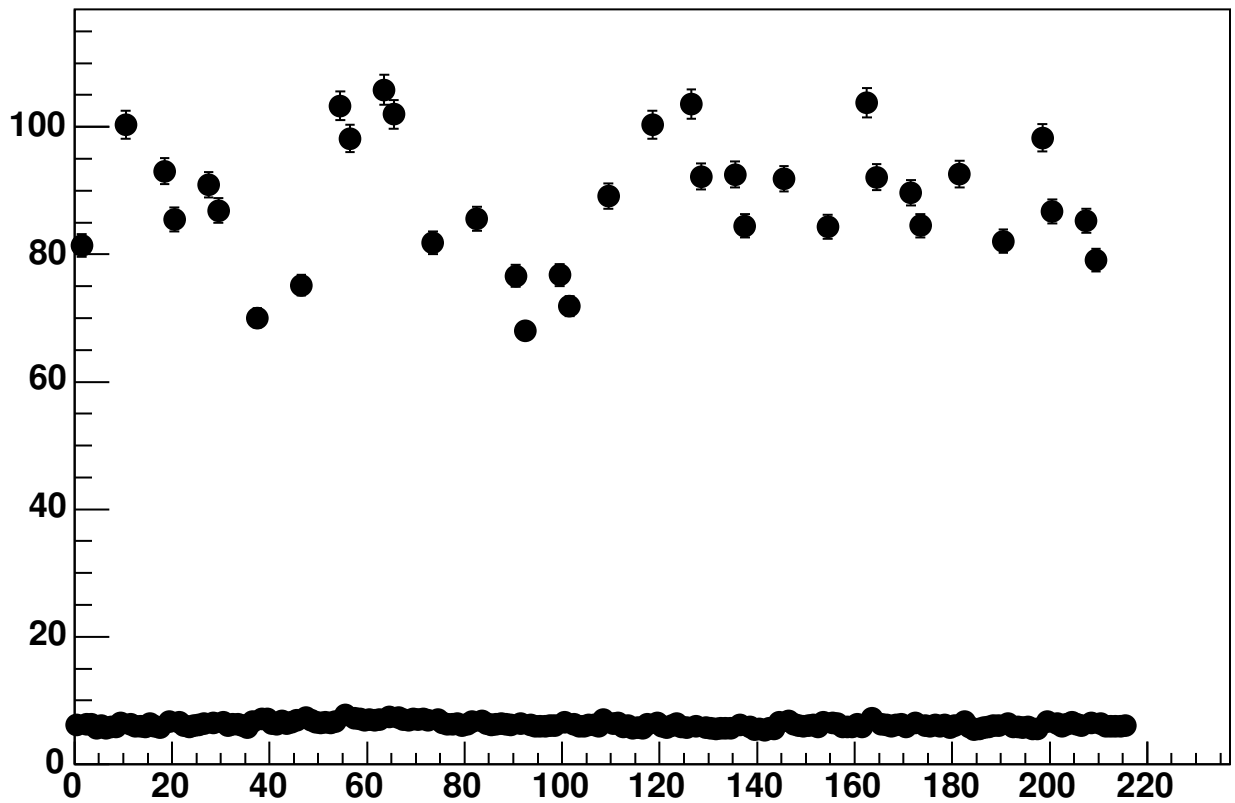
Enable 3, Hold=30, DAC=34816, ADC Noise vs 18*Chip+Chan



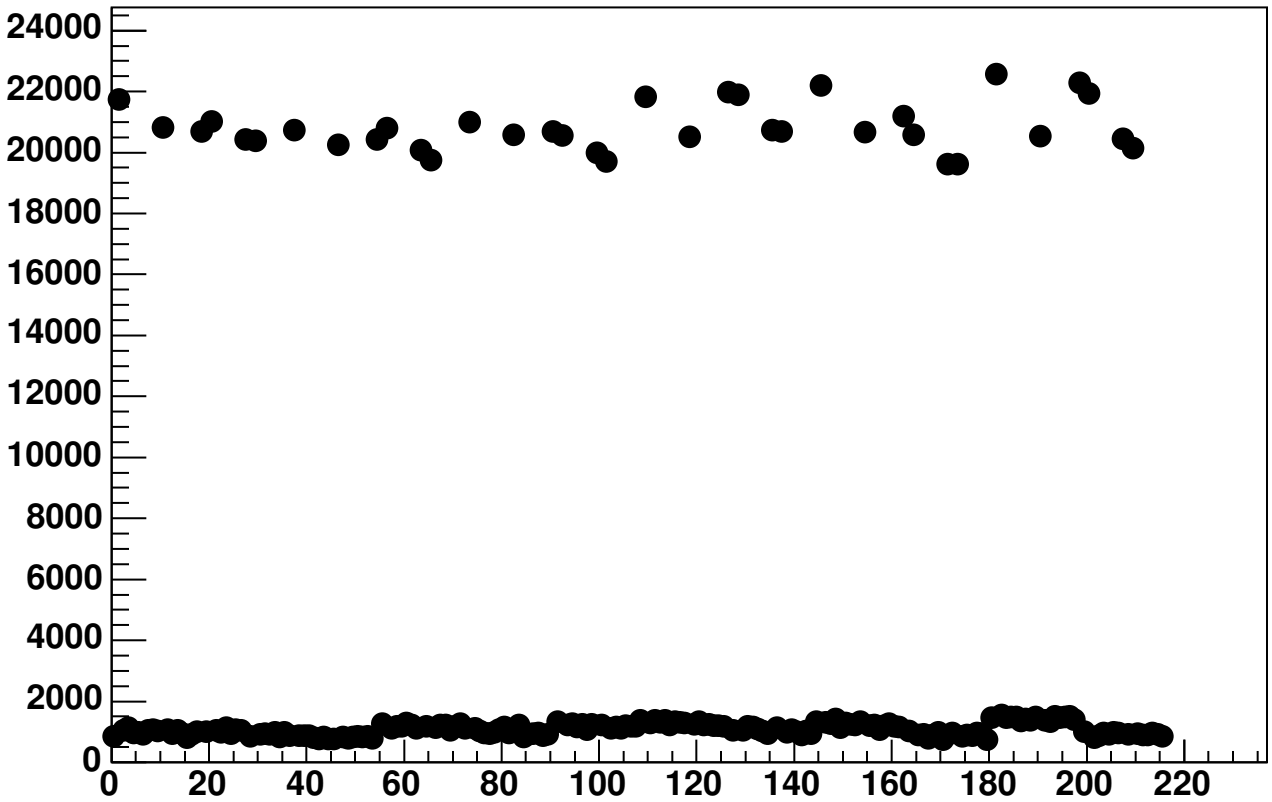
Enable 3, Hold=30, DAC=32768, ADC Mean vs 18*Chip+Chan



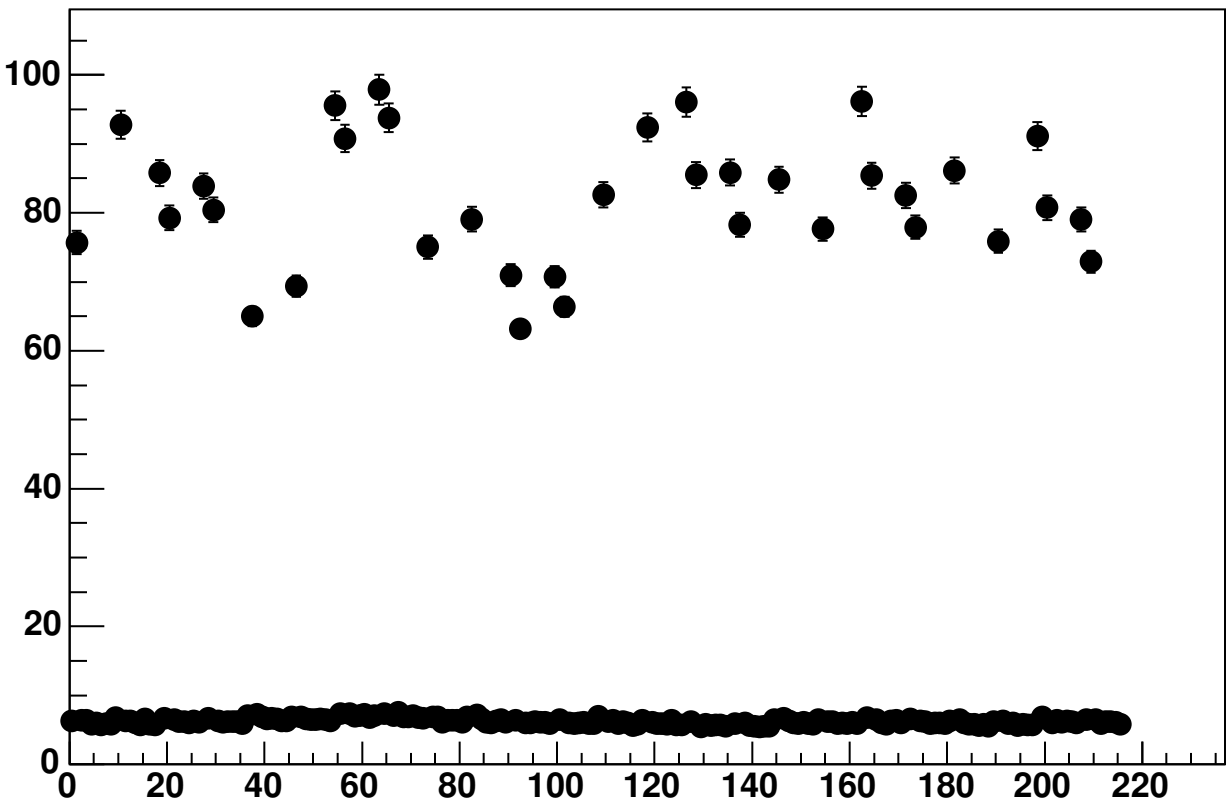
Enable 3, Hold=30, DAC=32768, ADC Noise vs 18*Chip+Chan



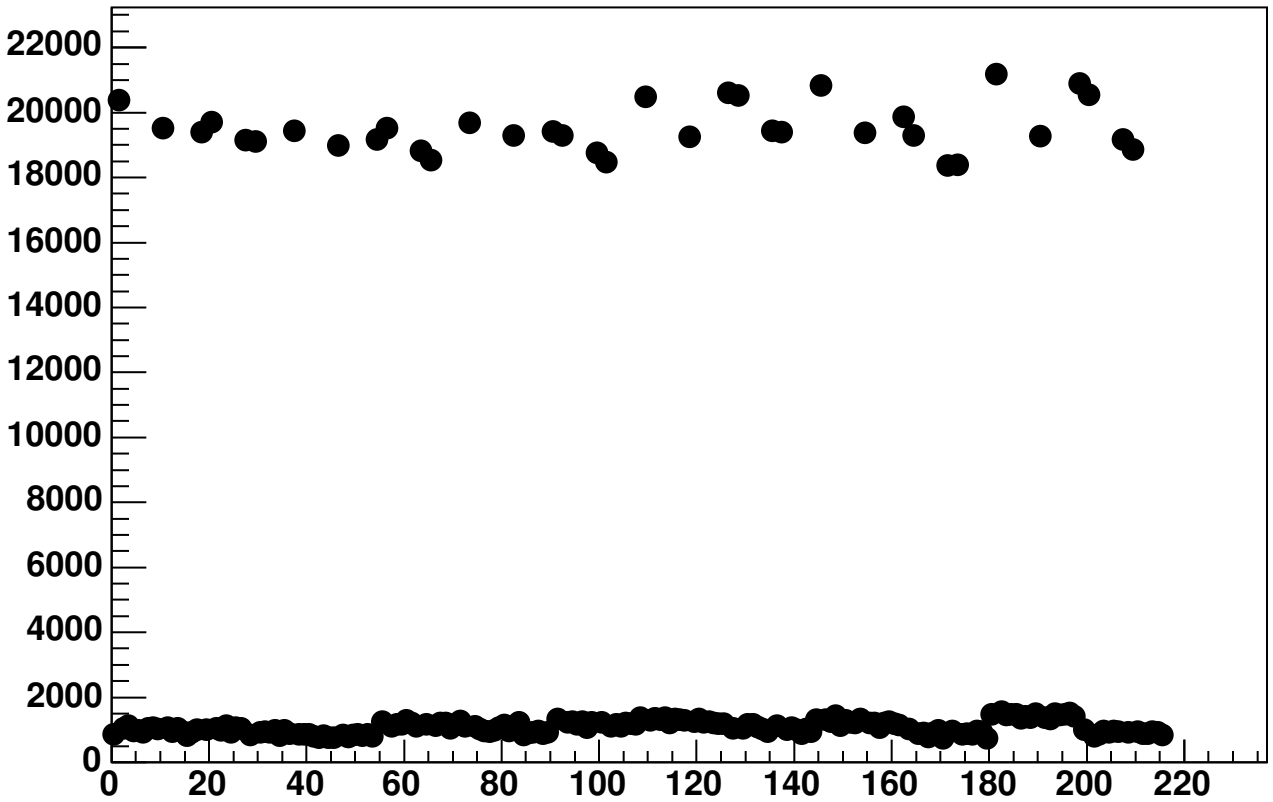
Enable 3, Hold=30, DAC=30720, ADC Mean vs 18*Chip+Chan



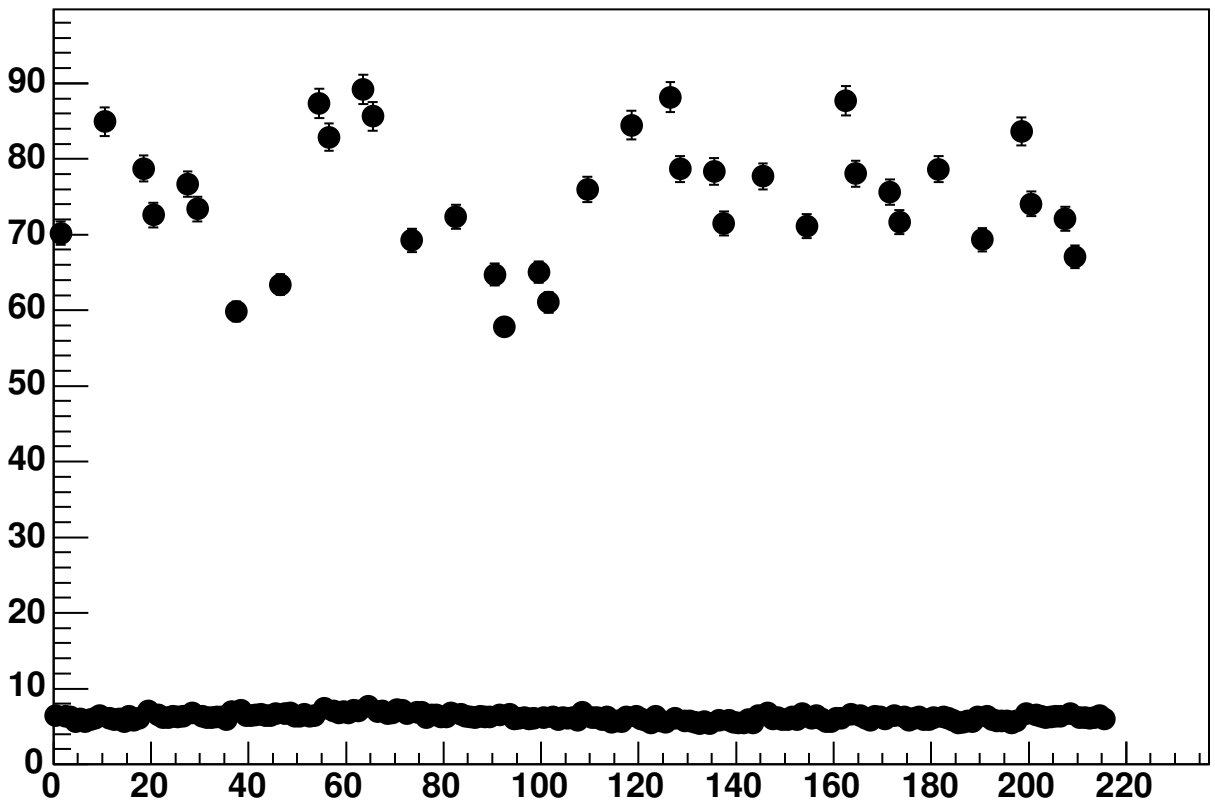
Enable 3, Hold=30, DAC=30720, ADC Noise vs 18*Chip+Chan



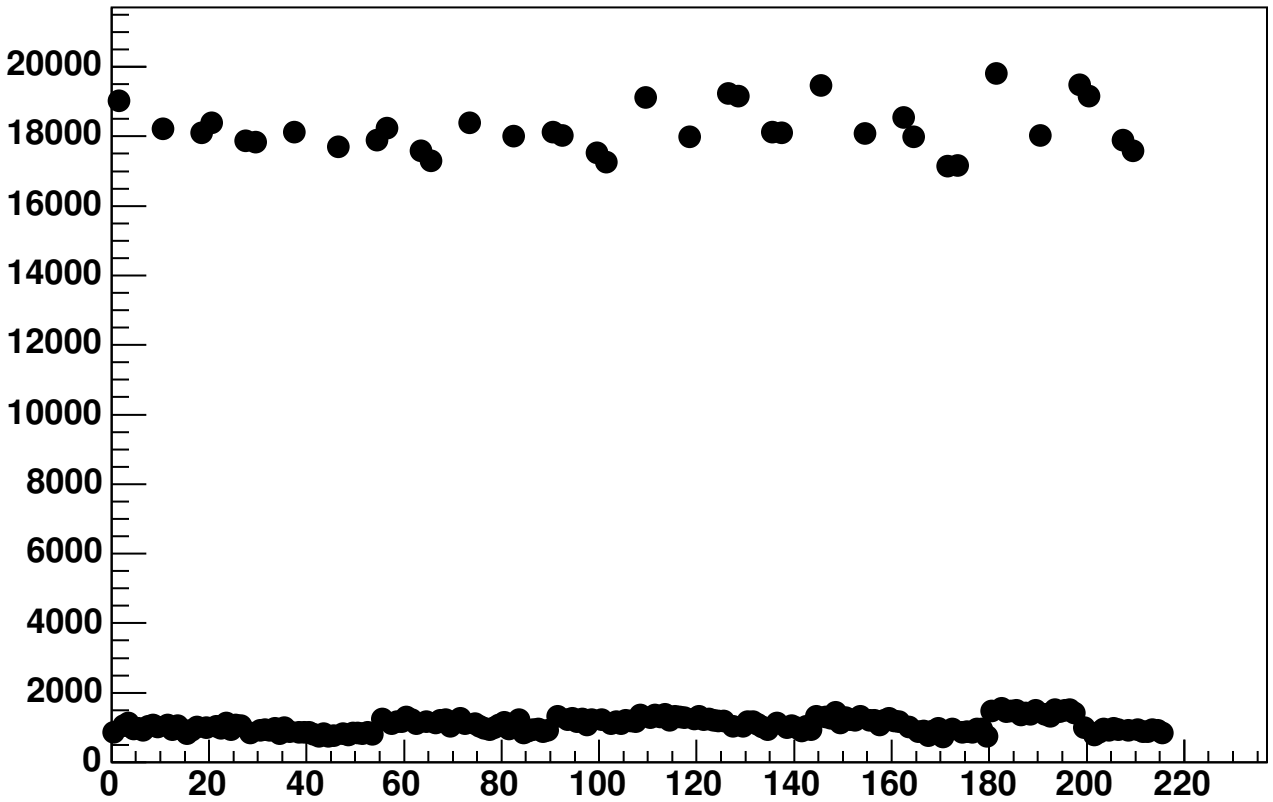
Enable 3, Hold=30, DAC=28672, ADC Mean vs 18*Chip+Chan



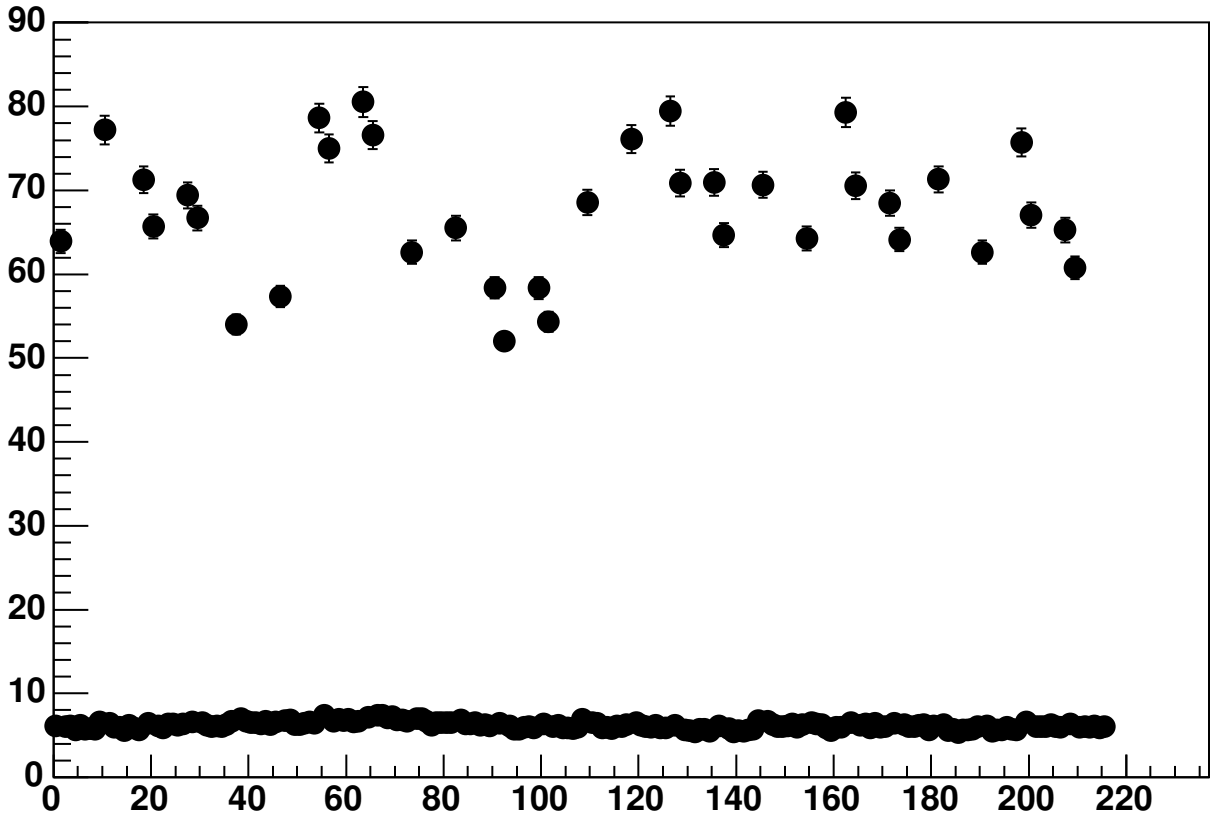
Enable 3, Hold=30, DAC=28672, ADC Noise vs 18*Chip+Chan



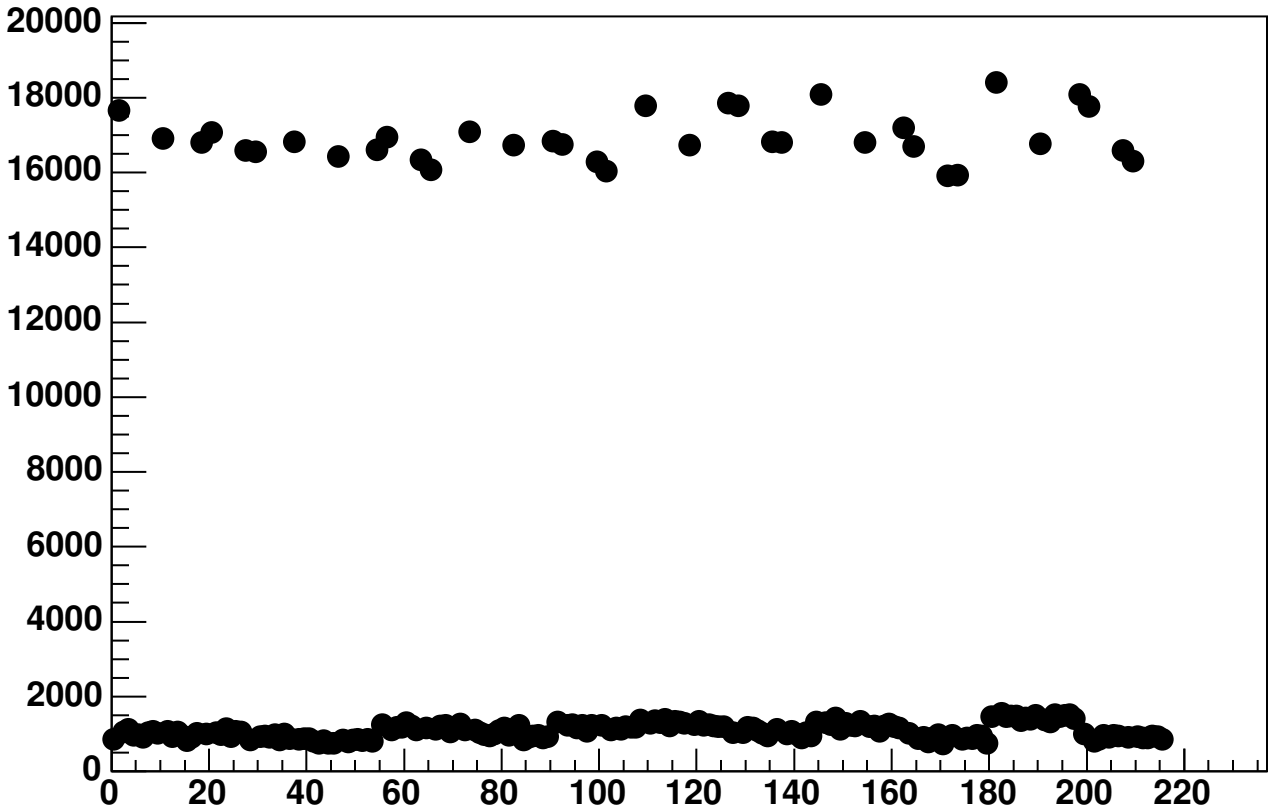
Enable 3, Hold=30, DAC=26624, ADC Mean vs 18*Chip+Chan



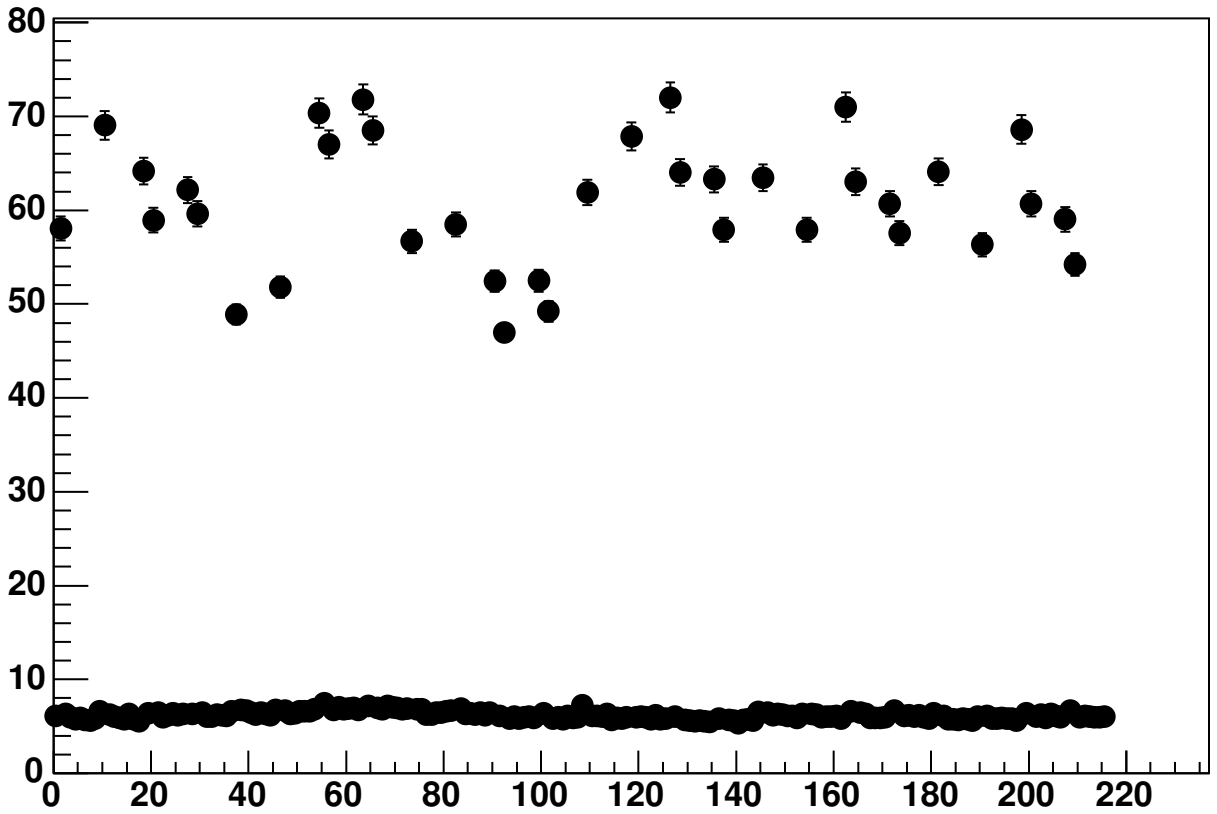
Enable 3, Hold=30, DAC=26624, ADC Noise vs 18*Chip+Chan



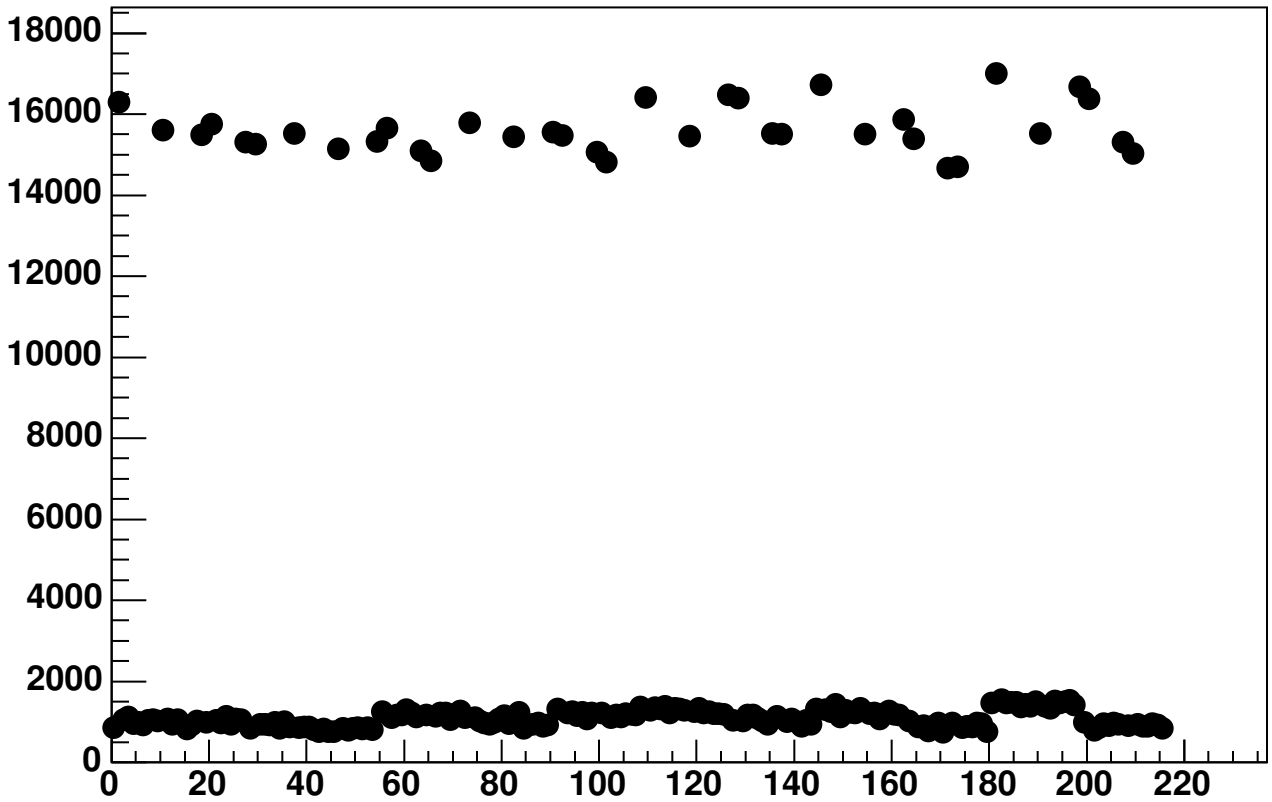
Enable 3, Hold=30, DAC=24576, ADC Mean vs 18*Chip+Chan



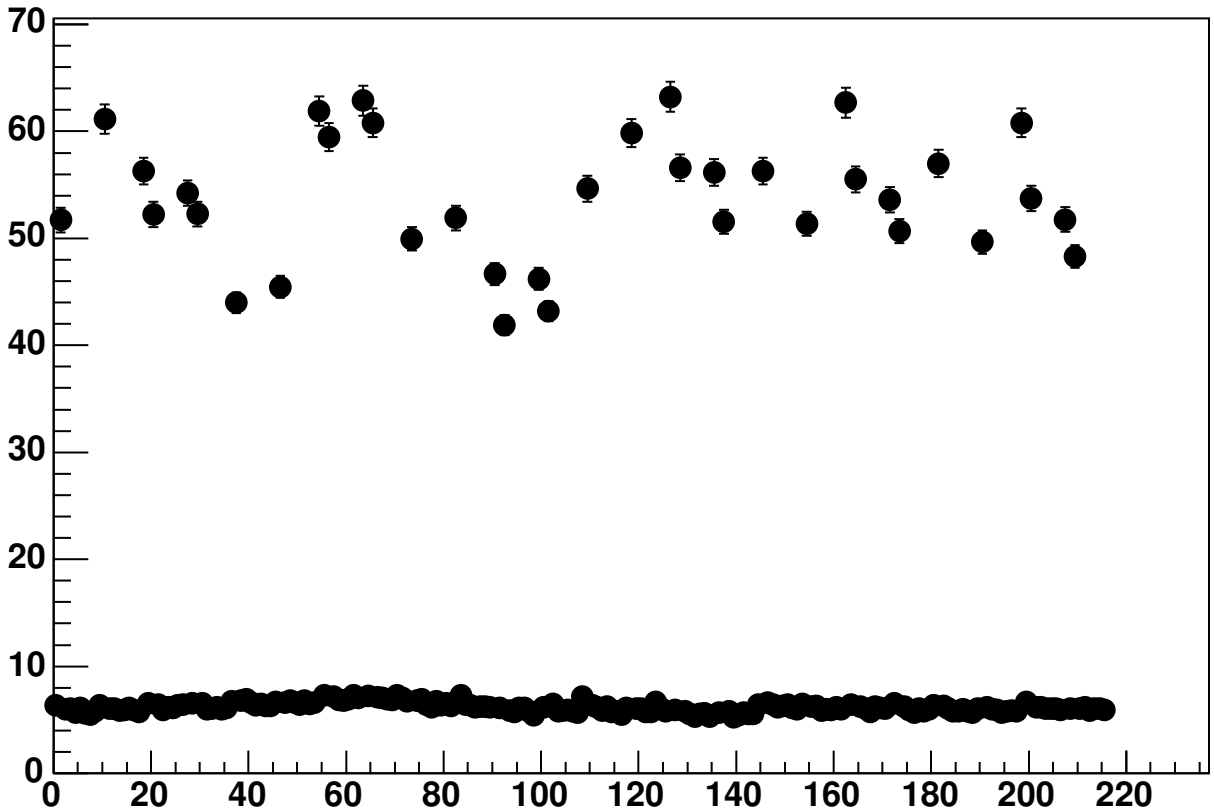
Enable 3, Hold=30, DAC=24576, ADC Noise vs 18*Chip+Chan



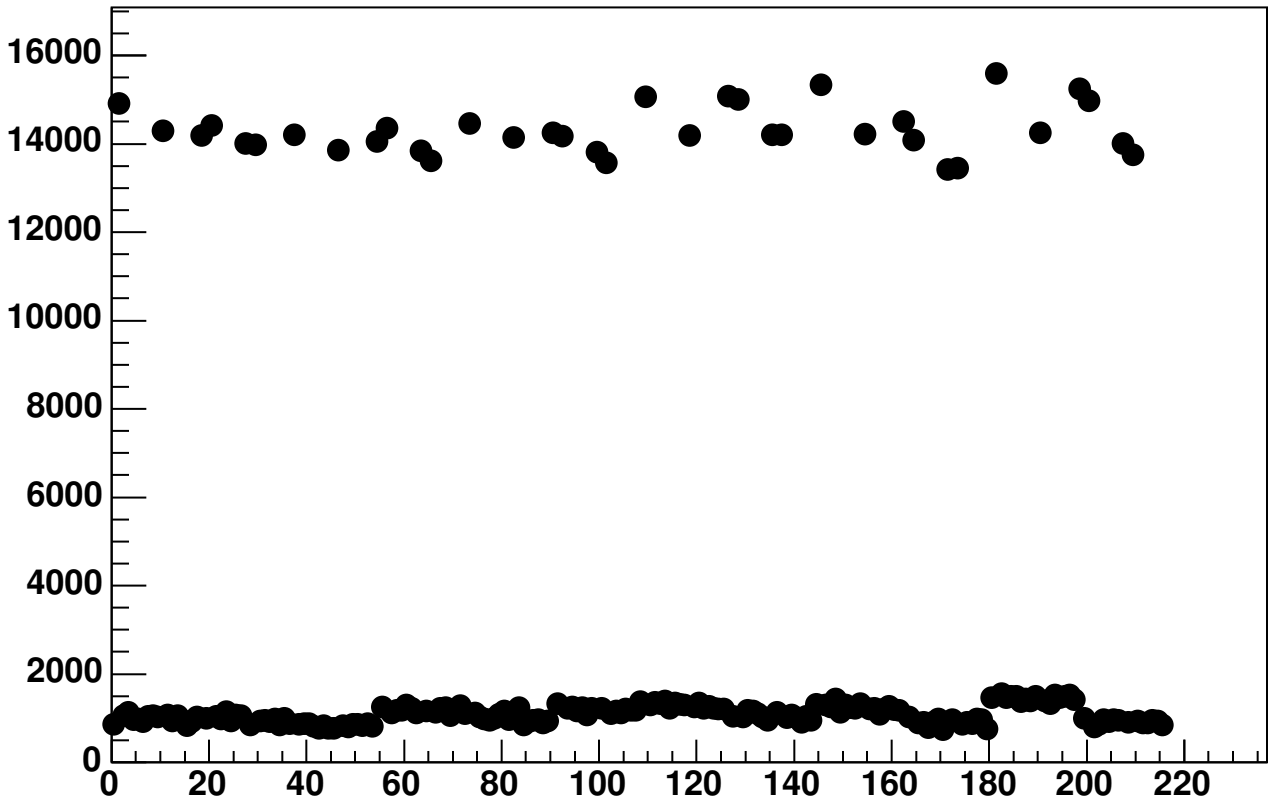
Enable 3, Hold=30, DAC=22528, ADC Mean vs 18*Chip+Chan



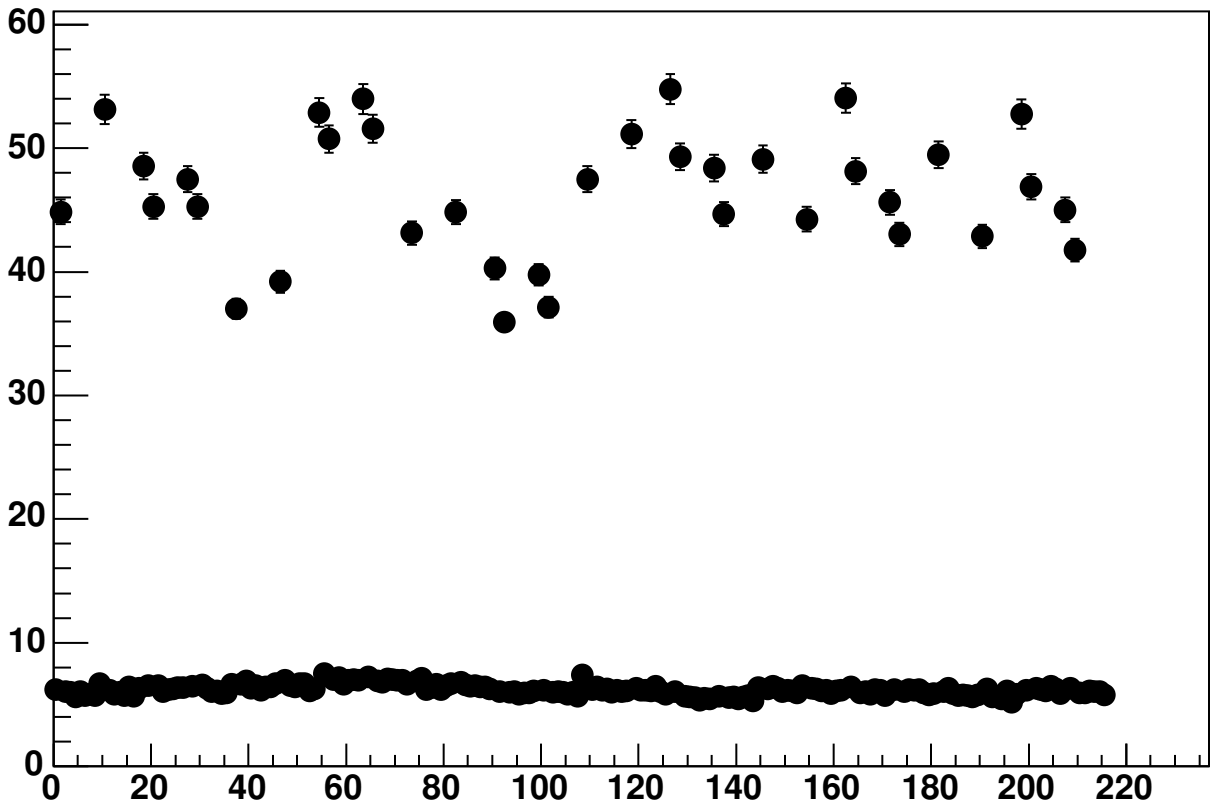
Enable 3, Hold=30, DAC=22528, ADC Noise vs 18*Chip+Chan



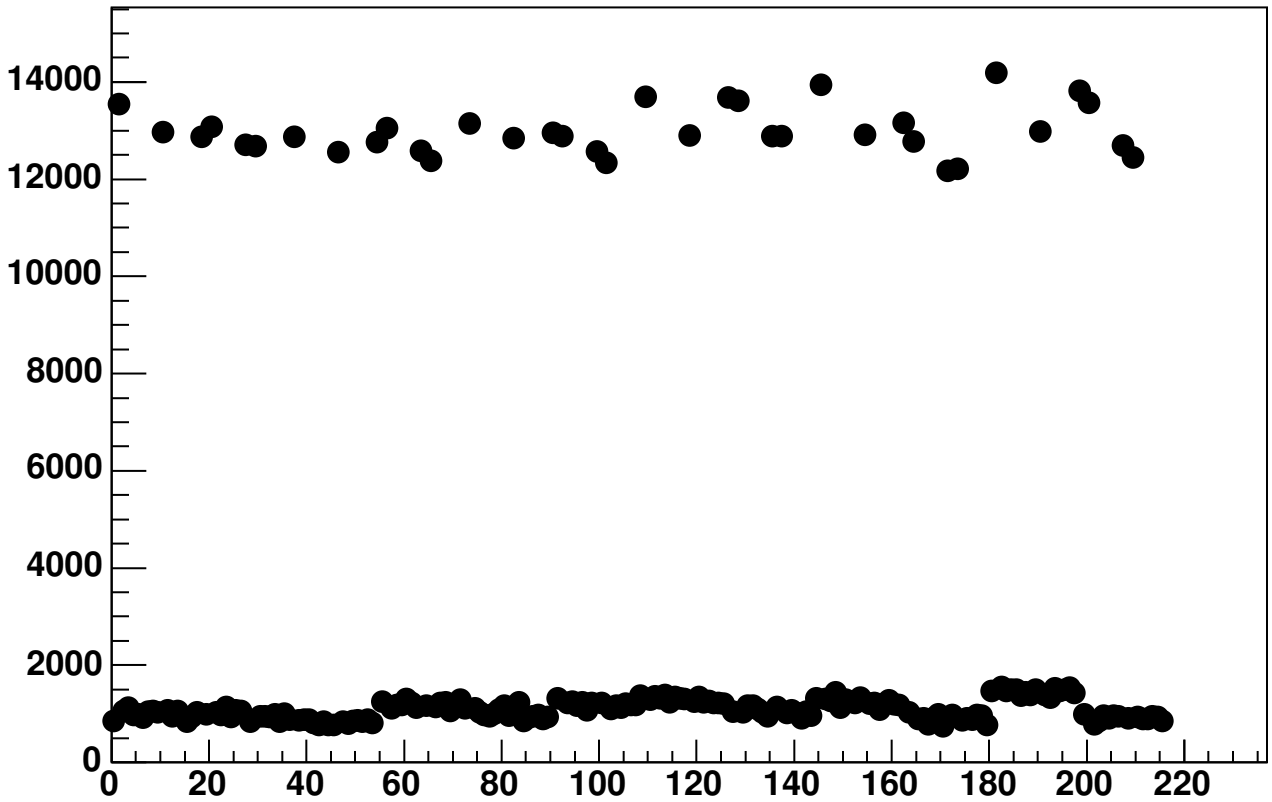
Enable 3, Hold=30, DAC=20480, ADC Mean vs 18*Chip+Chan



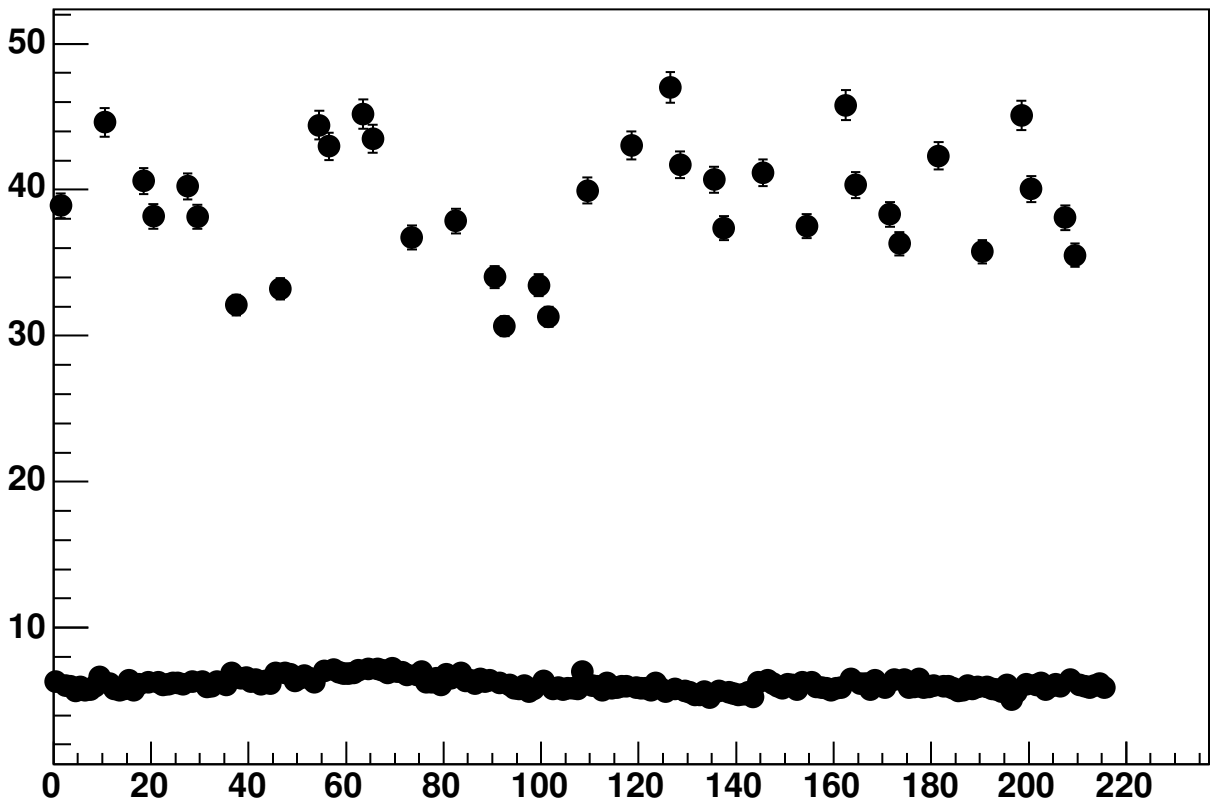
Enable 3, Hold=30, DAC=20480, ADC Noise vs 18*Chip+Chan



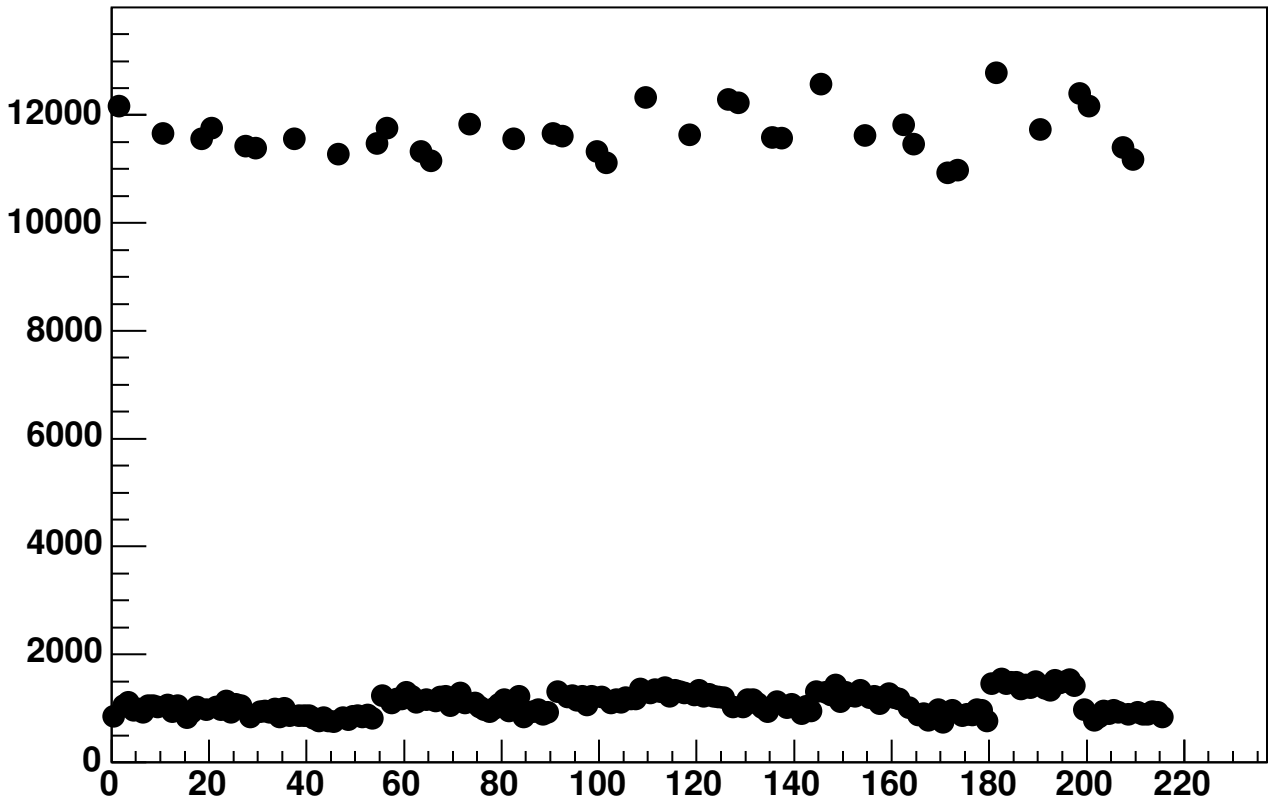
Enable 3, Hold=30, DAC=18432, ADC Mean vs 18*Chip+Chan



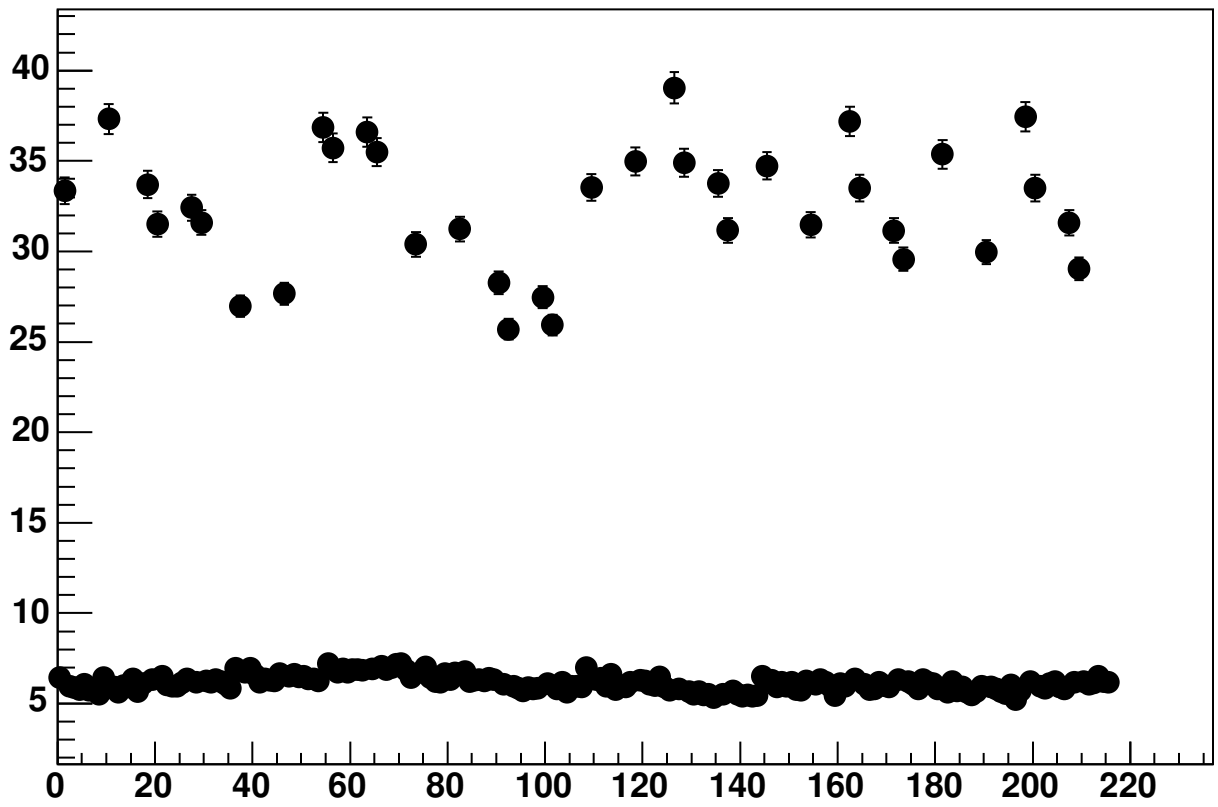
Enable 3, Hold=30, DAC=18432, ADC Noise vs 18*Chip+Chan



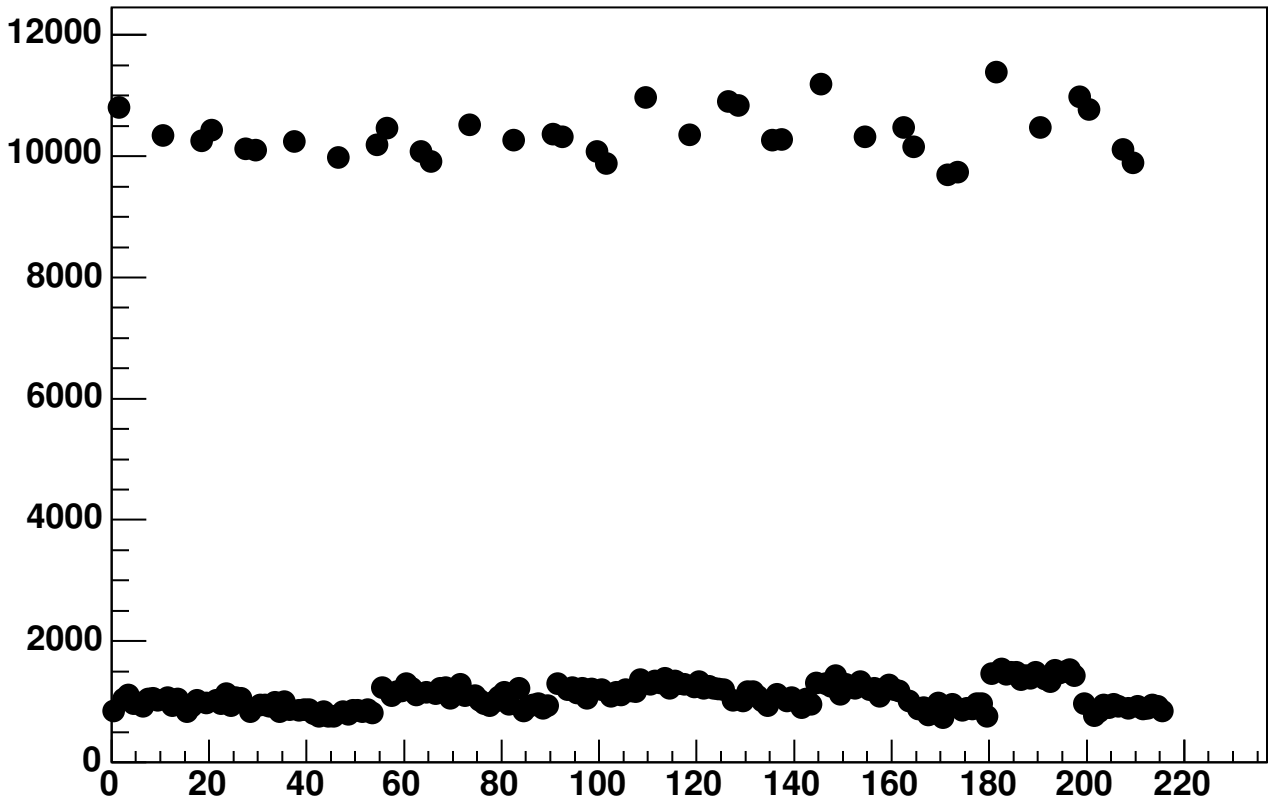
Enable 3, Hold=30, DAC=16384, ADC Mean vs 18*Chip+Chan



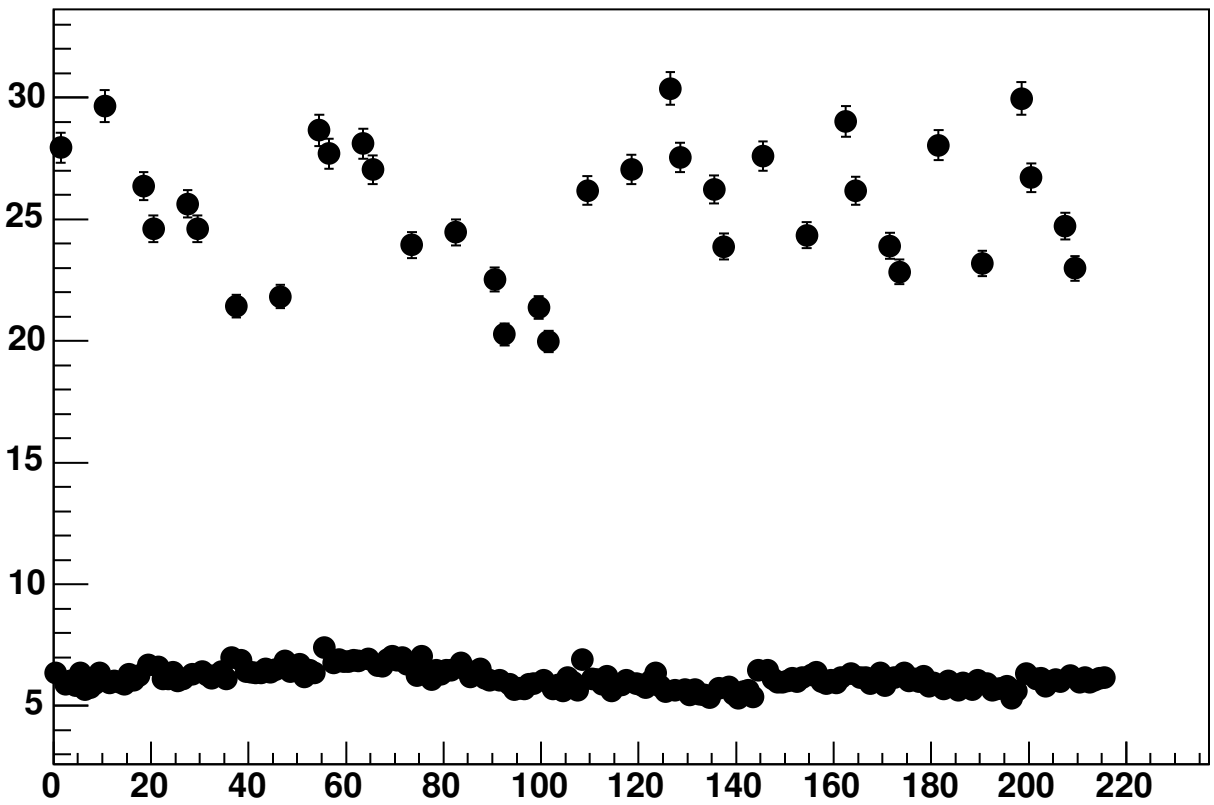
Enable 3, Hold=30, DAC=16384, ADC Noise vs 18*Chip+Chan



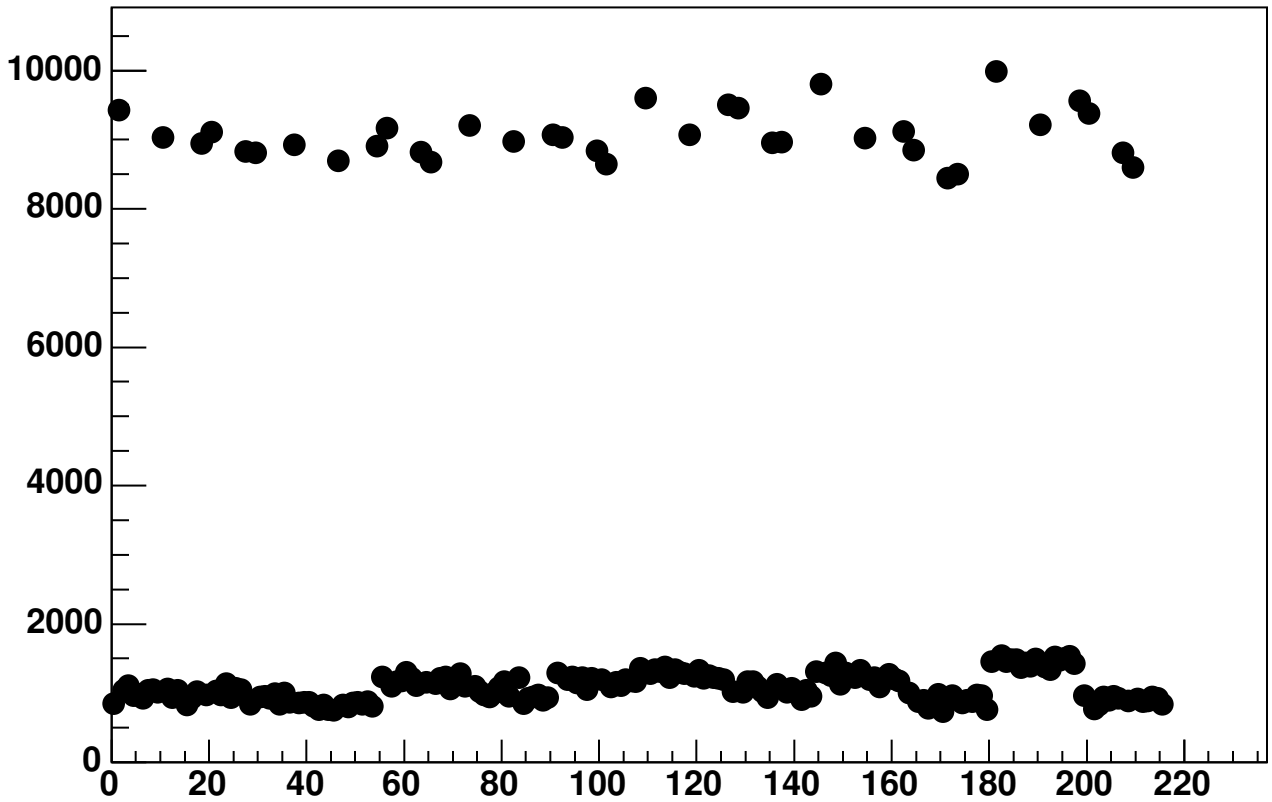
Enable 3, Hold=30, DAC=14336, ADC Mean vs 18*Chip+Chan



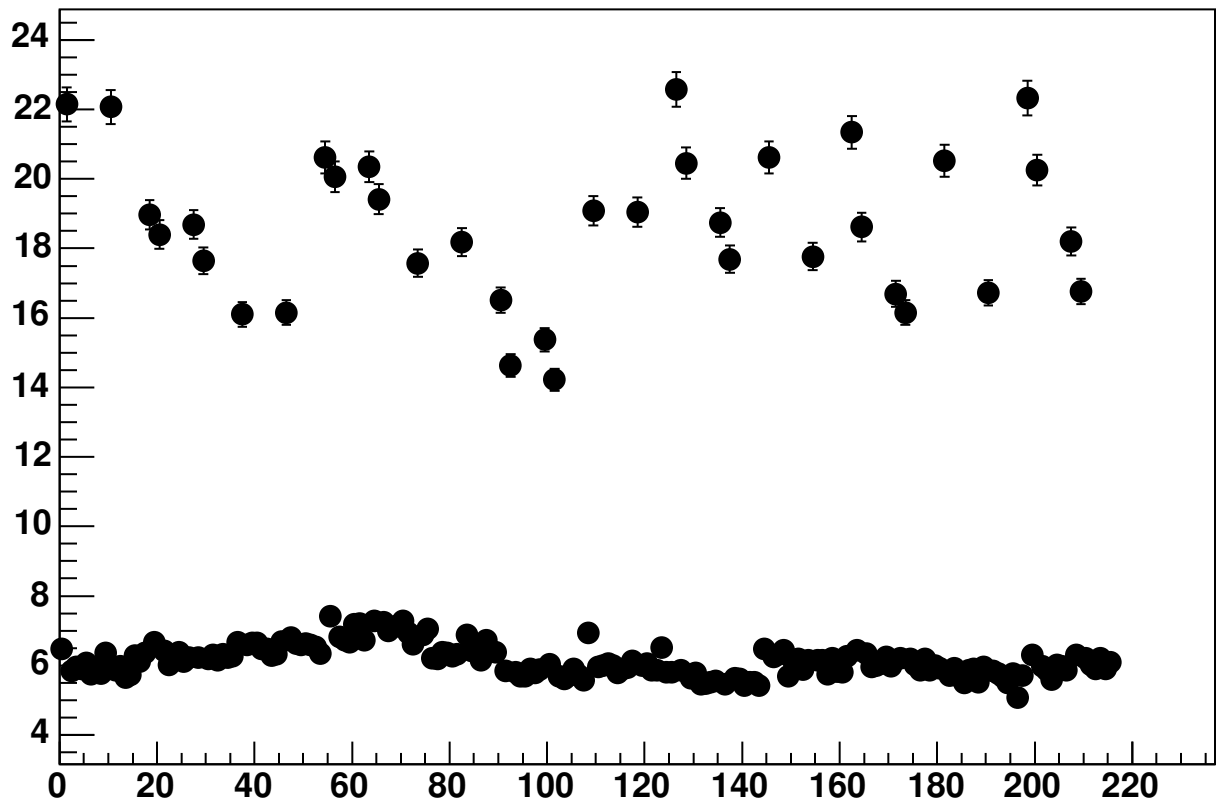
Enable 3, Hold=30, DAC=14336, ADC Noise vs 18*Chip+Chan



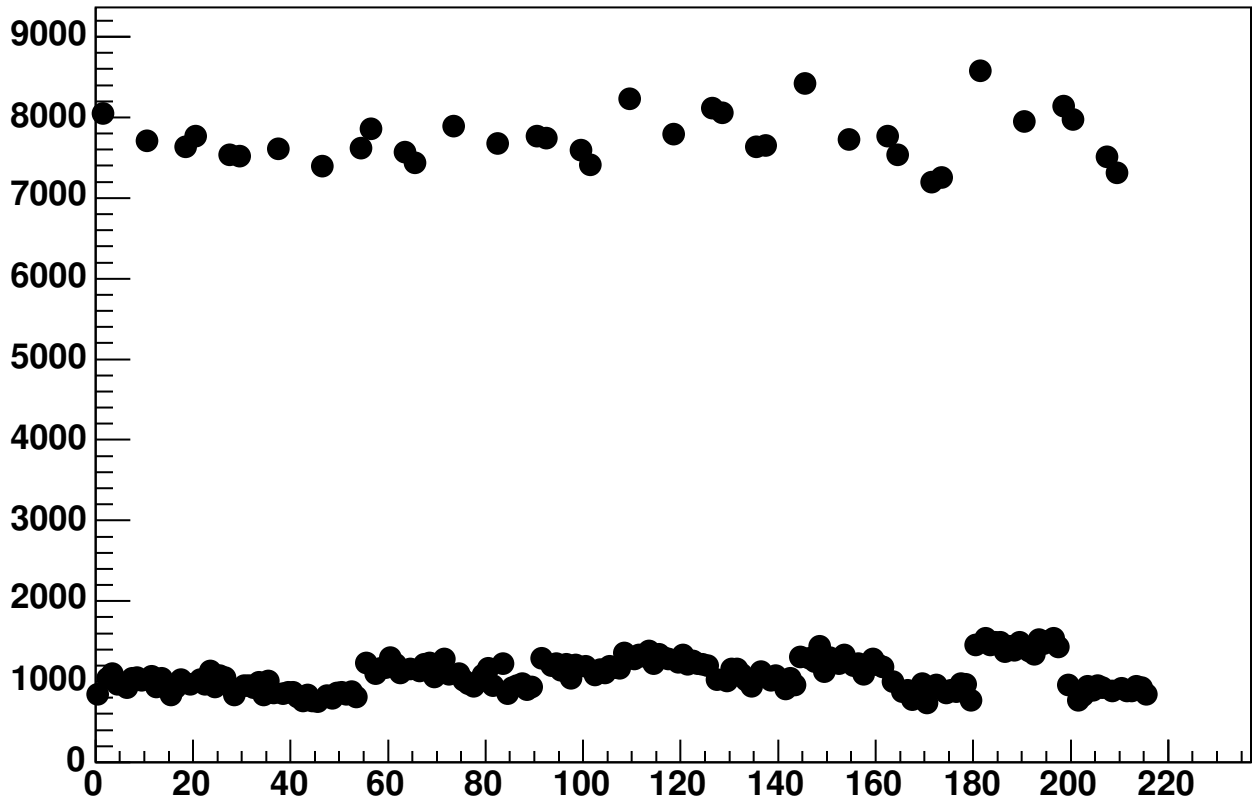
Enable 3, Hold=30, DAC=12288, ADC Mean vs 18*Chip+Chan



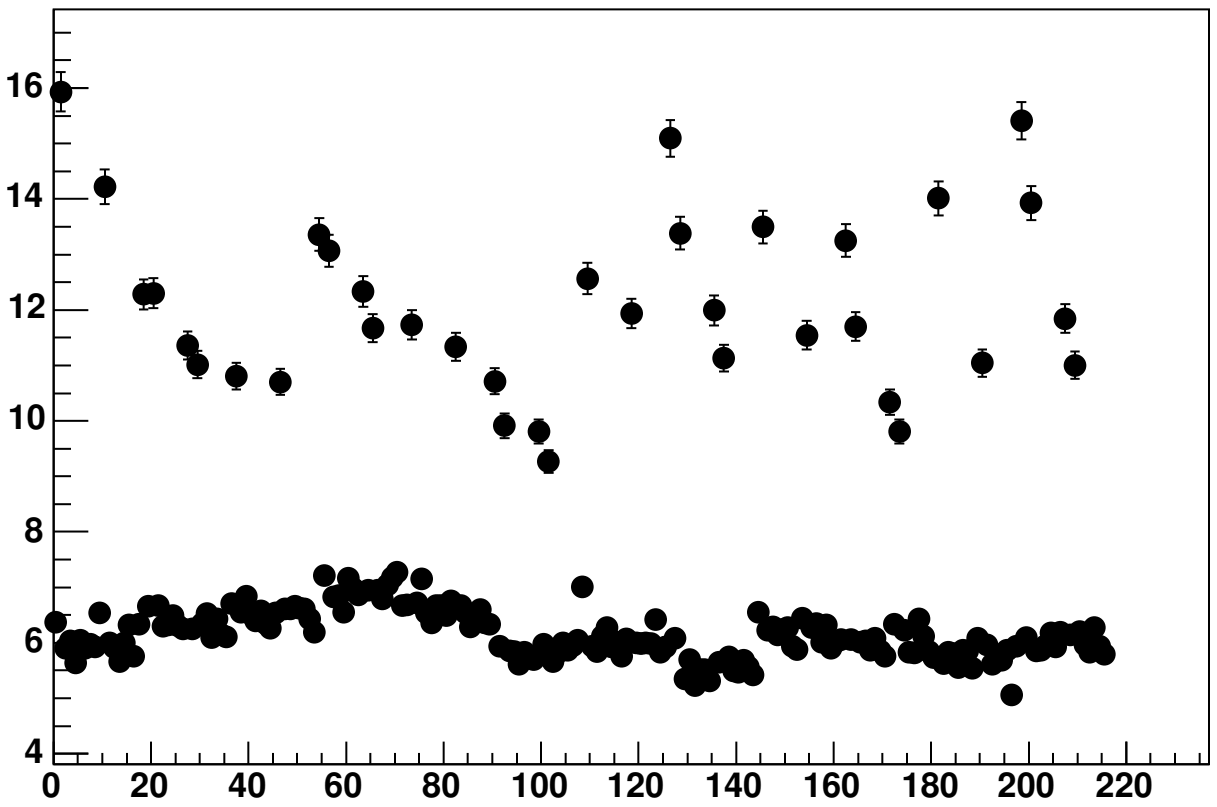
Enable 3, Hold=30, DAC=12288, ADC Noise vs 18*Chip+Chan



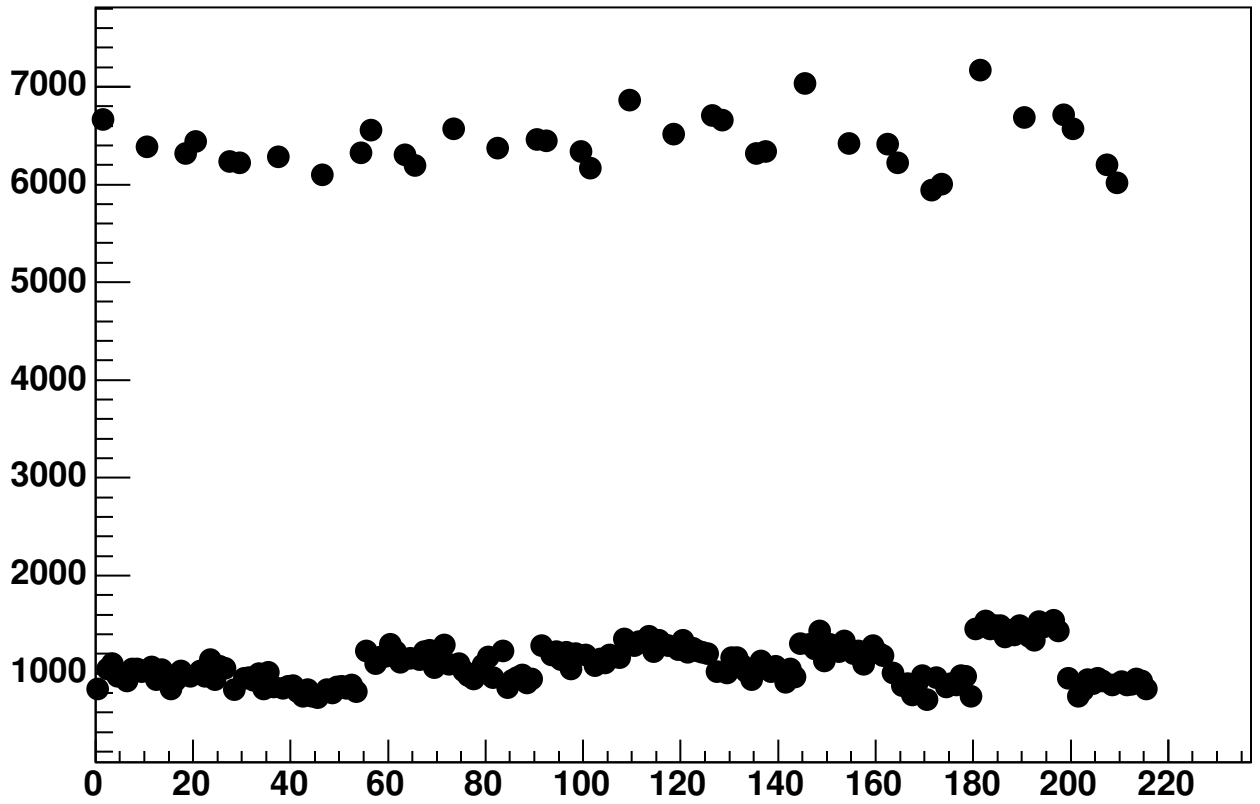
Enable 3, Hold=30, DAC=10240, ADC Mean vs 18*Chip+Chan



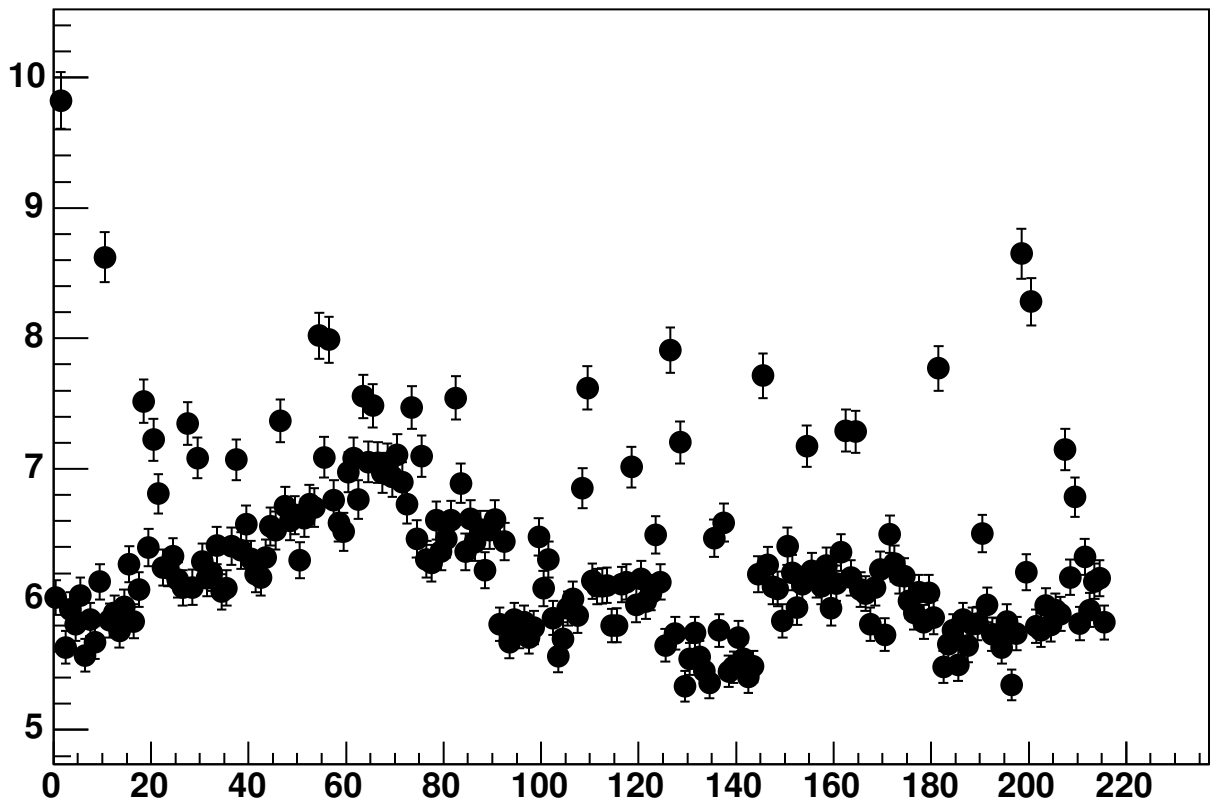
Enable 3, Hold=30, DAC=10240, ADC Noise vs 18*Chip+Chan



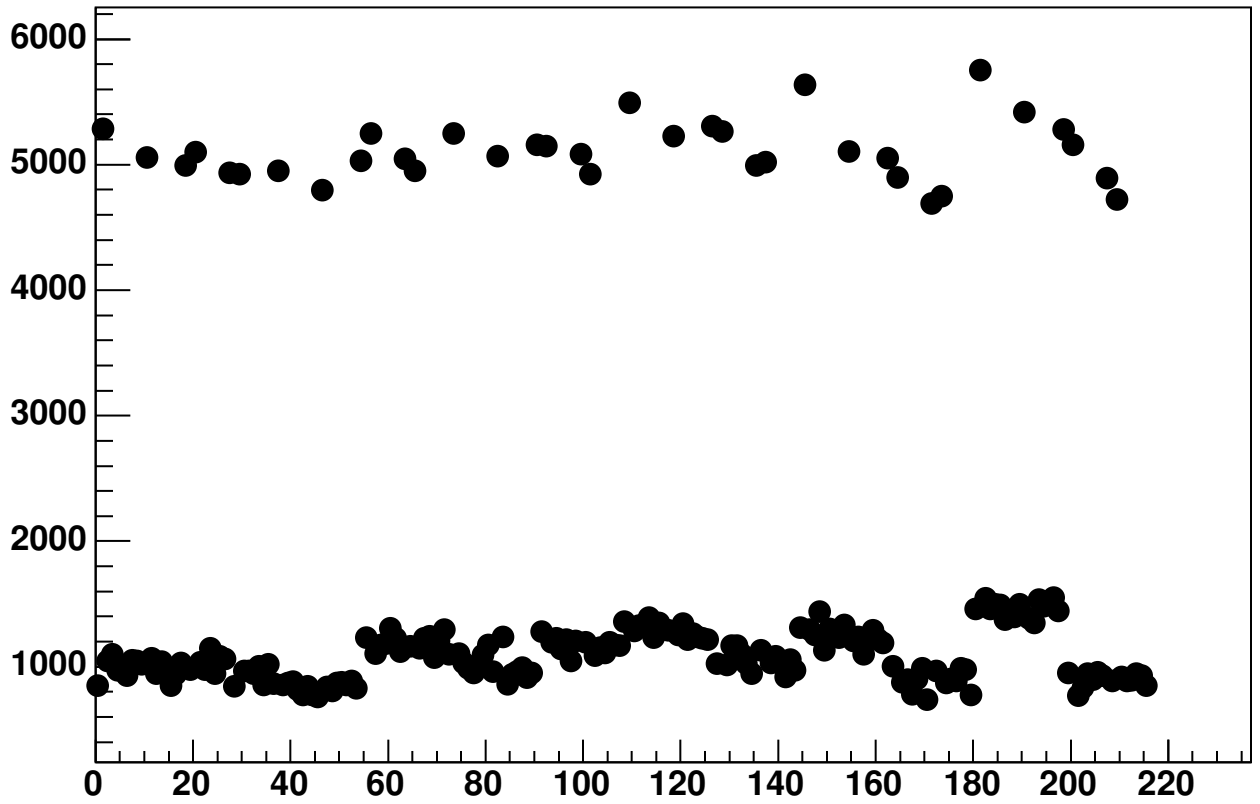
Enable 3, Hold=30, DAC=8192, ADC Mean vs 18*Chip+Chan



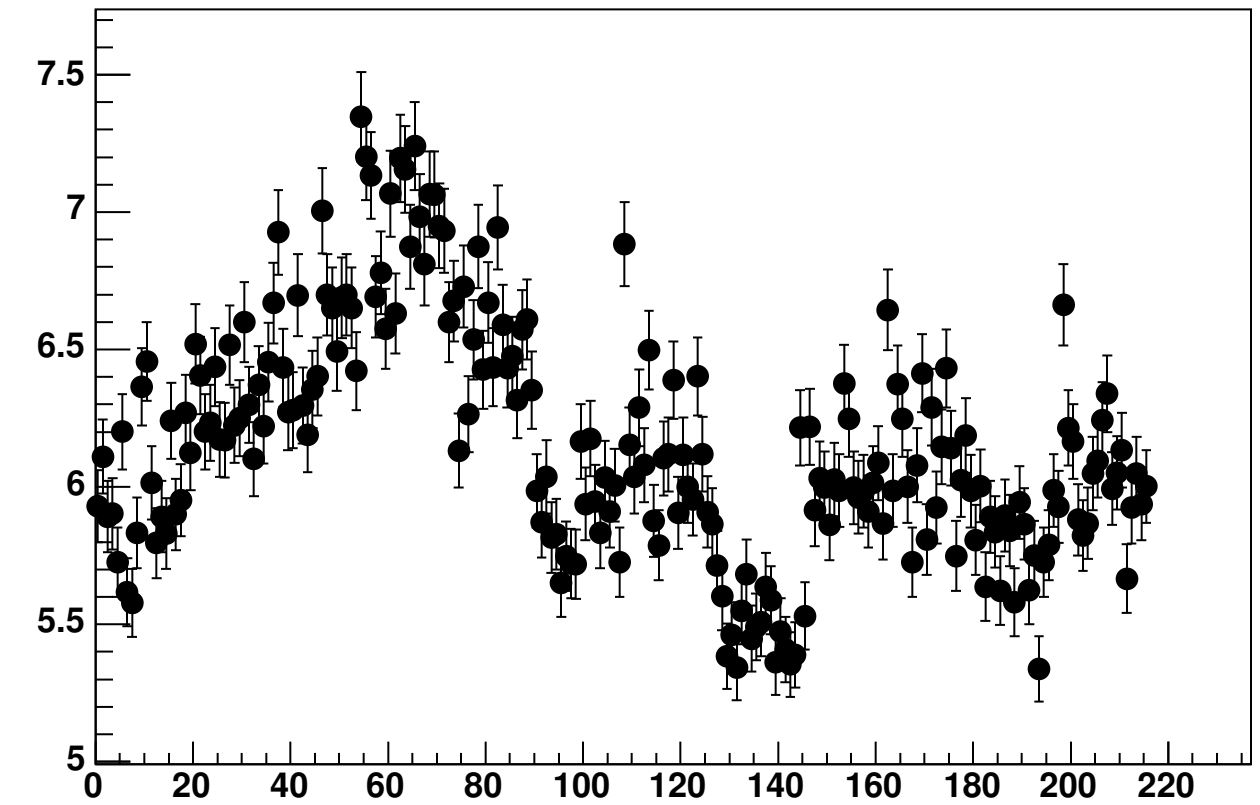
Enable 3, Hold=30, DAC=8192, ADC Noise vs 18*Chip+Chan



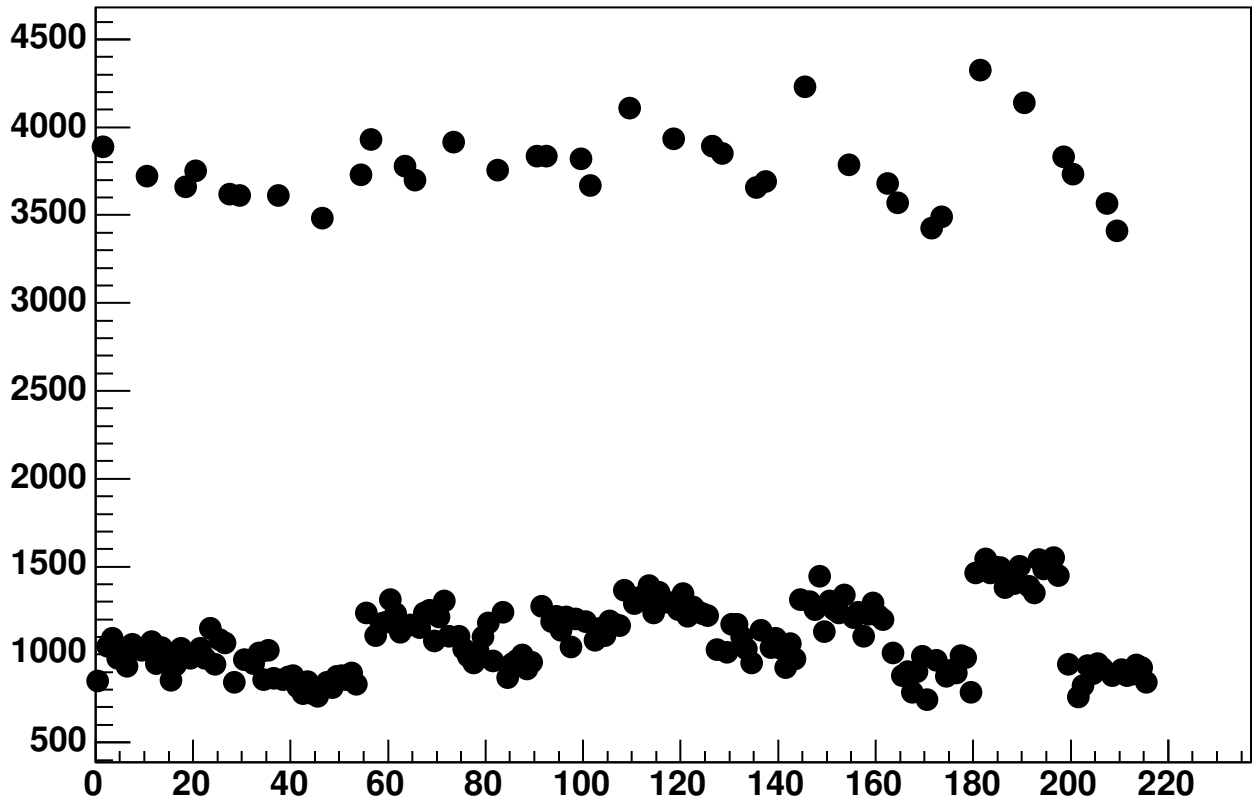
Enable 3, Hold=30, DAC=6144, ADC Mean vs 18*Chip+Chan



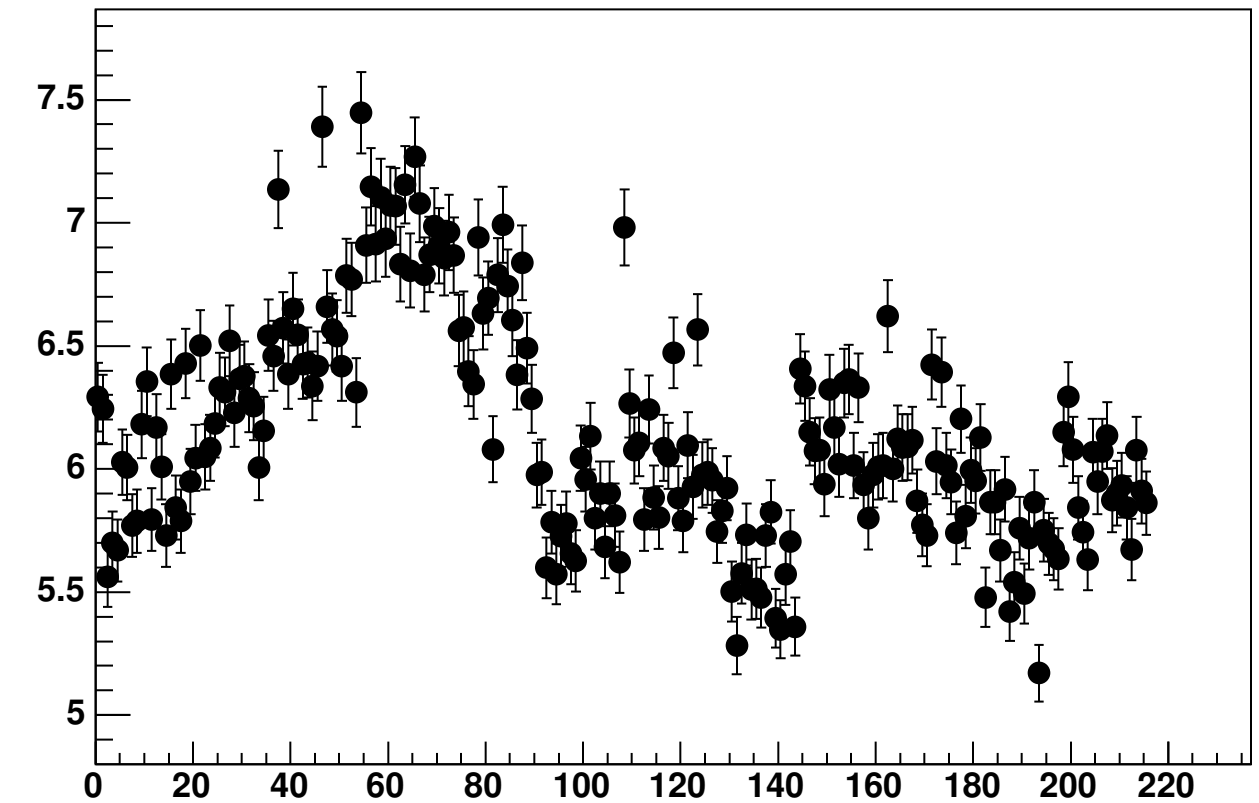
Enable 3, Hold=30, DAC=6144, ADC Noise vs 18*Chip+Chan



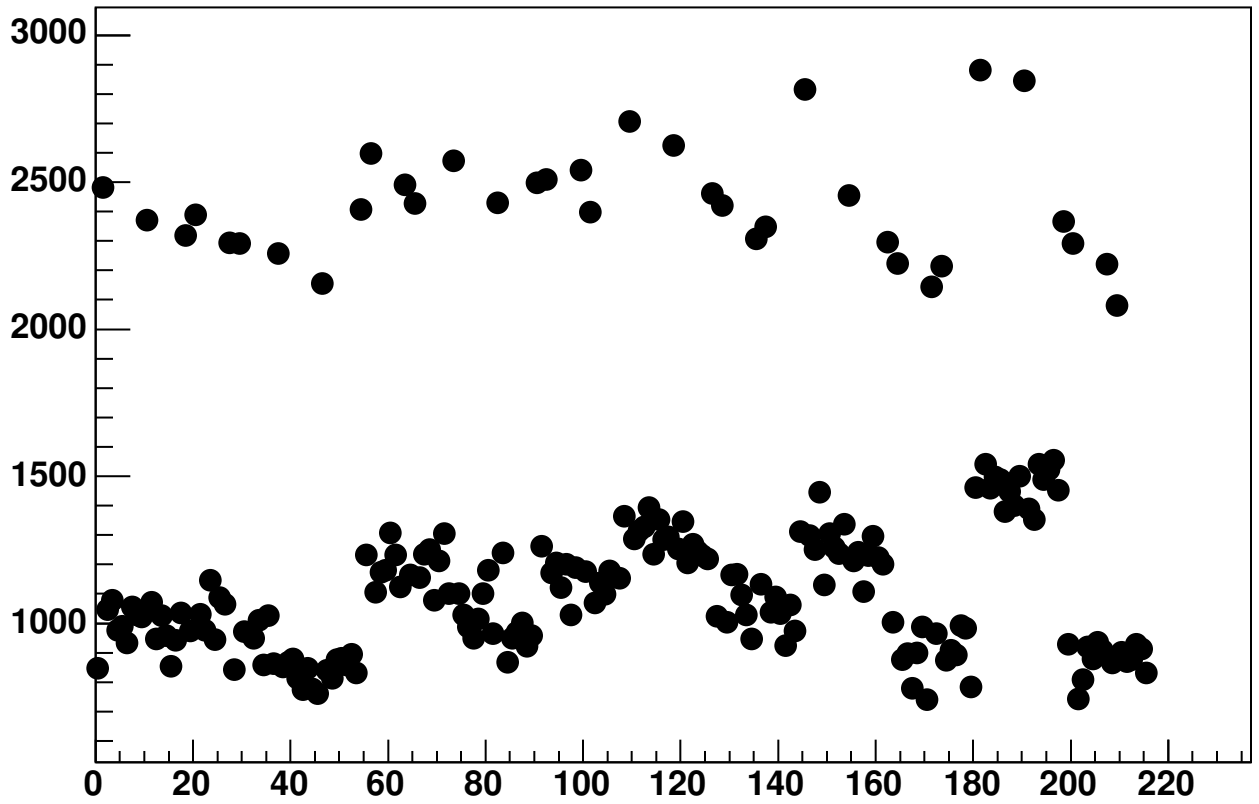
Enable 3, Hold=30, DAC=4096, ADC Mean vs 18*Chip+Chan



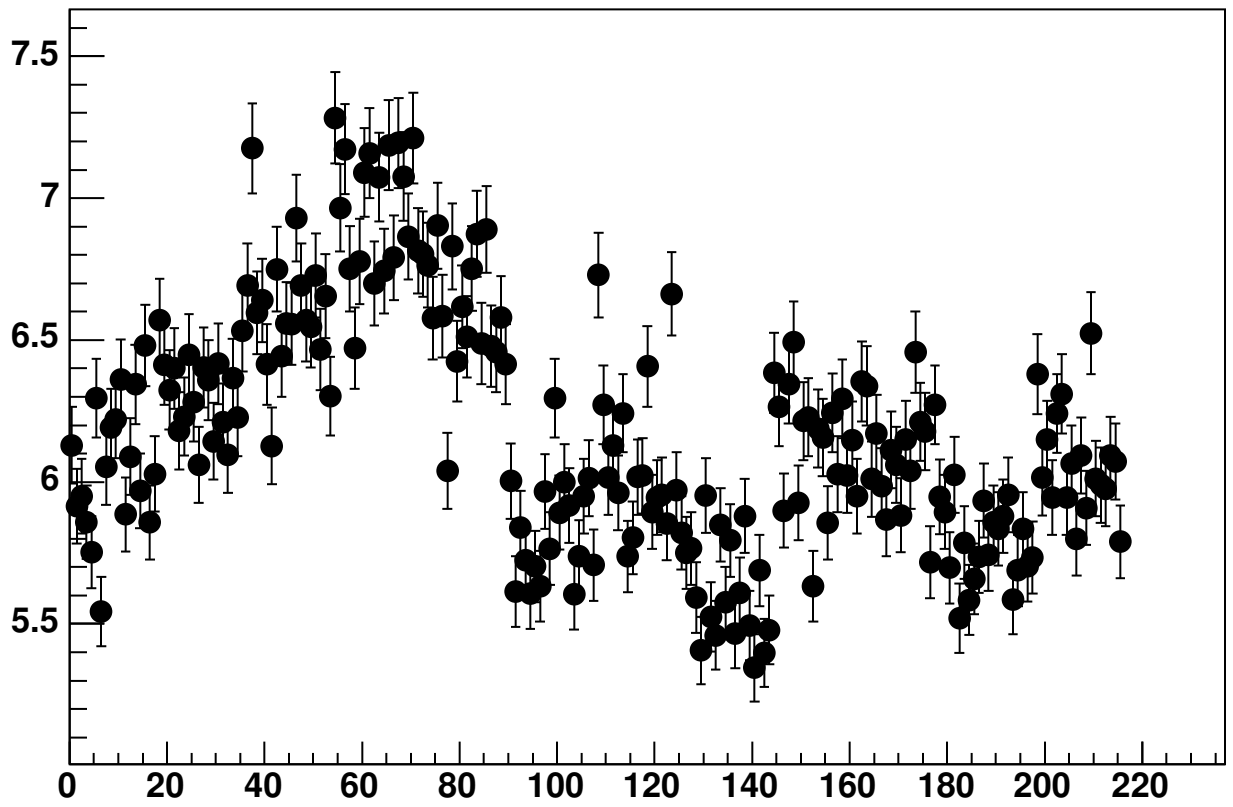
Enable 3, Hold=30, DAC=4096, ADC Noise vs 18*Chip+Chan



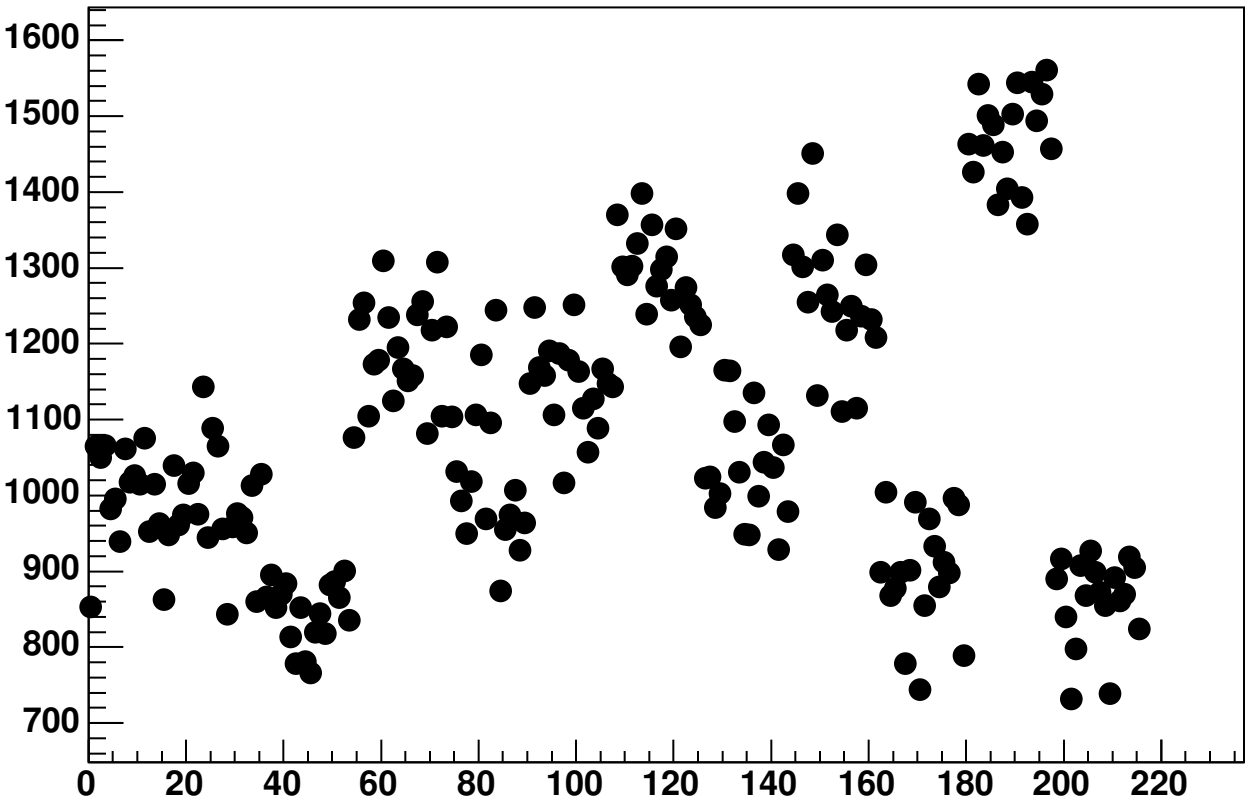
Enable 3, Hold=30, DAC=2048, ADC Mean vs 18*Chip+Chan



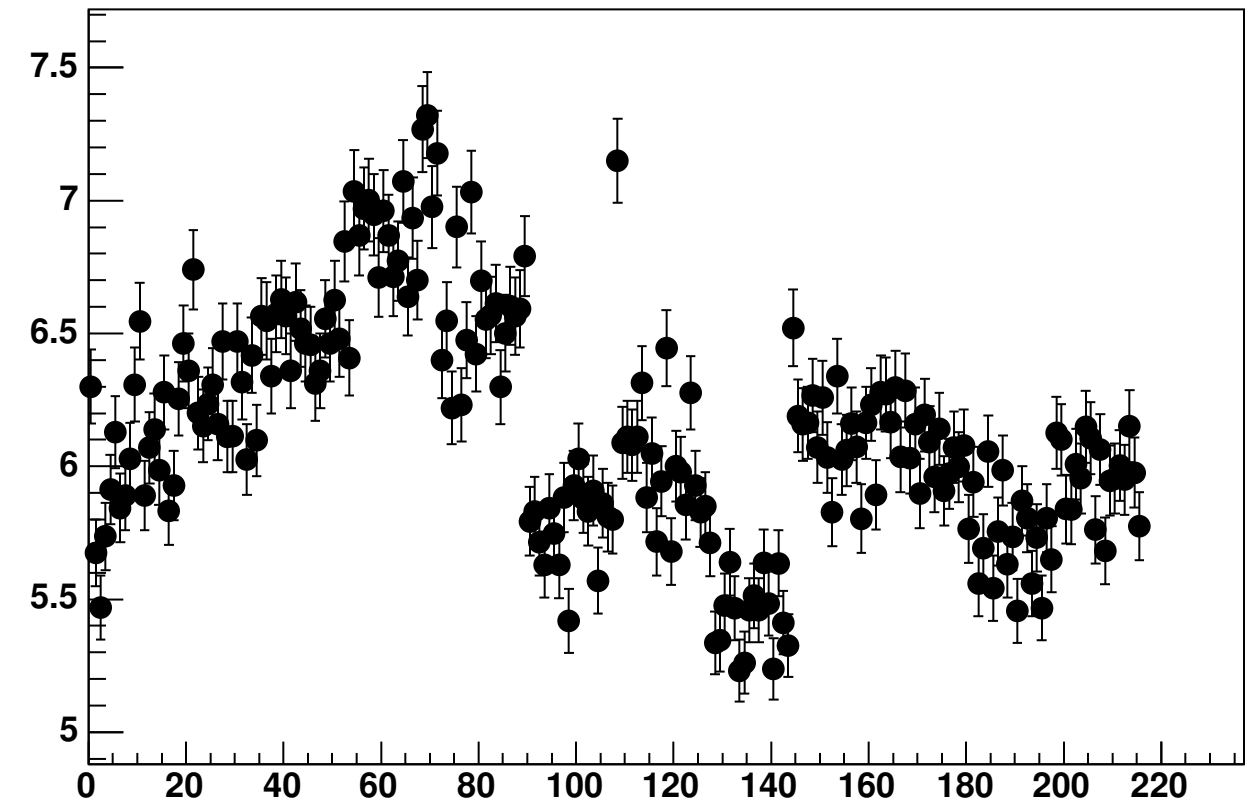
Enable 3, Hold=30, DAC=2048, ADC Noise vs 18*Chip+Chan



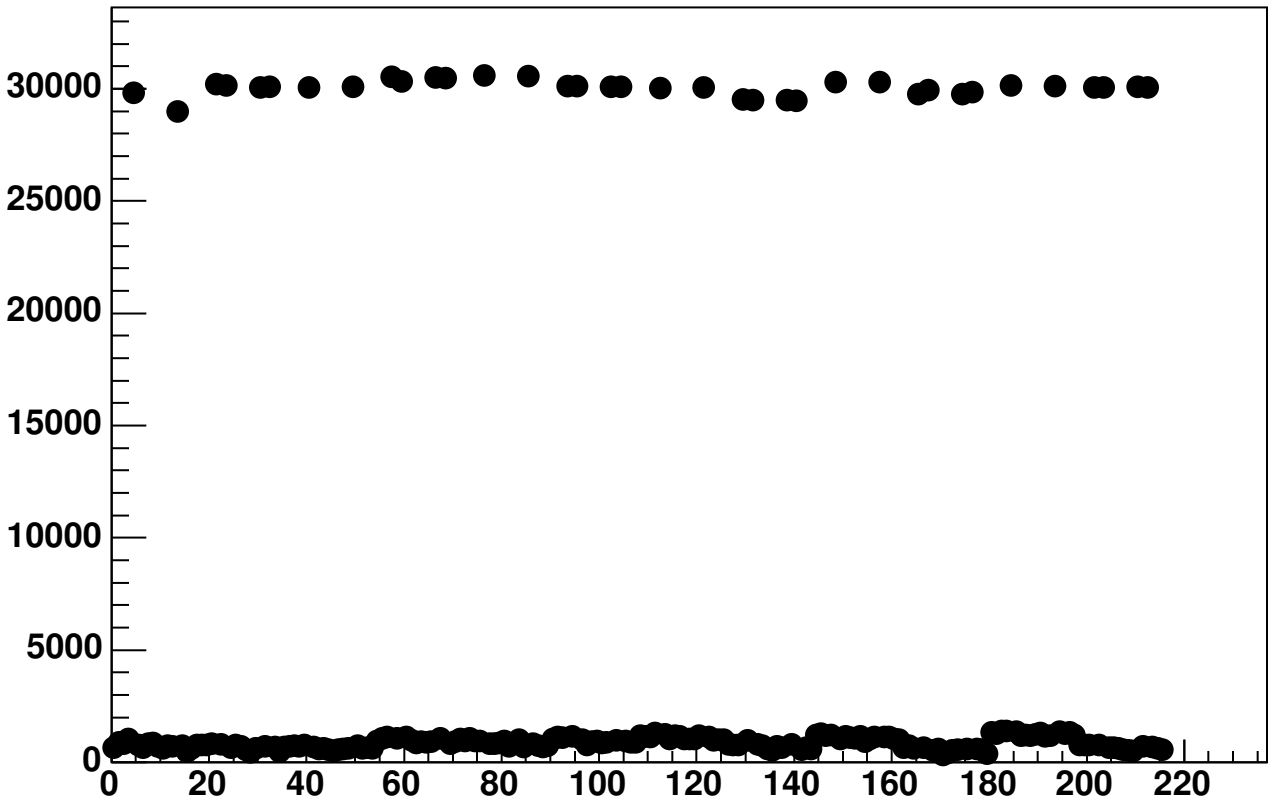
Enable 3, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



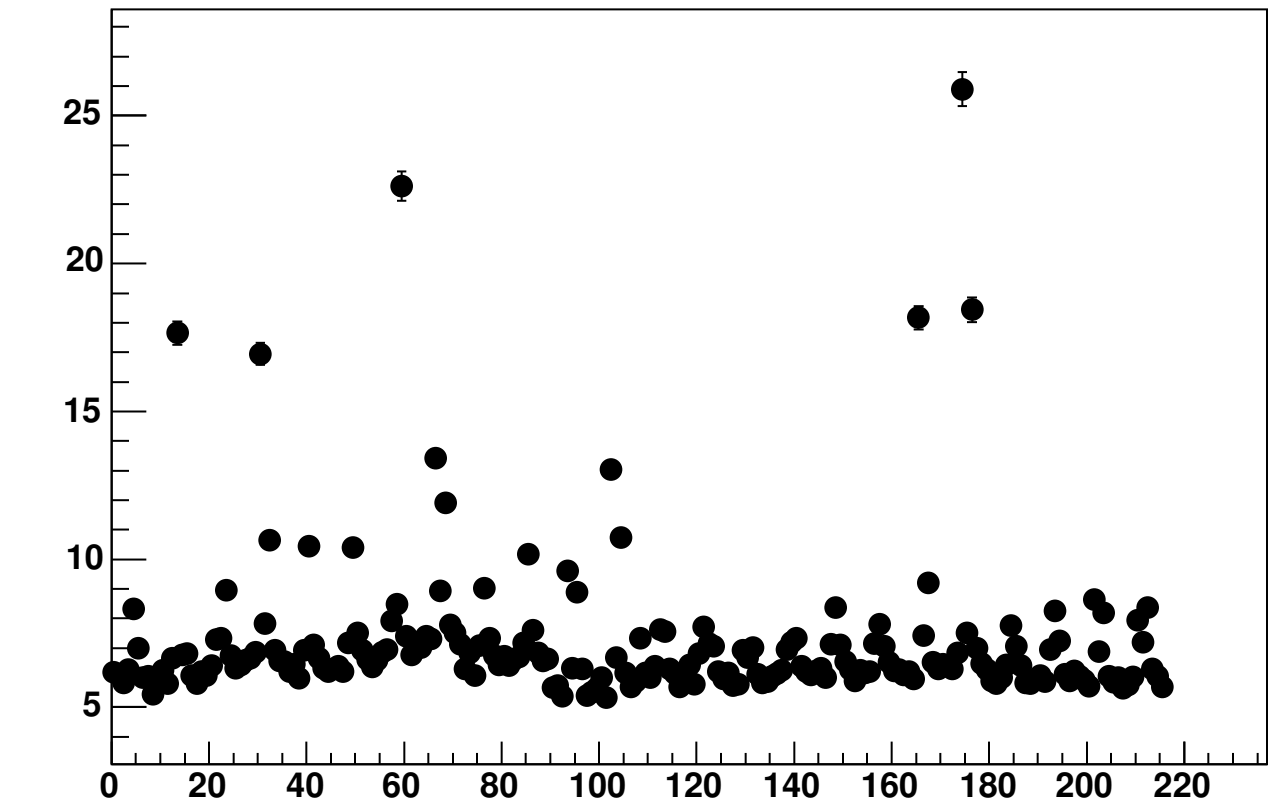
Enable 3, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



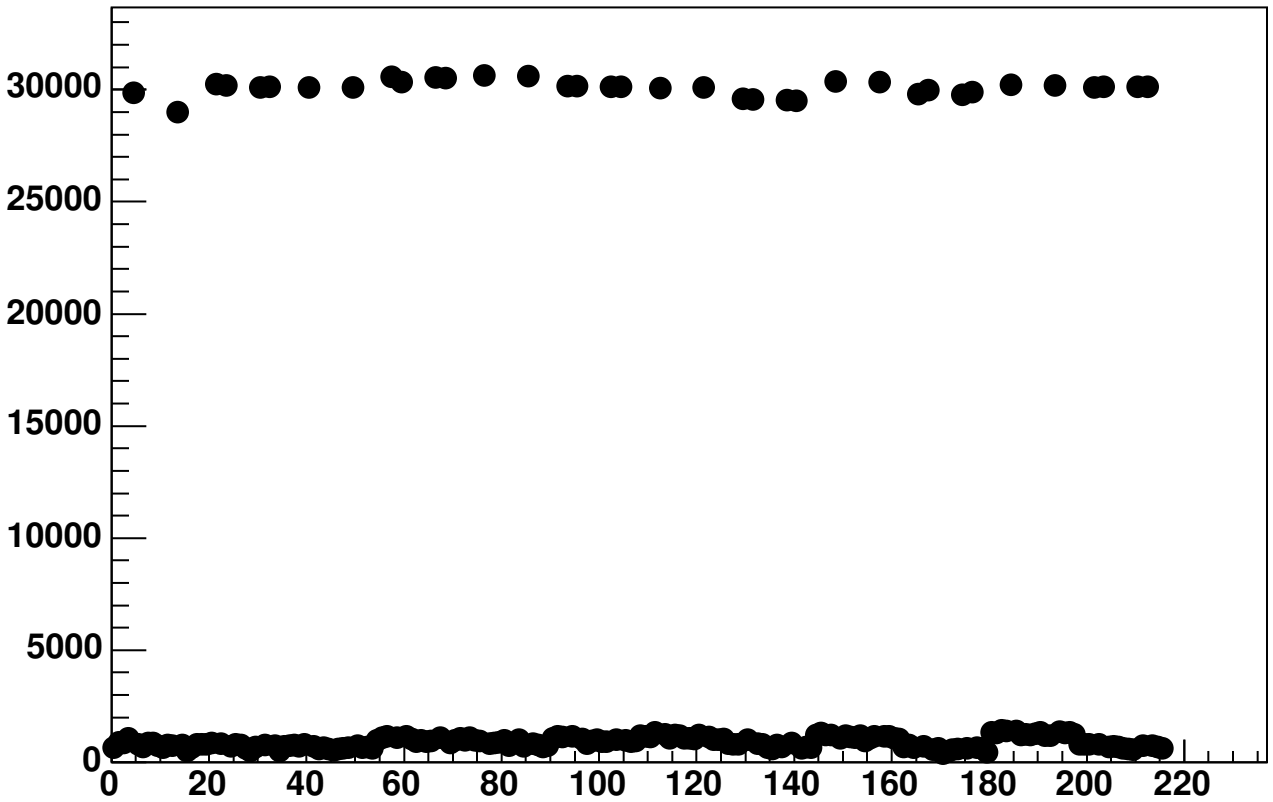
Enable 4, Hold=30, DAC=63488, ADC Mean vs 18*Chip+Chan



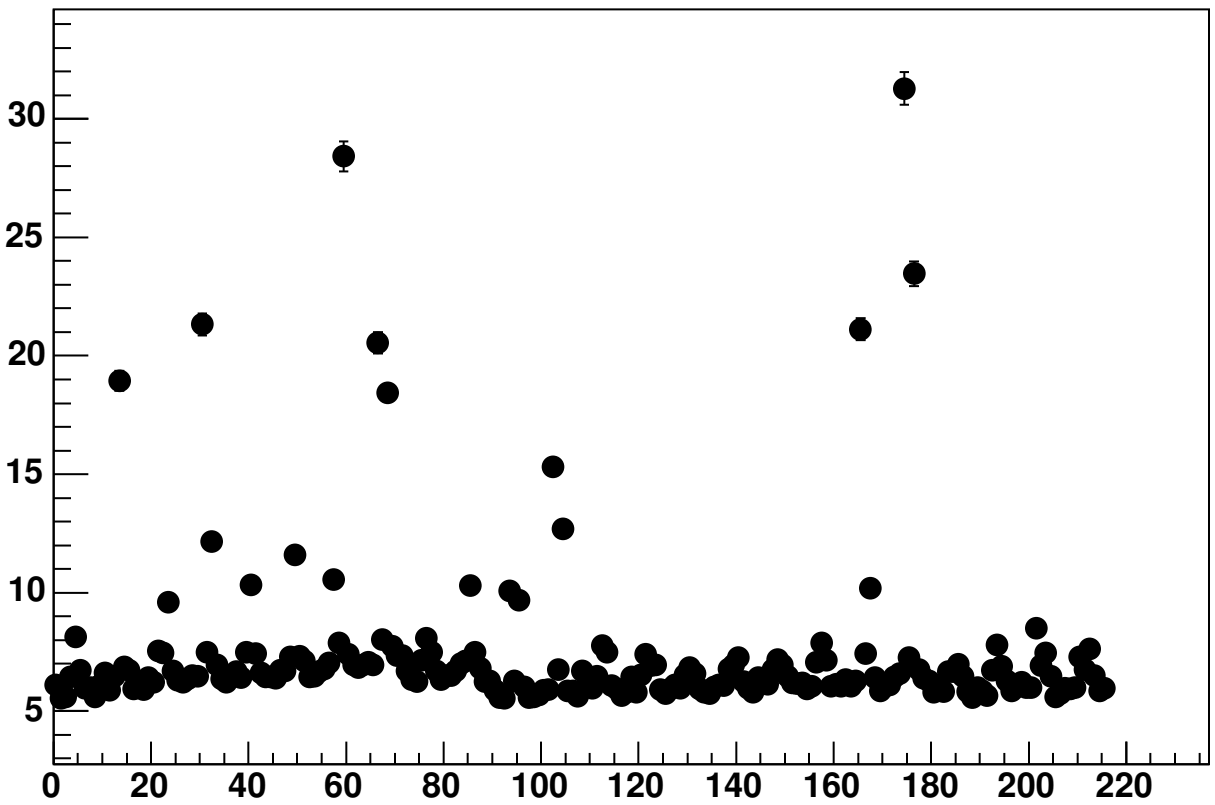
Enable 4, Hold=30, DAC=63488, ADC Noise vs 18*Chip+Chan



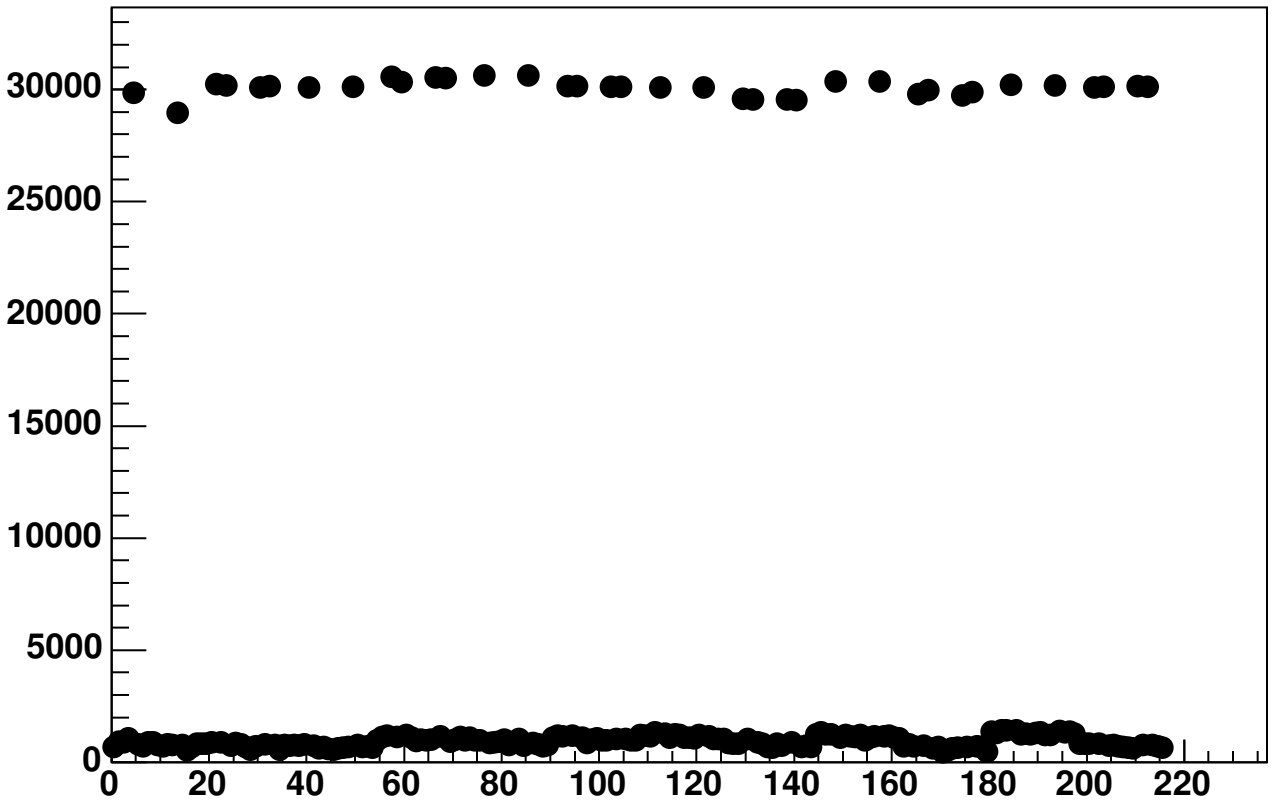
Enable 4, Hold=30, DAC=61440, ADC Mean vs 18*Chip+Chan



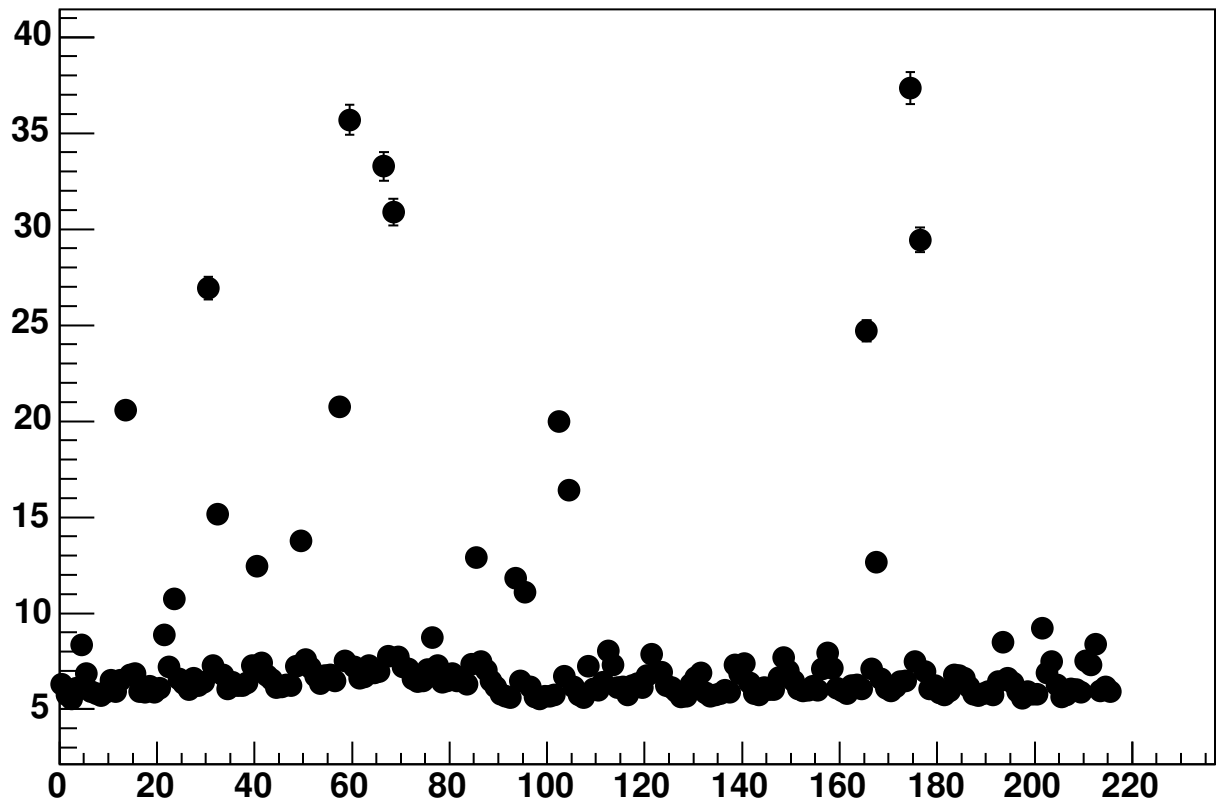
Enable 4, Hold=30, DAC=61440, ADC Noise vs 18*Chip+Chan



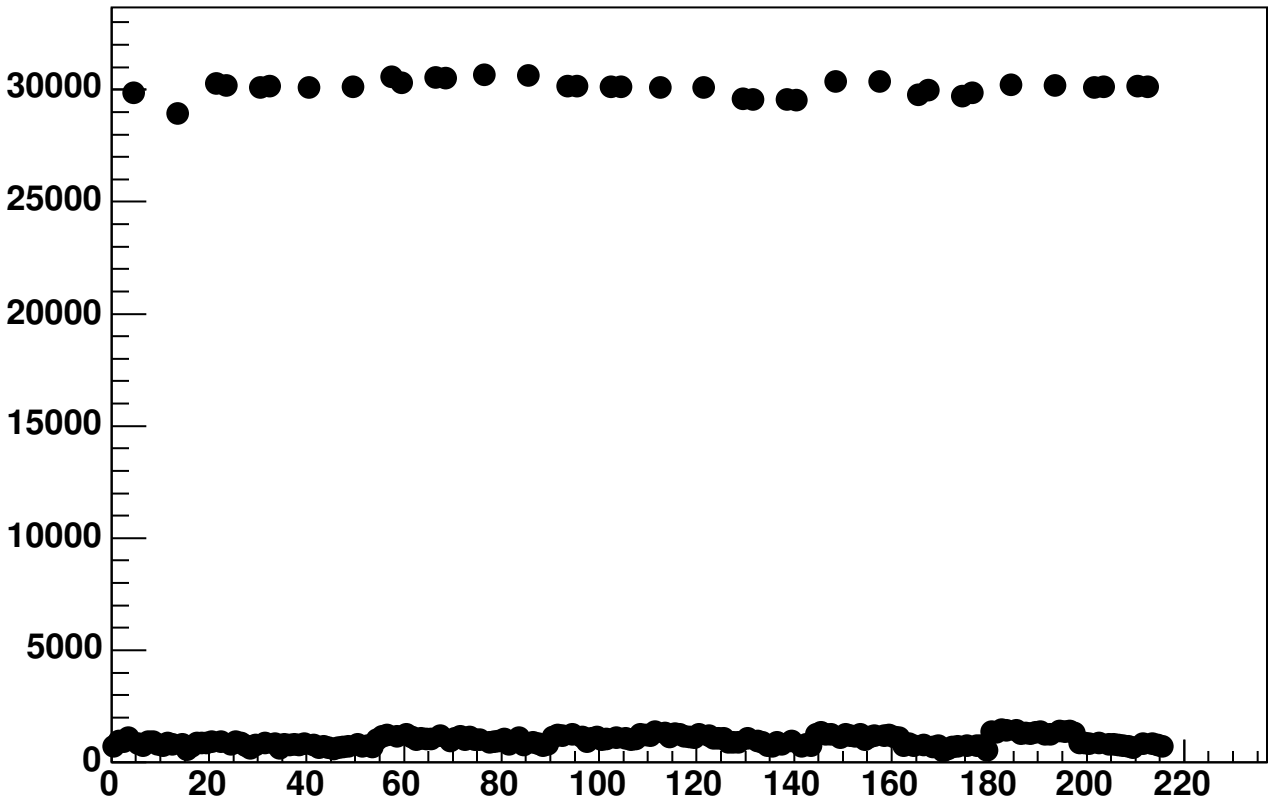
Enable 4, Hold=30, DAC=59392, ADC Mean vs 18*Chip+Chan



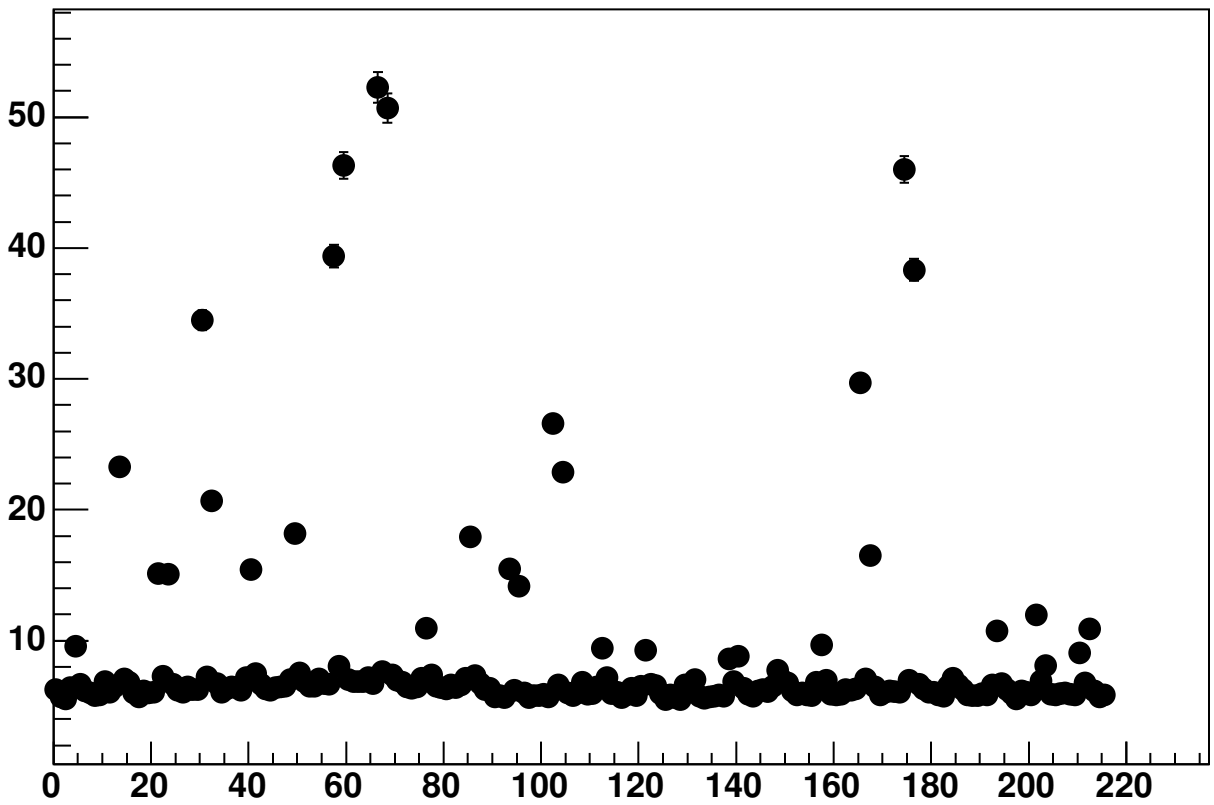
Enable 4, Hold=30, DAC=59392, ADC Noise vs 18*Chip+Chan



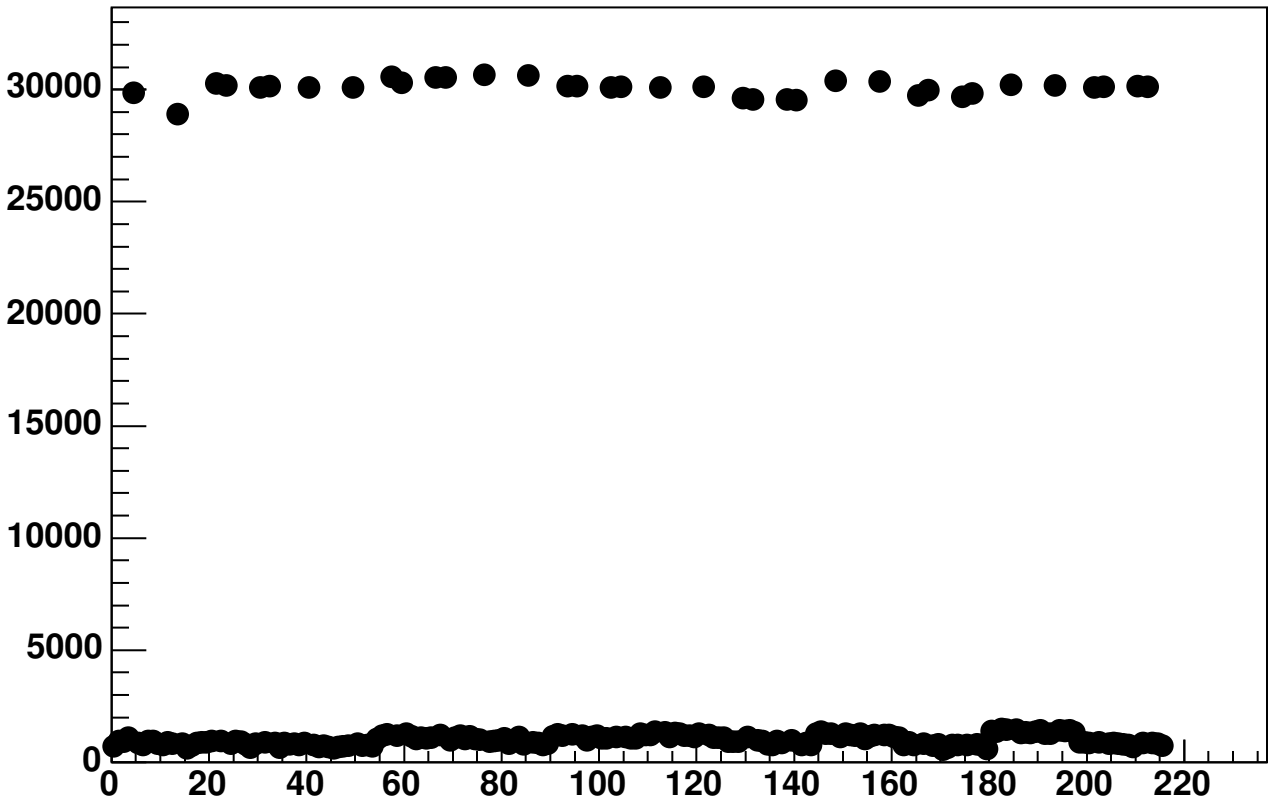
Enable 4, Hold=30, DAC=57344, ADC Mean vs 18*Chip+Chan



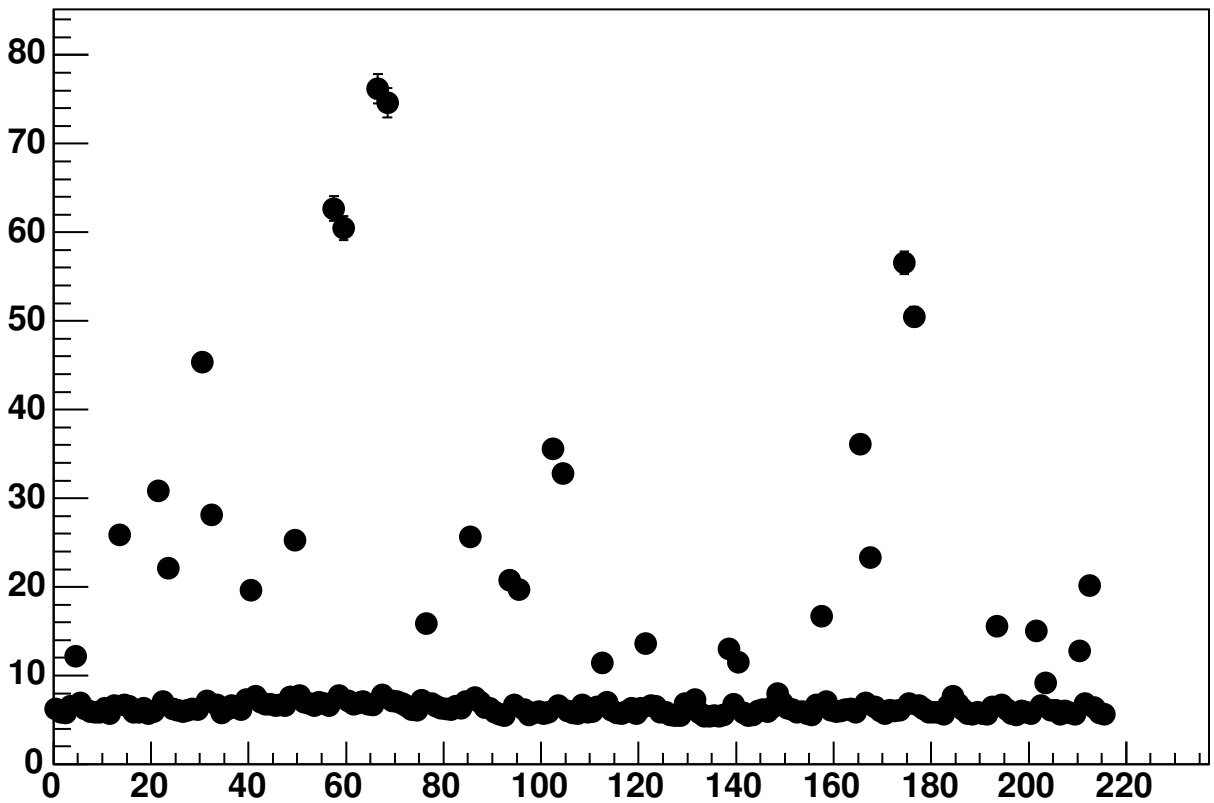
Enable 4, Hold=30, DAC=57344, ADC Noise vs 18*Chip+Chan



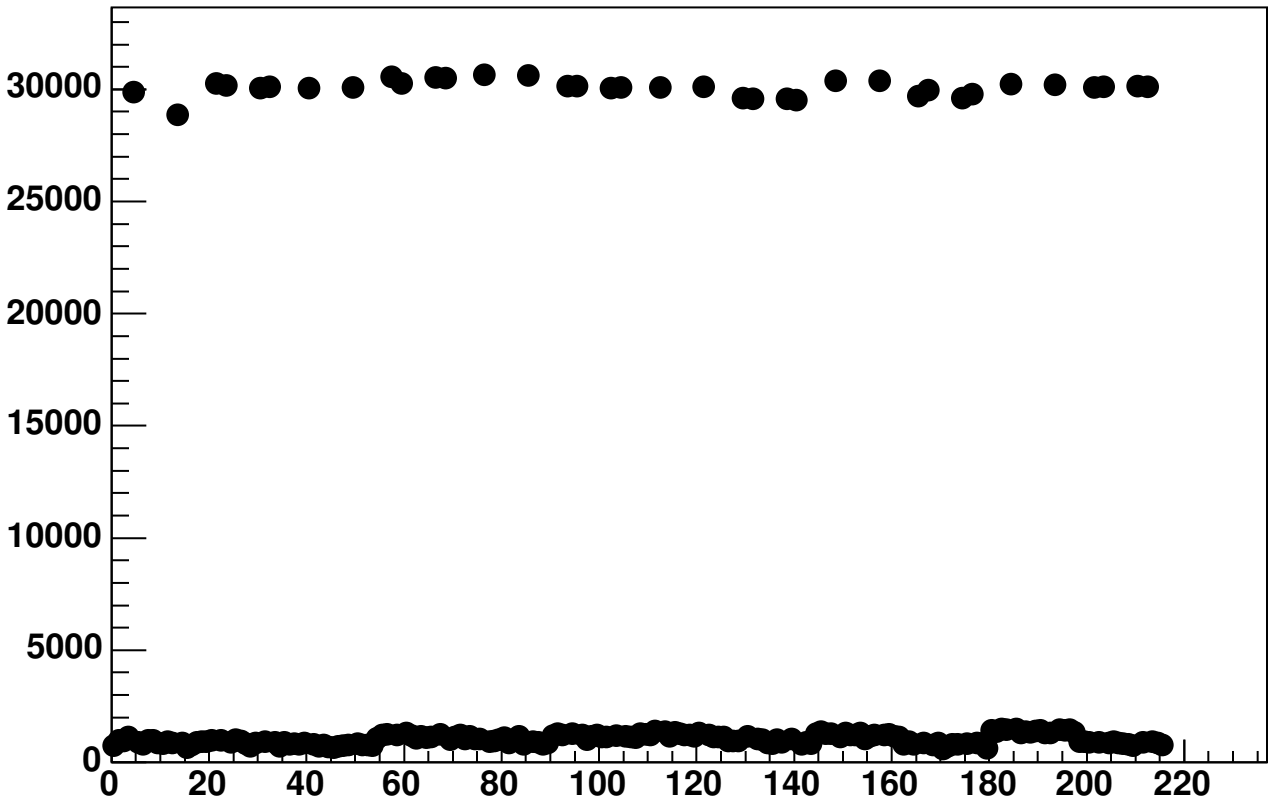
Enable 4, Hold=30, DAC=55296, ADC Mean vs 18*Chip+Chan



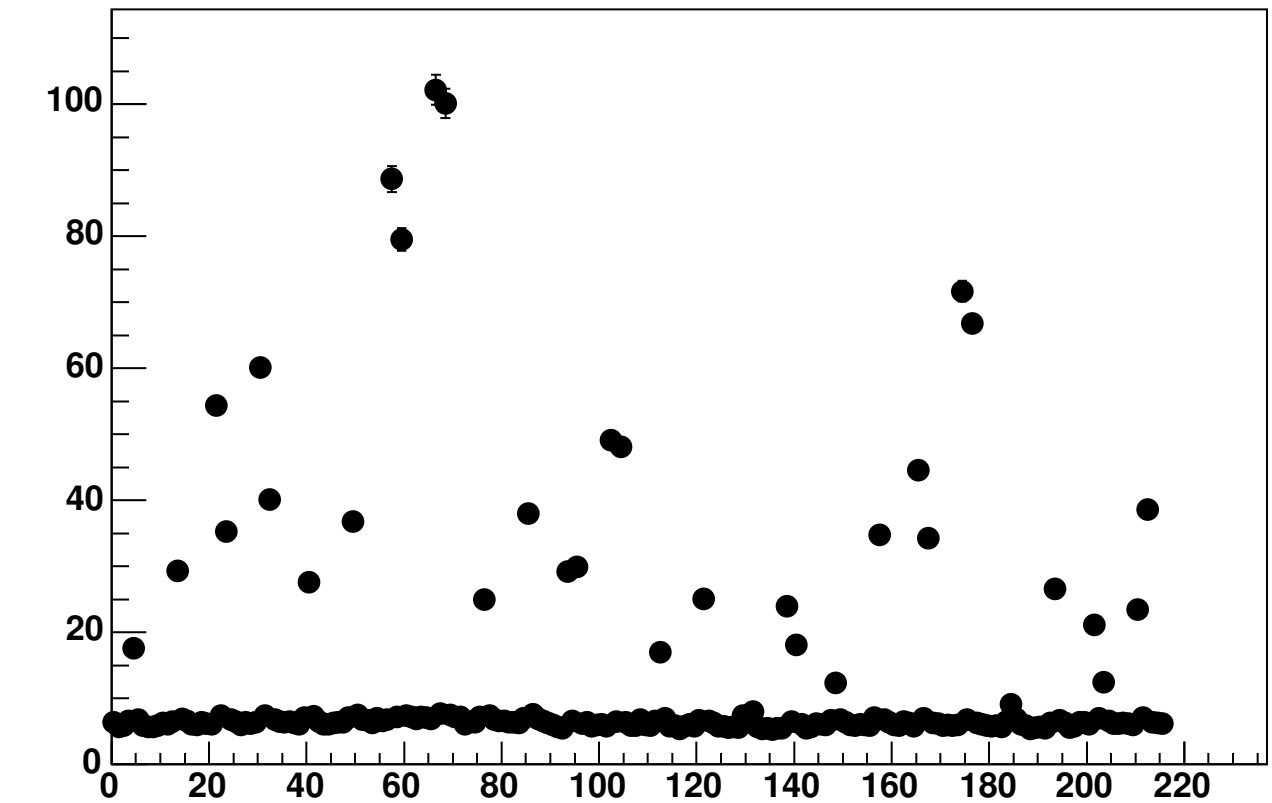
Enable 4, Hold=30, DAC=55296, ADC Noise vs 18*Chip+Chan



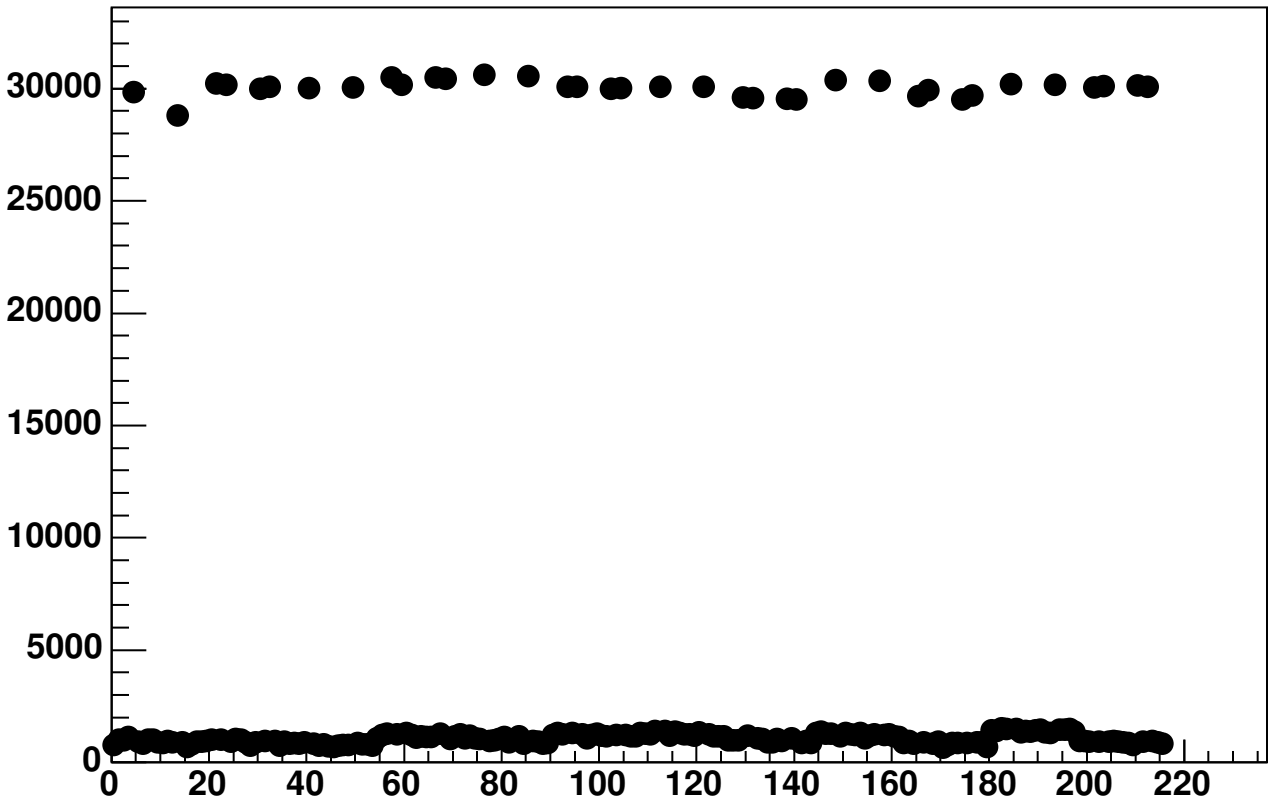
Enable 4, Hold=30, DAC=53248, ADC Mean vs 18*Chip+Chan



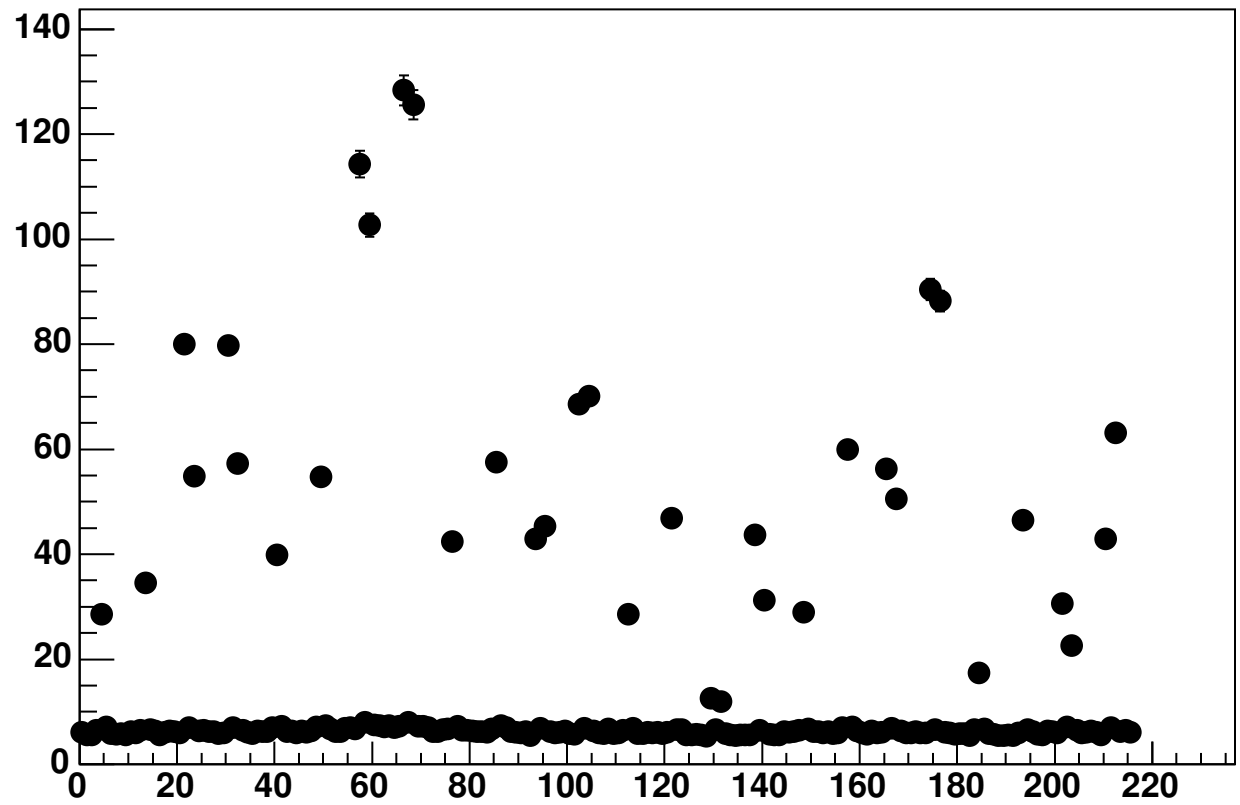
Enable 4, Hold=30, DAC=53248, ADC Noise vs 18*Chip+Chan



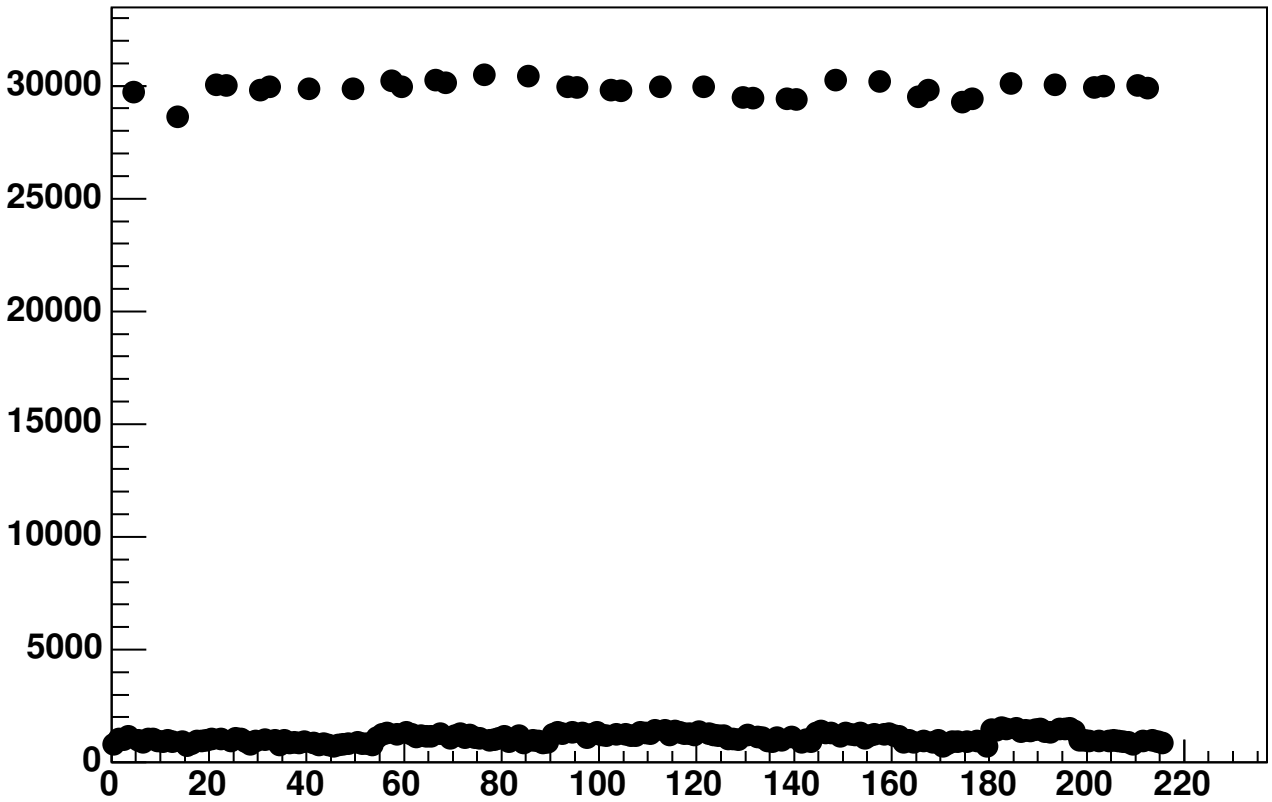
Enable 4, Hold=30, DAC=51200, ADC Mean vs 18*Chip+Chan



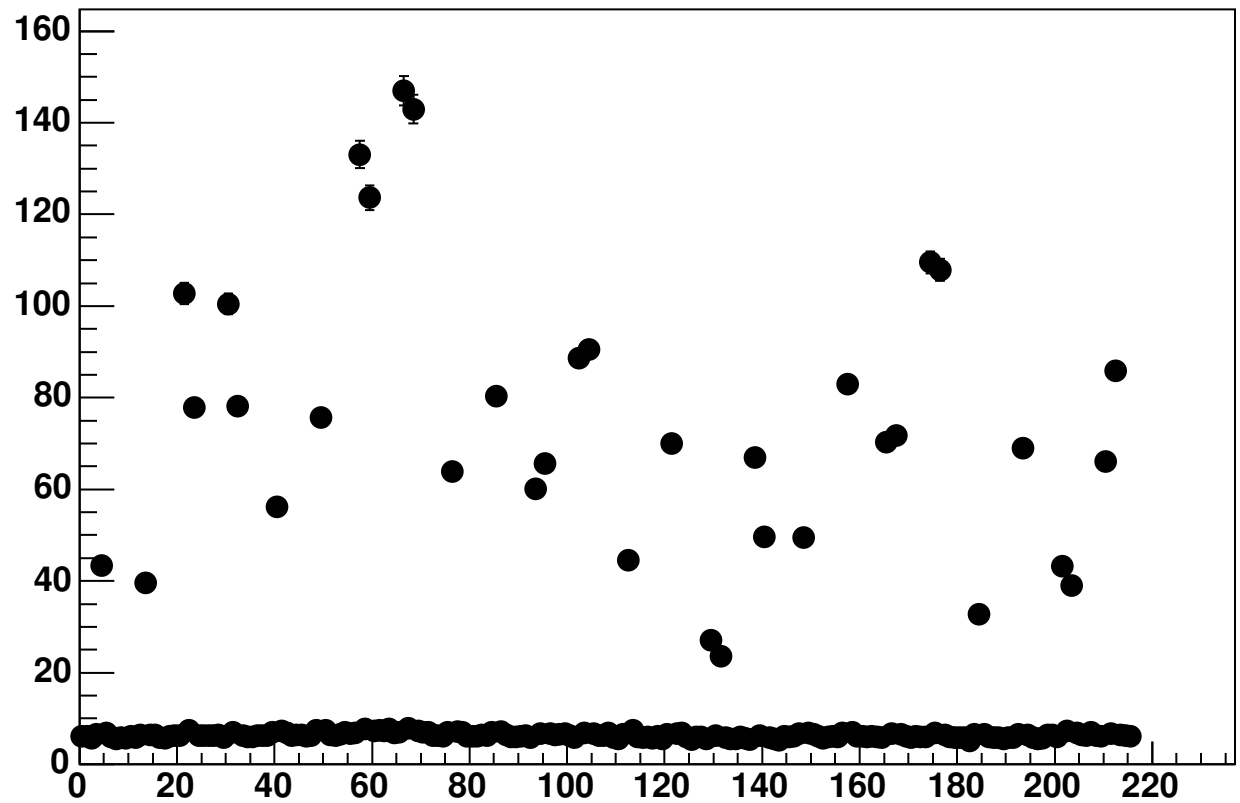
Enable 4, Hold=30, DAC=51200, ADC Noise vs 18*Chip+Chan



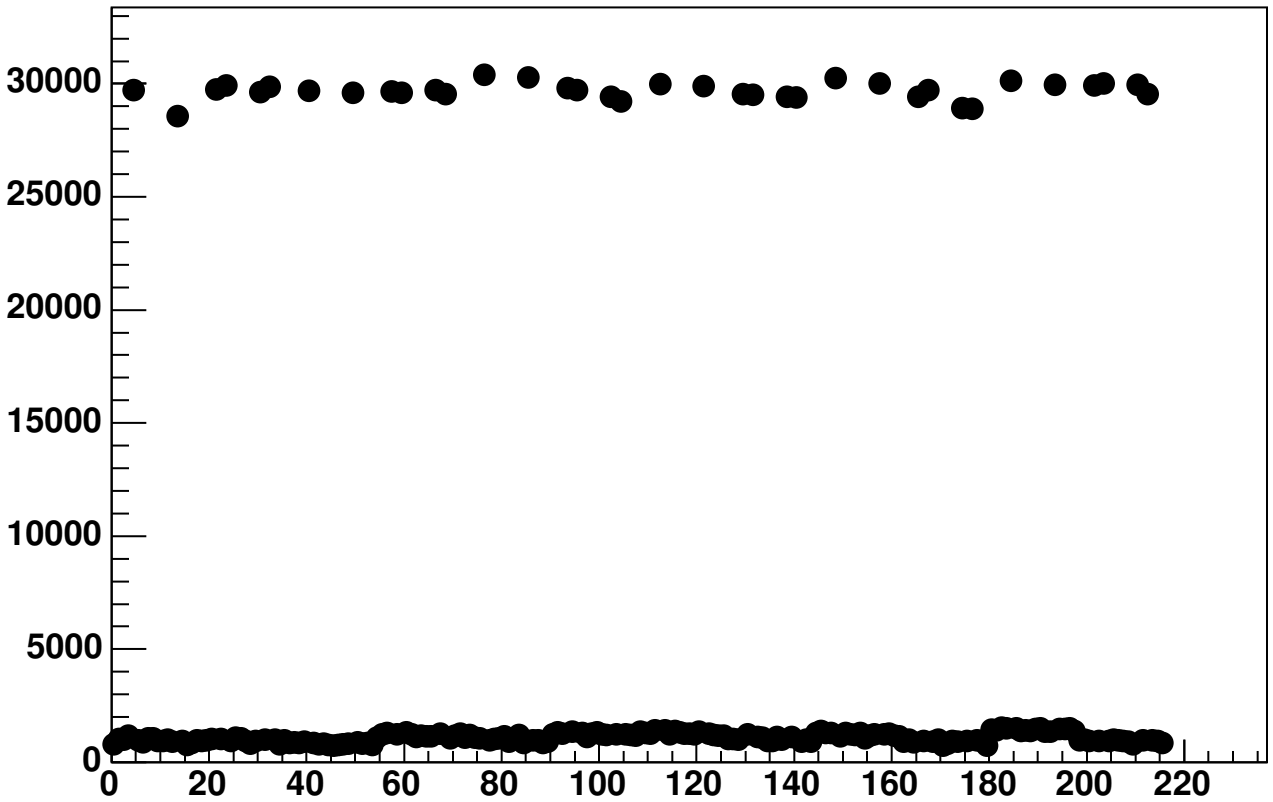
Enable 4, Hold=30, DAC=49152, ADC Mean vs 18*Chip+Chan



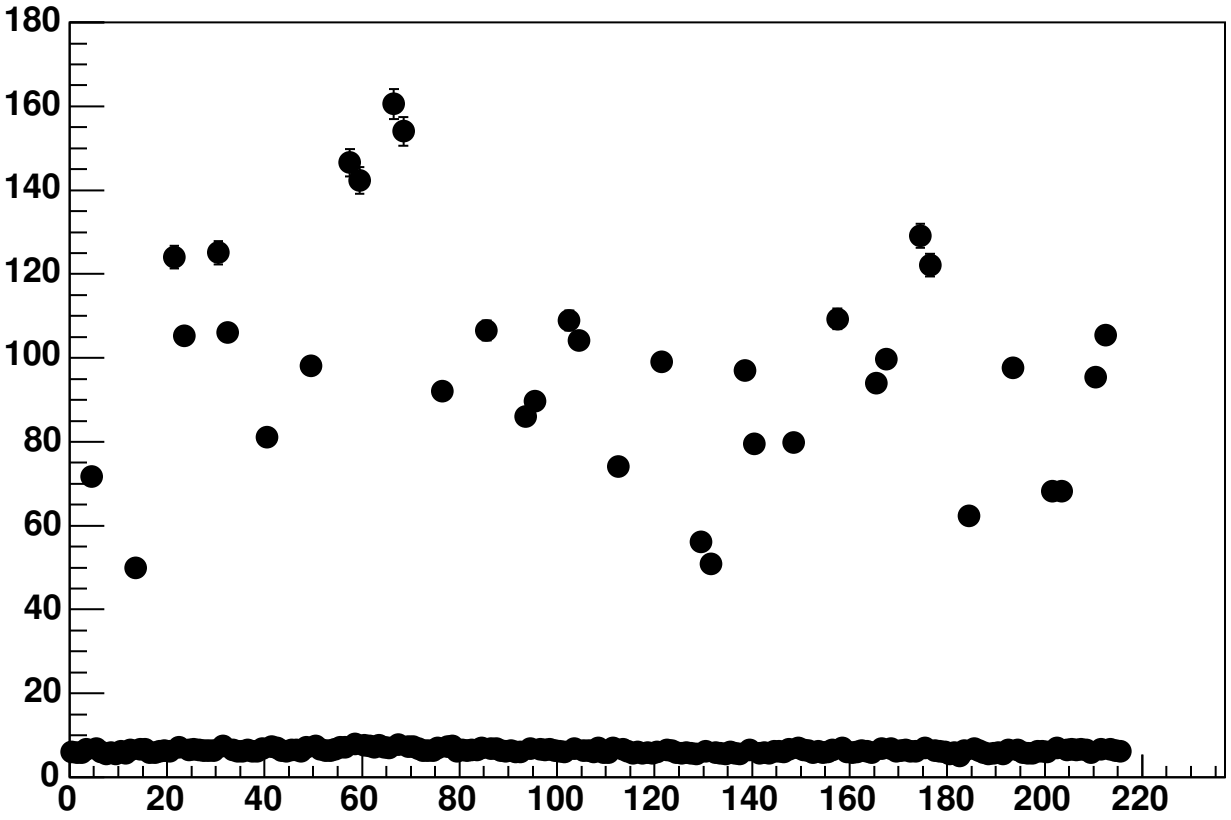
Enable 4, Hold=30, DAC=49152, ADC Noise vs 18*Chip+Chan



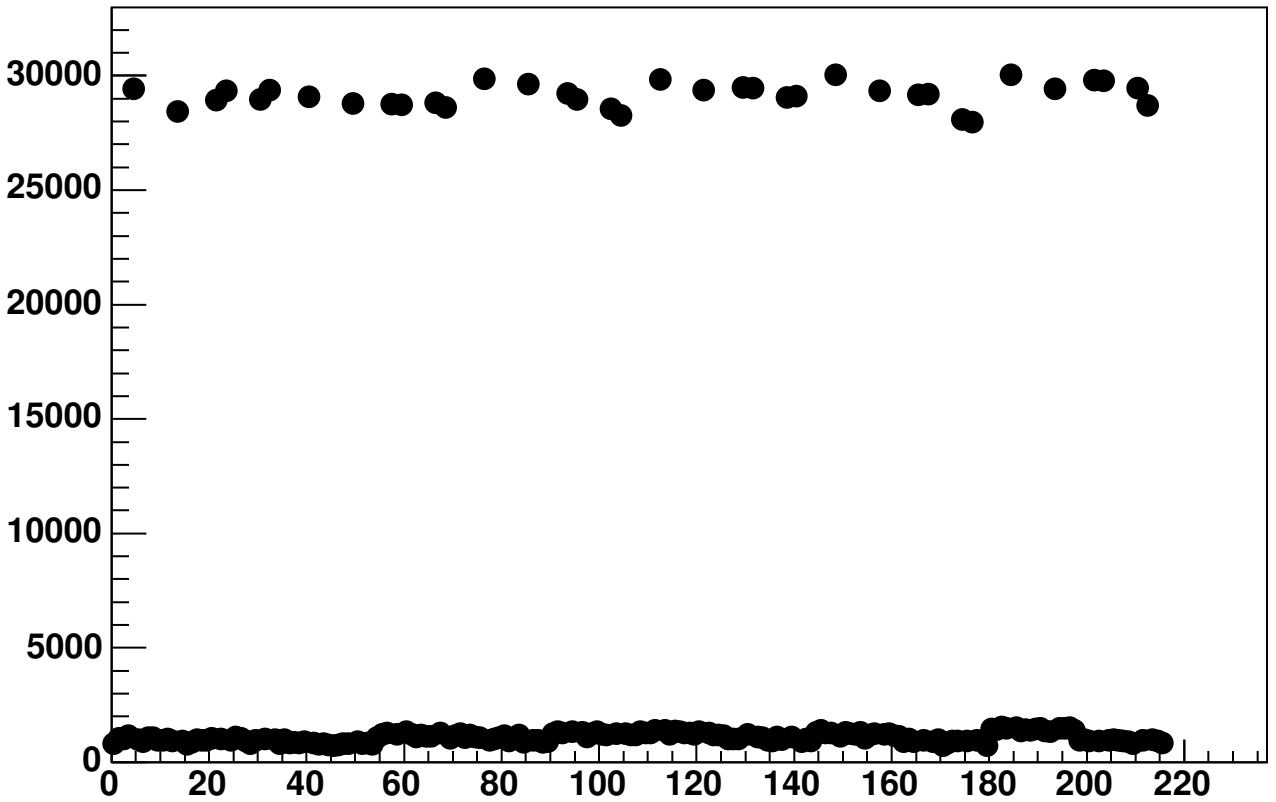
Enable 4, Hold=30, DAC=47104, ADC Mean vs 18*Chip+Chan



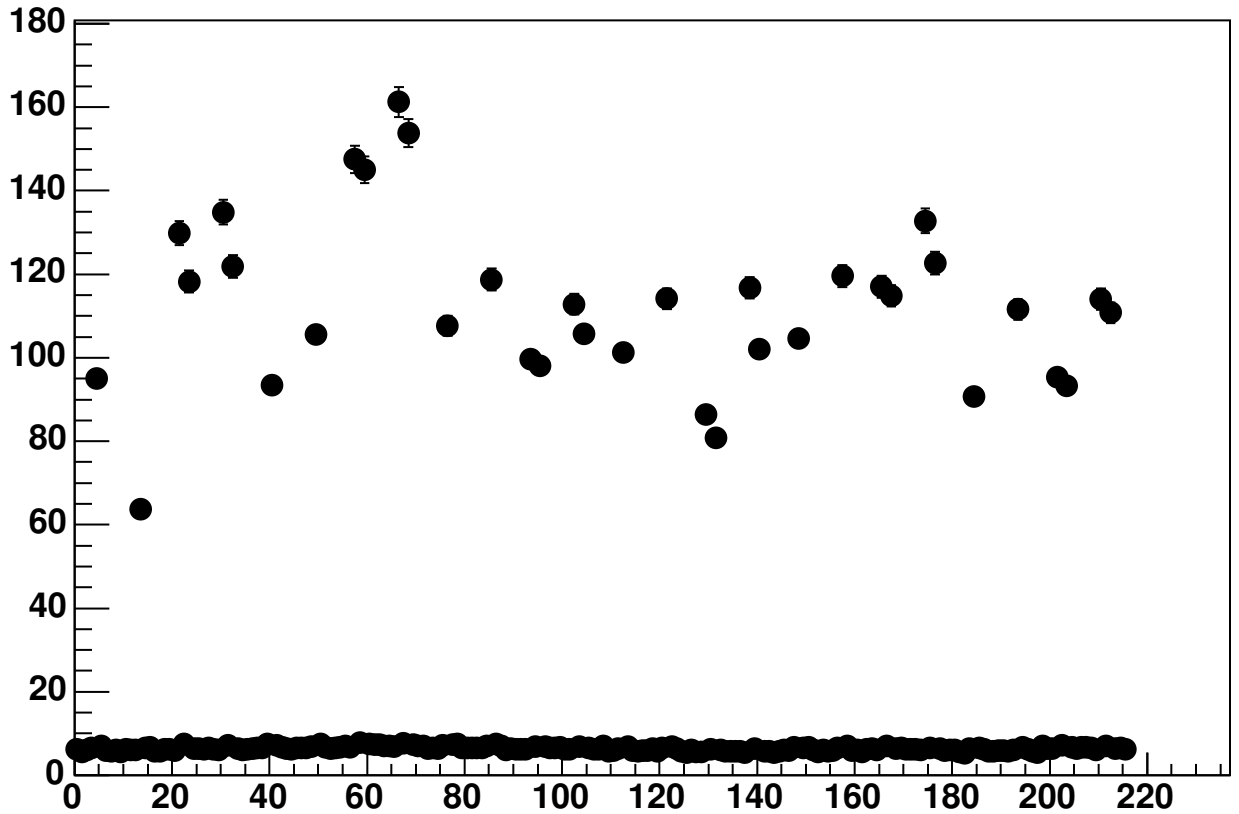
Enable 4, Hold=30, DAC=47104, ADC Noise vs 18*Chip+Chan



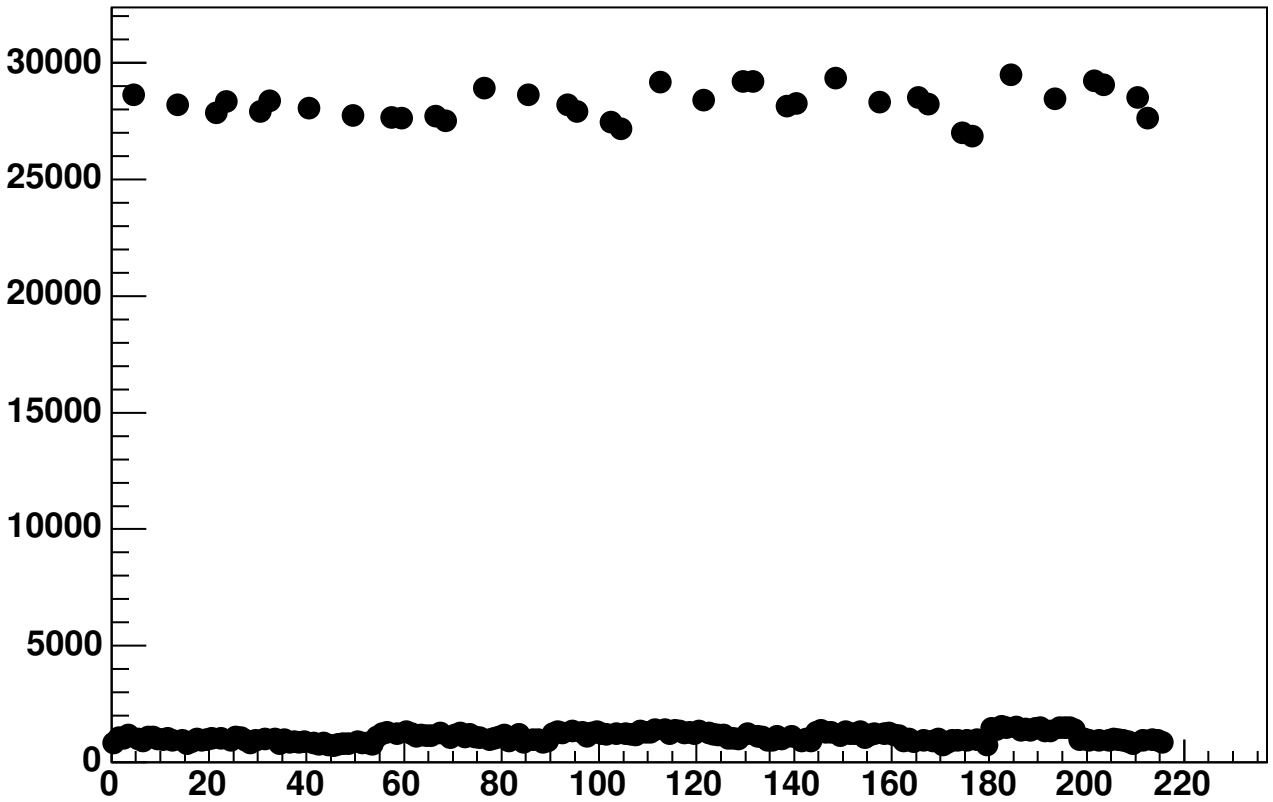
Enable 4, Hold=30, DAC=45056, ADC Mean vs 18*Chip+Chan



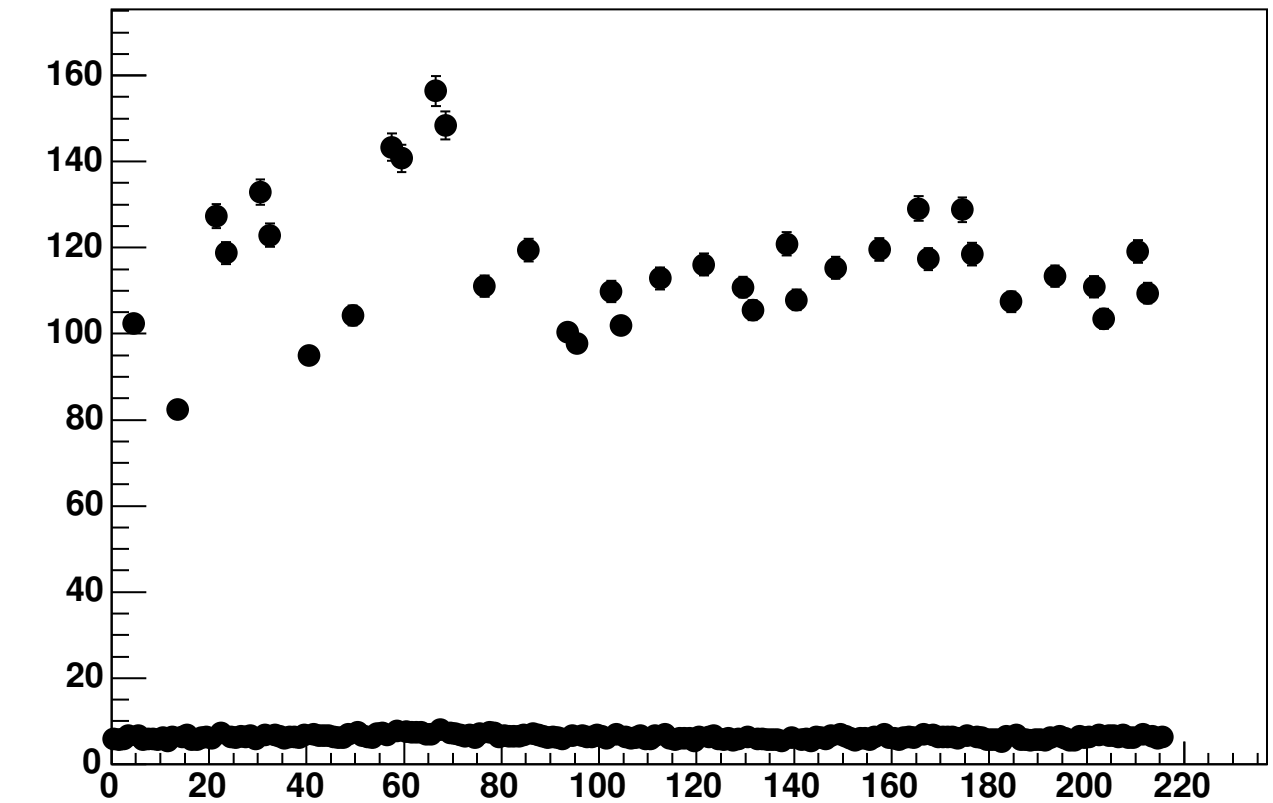
Enable 4, Hold=30, DAC=45056, ADC Noise vs 18*Chip+Chan



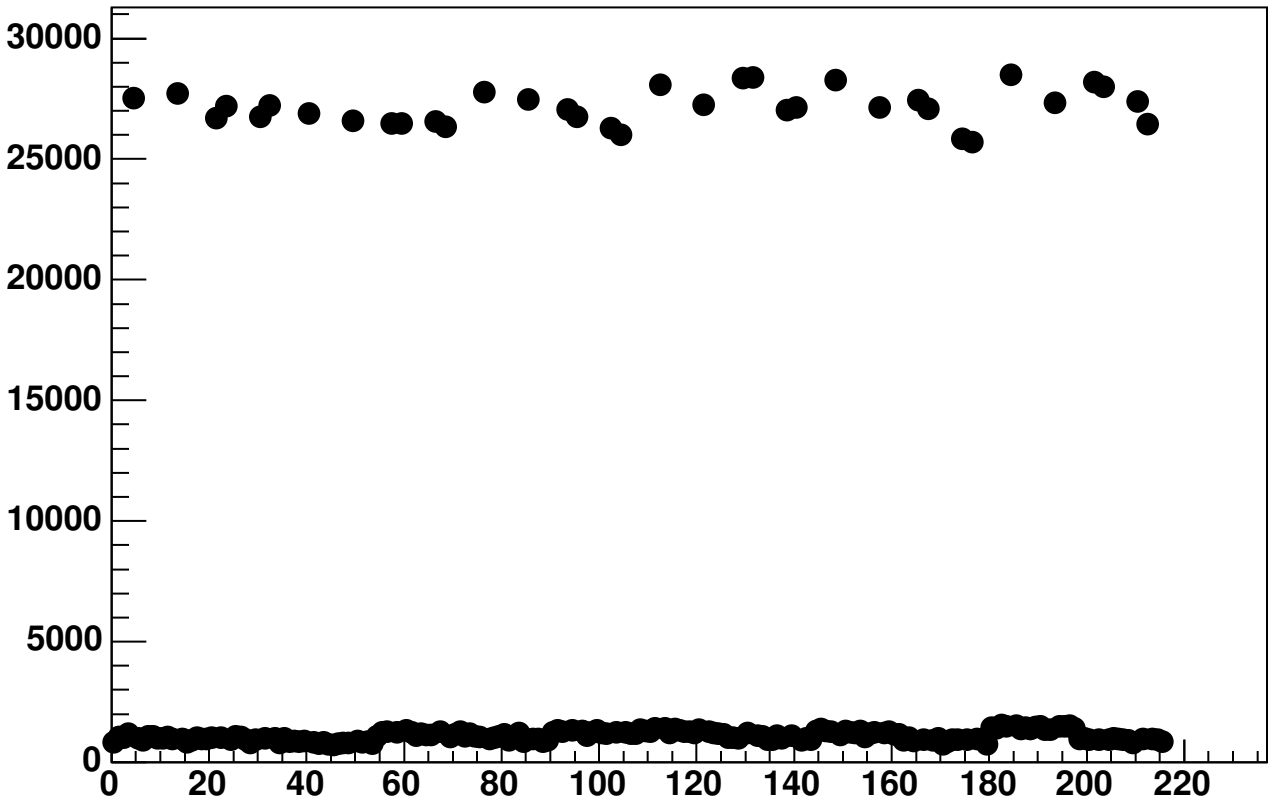
Enable 4, Hold=30, DAC=43008, ADC Mean vs 18*Chip+Chan



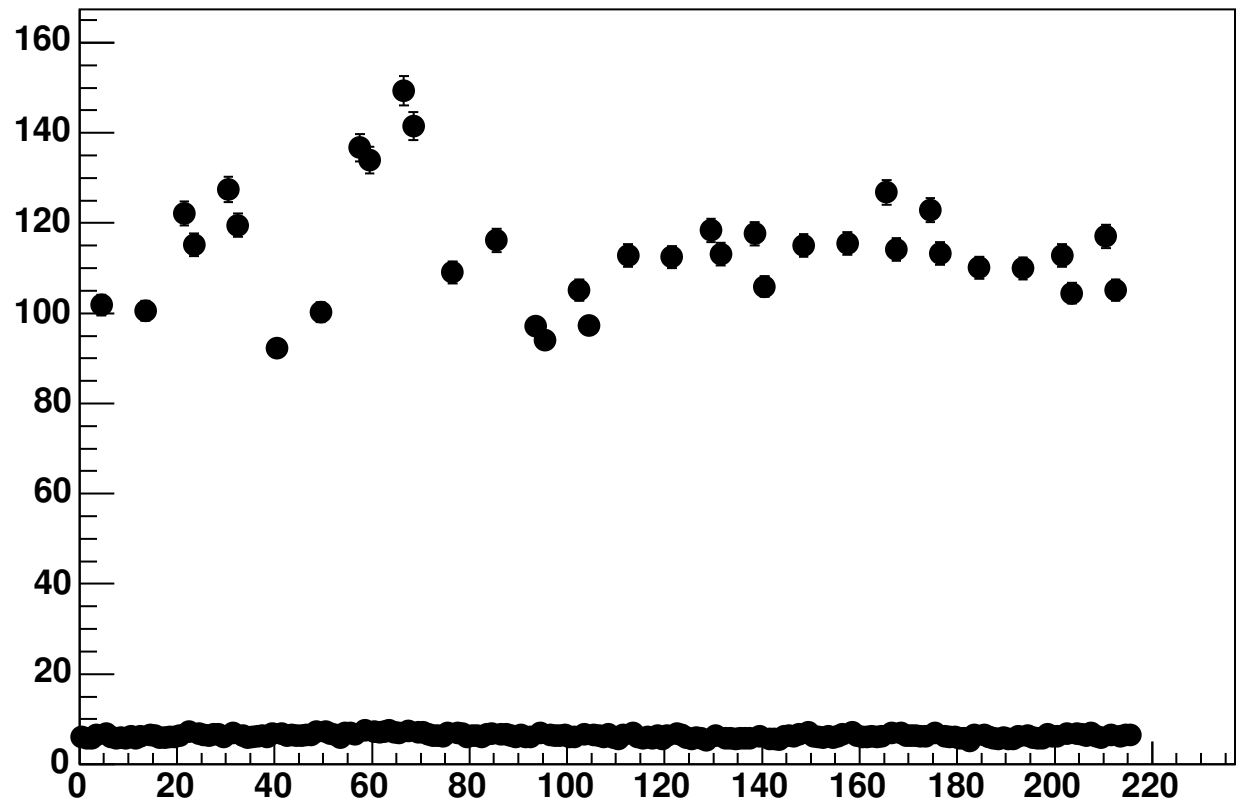
Enable 4, Hold=30, DAC=43008, ADC Noise vs 18*Chip+Chan



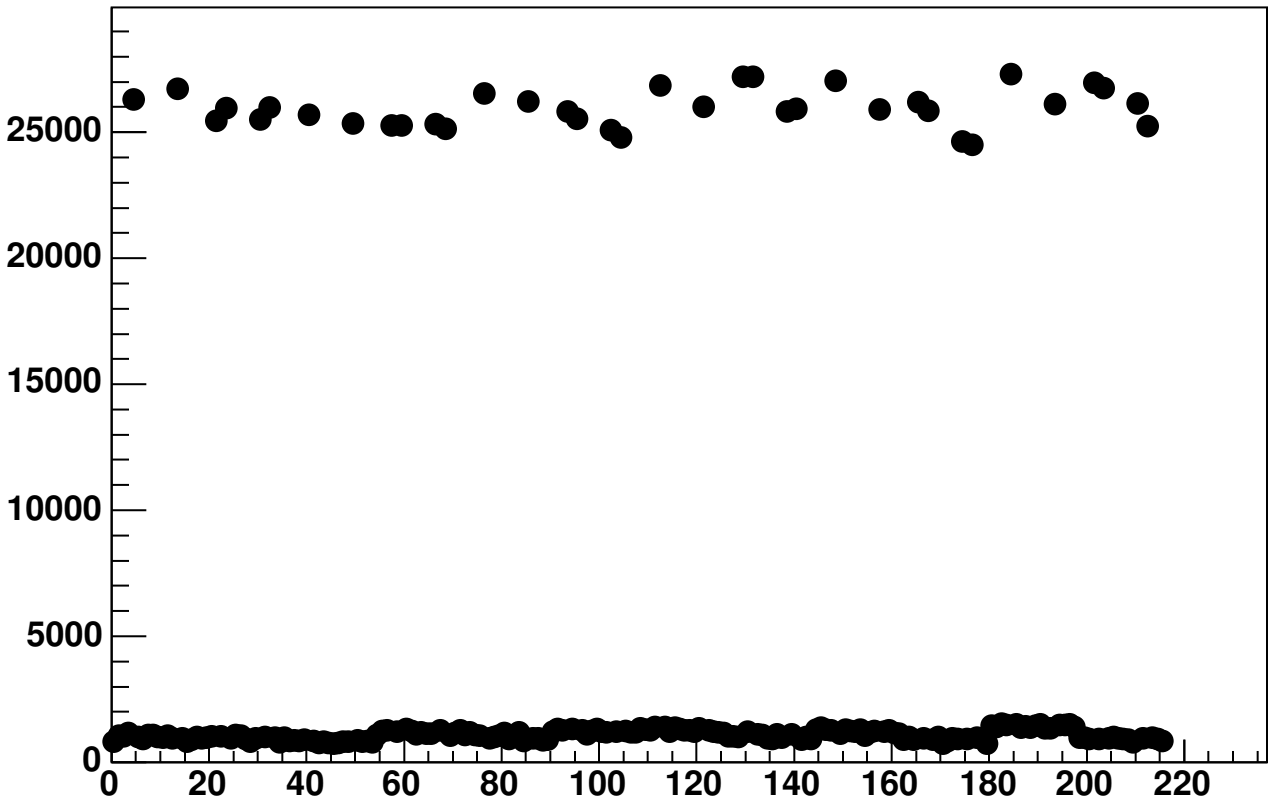
Enable 4, Hold=30, DAC=40960, ADC Mean vs 18*Chip+Chan



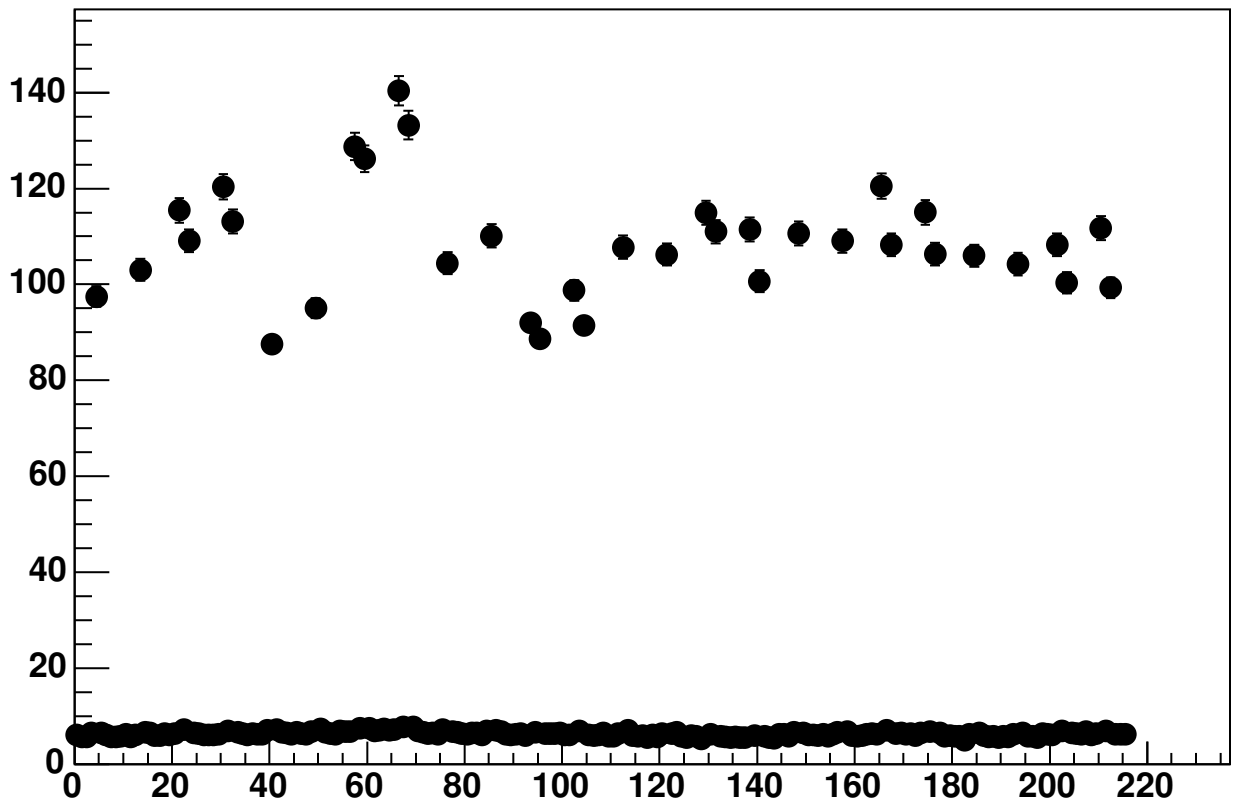
Enable 4, Hold=30, DAC=40960, ADC Noise vs 18*Chip+Chan



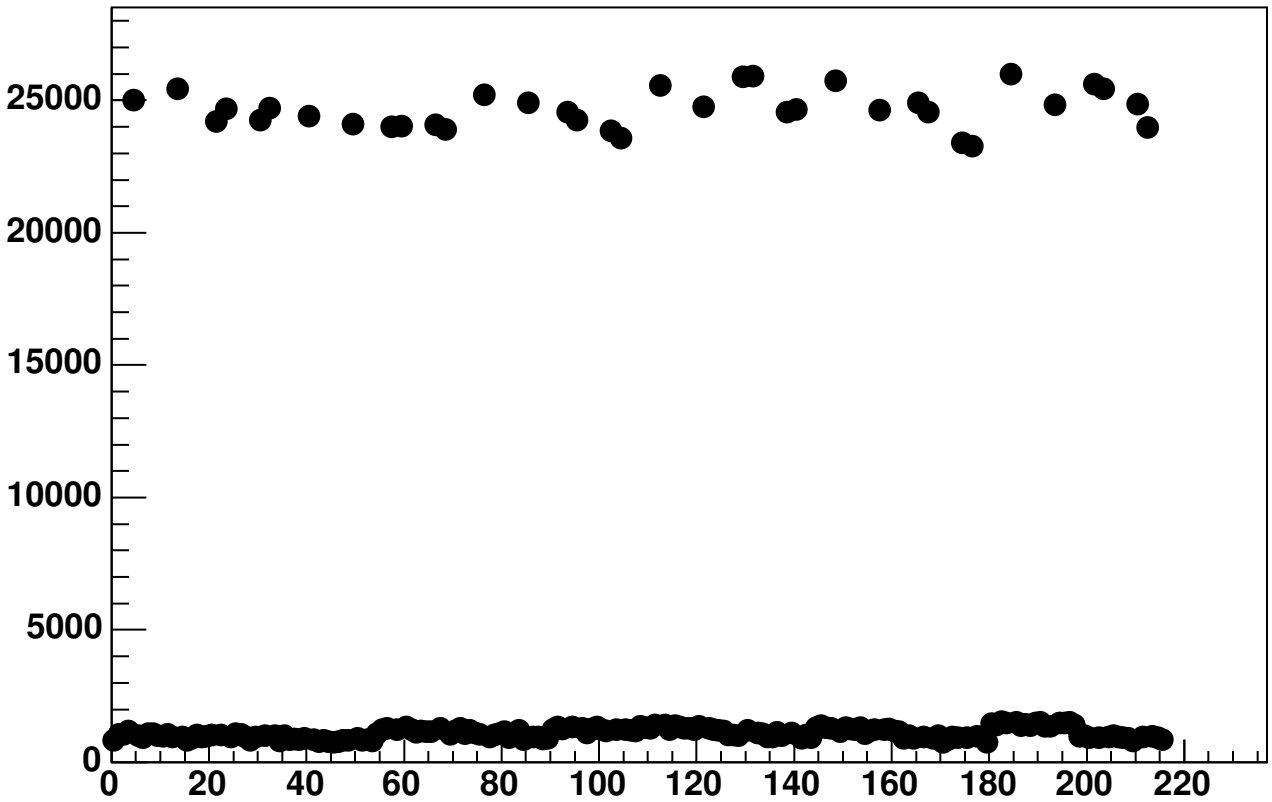
Enable 4, Hold=30, DAC=38912, ADC Mean vs 18*Chip+Chan



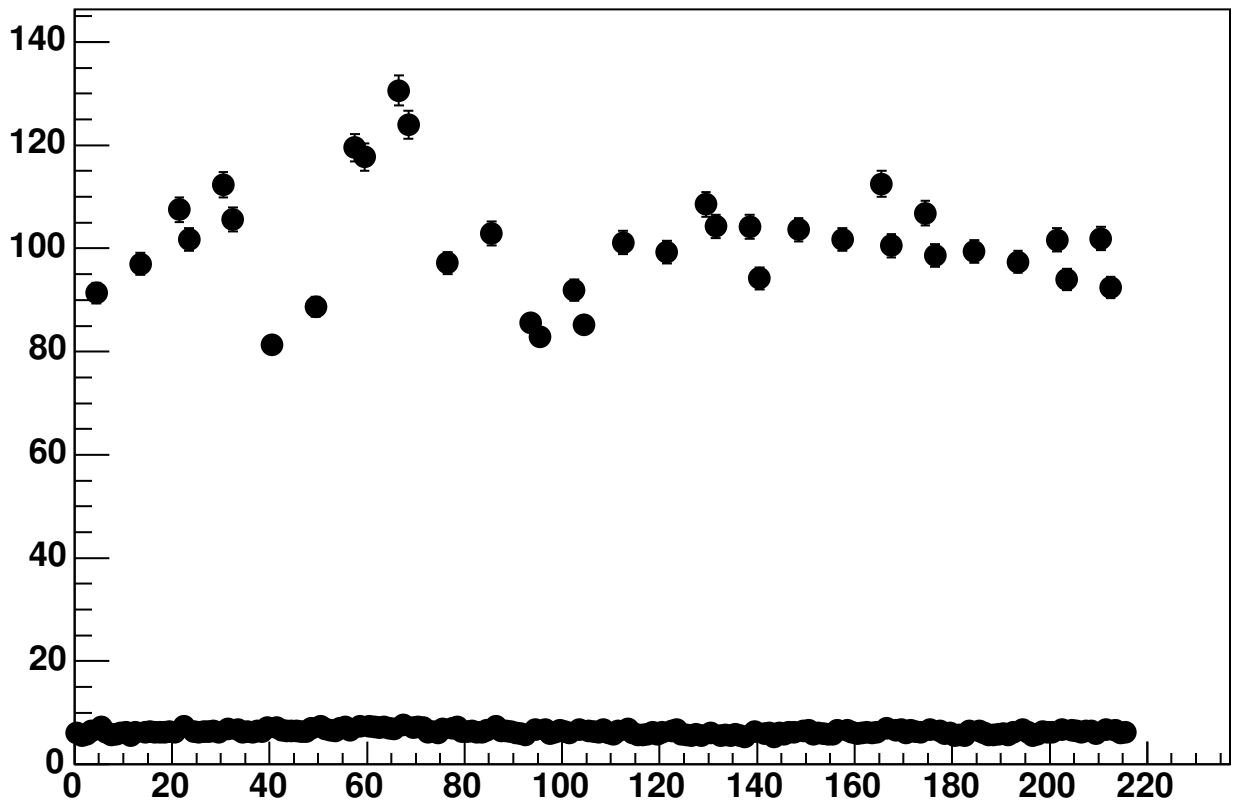
Enable 4, Hold=30, DAC=38912, ADC Noise vs 18*Chip+Chan



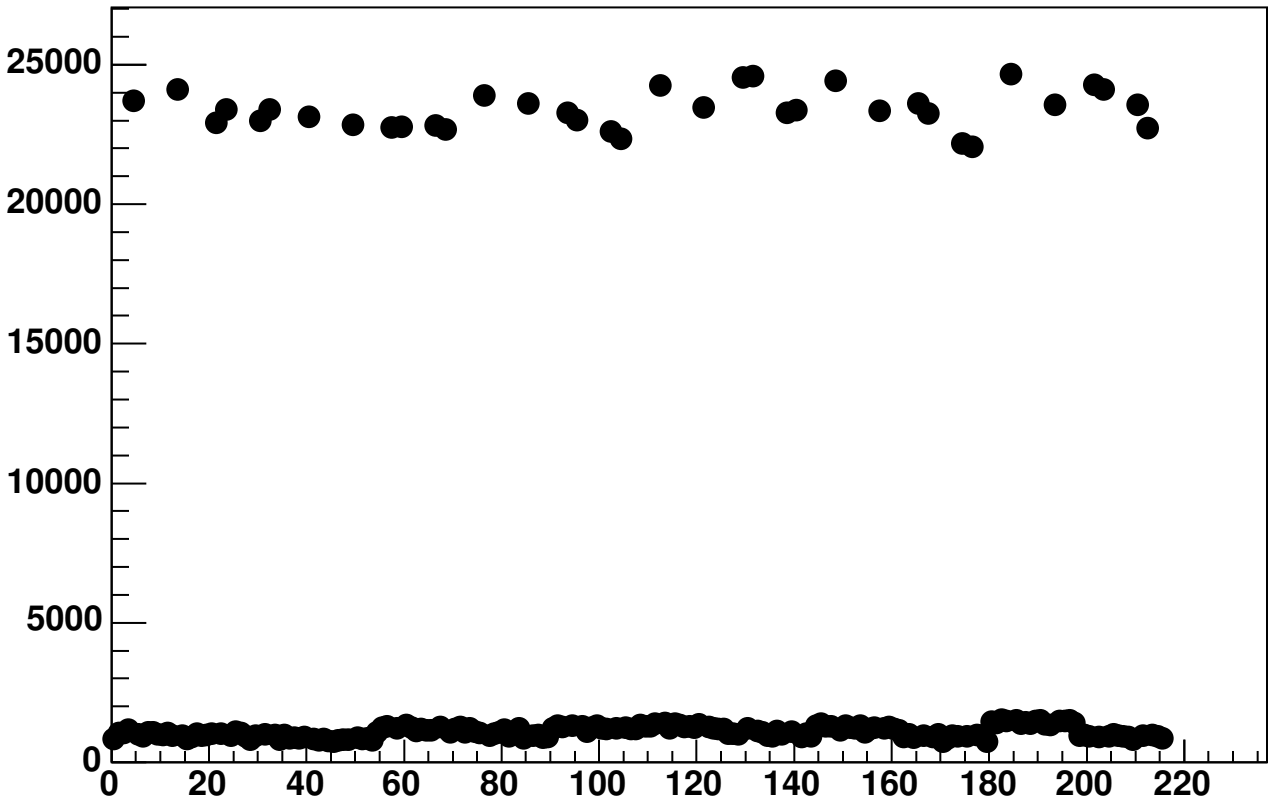
Enable 4, Hold=30, DAC=36864, ADC Mean vs 18*Chip+Chan



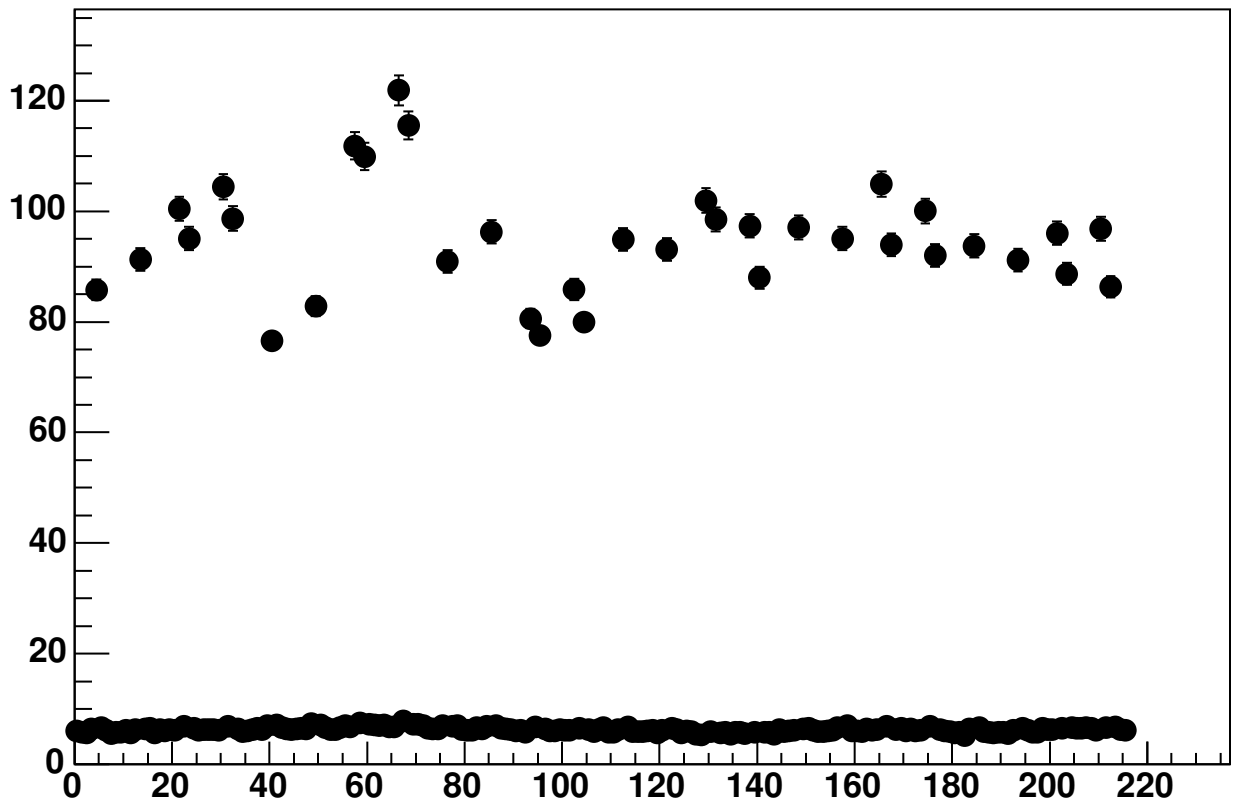
Enable 4, Hold=30, DAC=36864, ADC Noise vs 18*Chip+Chan



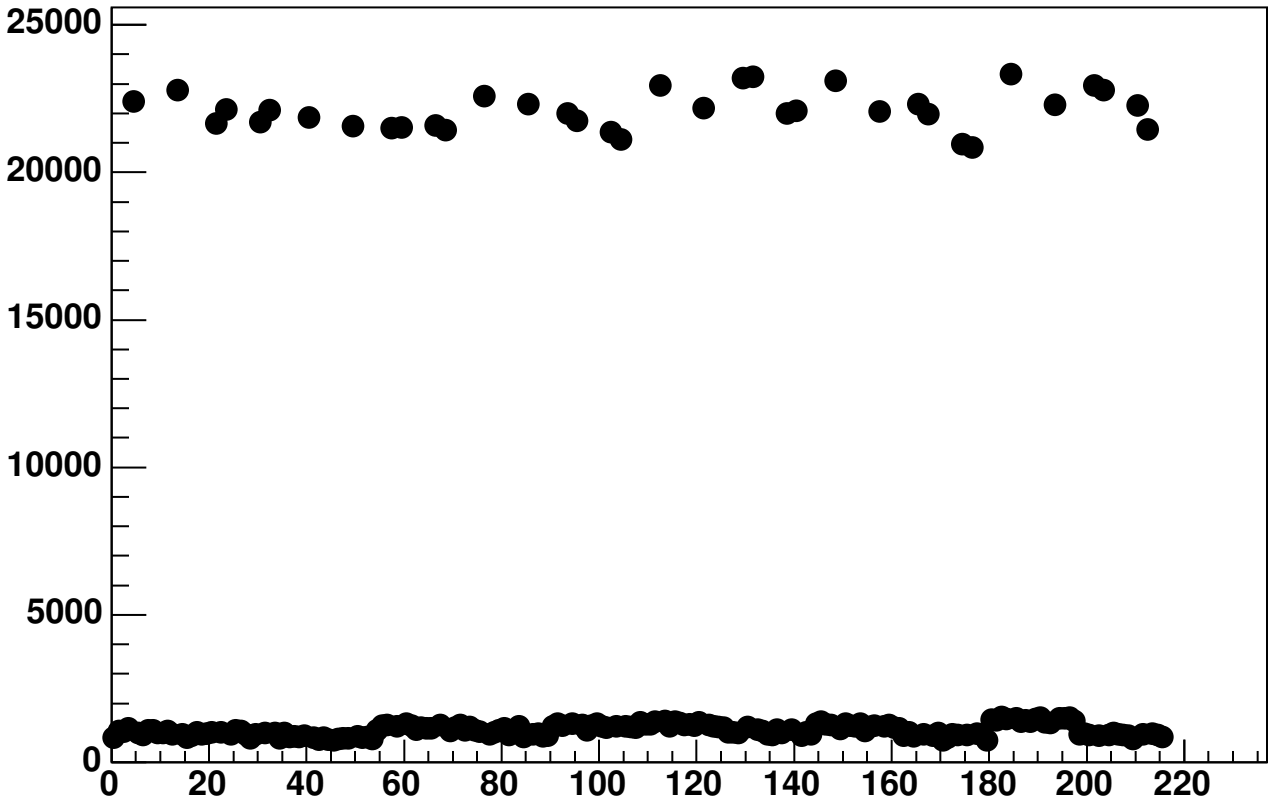
Enable 4, Hold=30, DAC=34816, ADC Mean vs 18*Chip+Chan



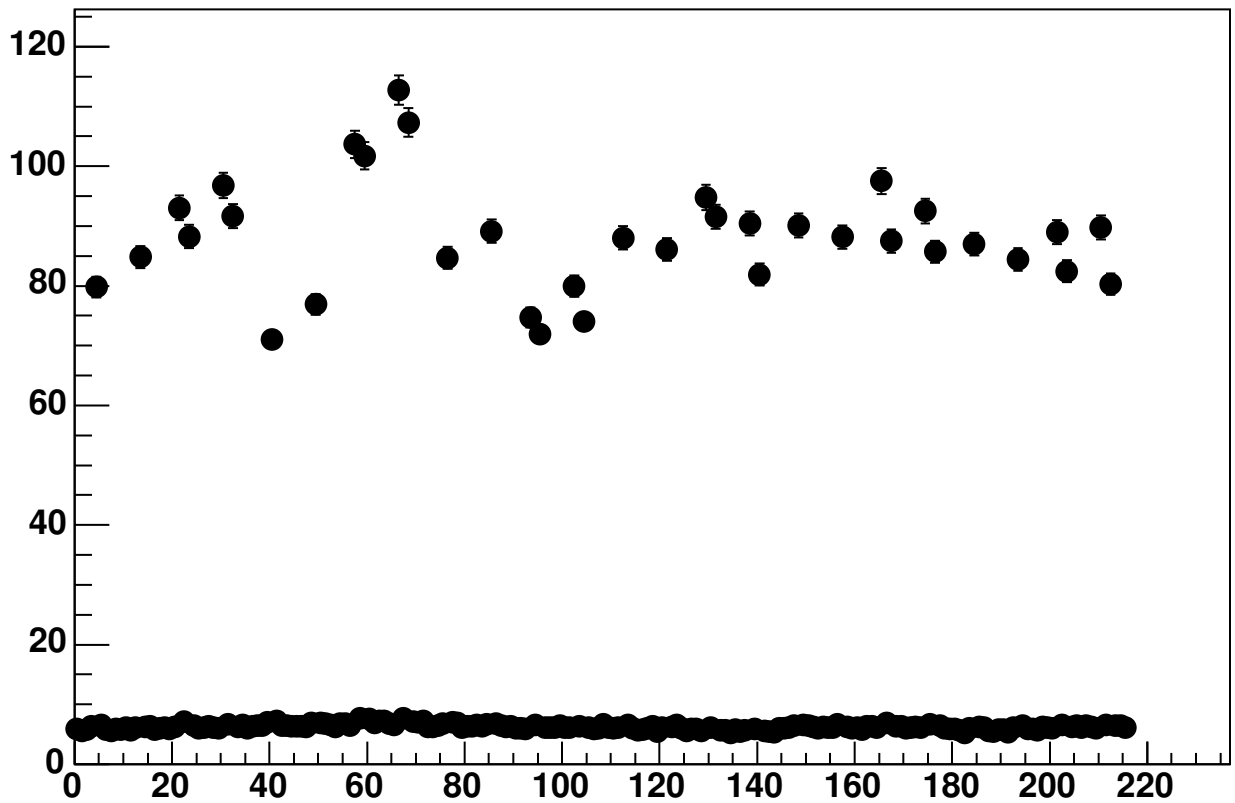
Enable 4, Hold=30, DAC=34816, ADC Noise vs 18*Chip+Chan



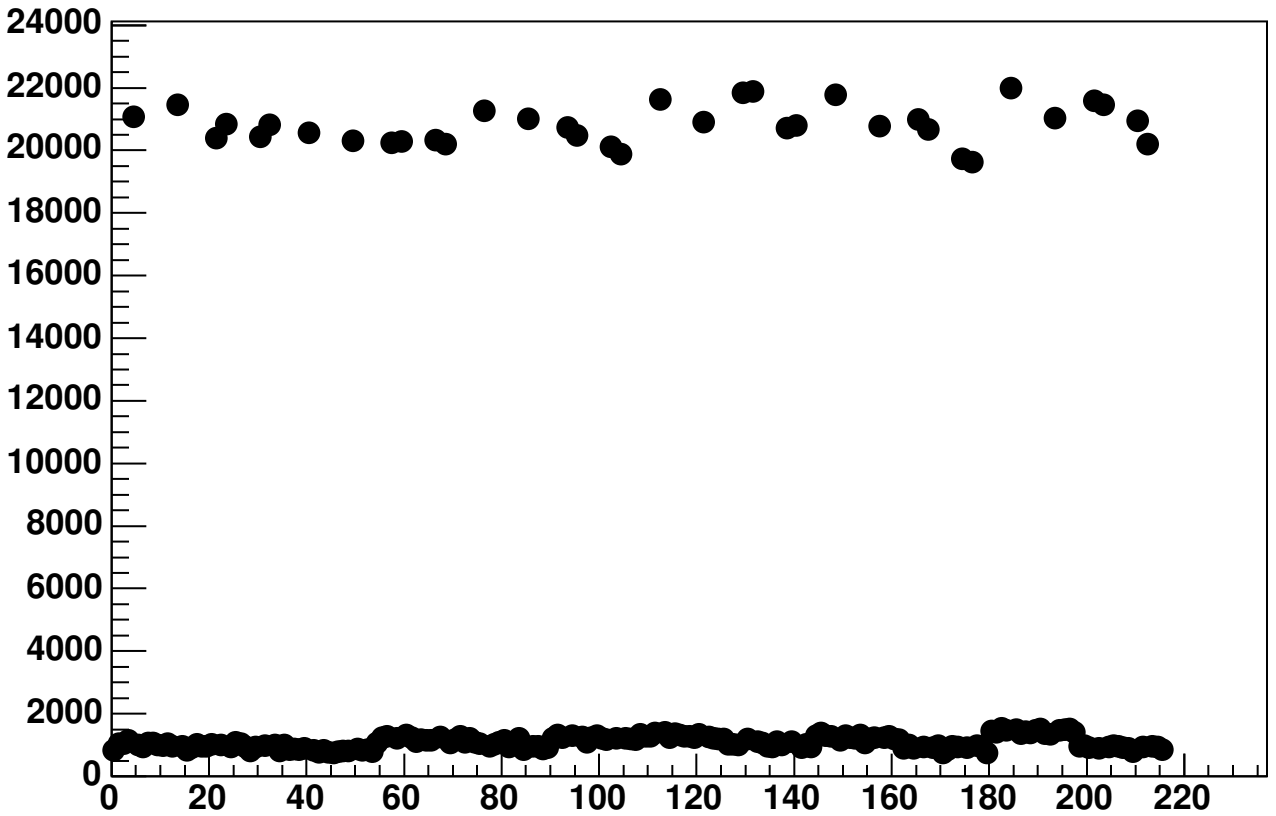
Enable 4, Hold=30, DAC=32768, ADC Mean vs 18*Chip+Chan



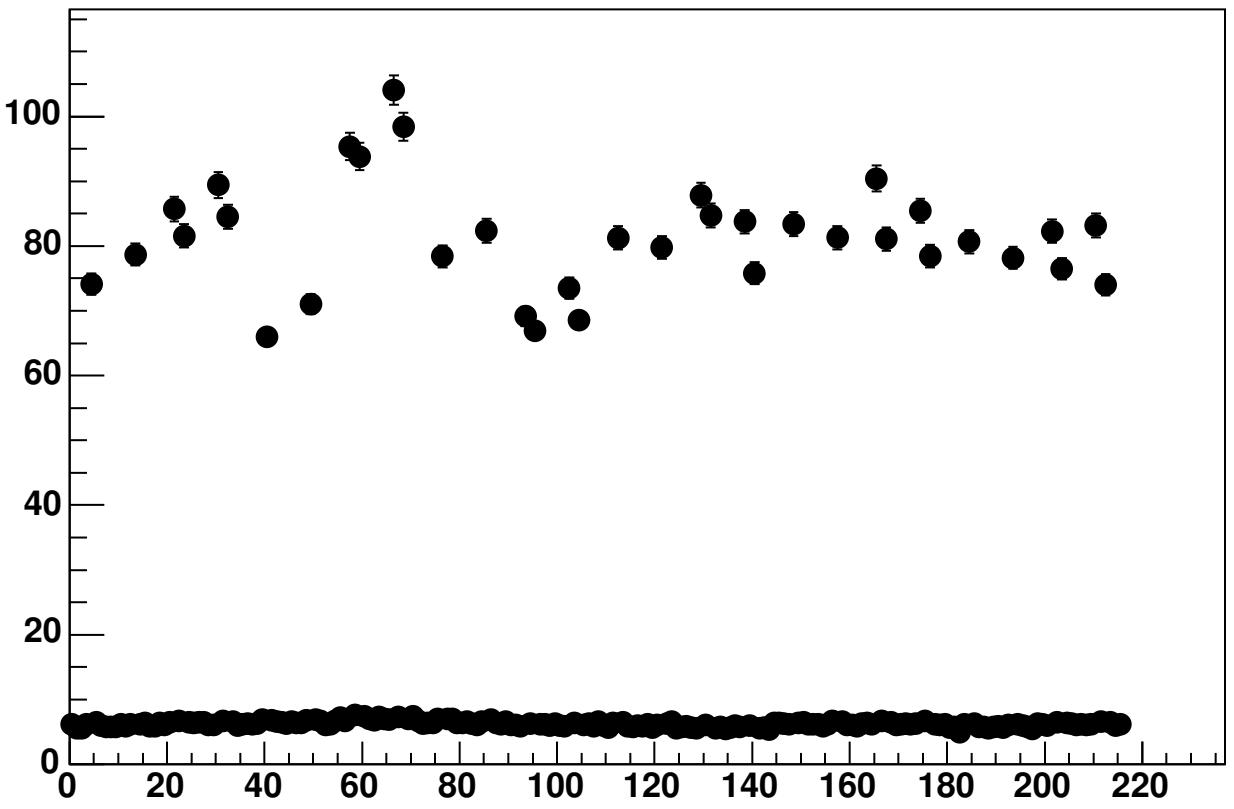
Enable 4, Hold=30, DAC=32768, ADC Noise vs 18*Chip+Chan



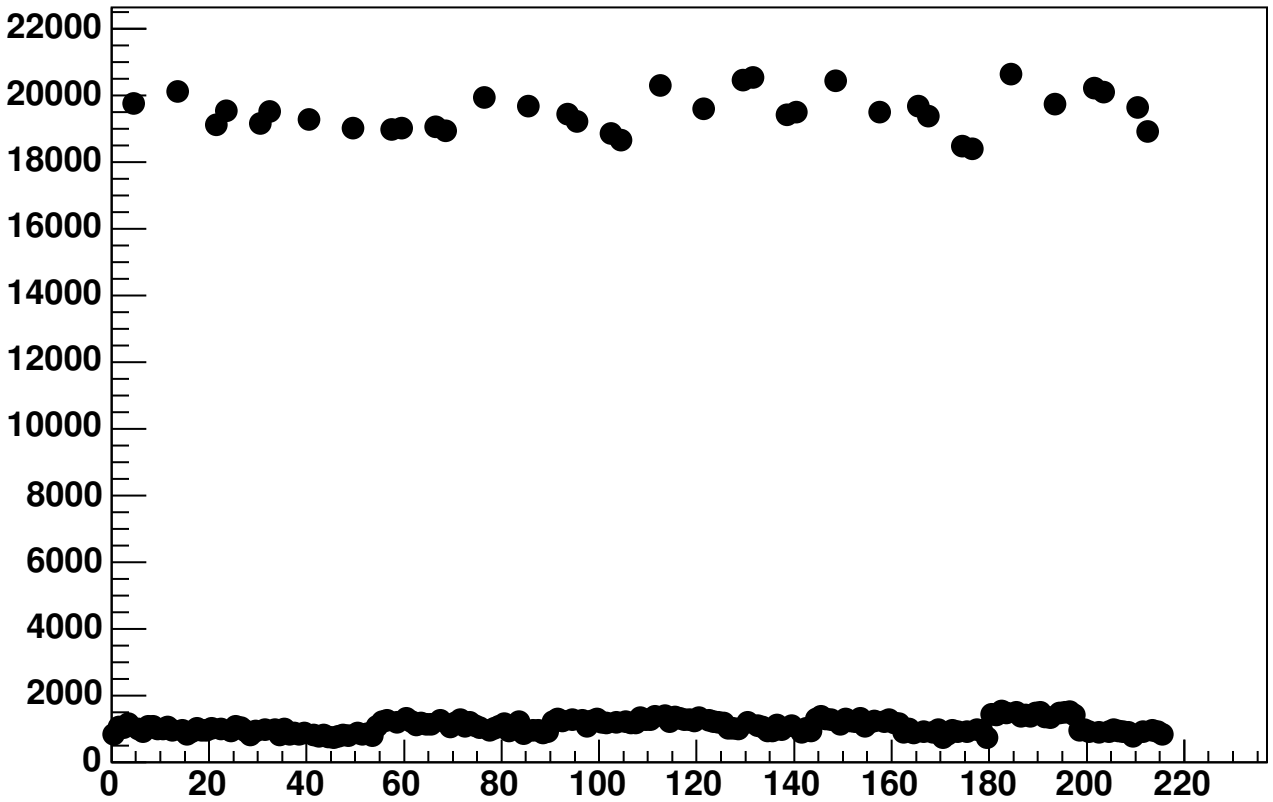
Enable 4, Hold=30, DAC=30720, ADC Mean vs 18*Chip+Chan



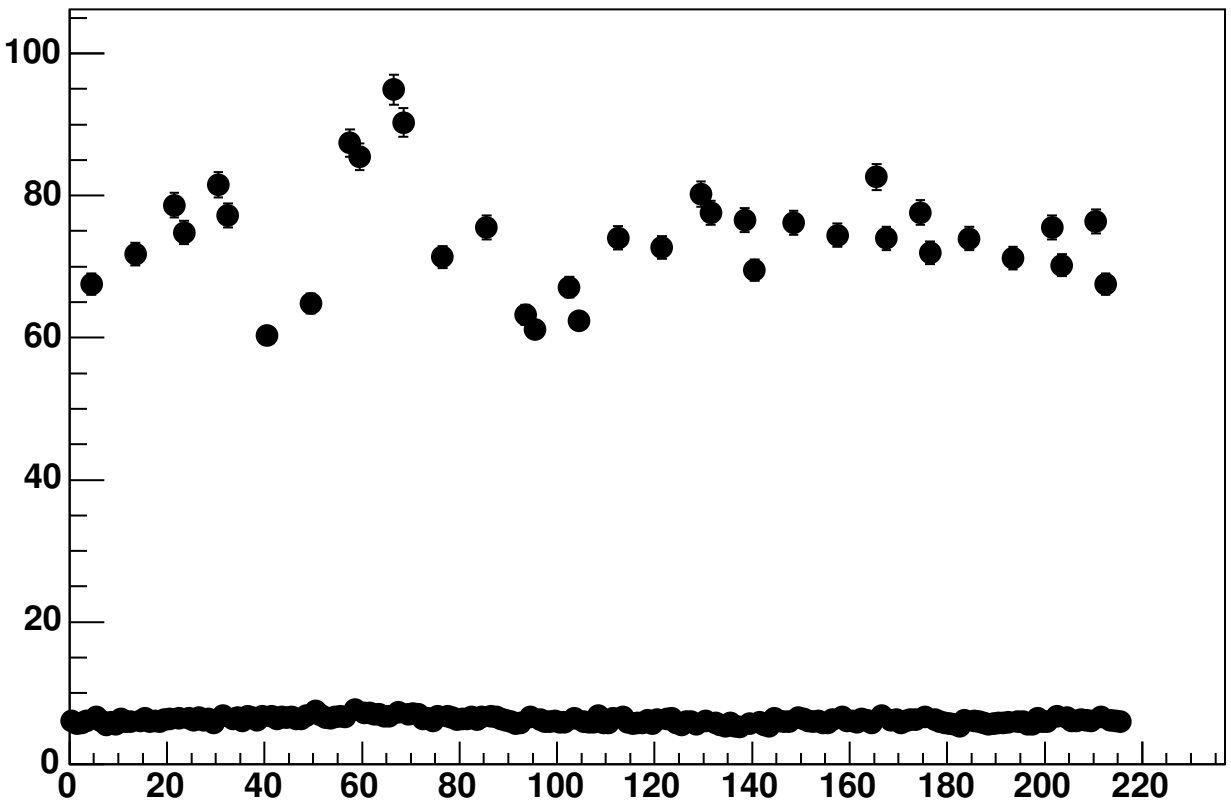
Enable 4, Hold=30, DAC=30720, ADC Noise vs 18*Chip+Chan



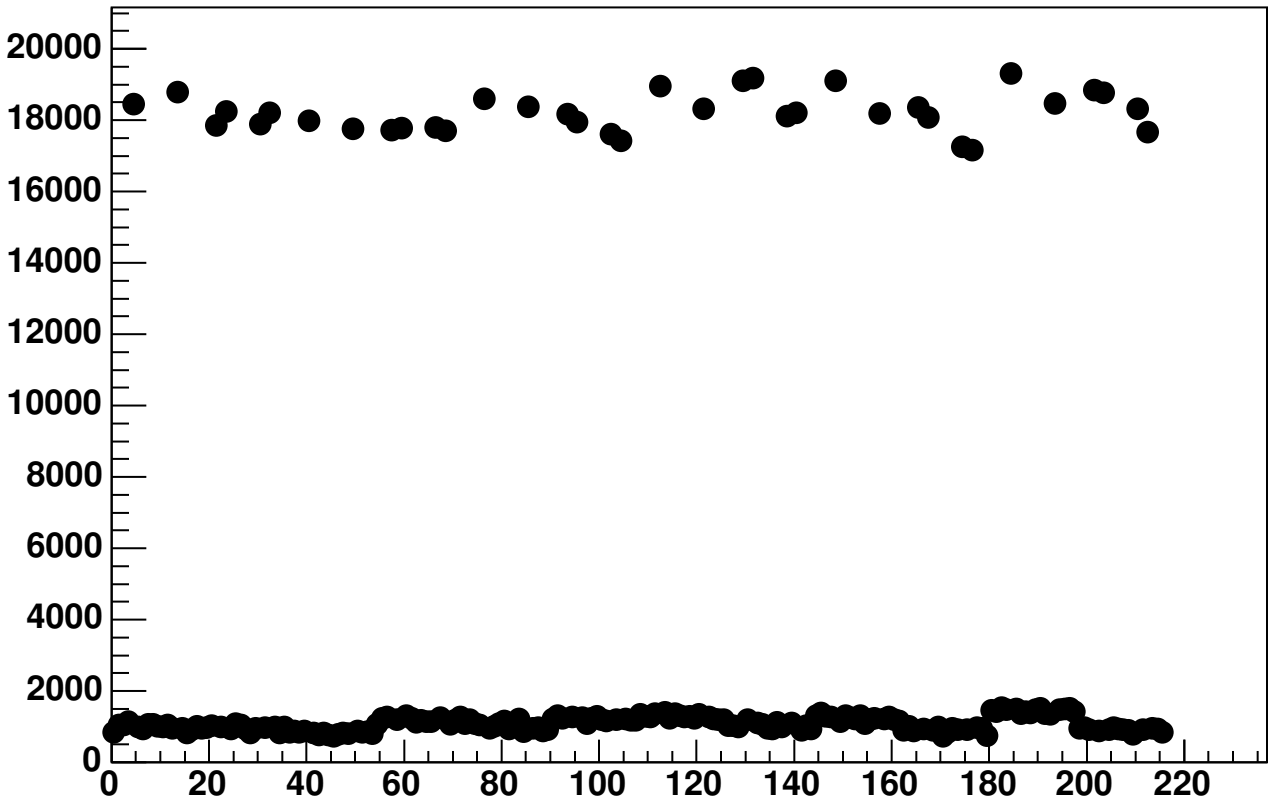
Enable 4, Hold=30, DAC=28672, ADC Mean vs 18*Chip+Chan



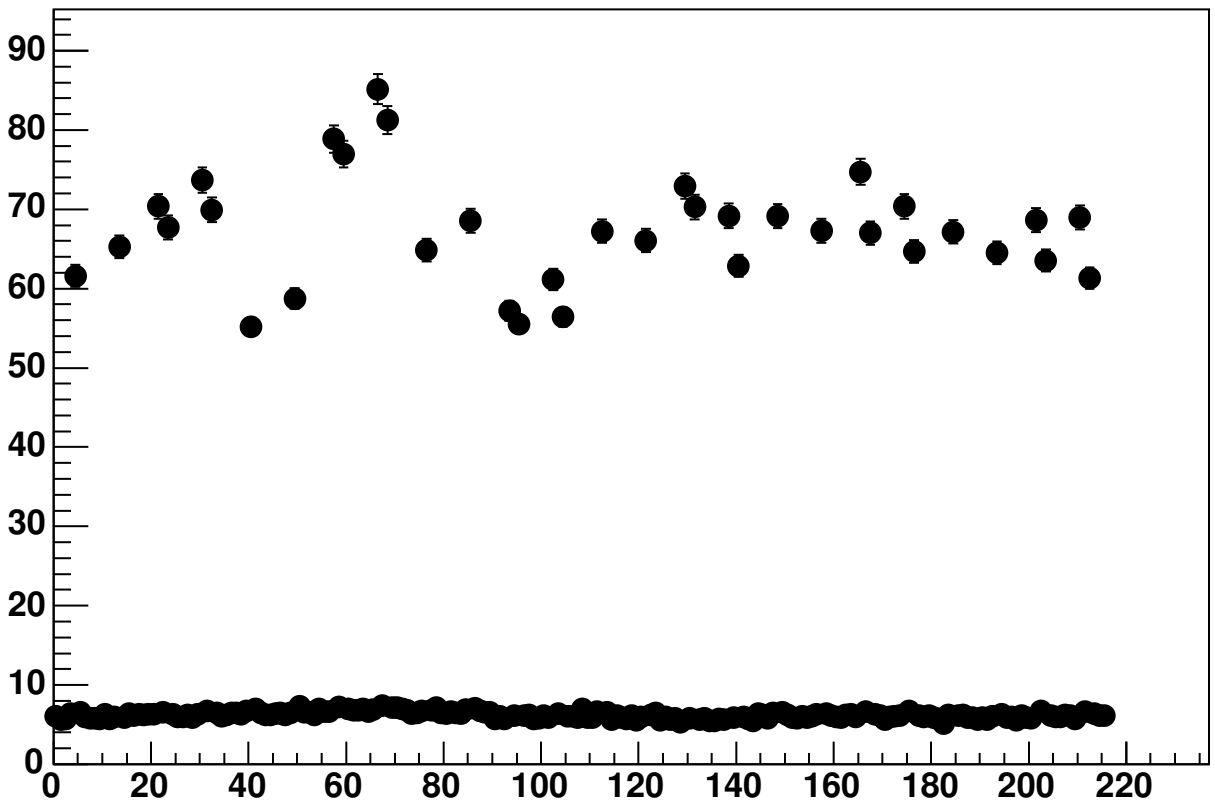
Enable 4, Hold=30, DAC=28672, ADC Noise vs 18*Chip+Chan



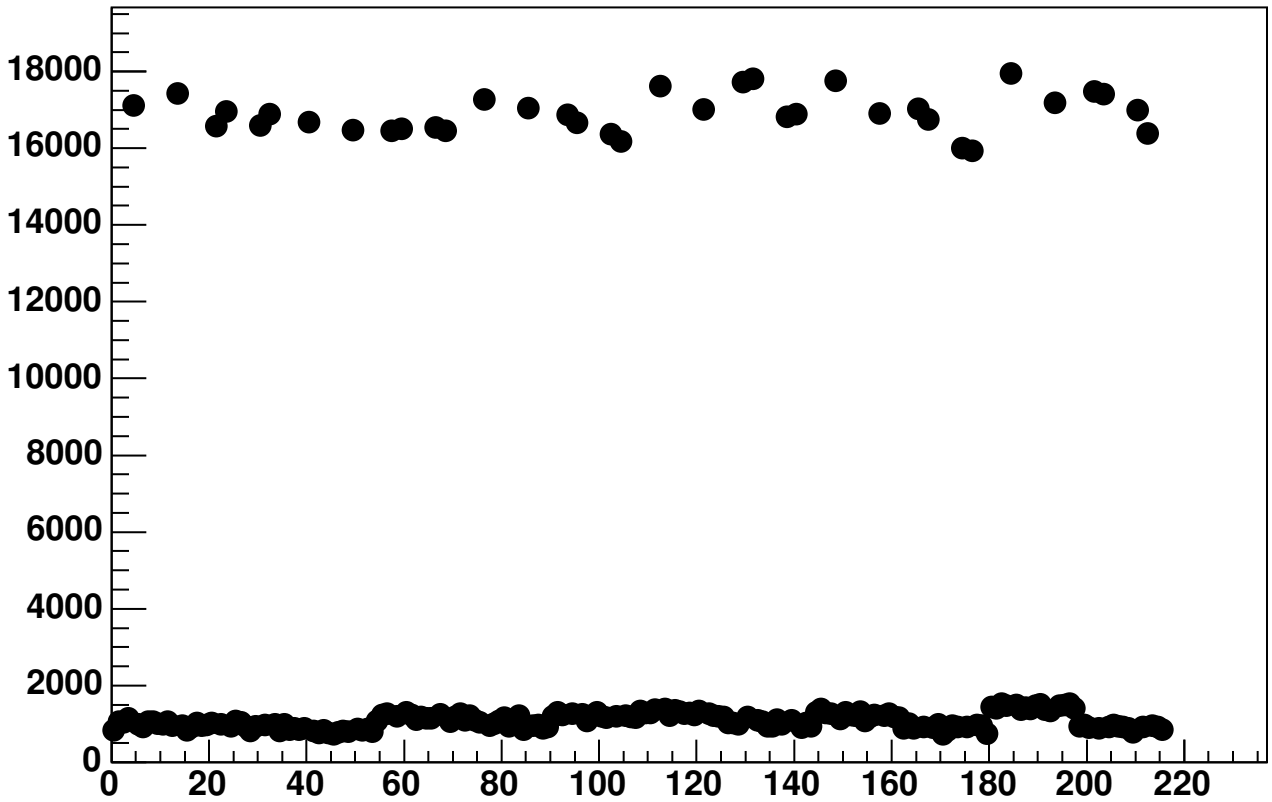
Enable 4, Hold=30, DAC=26624, ADC Mean vs 18*Chip+Chan



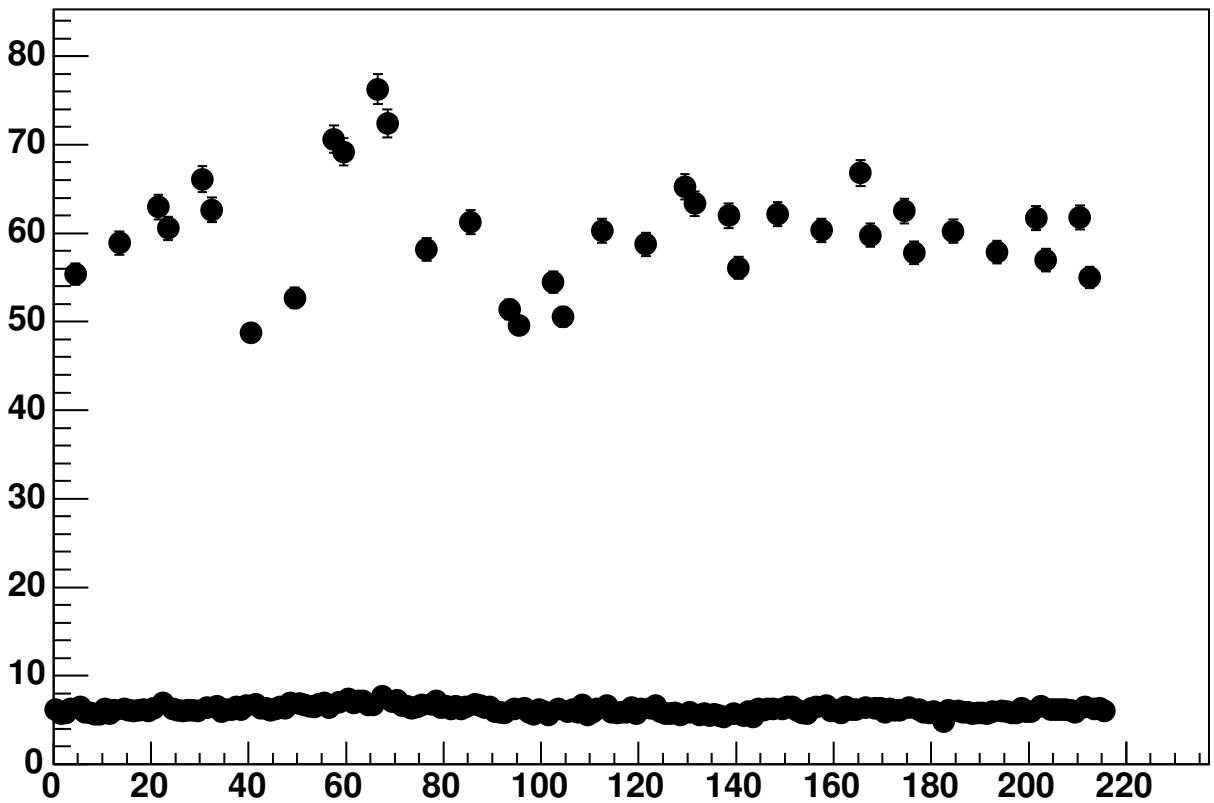
Enable 4, Hold=30, DAC=26624, ADC Noise vs 18*Chip+Chan



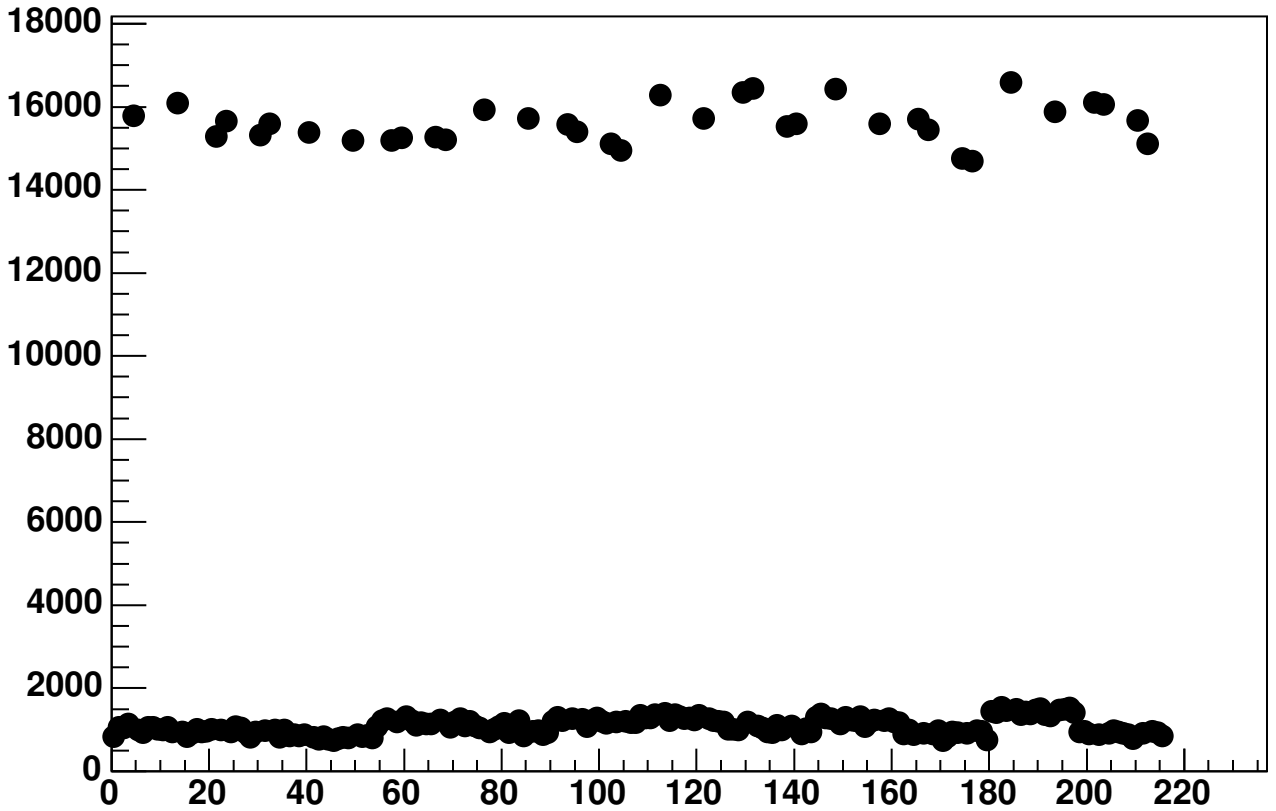
Enable 4, Hold=30, DAC=24576, ADC Mean vs 18*Chip+Chan



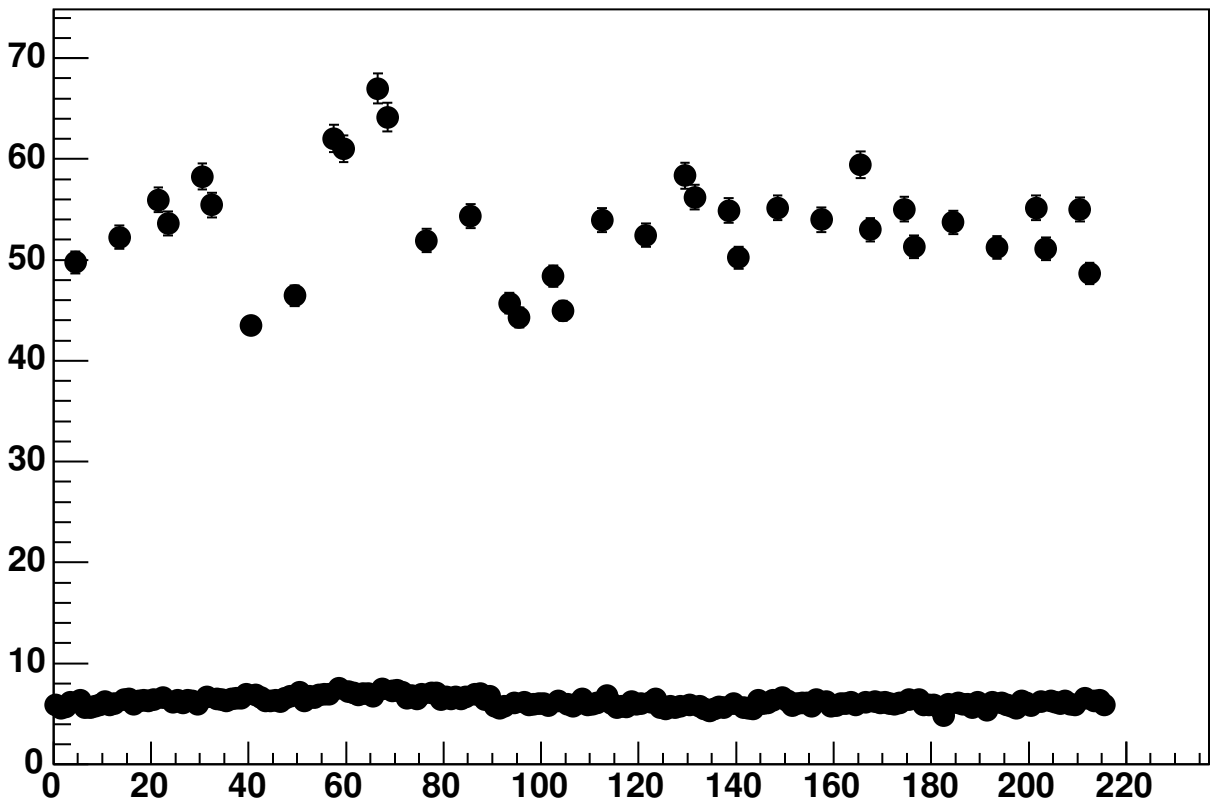
Enable 4, Hold=30, DAC=24576, ADC Noise vs 18*Chip+Chan



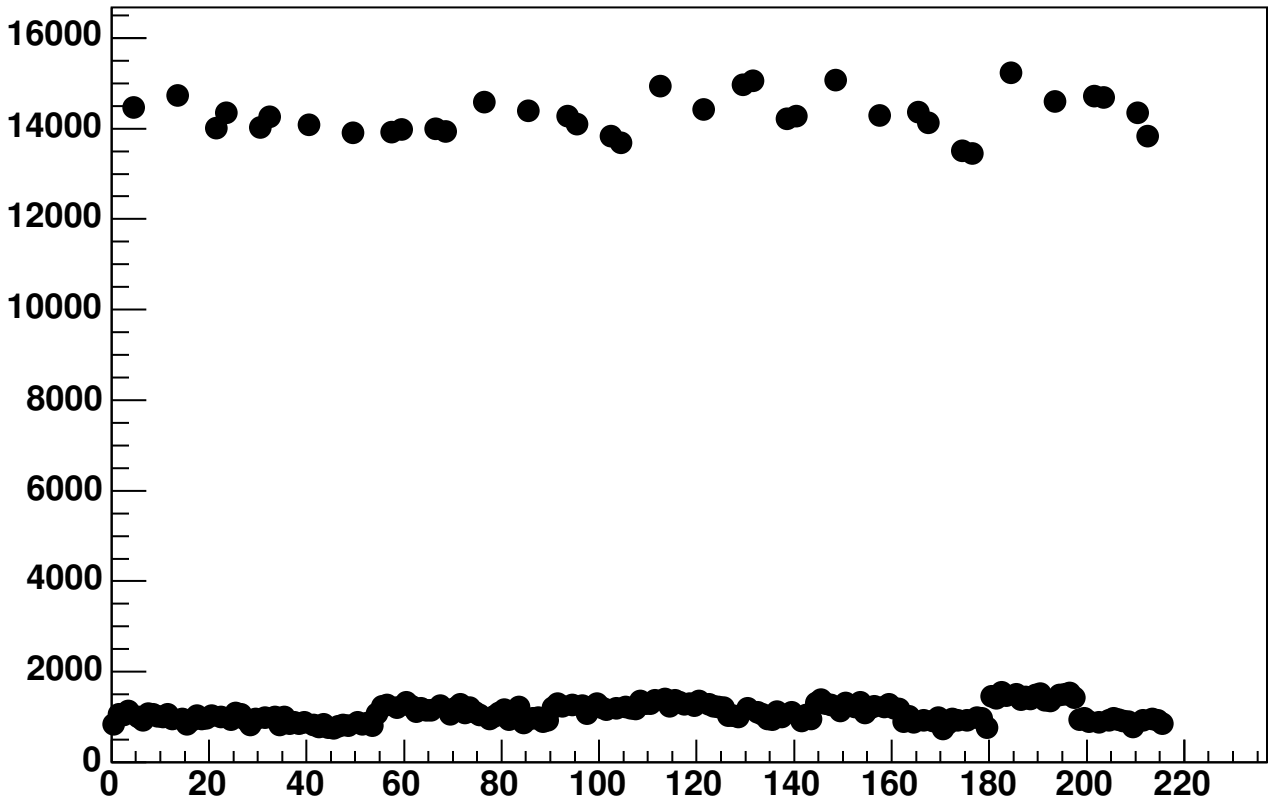
Enable 4, Hold=30, DAC=22528, ADC Mean vs 18*Chip+Chan



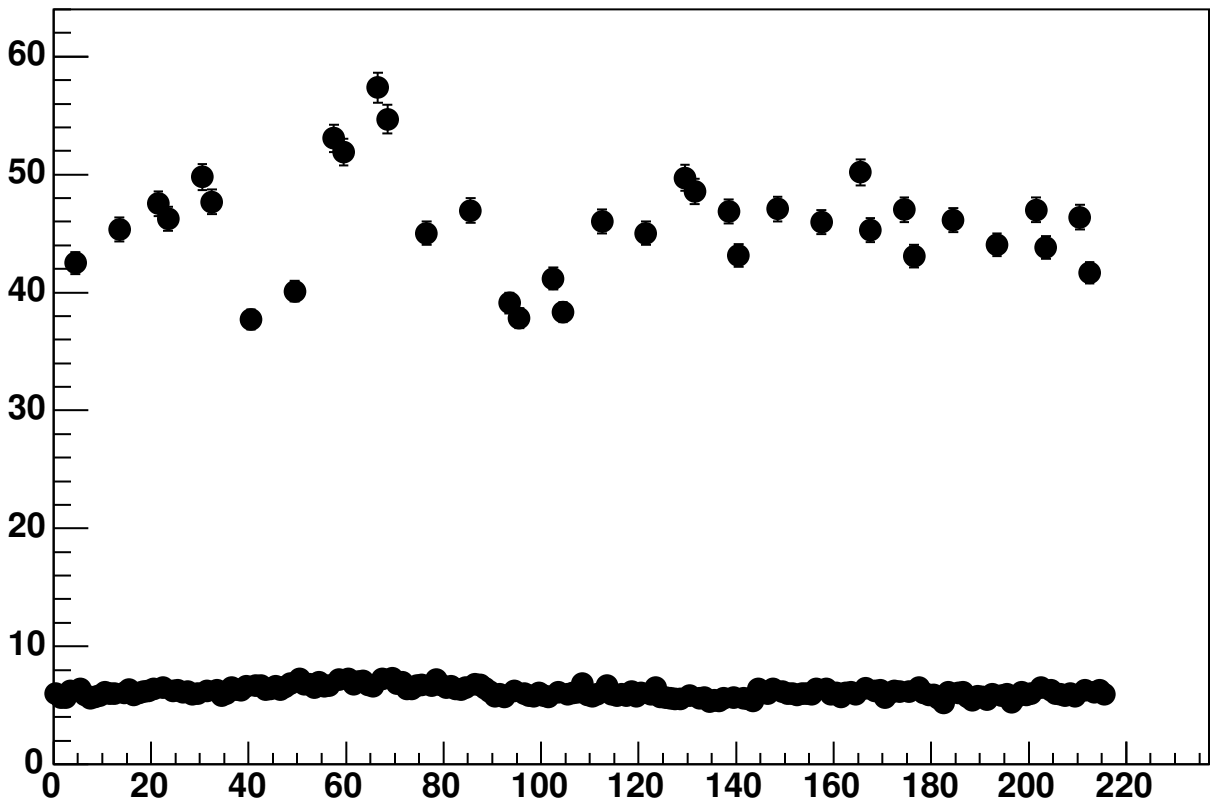
Enable 4, Hold=30, DAC=22528, ADC Noise vs 18*Chip+Chan



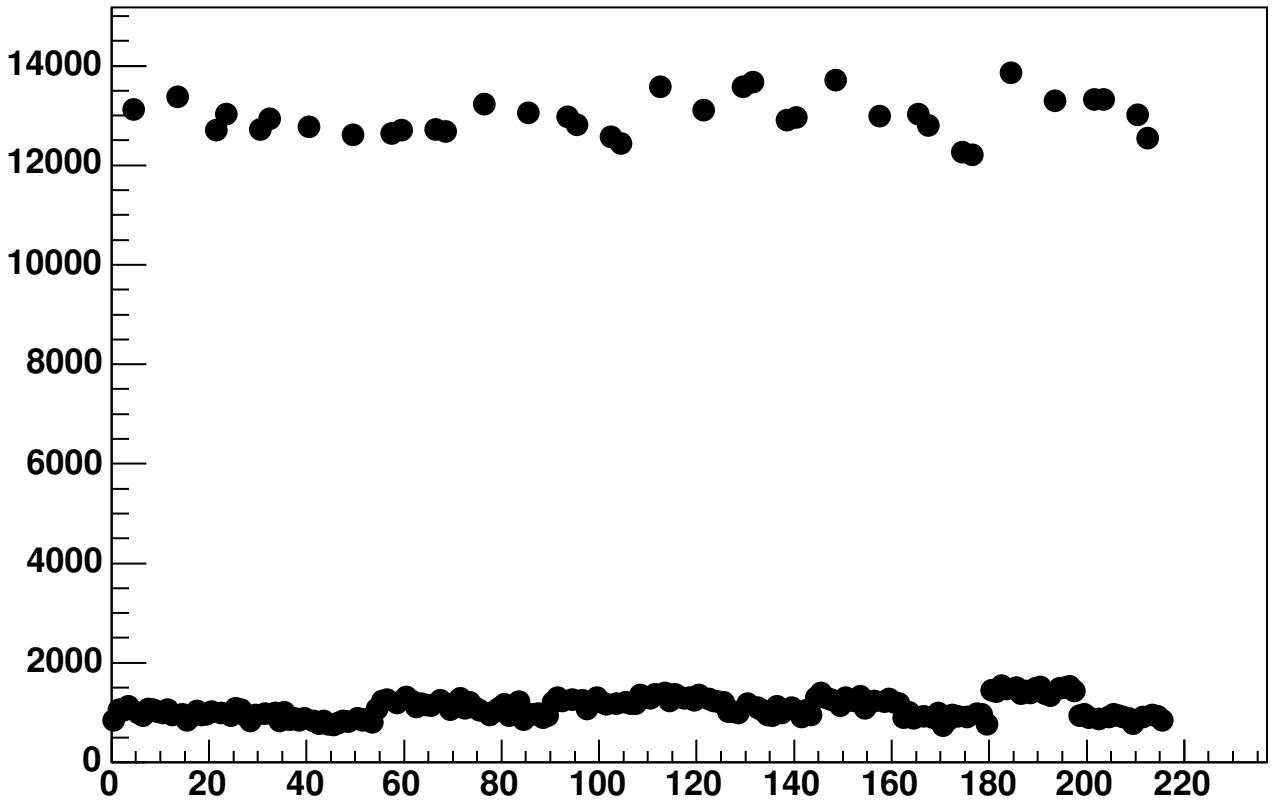
Enable 4, Hold=30, DAC=20480, ADC Mean vs 18*Chip+Chan



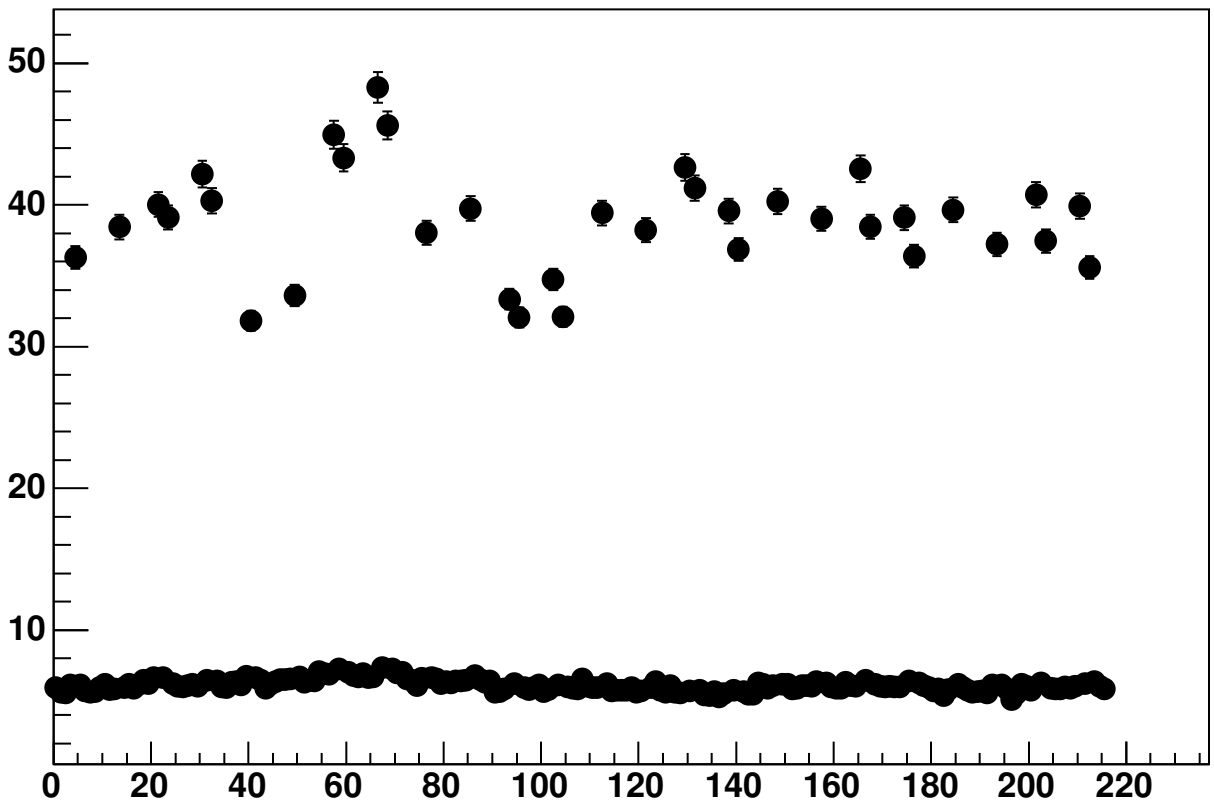
Enable 4, Hold=30, DAC=20480, ADC Noise vs 18*Chip+Chan



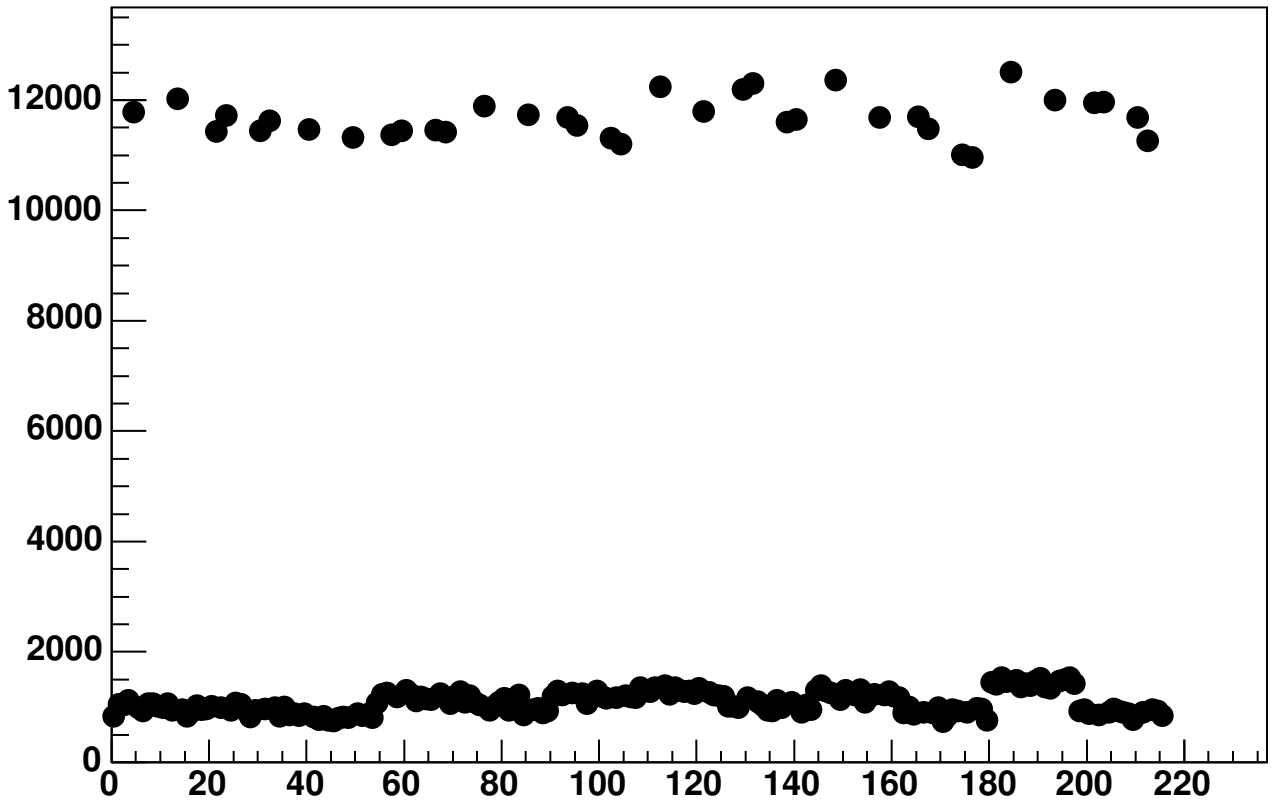
Enable 4, Hold=30, DAC=18432, ADC Mean vs 18*Chip+Chan



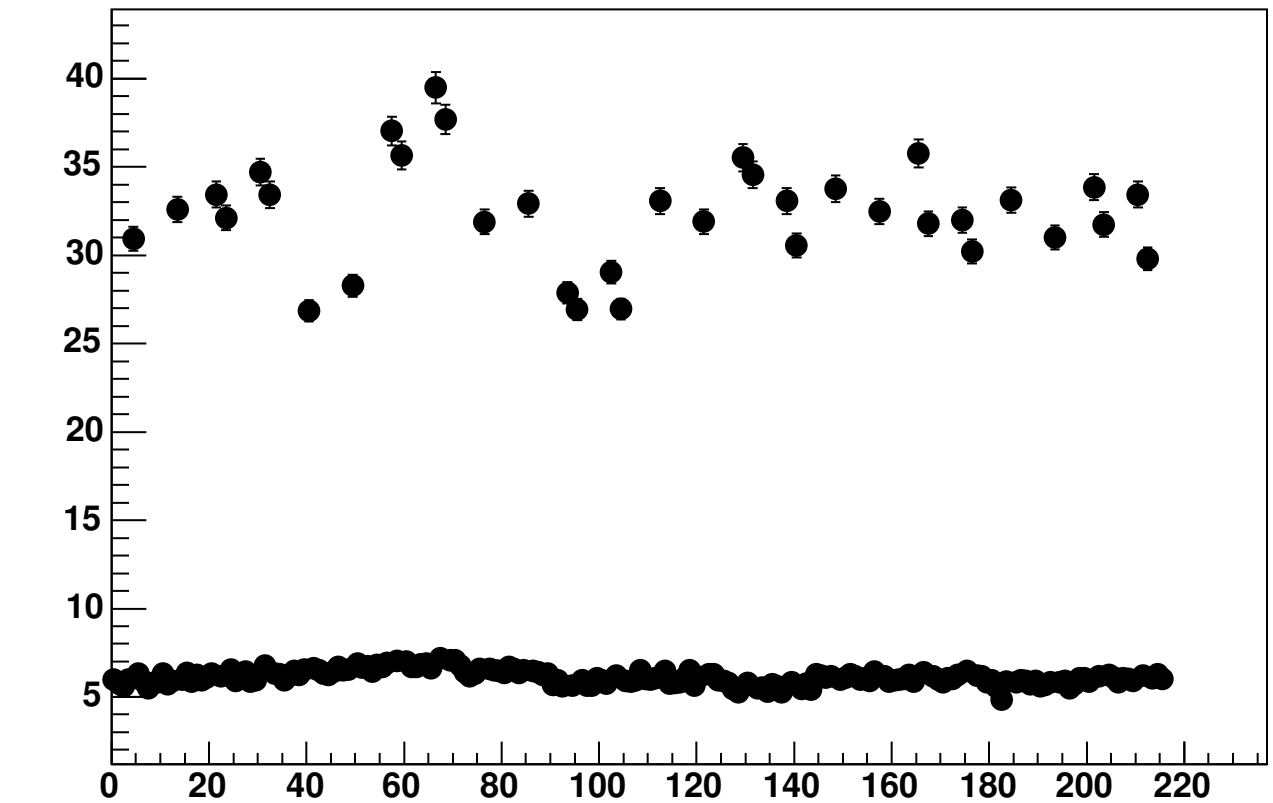
Enable 4, Hold=30, DAC=18432, ADC Noise vs 18*Chip+Chan



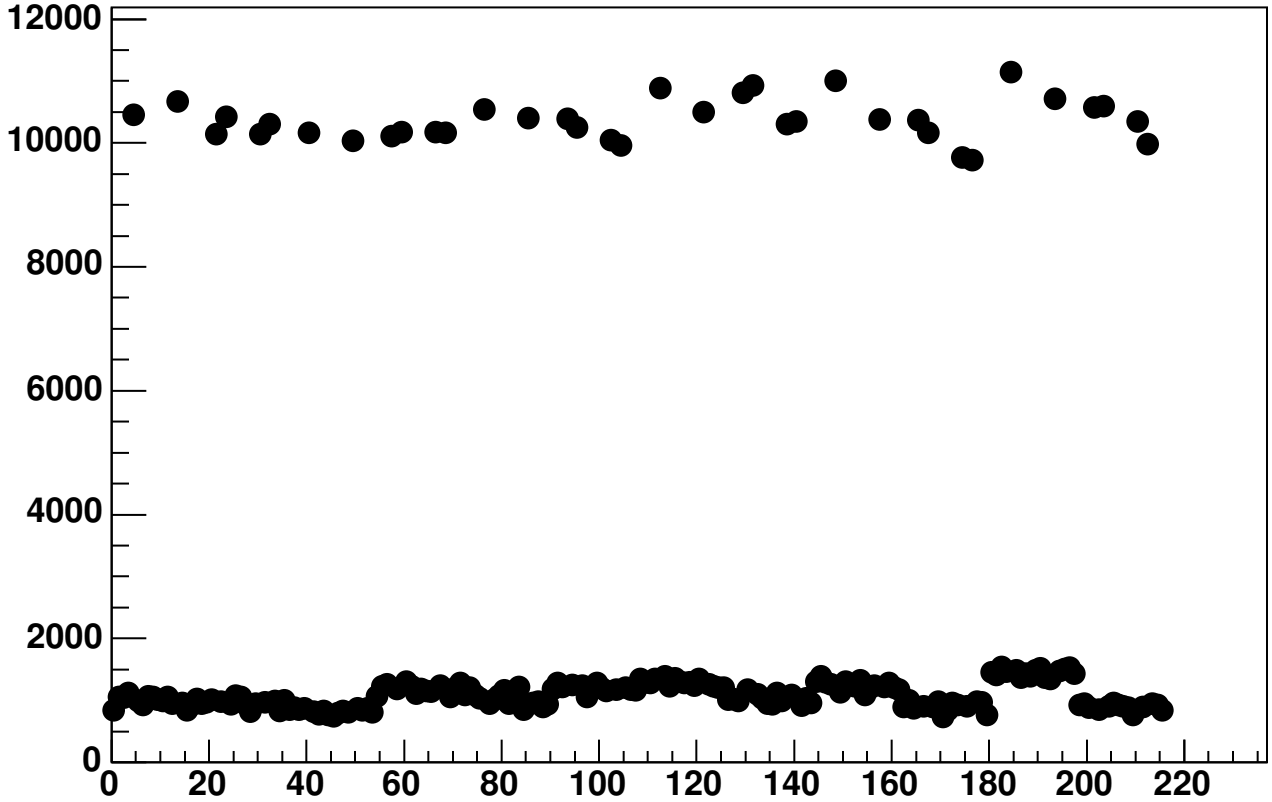
Enable 4, Hold=30, DAC=16384, ADC Mean vs 18*Chip+Chan



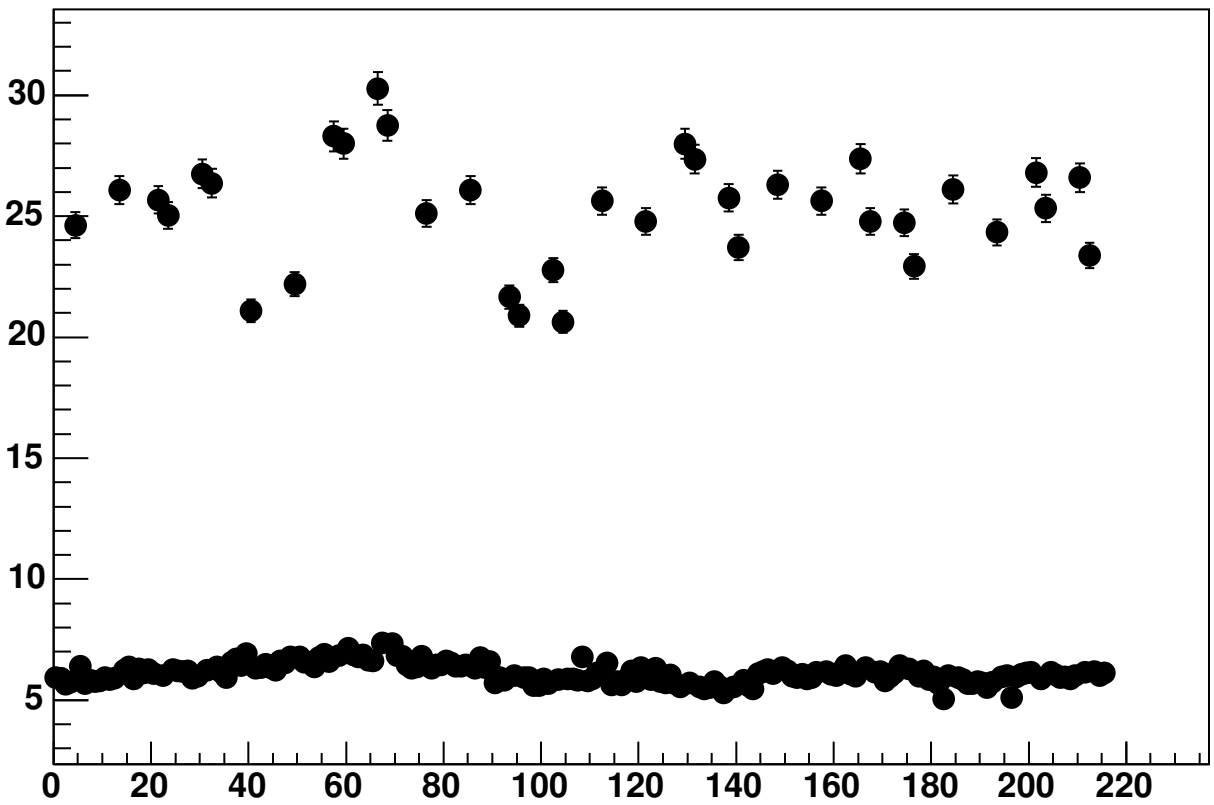
Enable 4, Hold=30, DAC=16384, ADC Noise vs 18*Chip+Chan



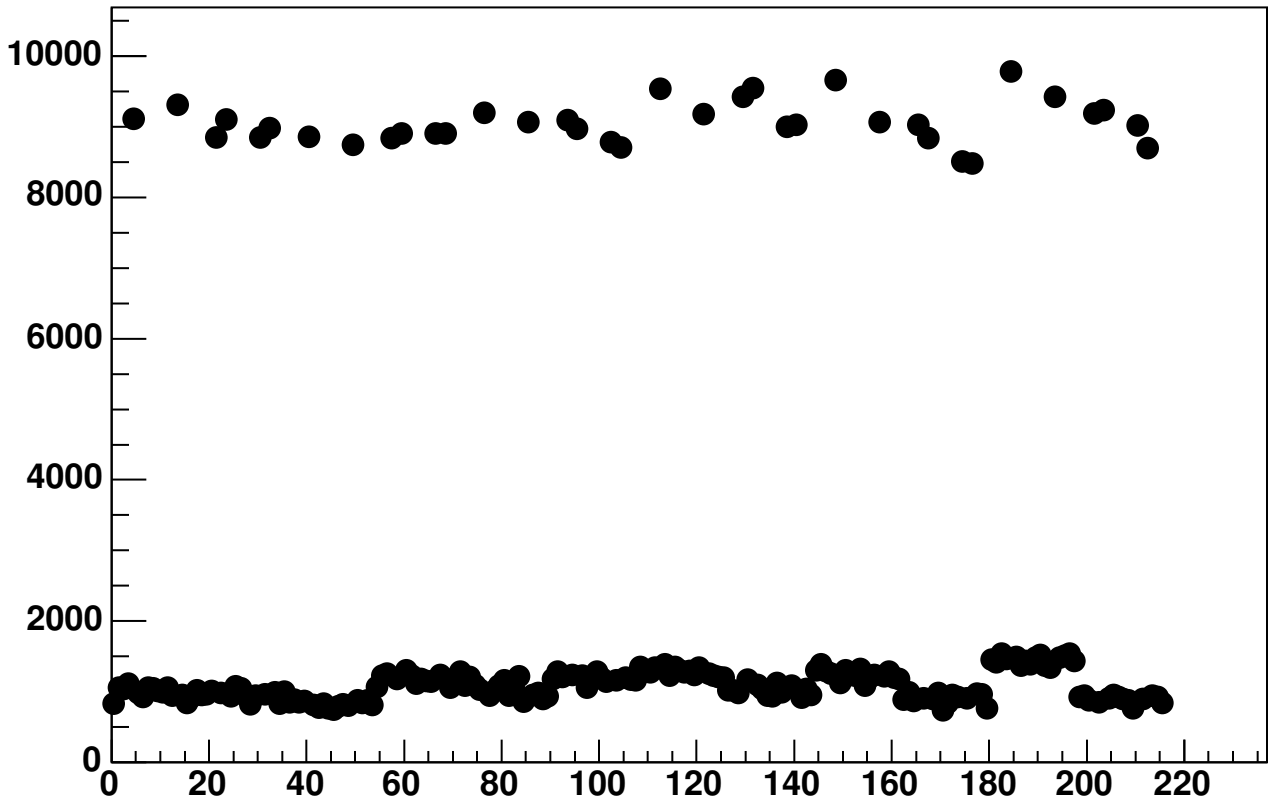
Enable 4, Hold=30, DAC=14336, ADC Mean vs 18*Chip+Chan



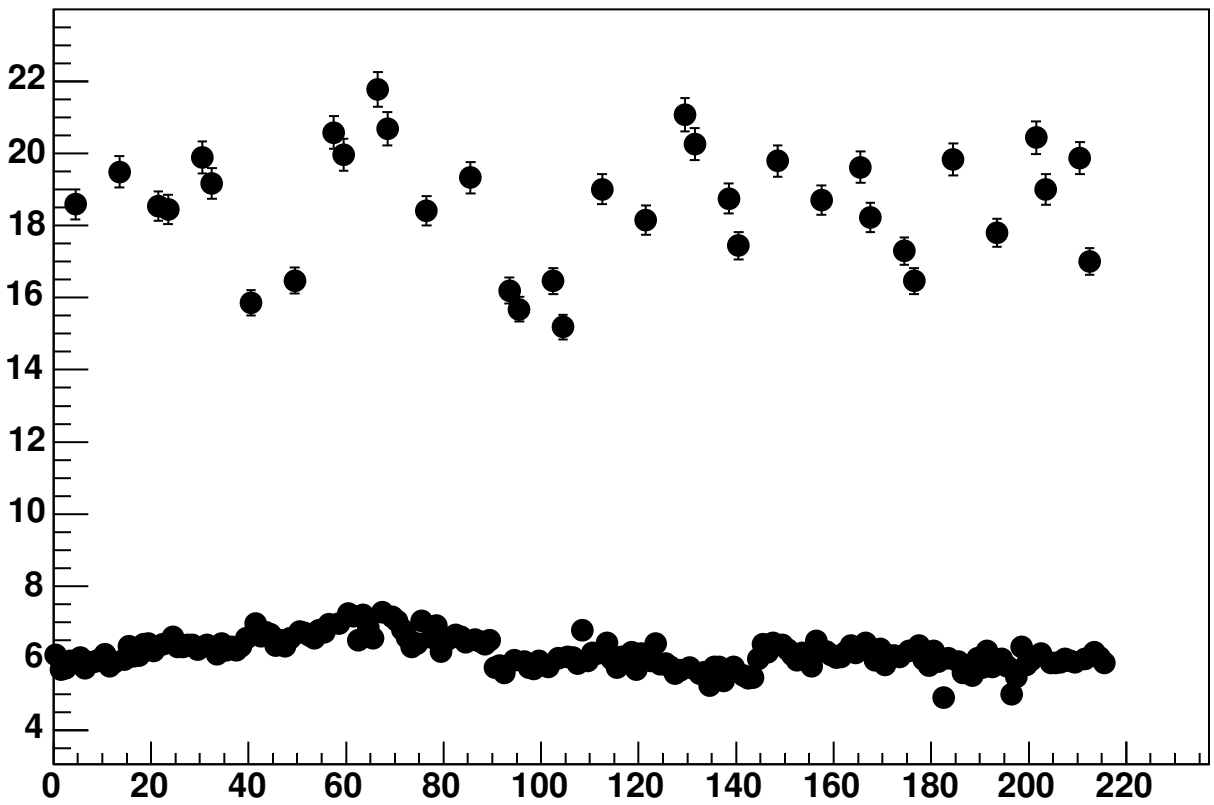
Enable 4, Hold=30, DAC=14336, ADC Noise vs 18*Chip+Chan



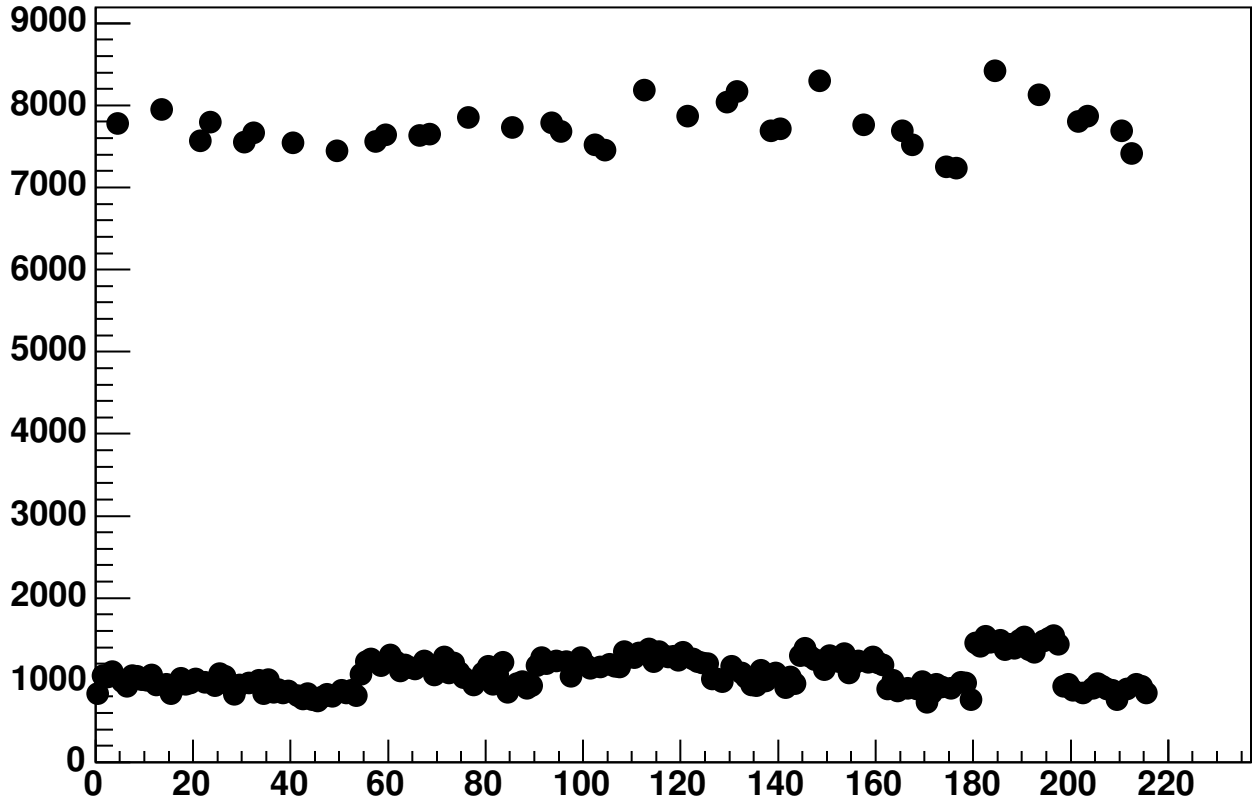
Enable 4, Hold=30, DAC=12288, ADC Mean vs 18*Chip+Chan



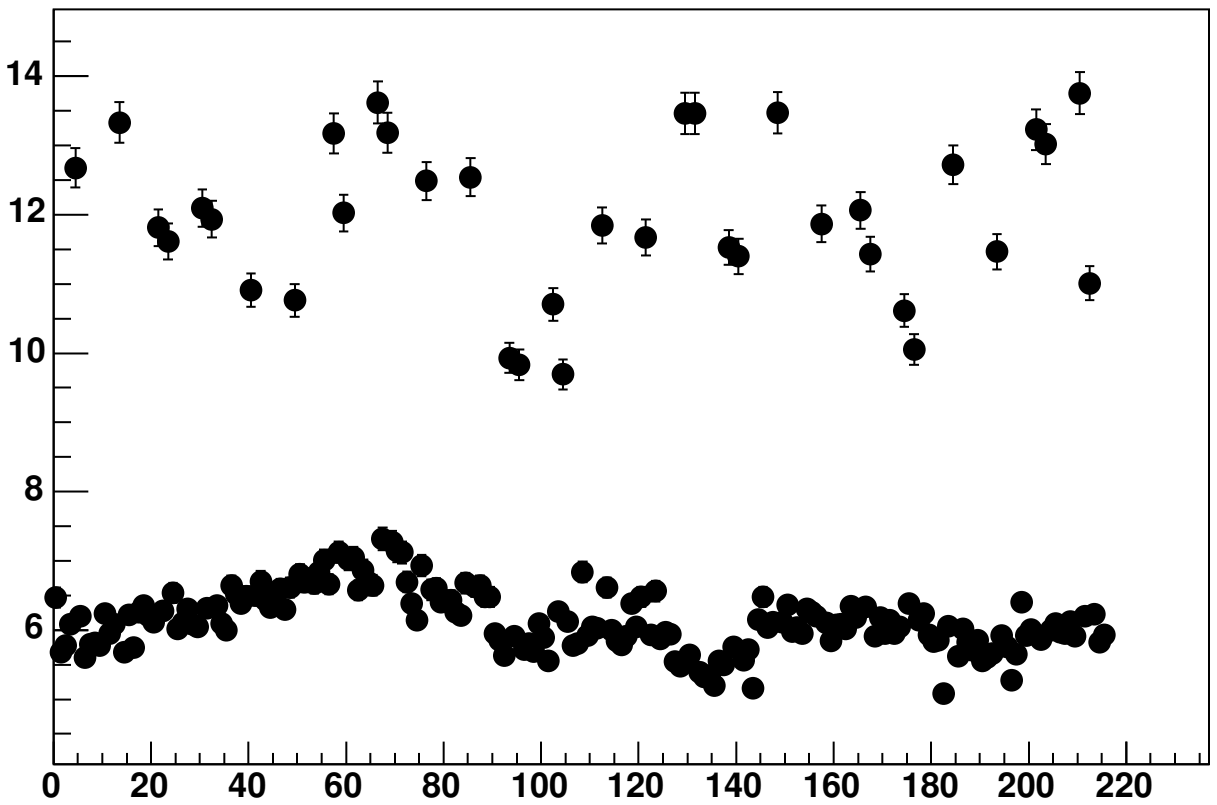
Enable 4, Hold=30, DAC=12288, ADC Noise vs 18*Chip+Chan



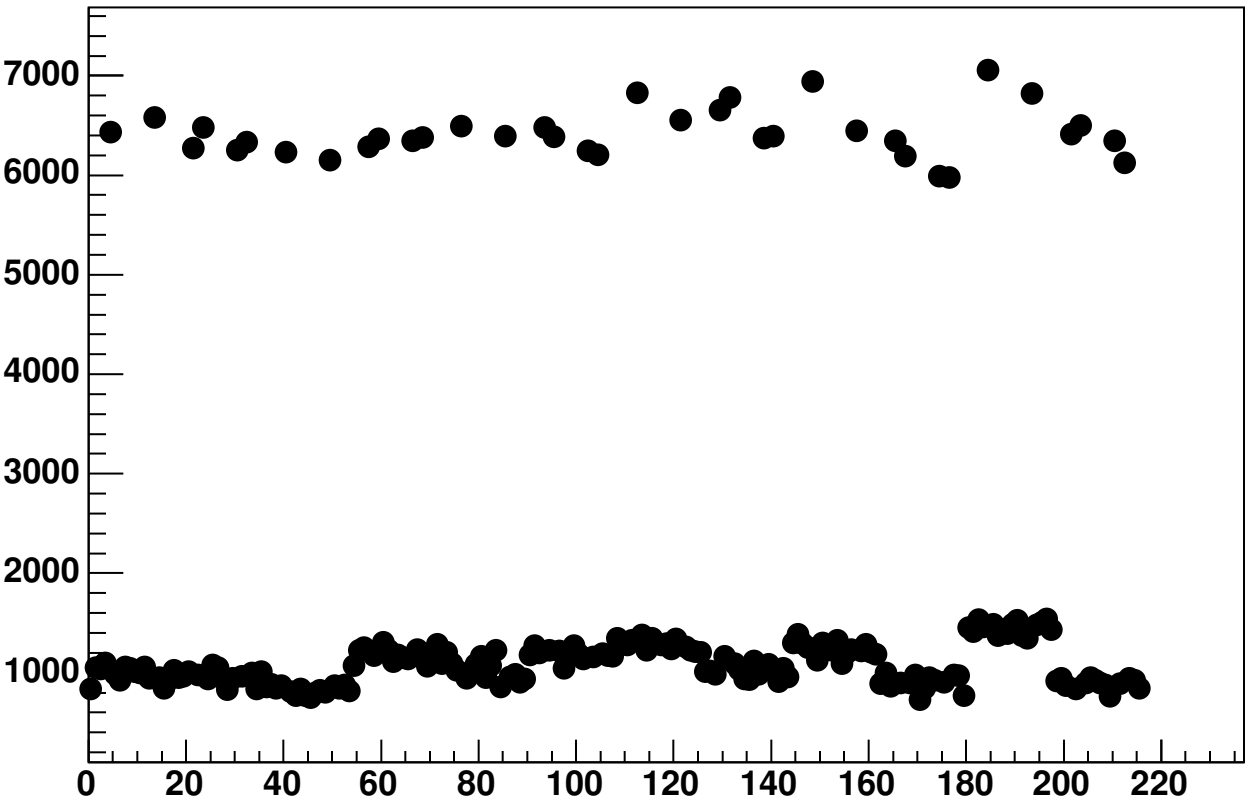
Enable 4, Hold=30, DAC=10240, ADC Mean vs 18*Chip+Chan



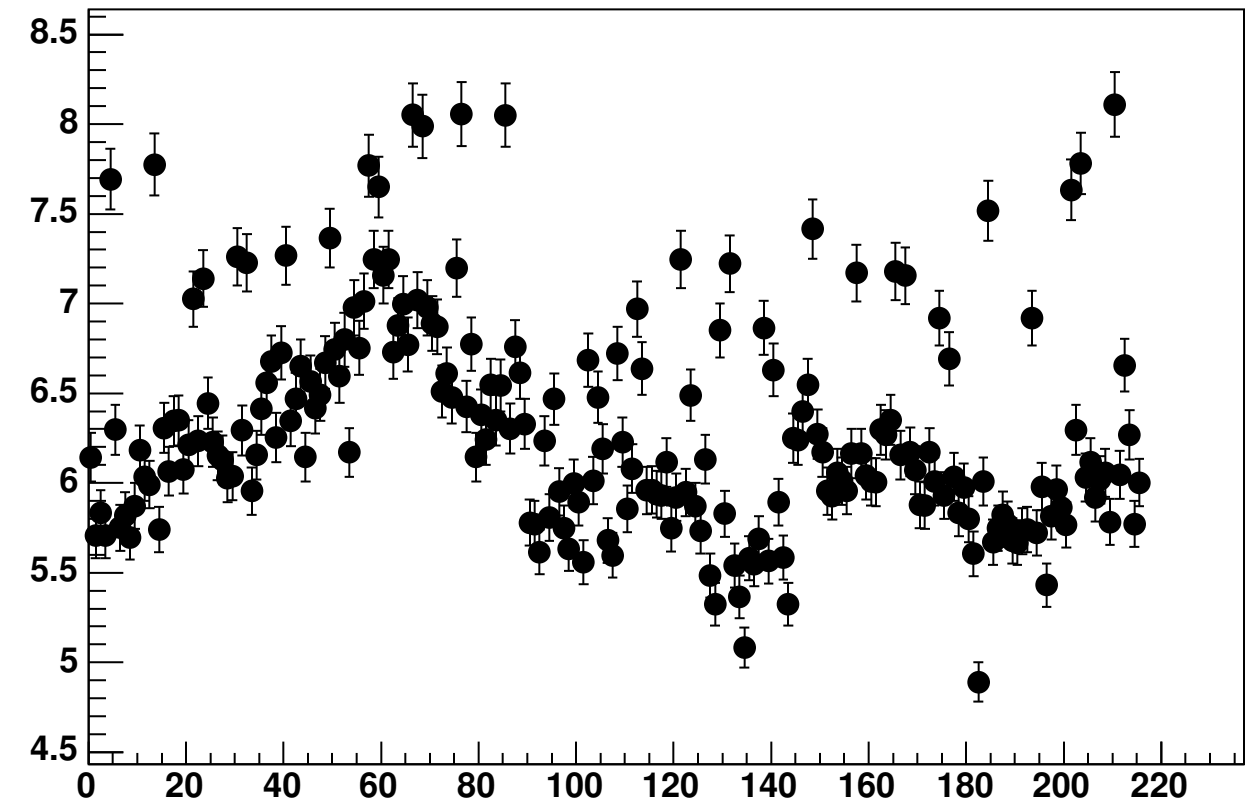
Enable 4, Hold=30, DAC=10240, ADC Noise vs 18*Chip+Chan



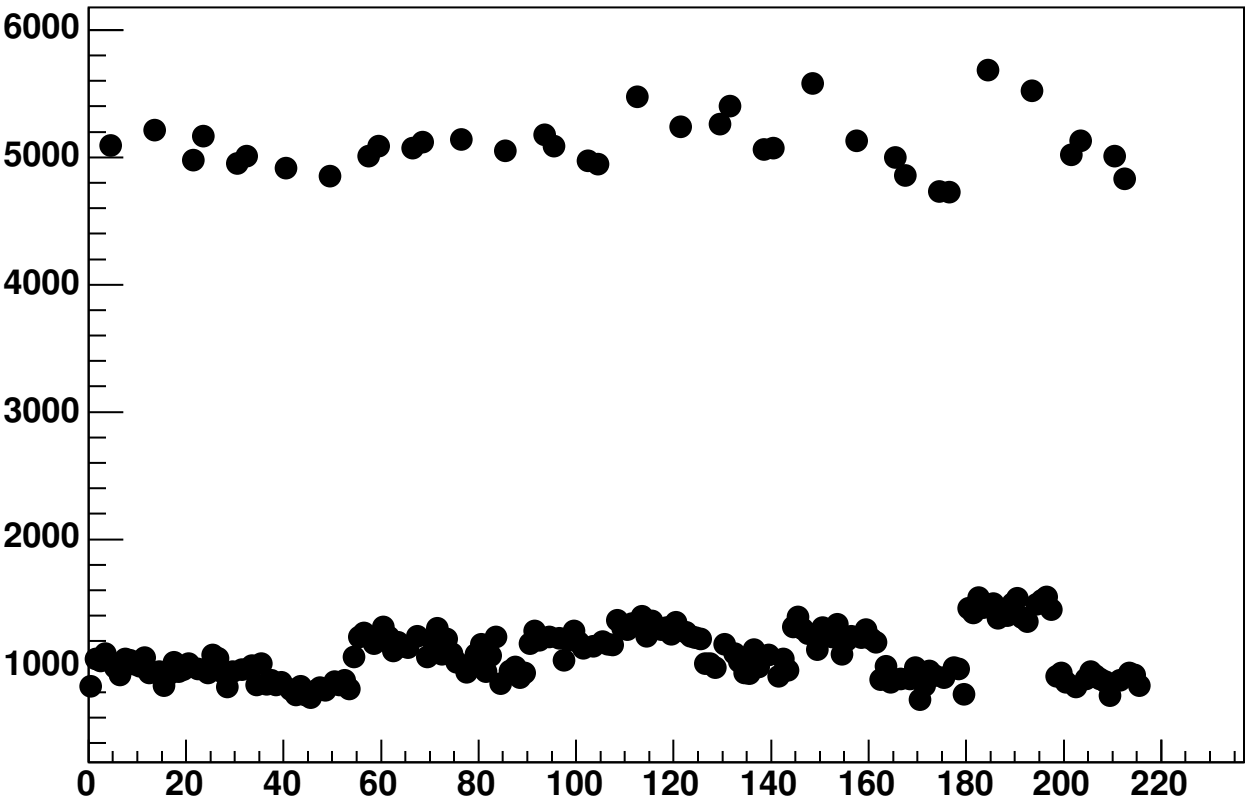
Enable 4, Hold=30, DAC=8192, ADC Mean vs 18*Chip+Chan



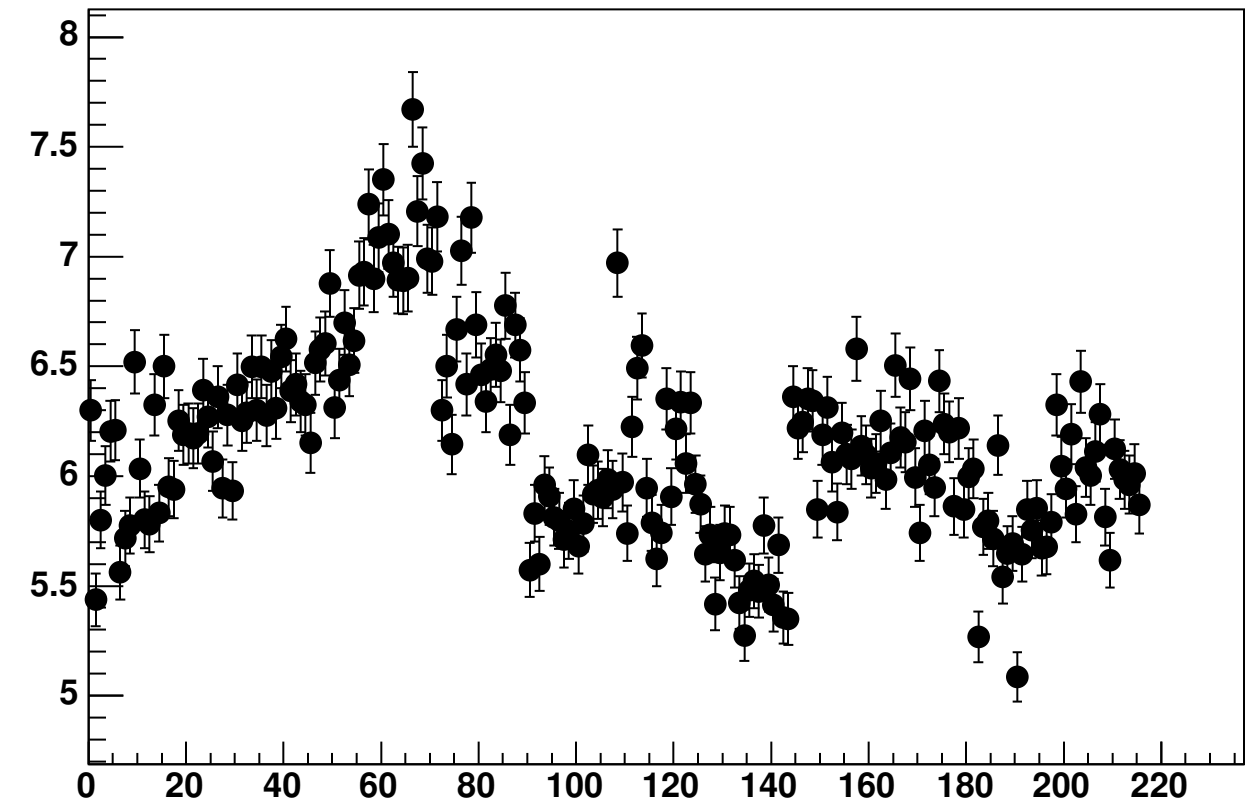
Enable 4, Hold=30, DAC=8192, ADC Noise vs 18*Chip+Chan



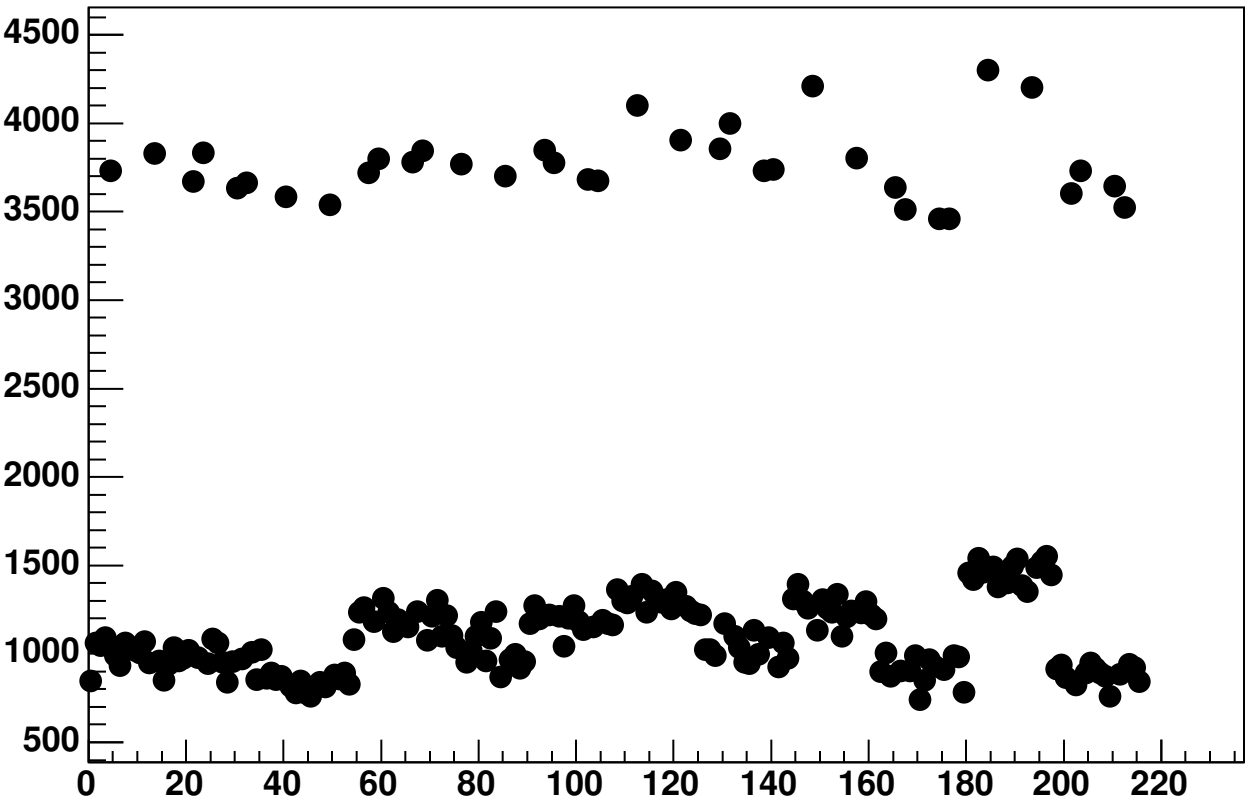
Enable 4, Hold=30, DAC=6144, ADC Mean vs 18*Chip+Chan



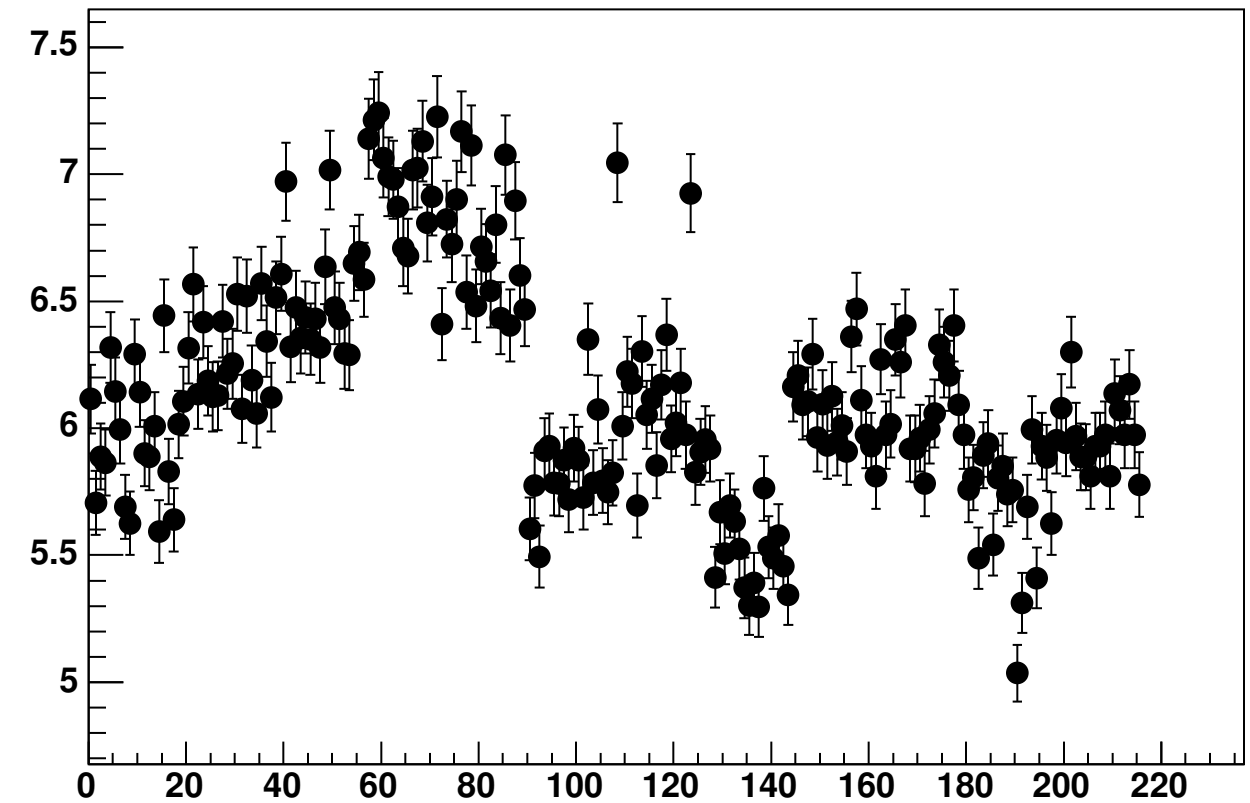
Enable 4, Hold=30, DAC=6144, ADC Noise vs 18*Chip+Chan



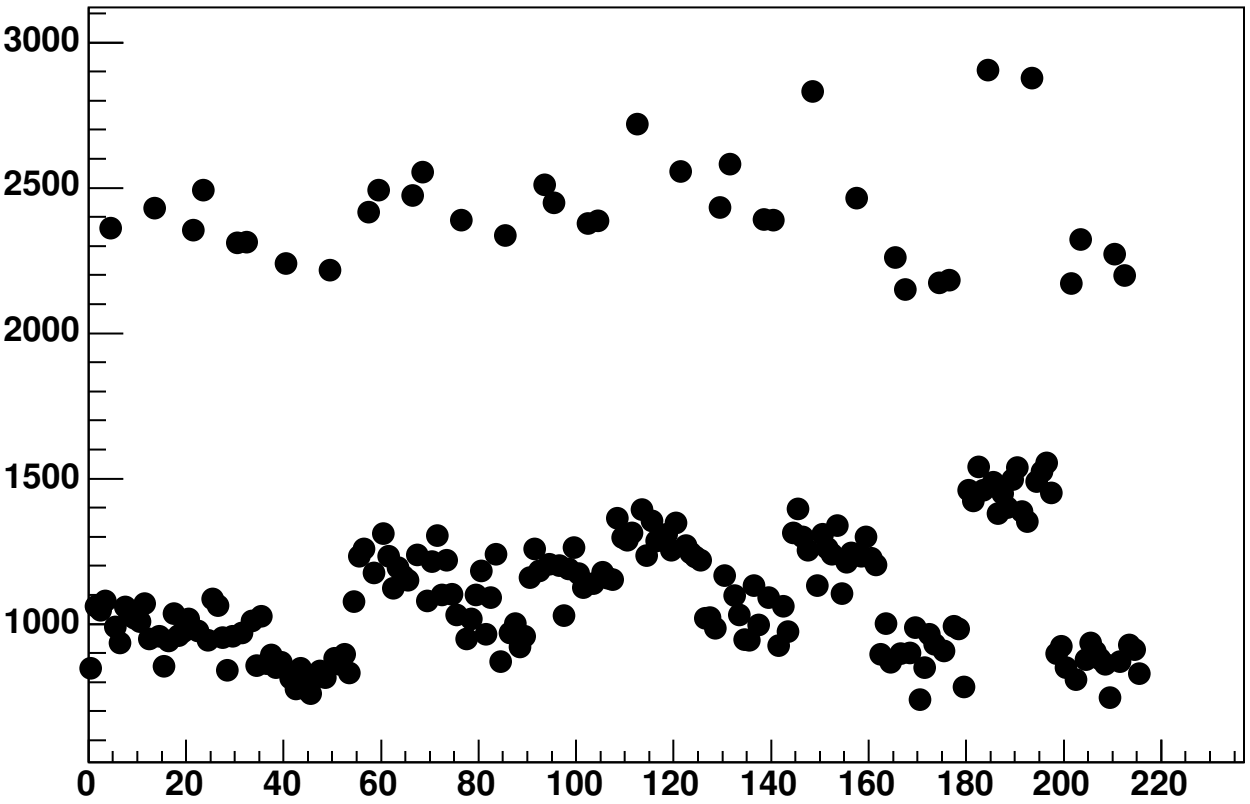
Enable 4, Hold=30, DAC=4096, ADC Mean vs 18*Chip+Chan



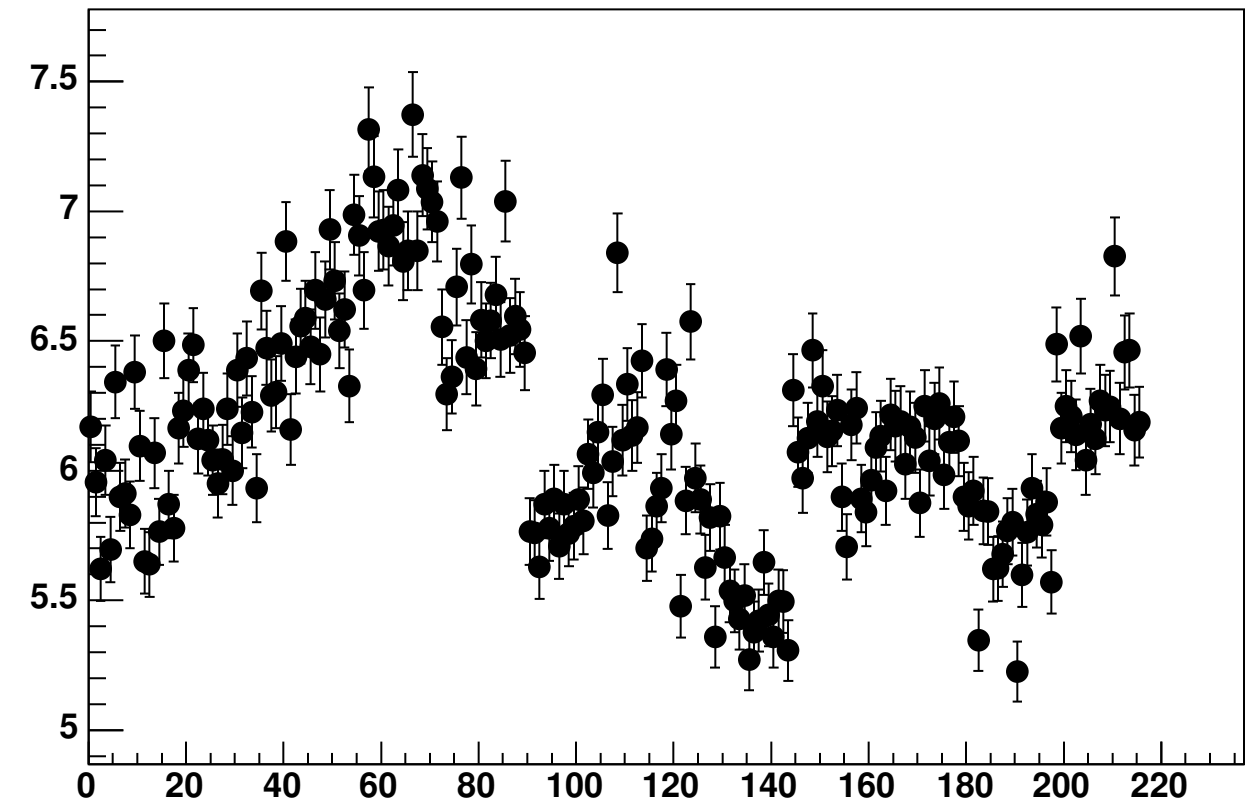
Enable 4, Hold=30, DAC=4096, ADC Noise vs 18*Chip+Chan



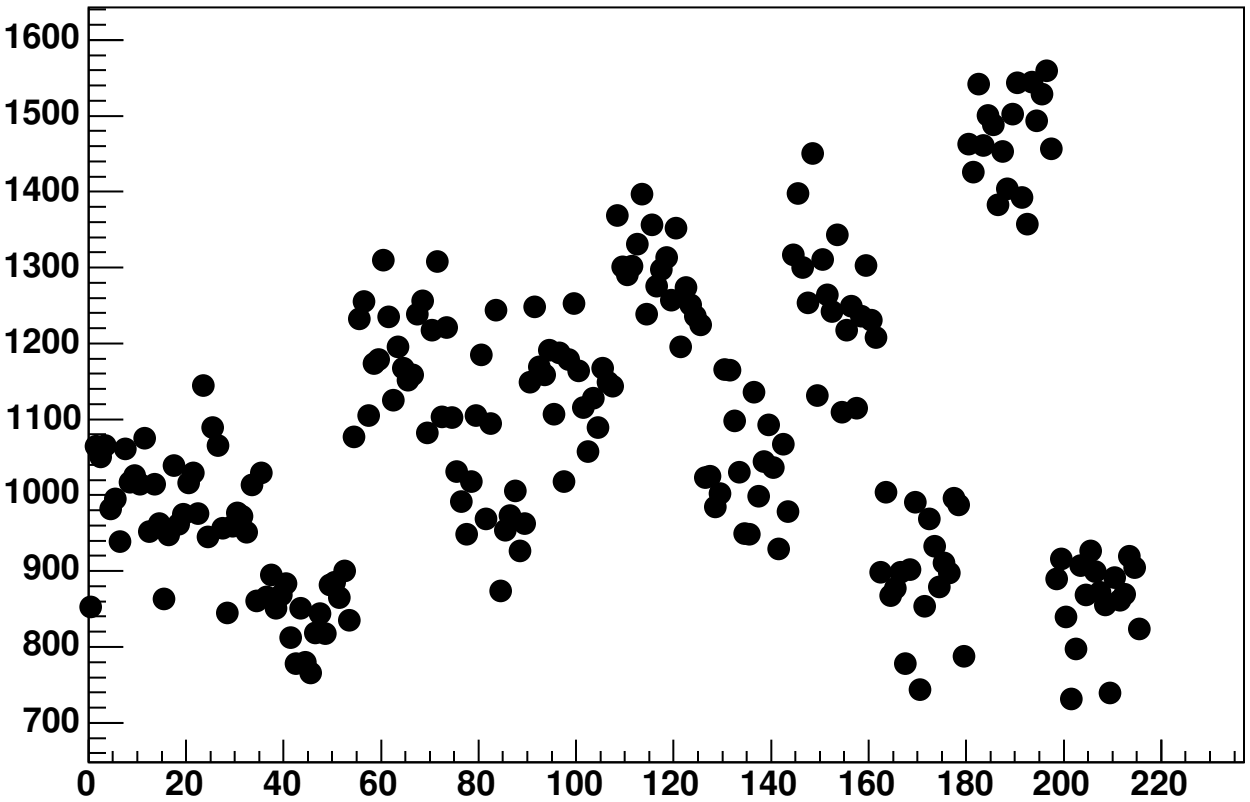
Enable 4, Hold=30, DAC=2048, ADC Mean vs 18*Chip+Chan



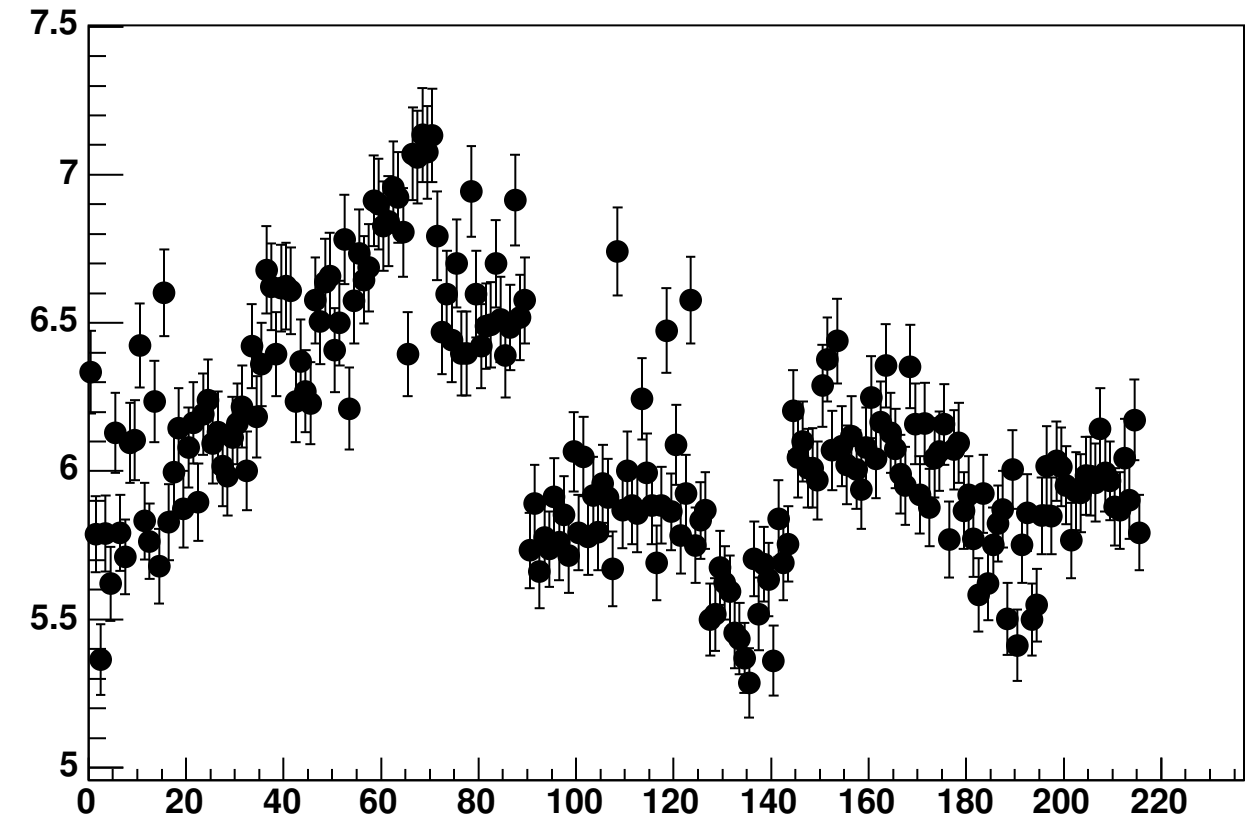
Enable 4, Hold=30, DAC=2048, ADC Noise vs 18*Chip+Chan



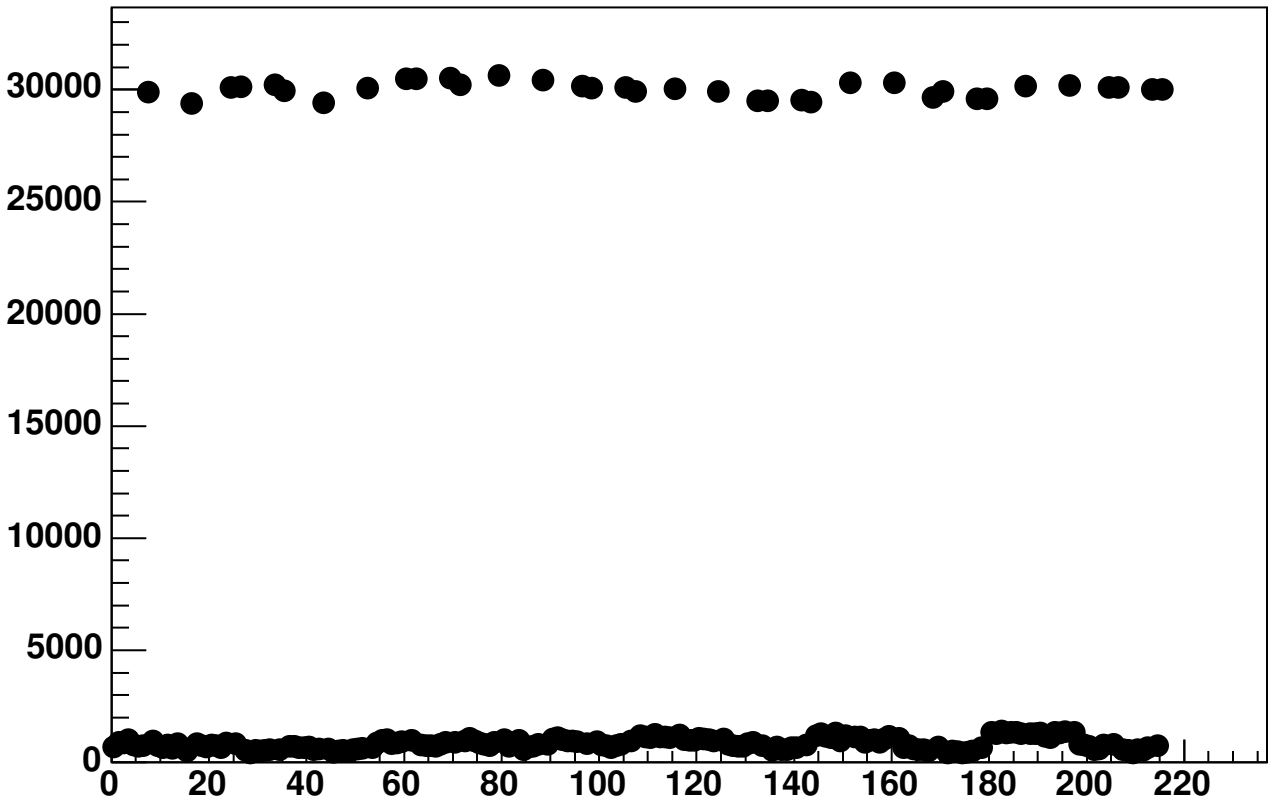
Enable 4, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



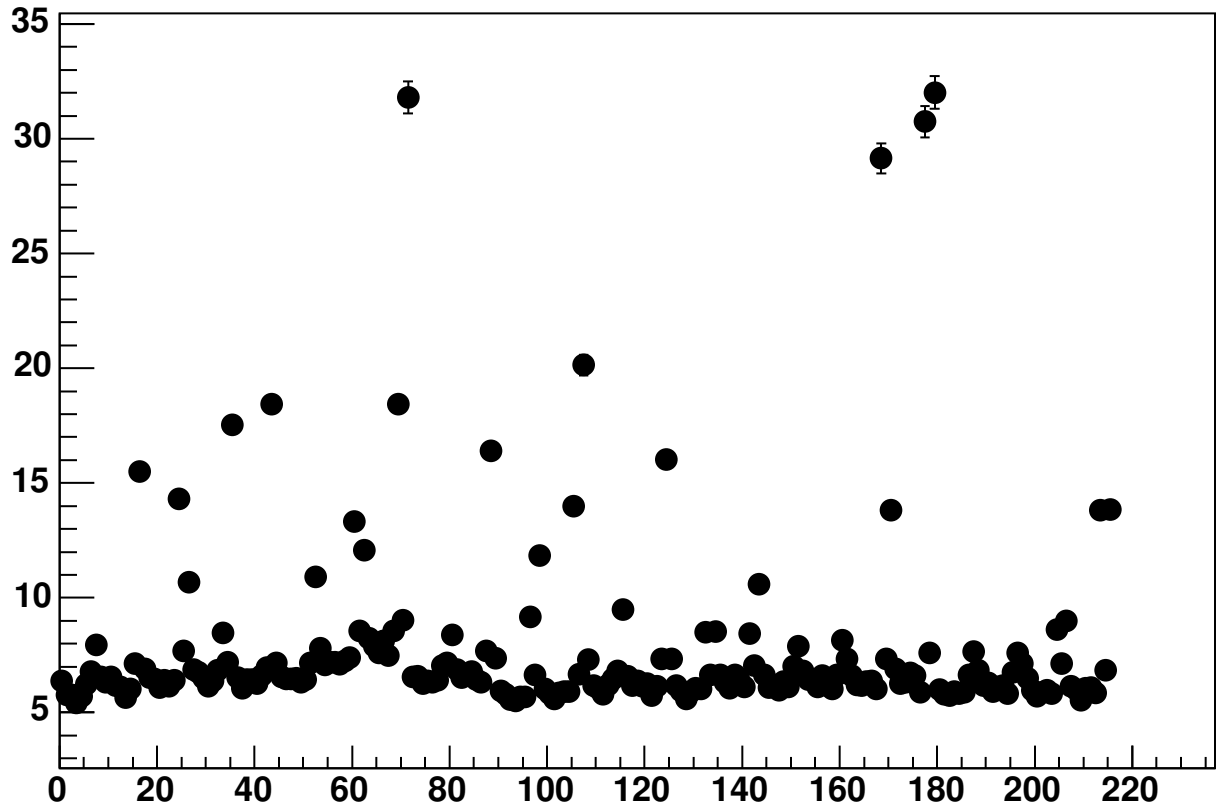
Enable 4, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



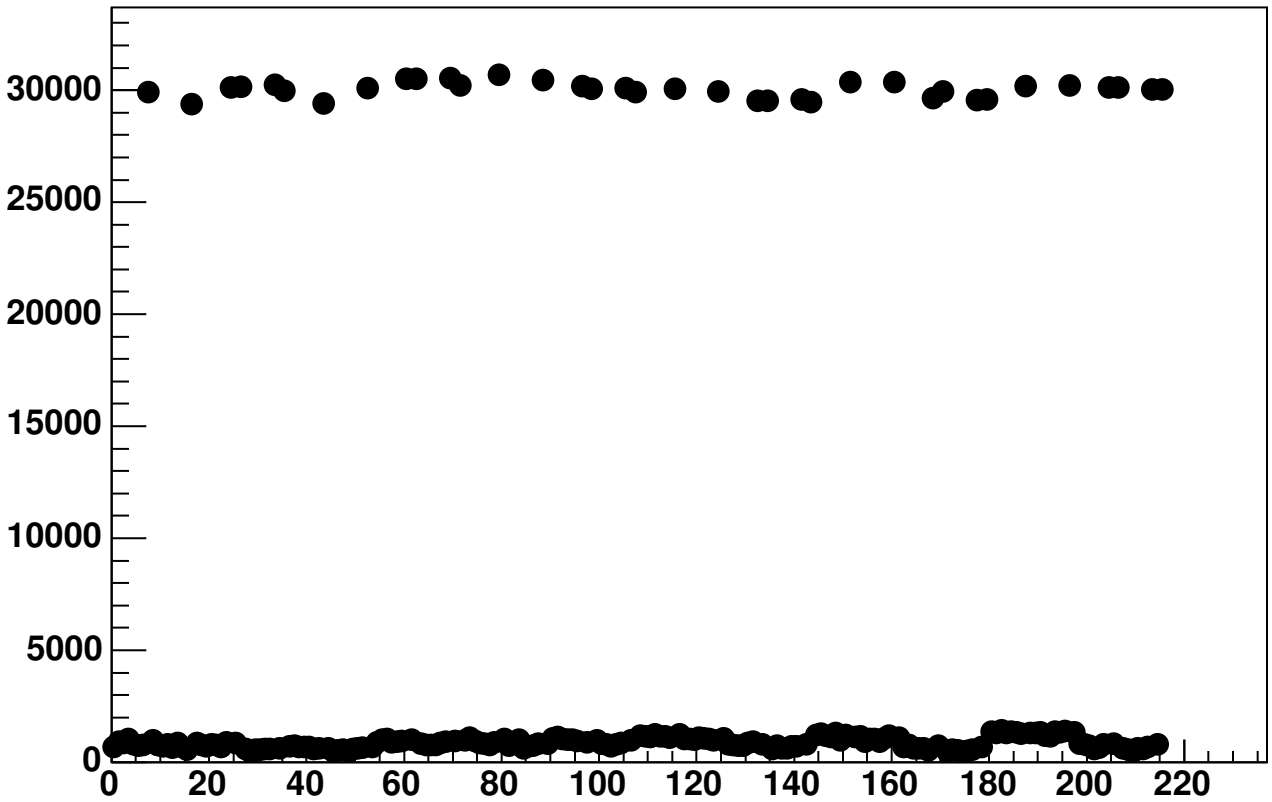
Enable 5, Hold=30, DAC=63488, ADC Mean vs 18*Chip+Chan



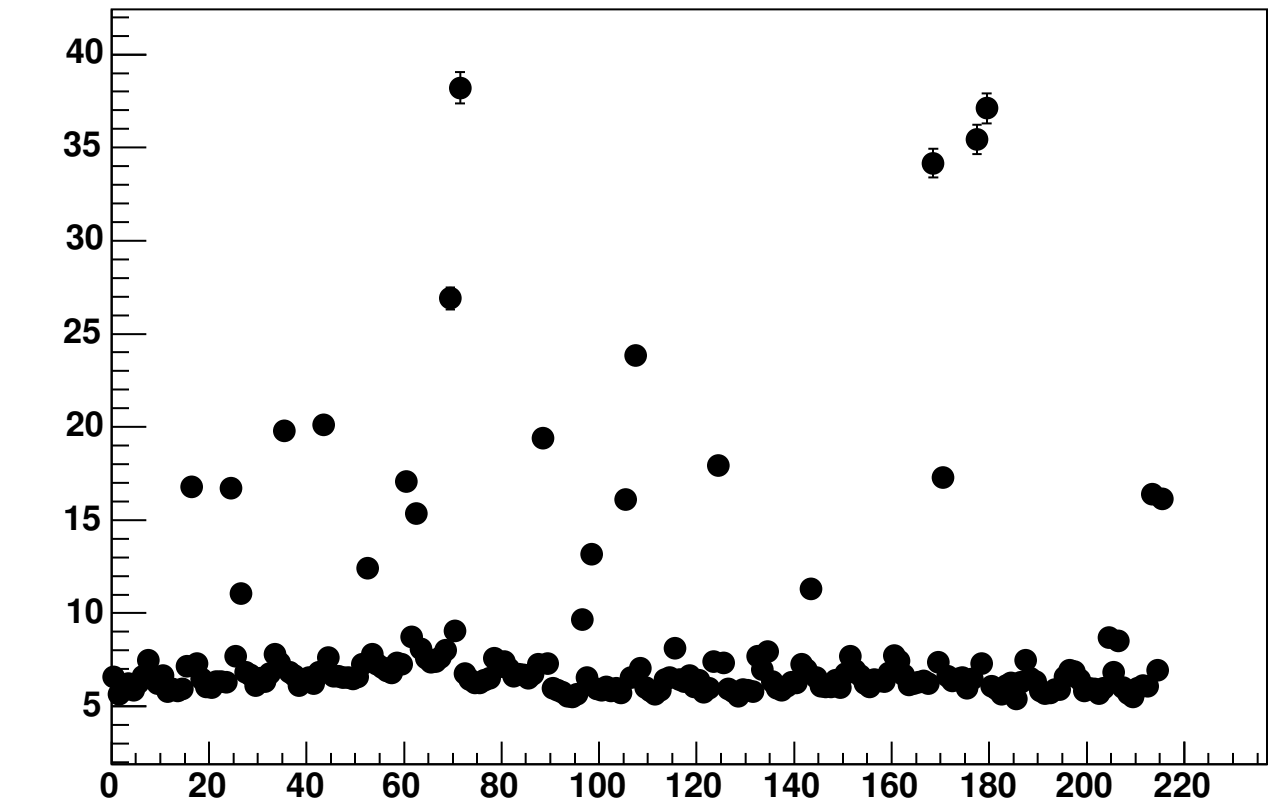
Enable 5, Hold=30, DAC=63488, ADC Noise vs 18*Chip+Chan



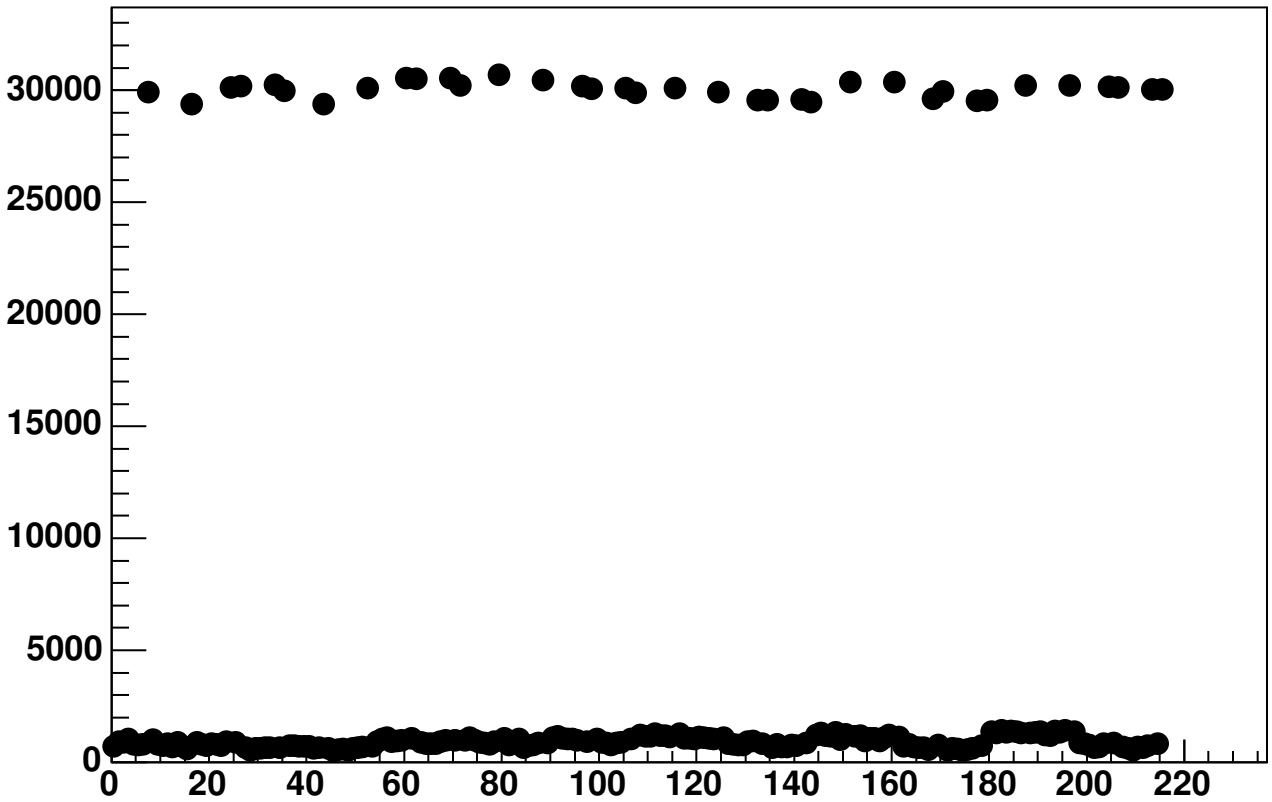
Enable 5, Hold=30, DAC=61440, ADC Mean vs 18*Chip+Chan



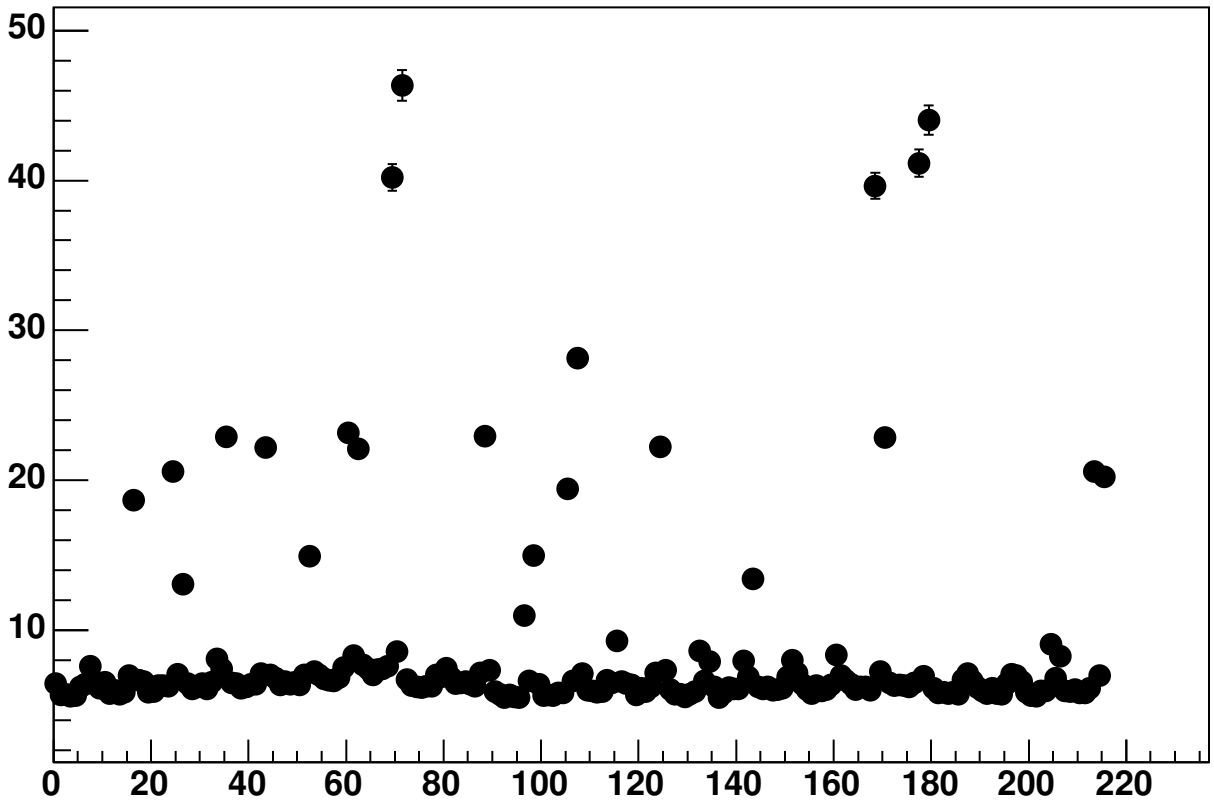
Enable 5, Hold=30, DAC=61440, ADC Noise vs 18*Chip+Chan



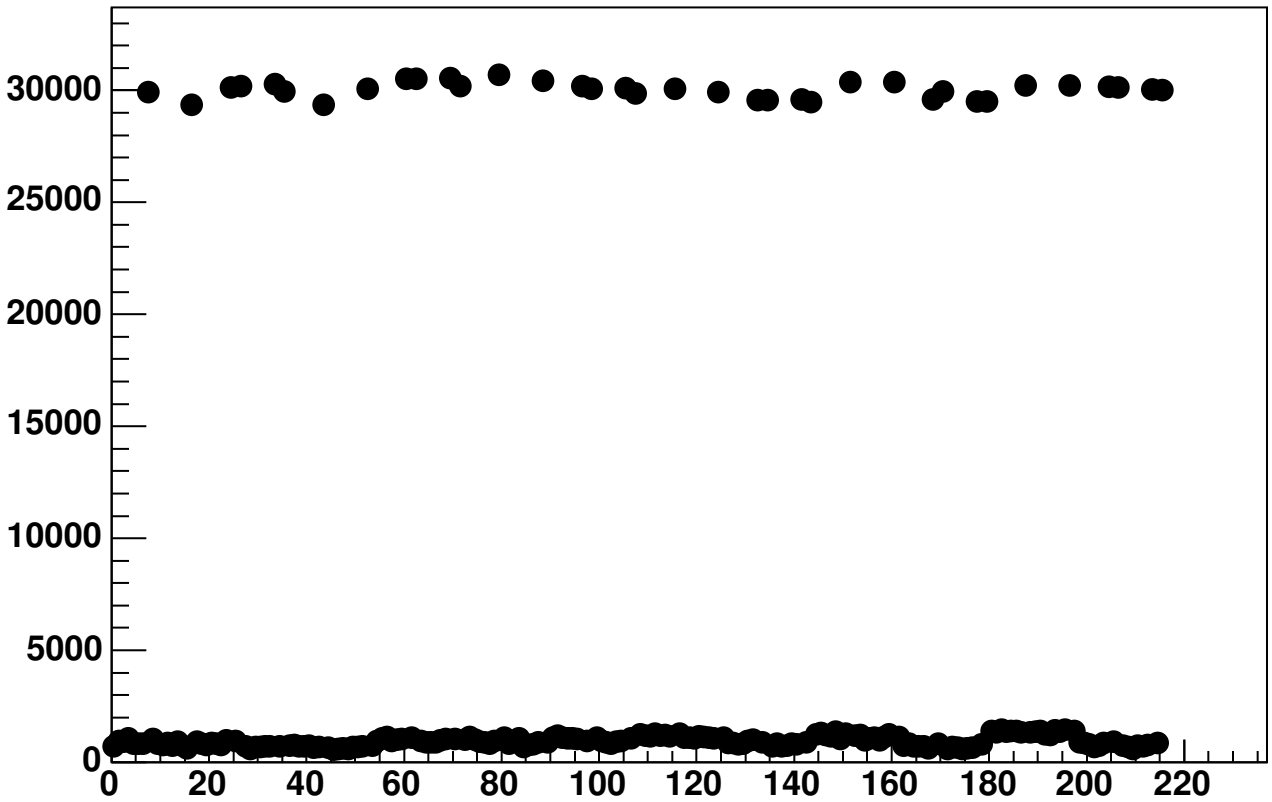
Enable 5, Hold=30, DAC=59392, ADC Mean vs 18*Chip+Chan



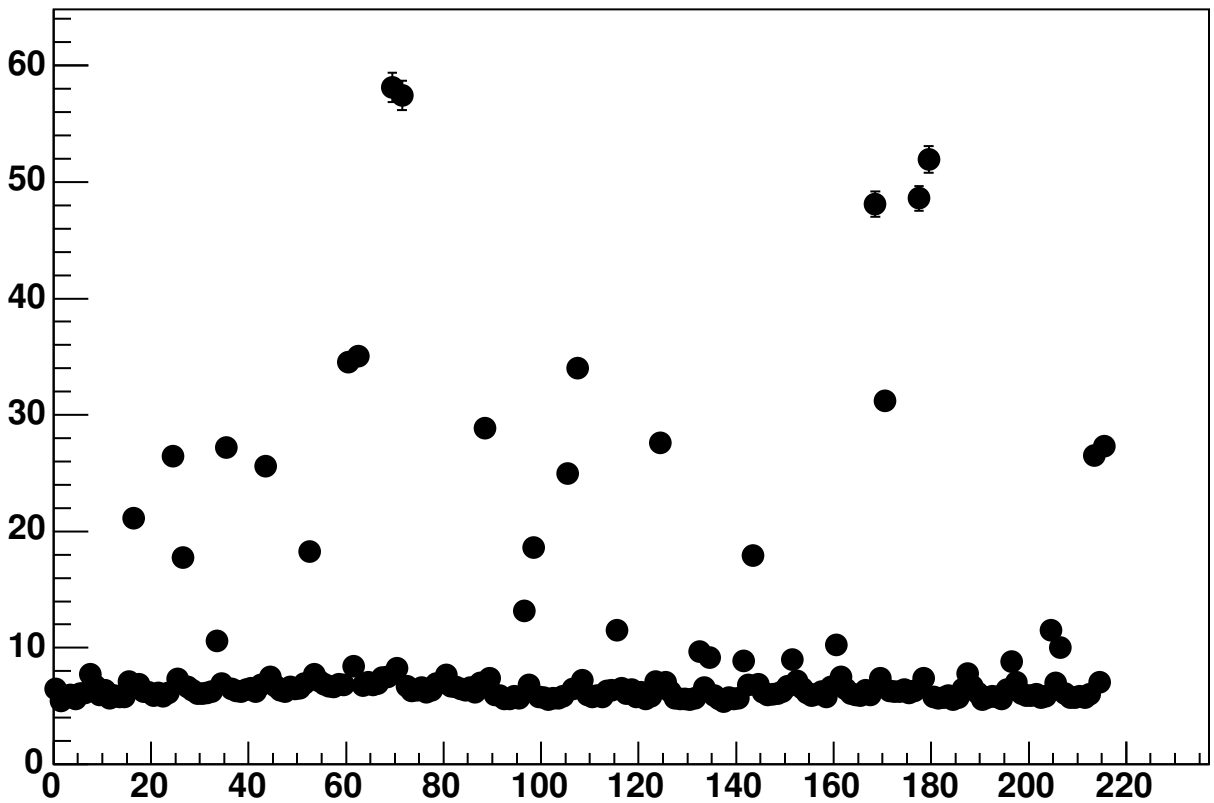
Enable 5, Hold=30, DAC=59392, ADC Noise vs 18*Chip+Chan



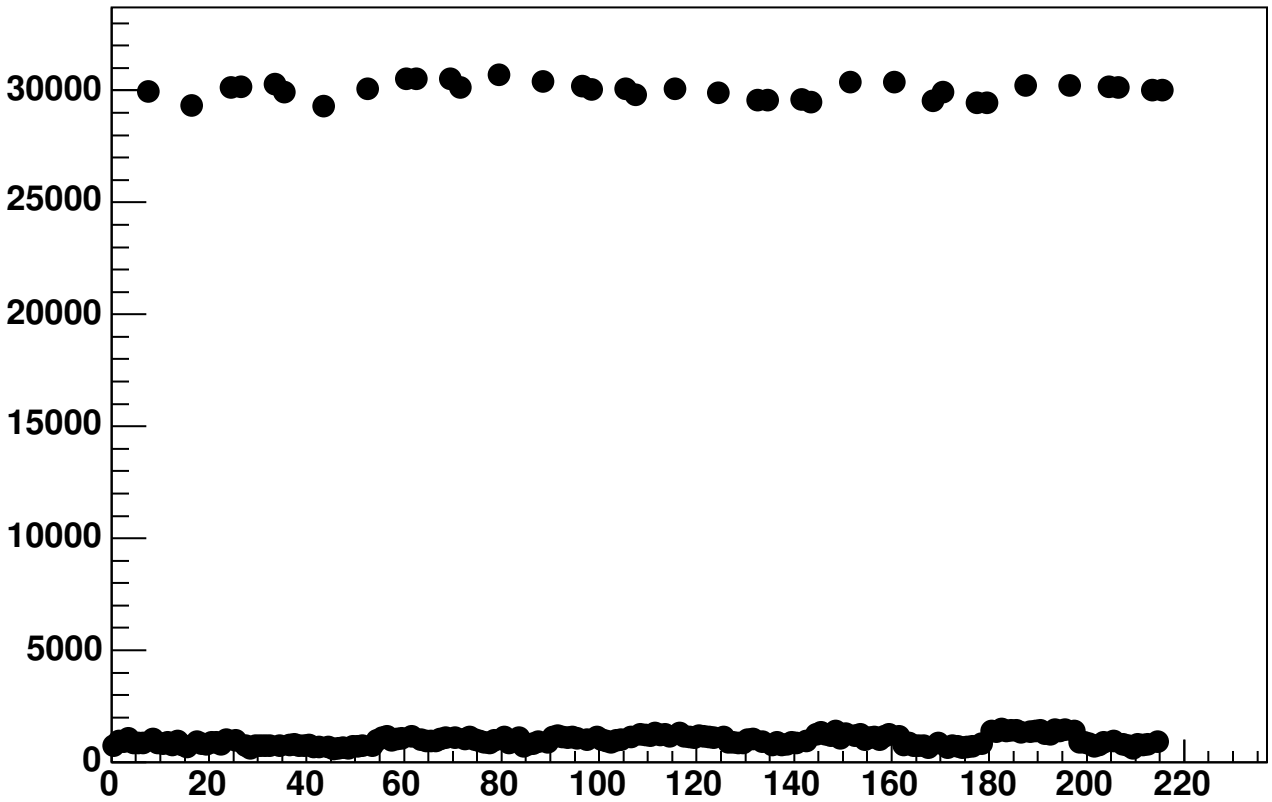
Enable 5, Hold=30, DAC=57344, ADC Mean vs 18*Chip+Chan



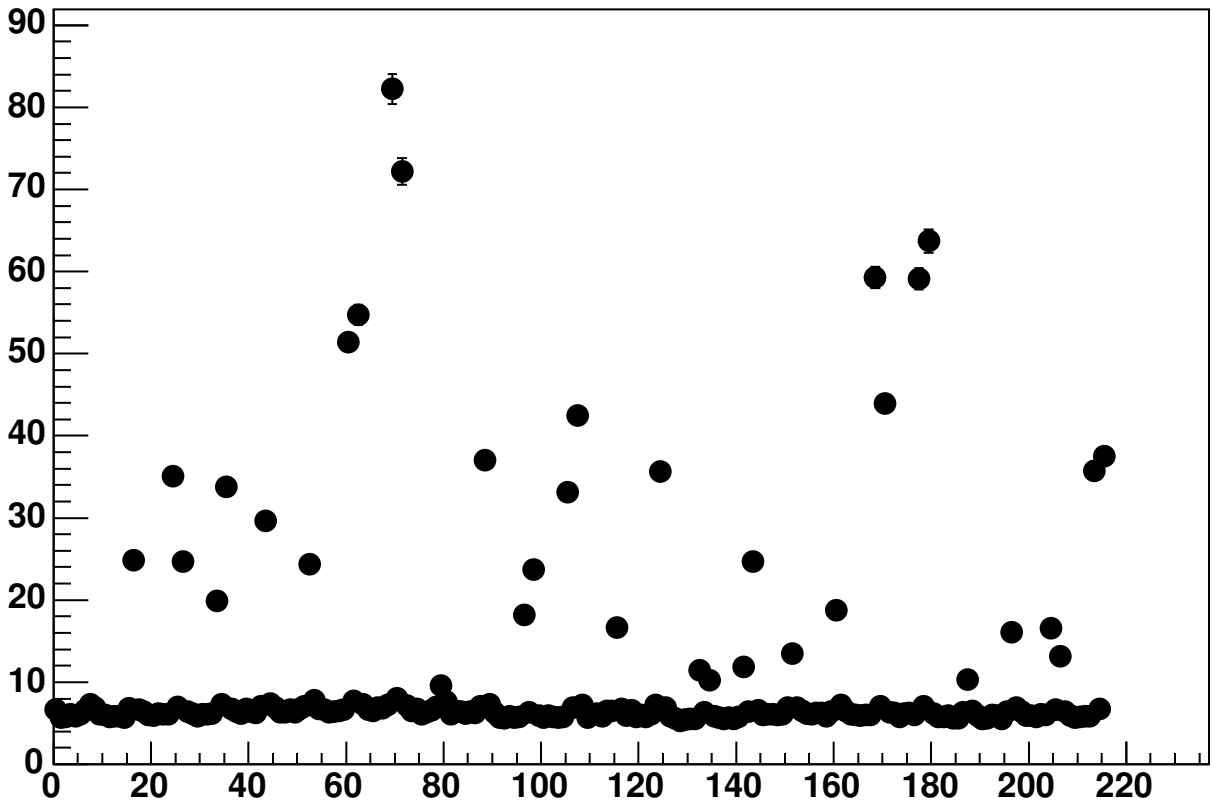
Enable 5, Hold=30, DAC=57344, ADC Noise vs 18*Chip+Chan



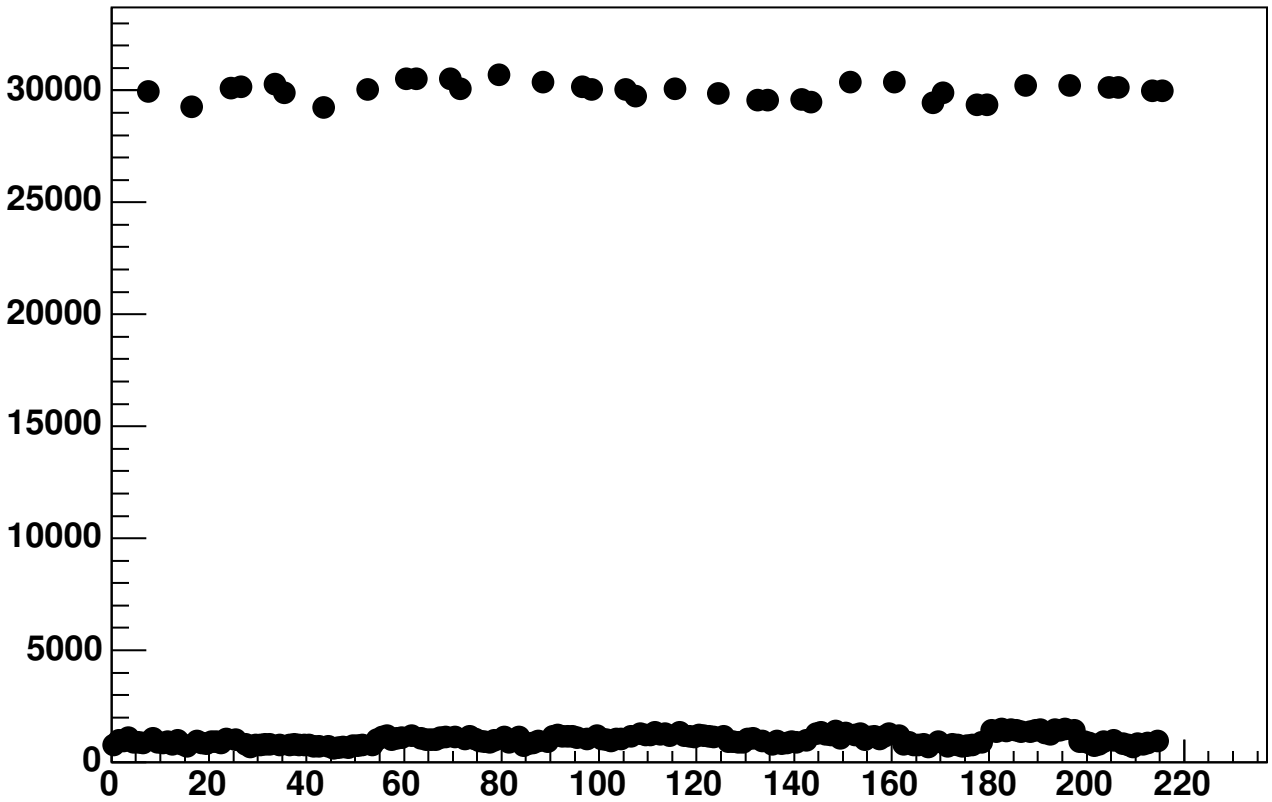
Enable 5, Hold=30, DAC=55296, ADC Mean vs 18*Chip+Chan



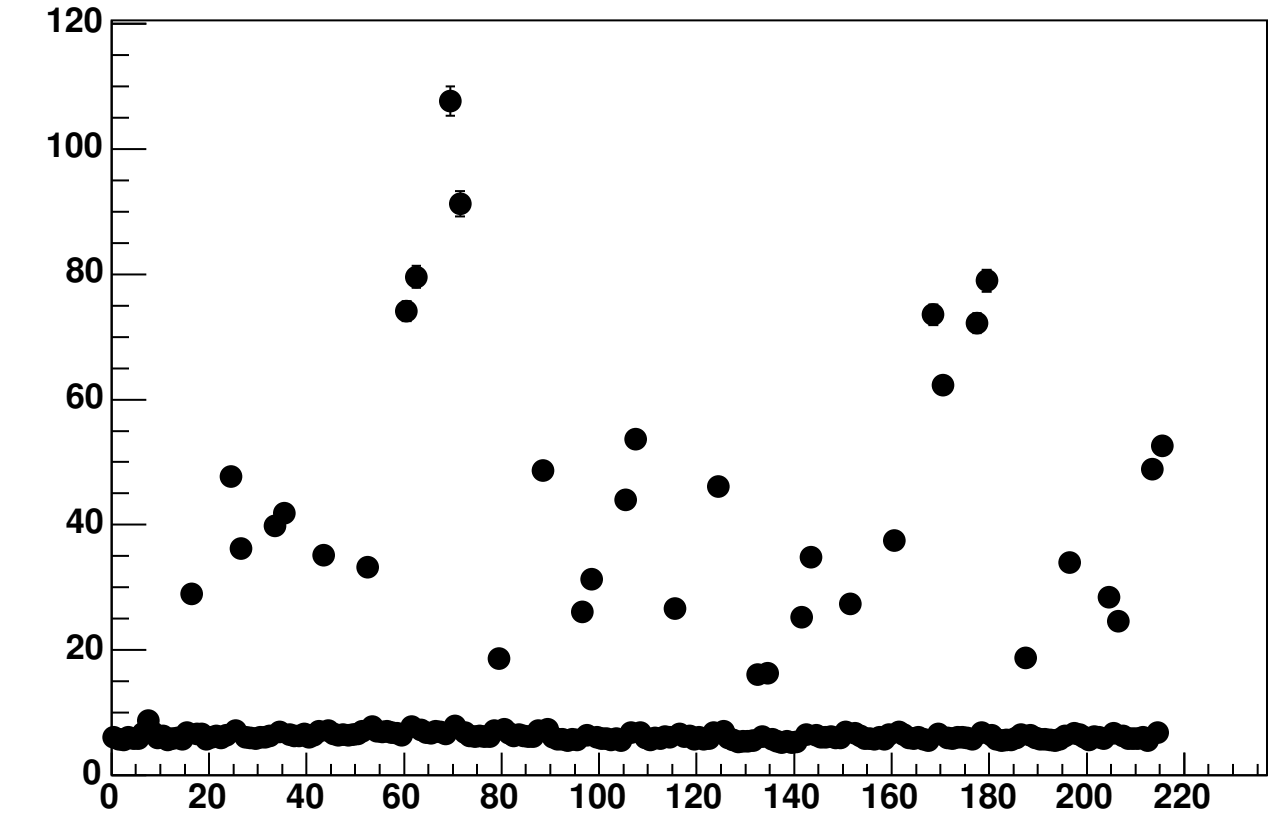
Enable 5, Hold=30, DAC=55296, ADC Noise vs 18*Chip+Chan



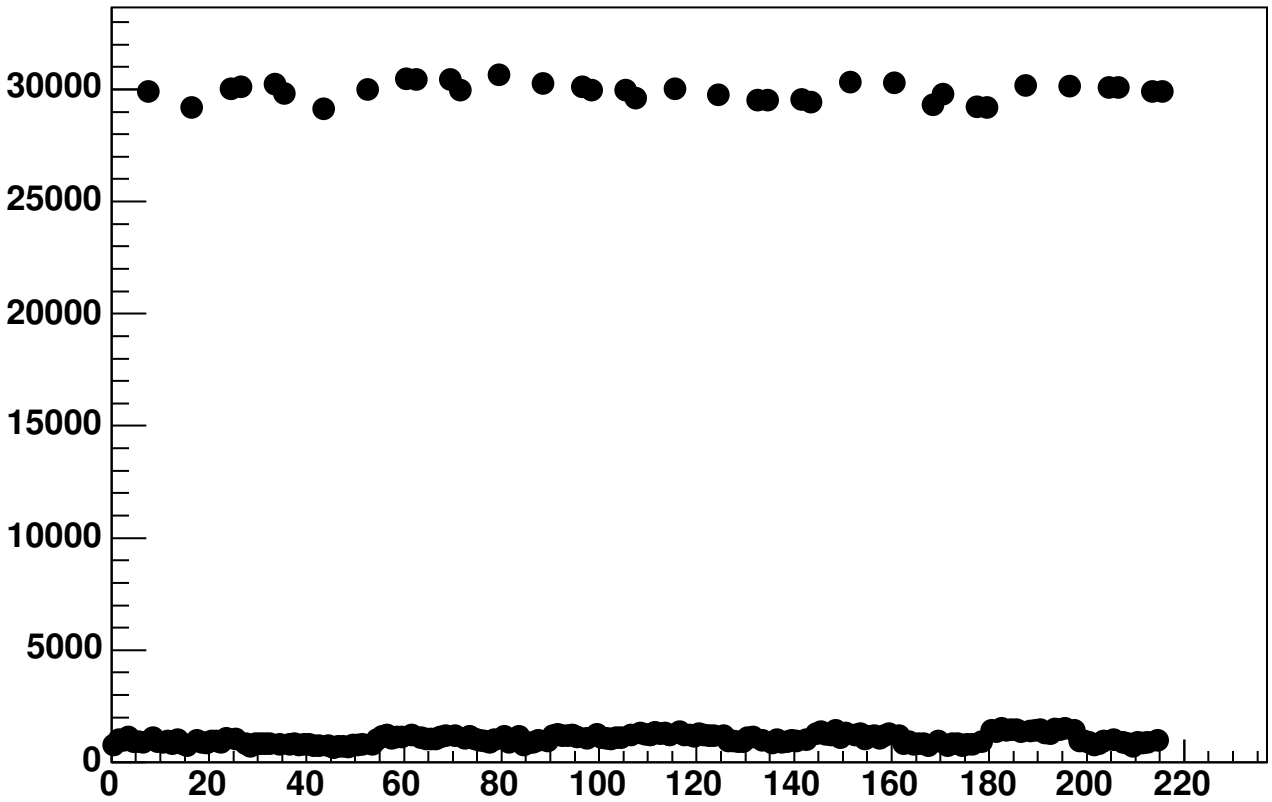
Enable 5, Hold=30, DAC=53248, ADC Mean vs 18*Chip+Chan



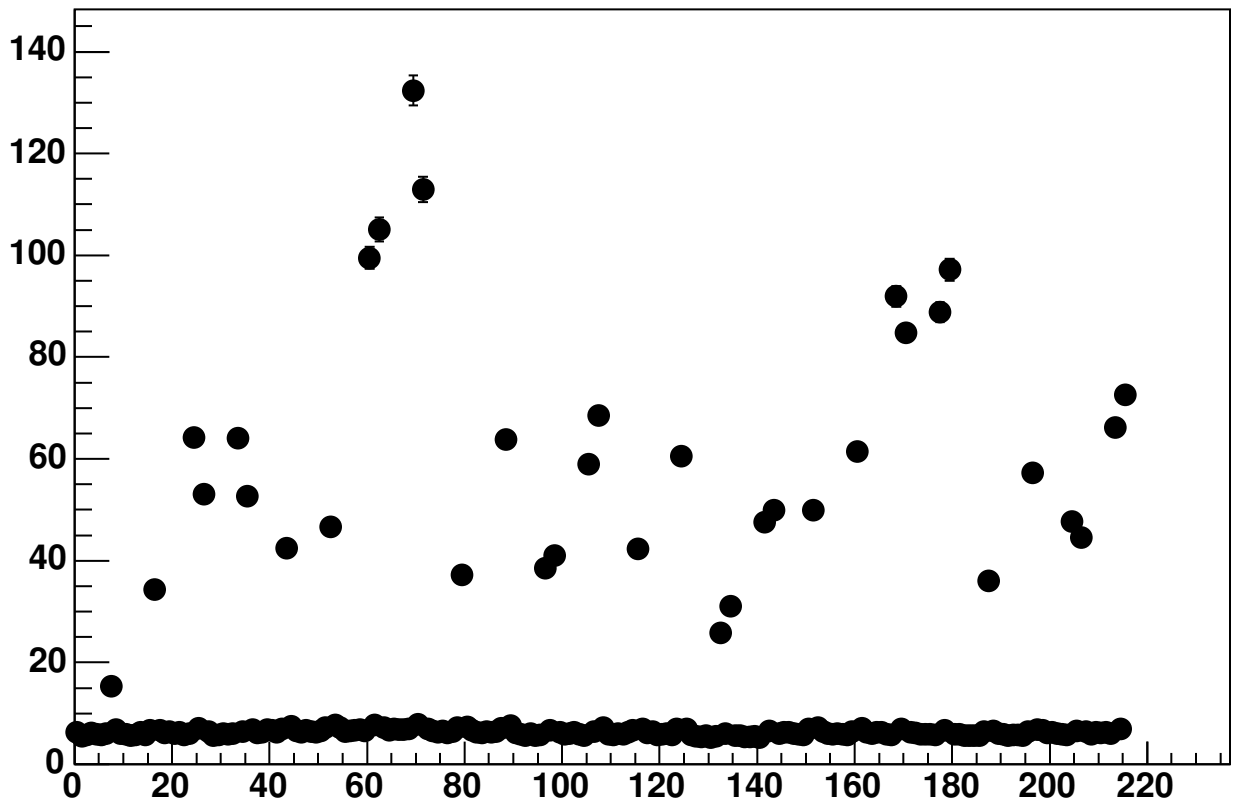
Enable 5, Hold=30, DAC=53248, ADC Noise vs 18*Chip+Chan



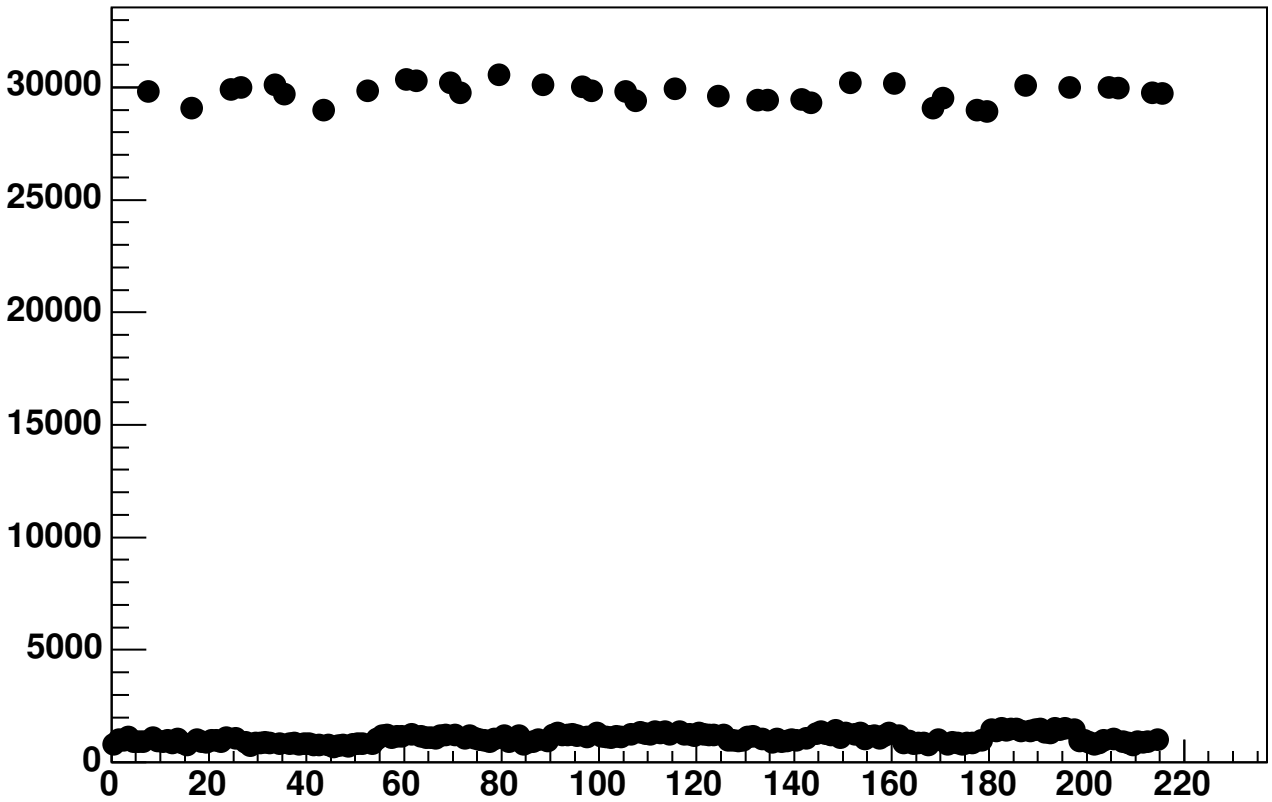
Enable 5, Hold=30, DAC=51200, ADC Mean vs 18*Chip+Chan



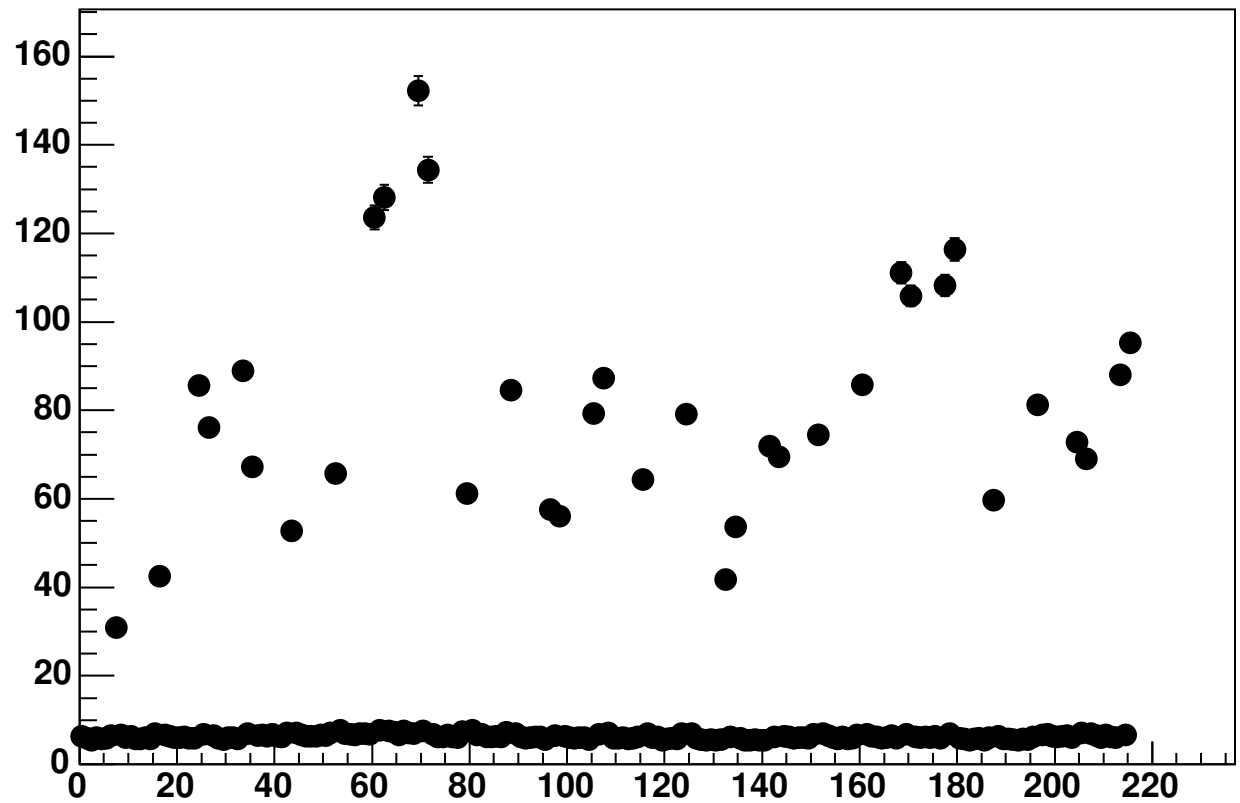
Enable 5, Hold=30, DAC=51200, ADC Noise vs 18*Chip+Chan



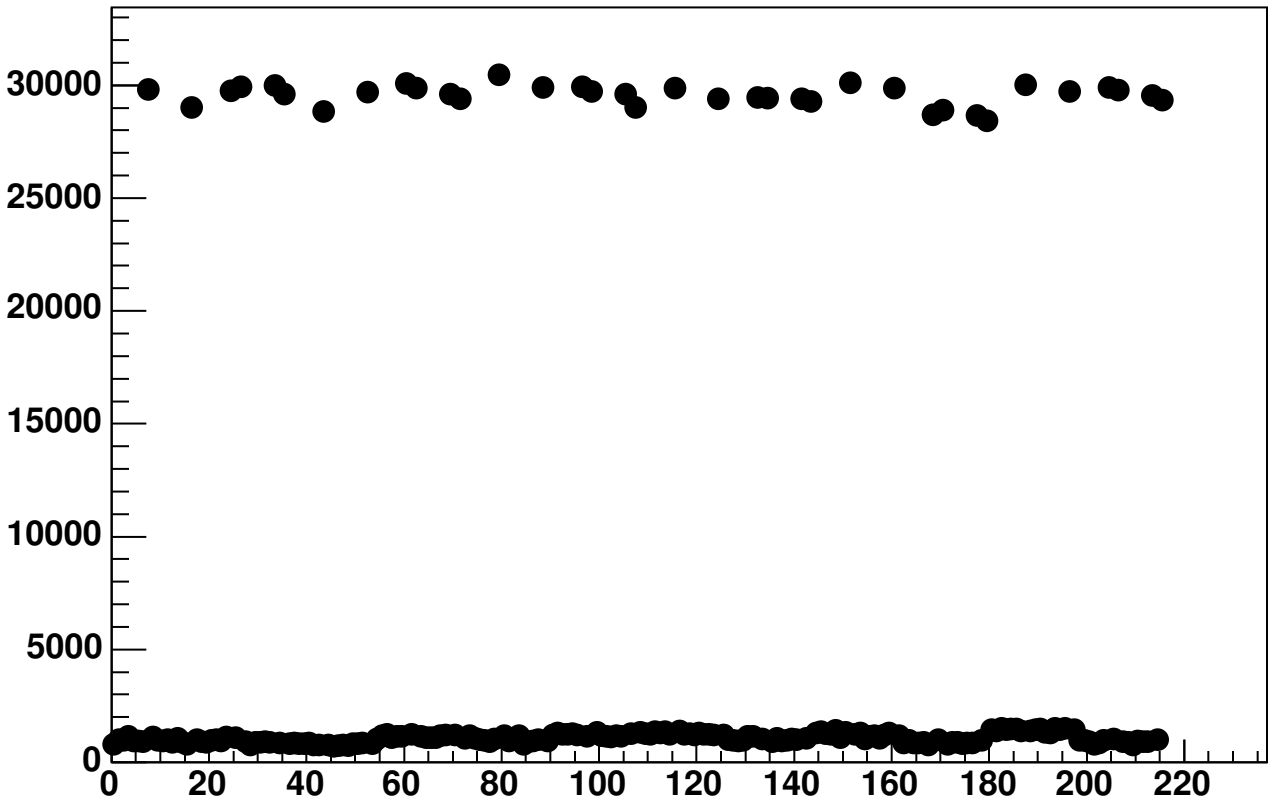
Enable 5, Hold=30, DAC=49152, ADC Mean vs 18*Chip+Chan



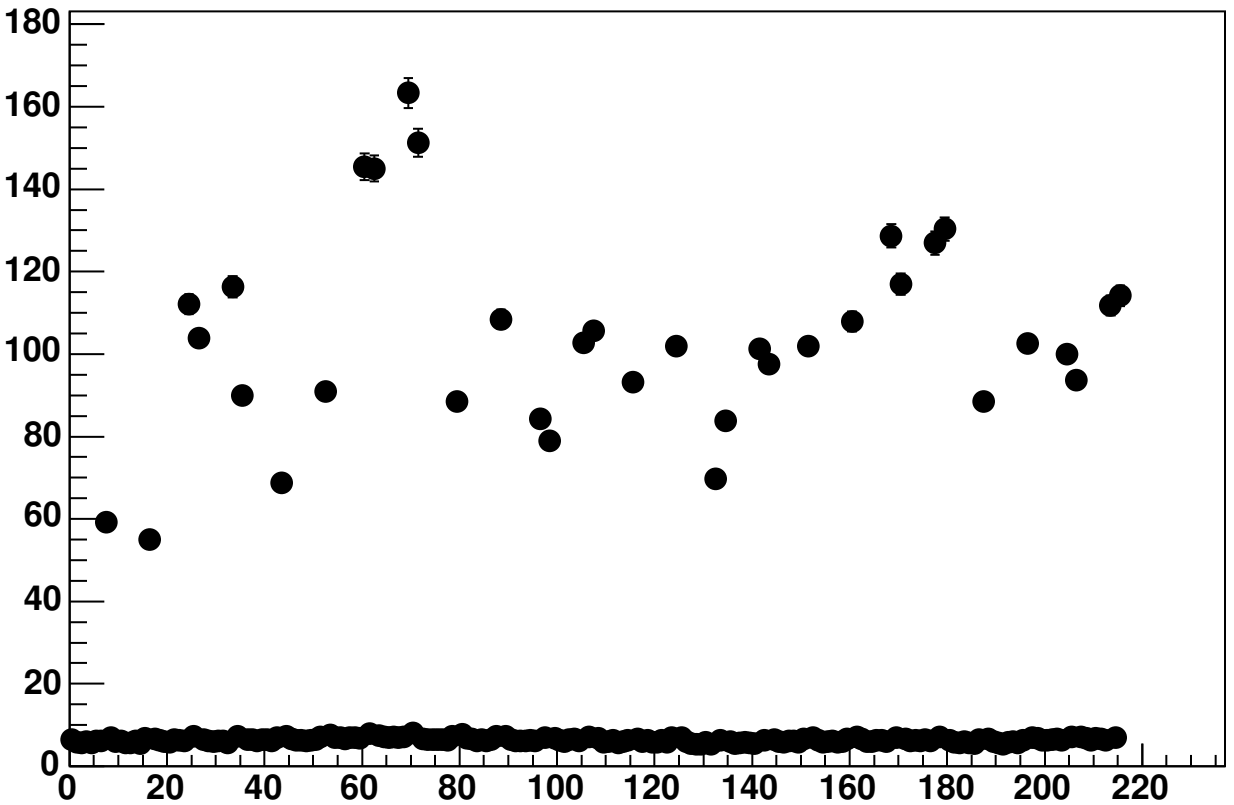
Enable 5, Hold=30, DAC=49152, ADC Noise vs 18*Chip+Chan



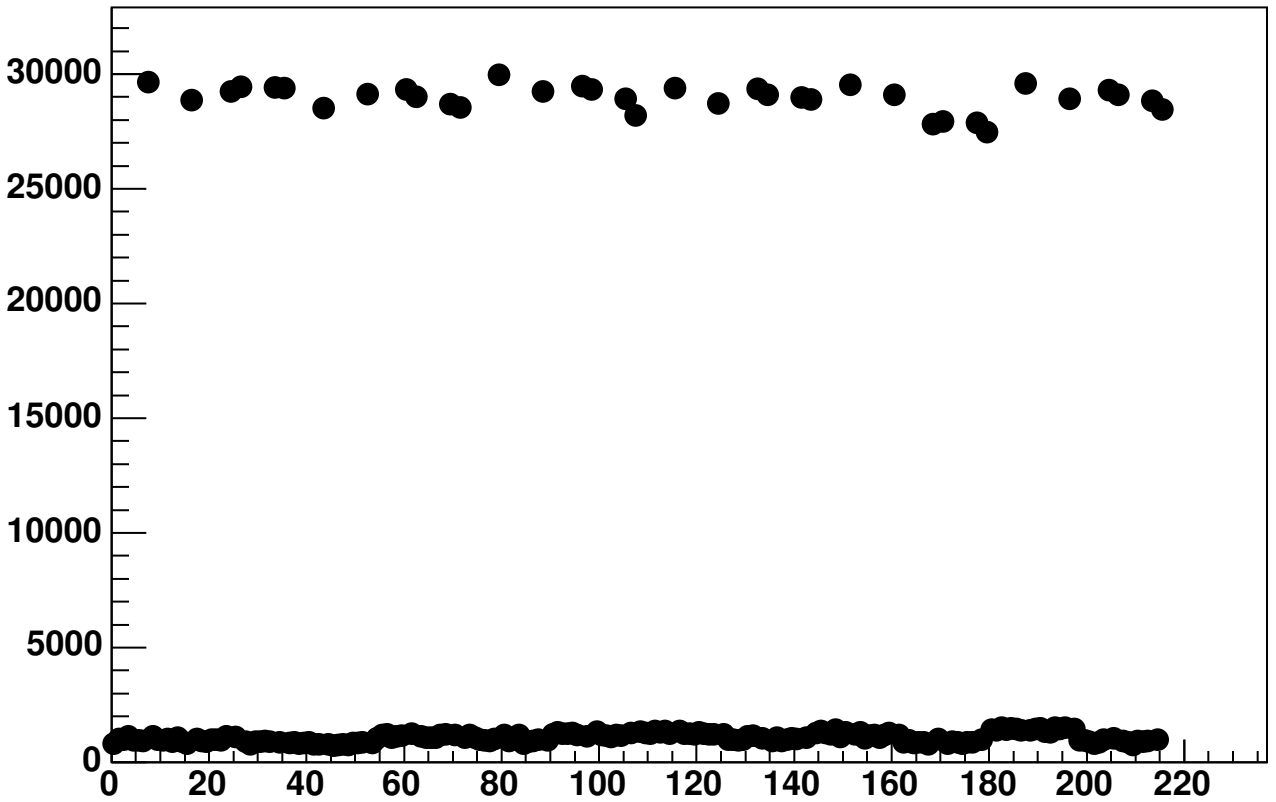
Enable 5, Hold=30, DAC=47104, ADC Mean vs 18*Chip+Chan



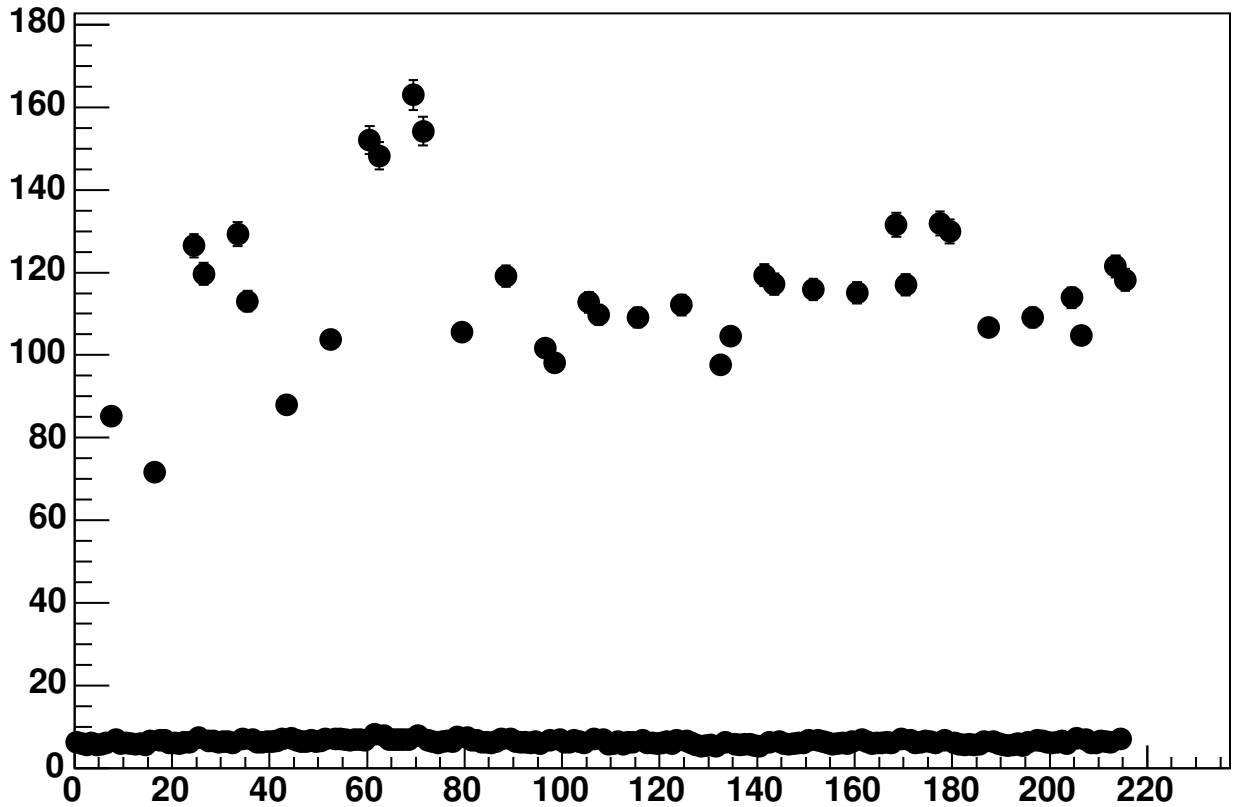
Enable 5, Hold=30, DAC=47104, ADC Noise vs 18*Chip+Chan



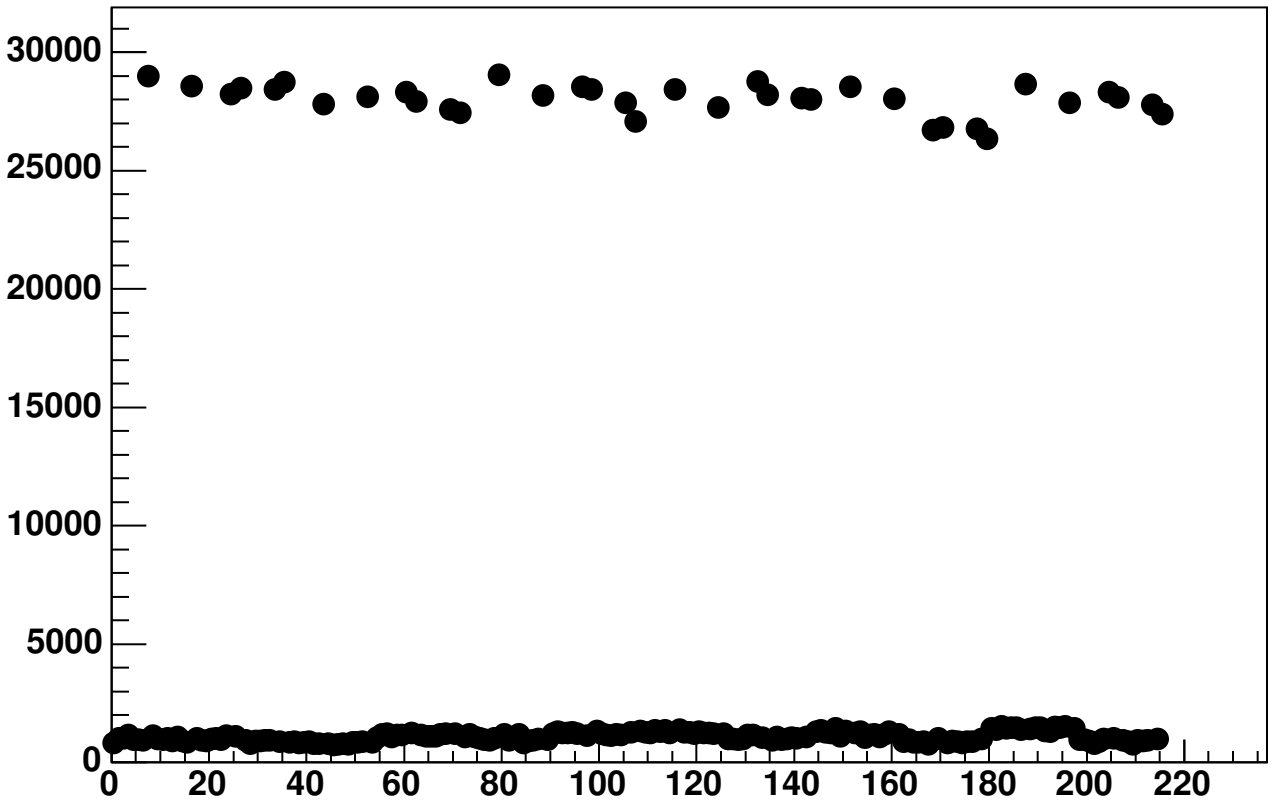
Enable 5, Hold=30, DAC=45056, ADC Mean vs 18*Chip+Chan



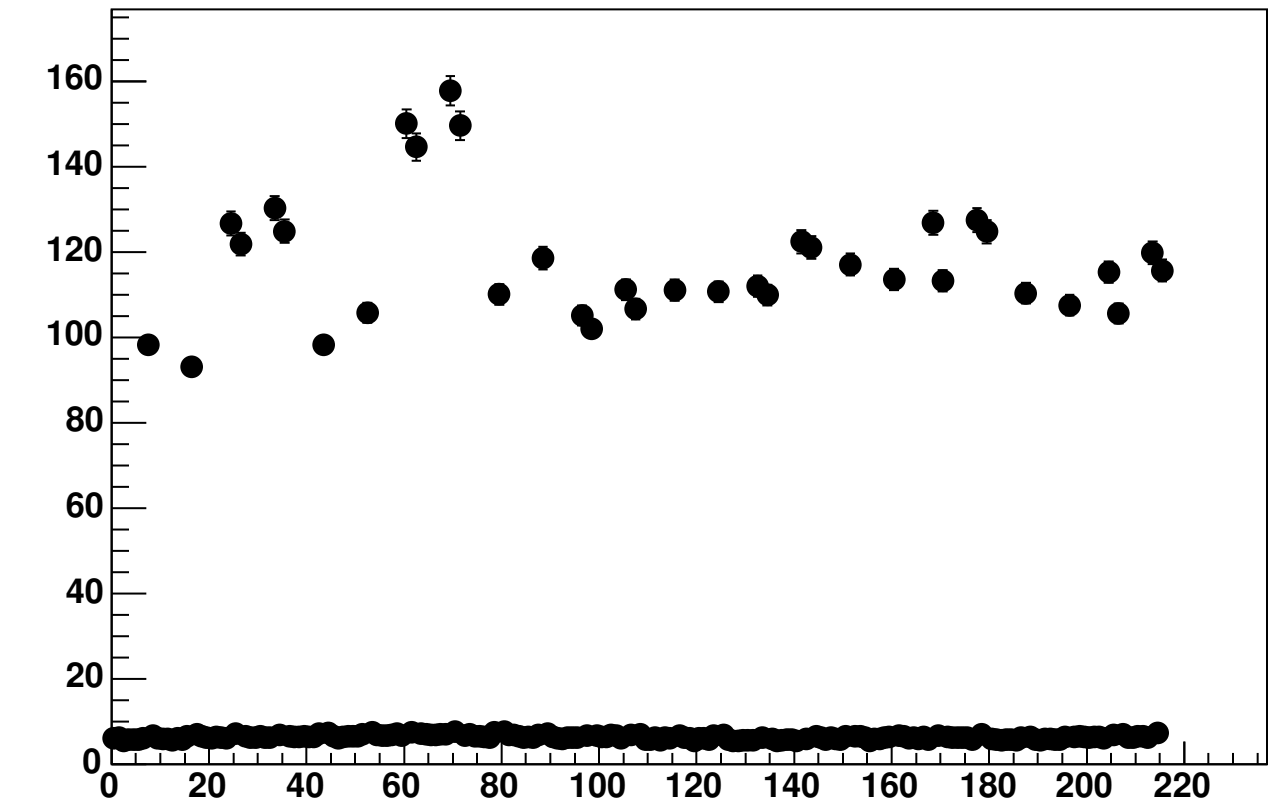
Enable 5, Hold=30, DAC=45056, ADC Noise vs 18*Chip+Chan



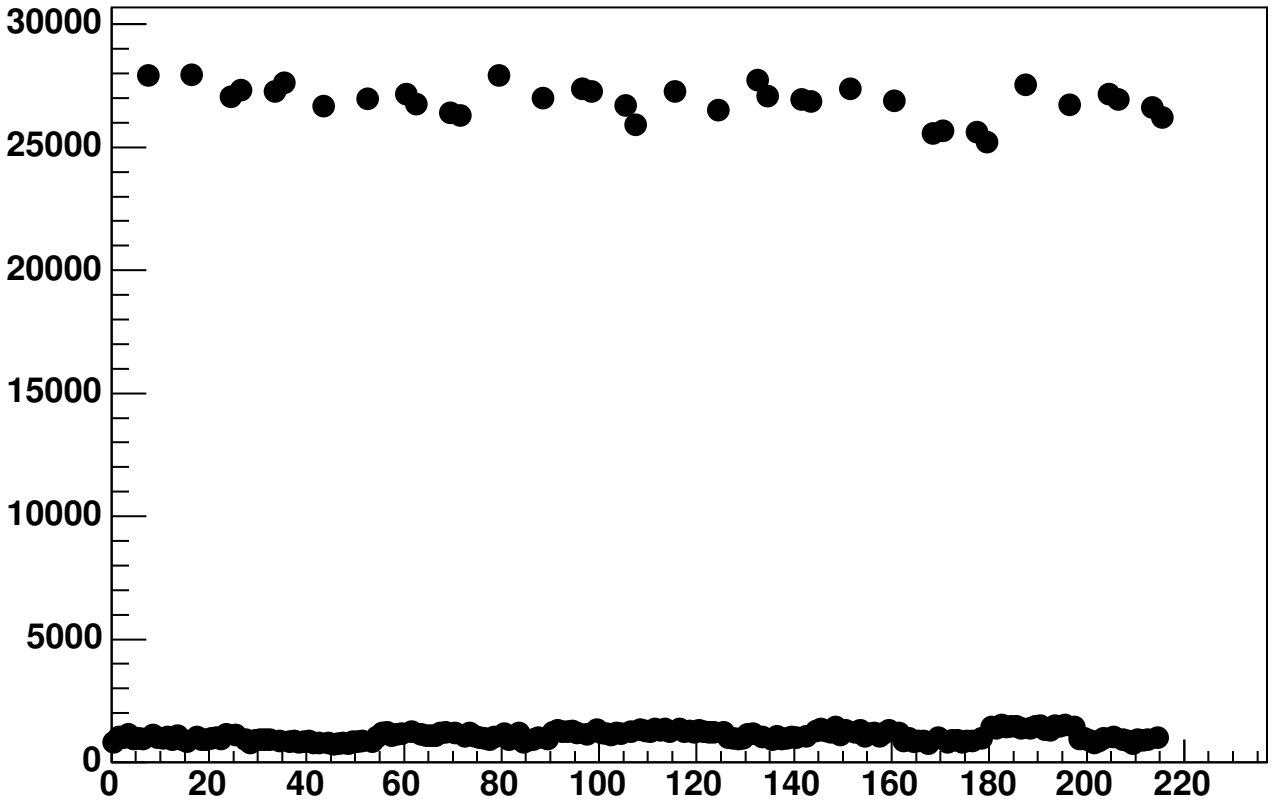
Enable 5, Hold=30, DAC=43008, ADC Mean vs 18*Chip+Chan



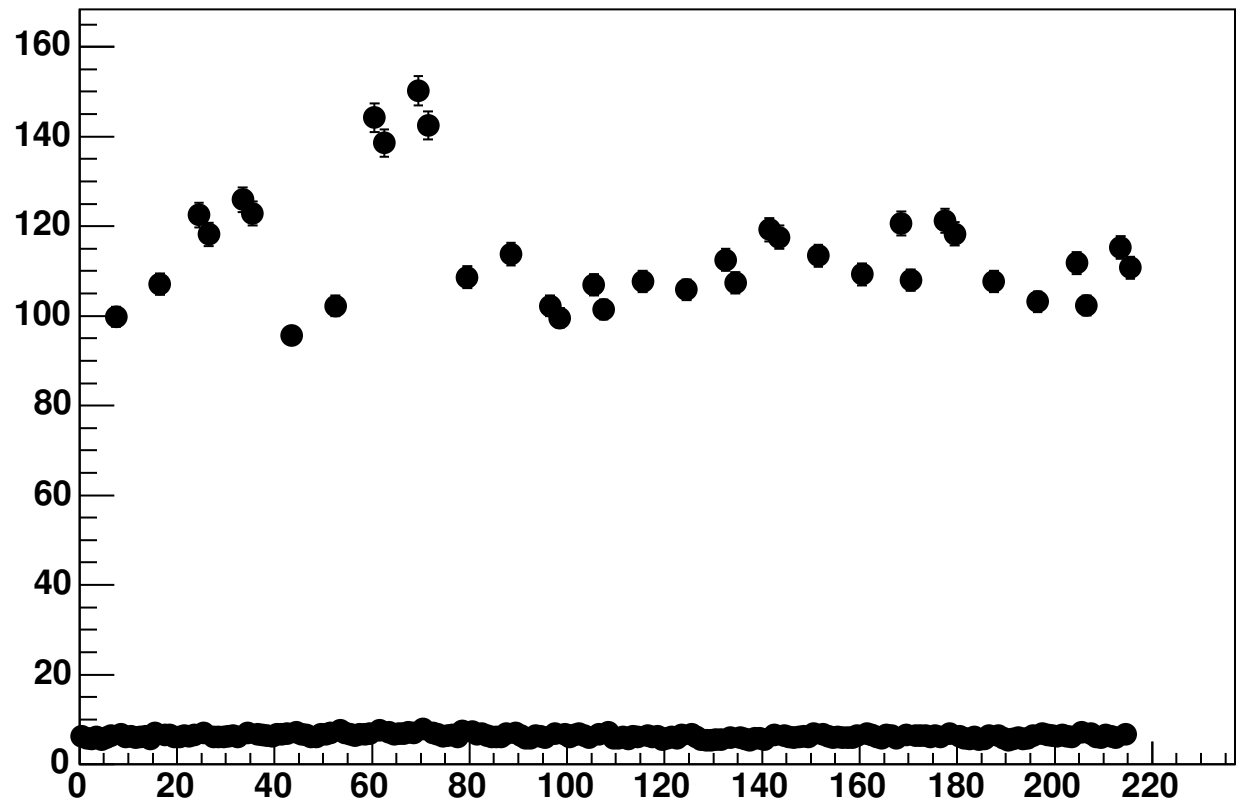
Enable 5, Hold=30, DAC=43008, ADC Noise vs 18*Chip+Chan



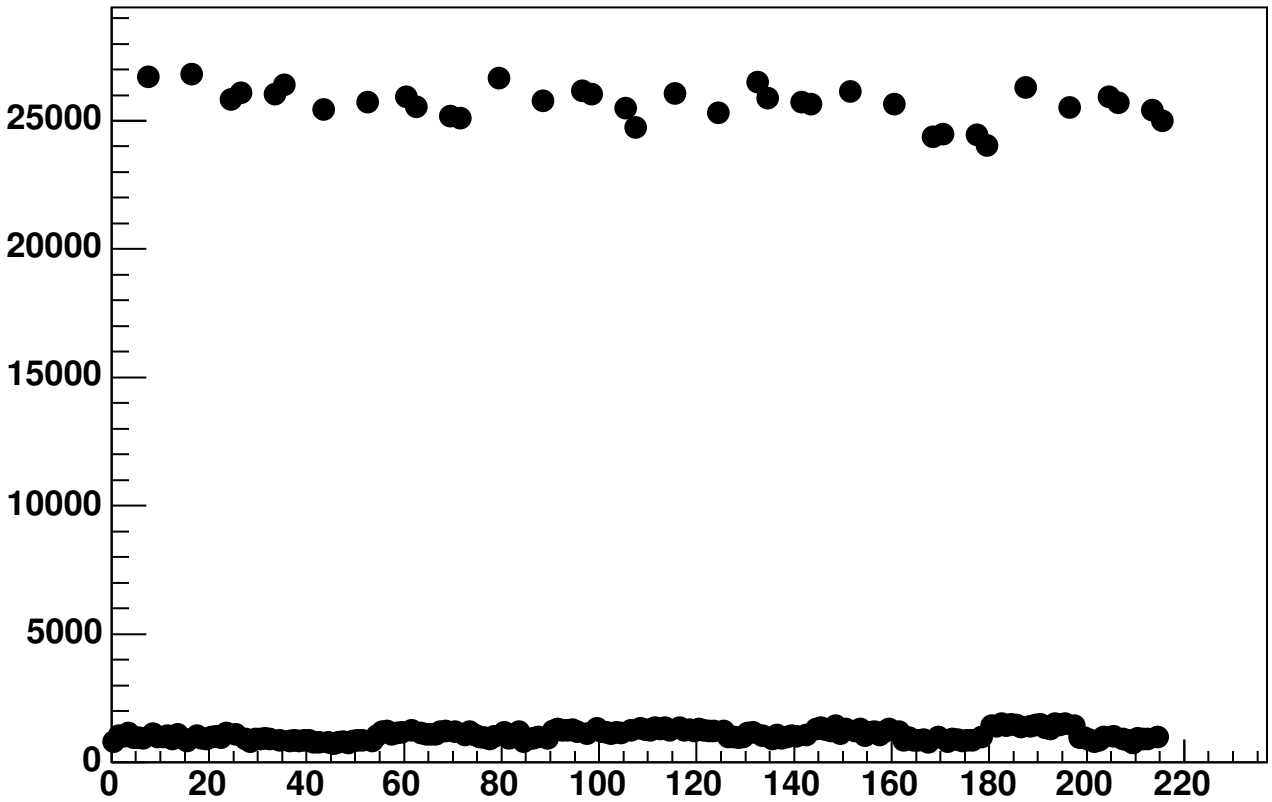
Enable 5, Hold=30, DAC=40960, ADC Mean vs 18*Chip+Chan



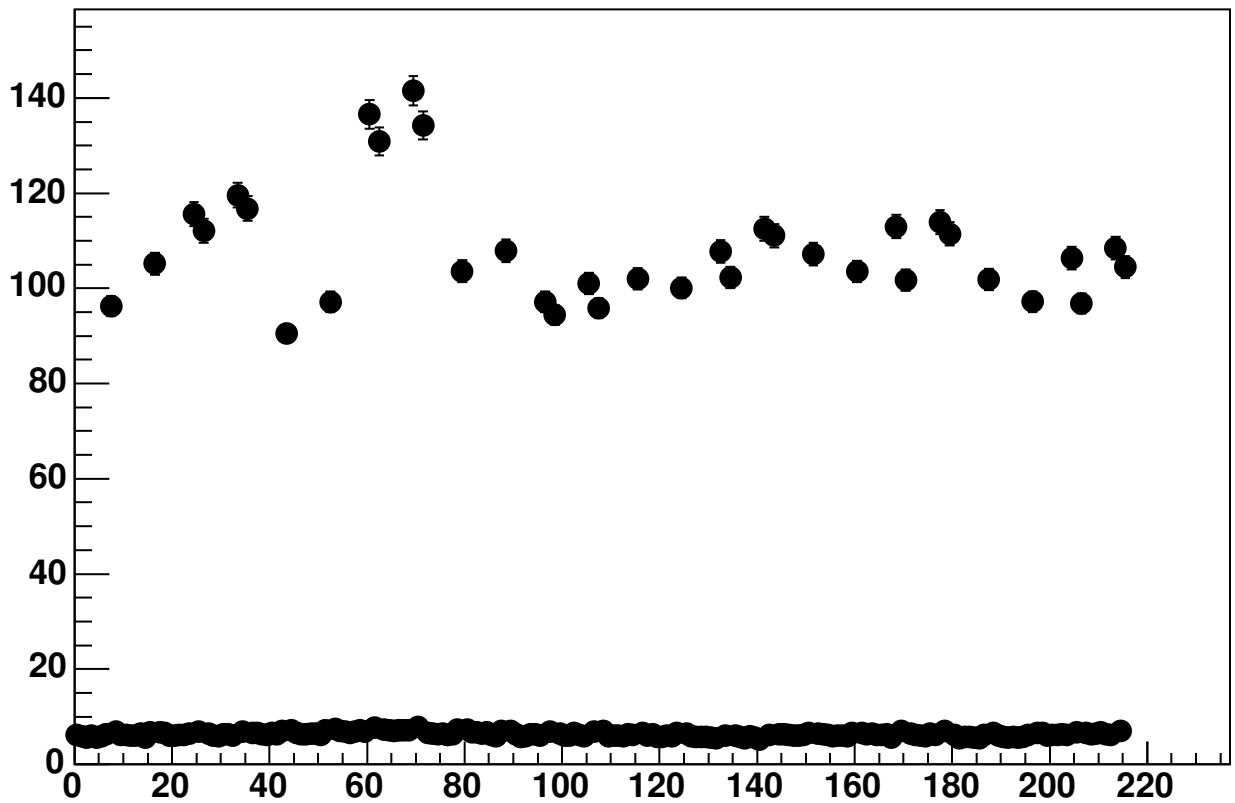
Enable 5, Hold=30, DAC=40960, ADC Noise vs 18*Chip+Chan



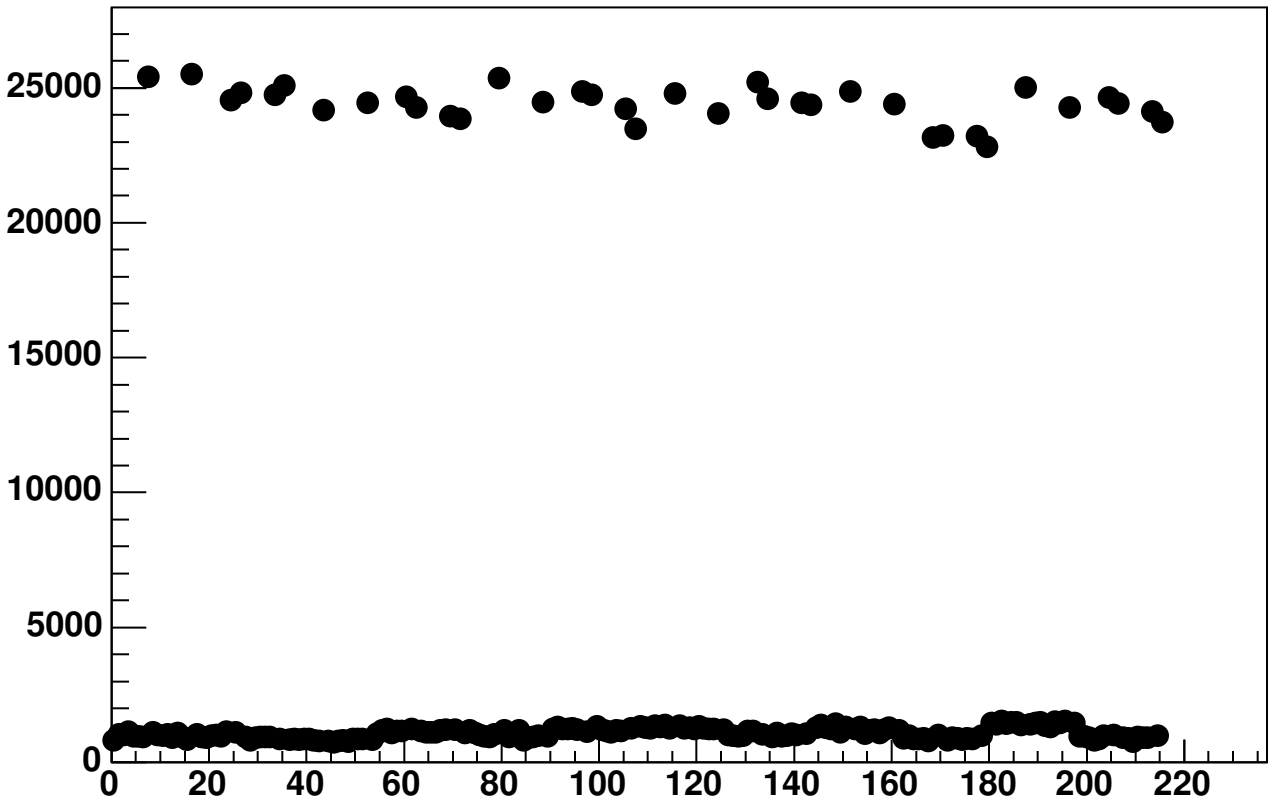
Enable 5, Hold=30, DAC=38912, ADC Mean vs 18*Chip+Chan



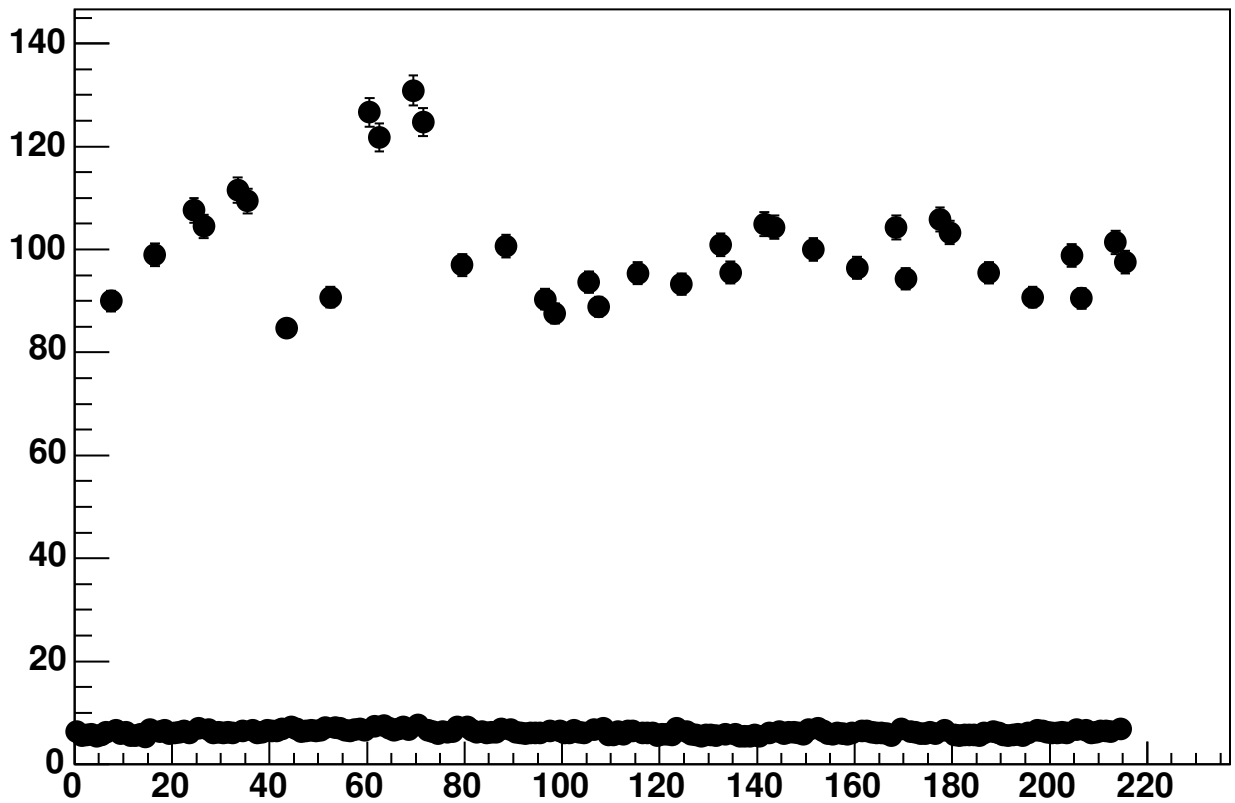
Enable 5, Hold=30, DAC=38912, ADC Noise vs 18*Chip+Chan



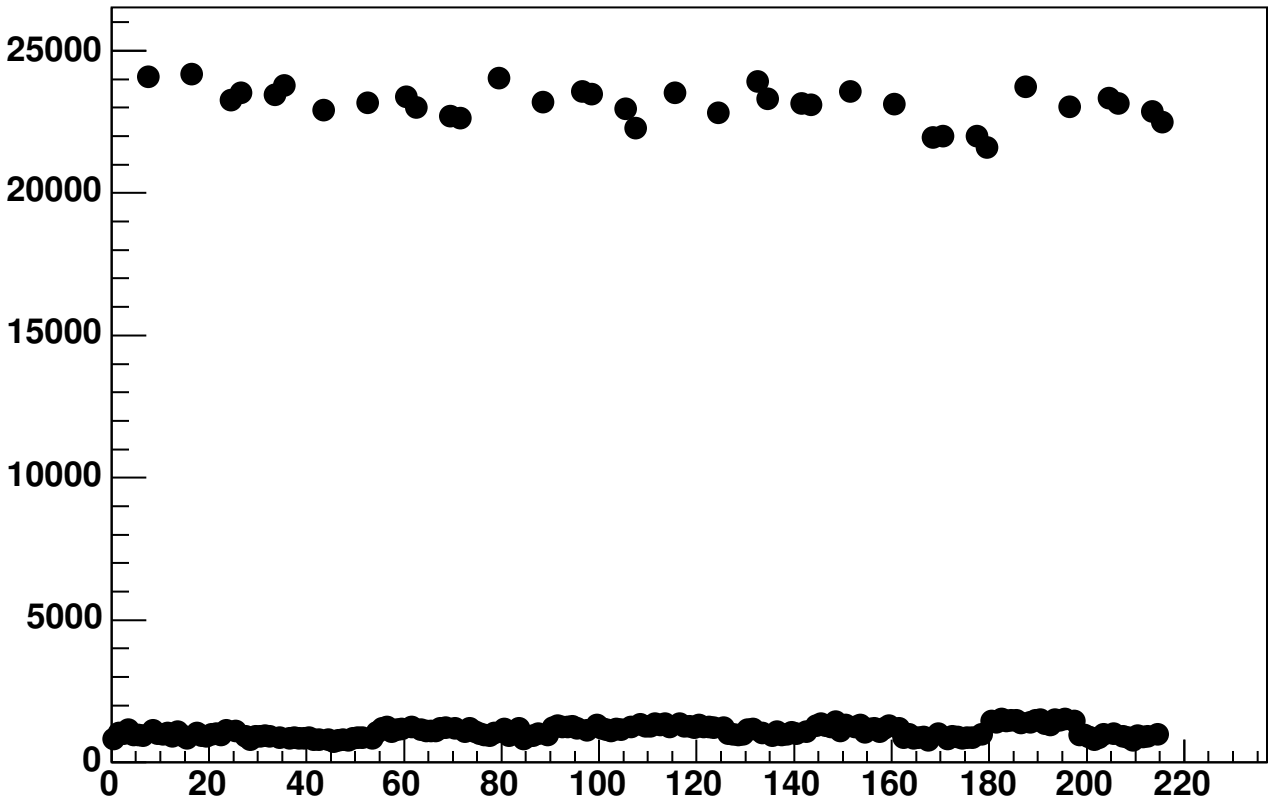
Enable 5, Hold=30, DAC=36864, ADC Mean vs 18*Chip+Chan



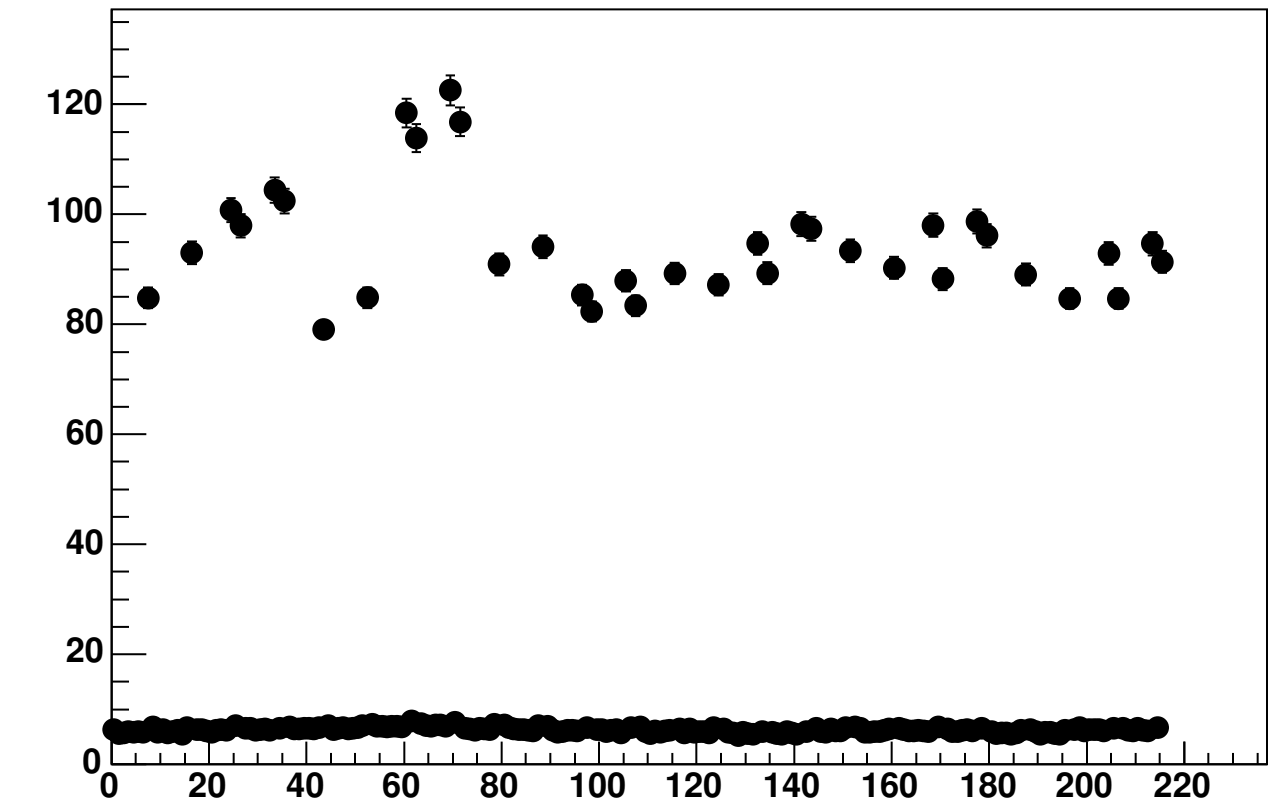
Enable 5, Hold=30, DAC=36864, ADC Noise vs 18*Chip+Chan



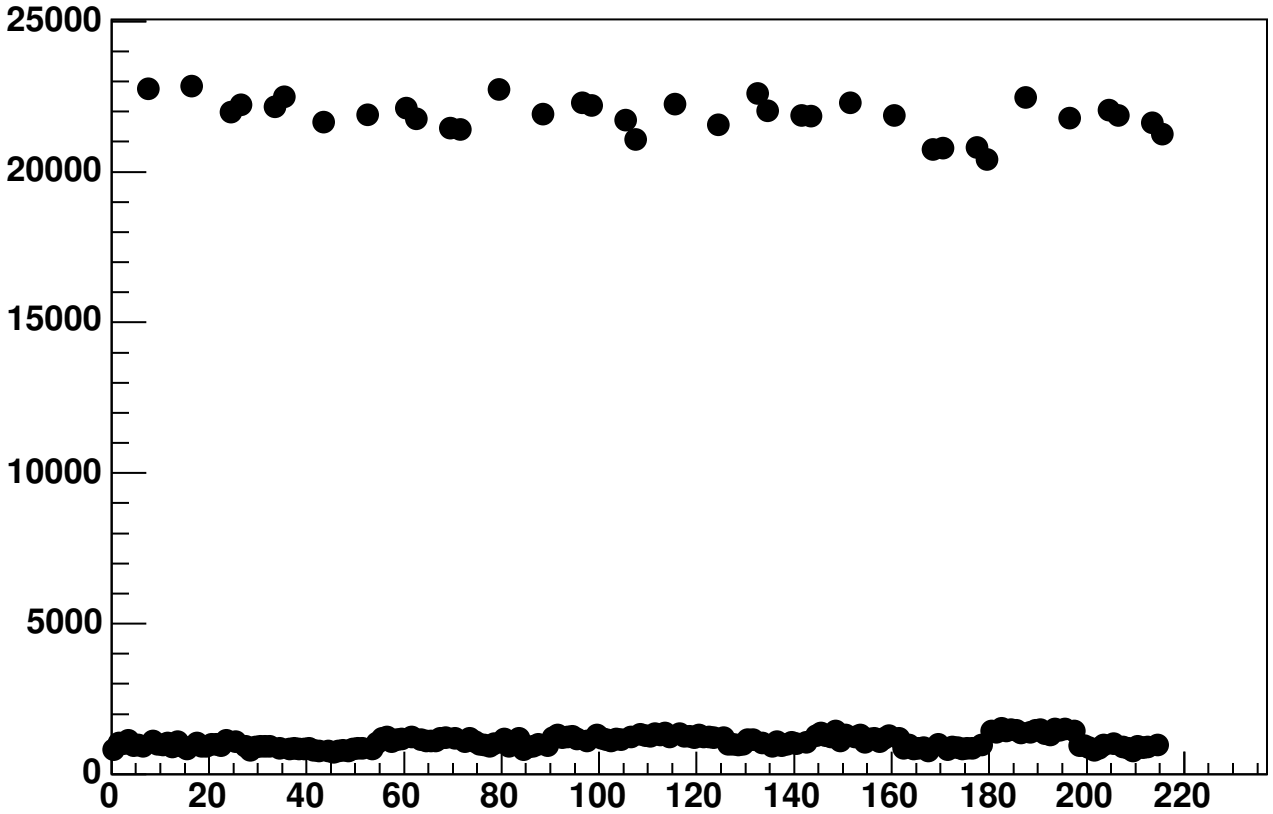
Enable 5, Hold=30, DAC=34816, ADC Mean vs 18*Chip+Chan



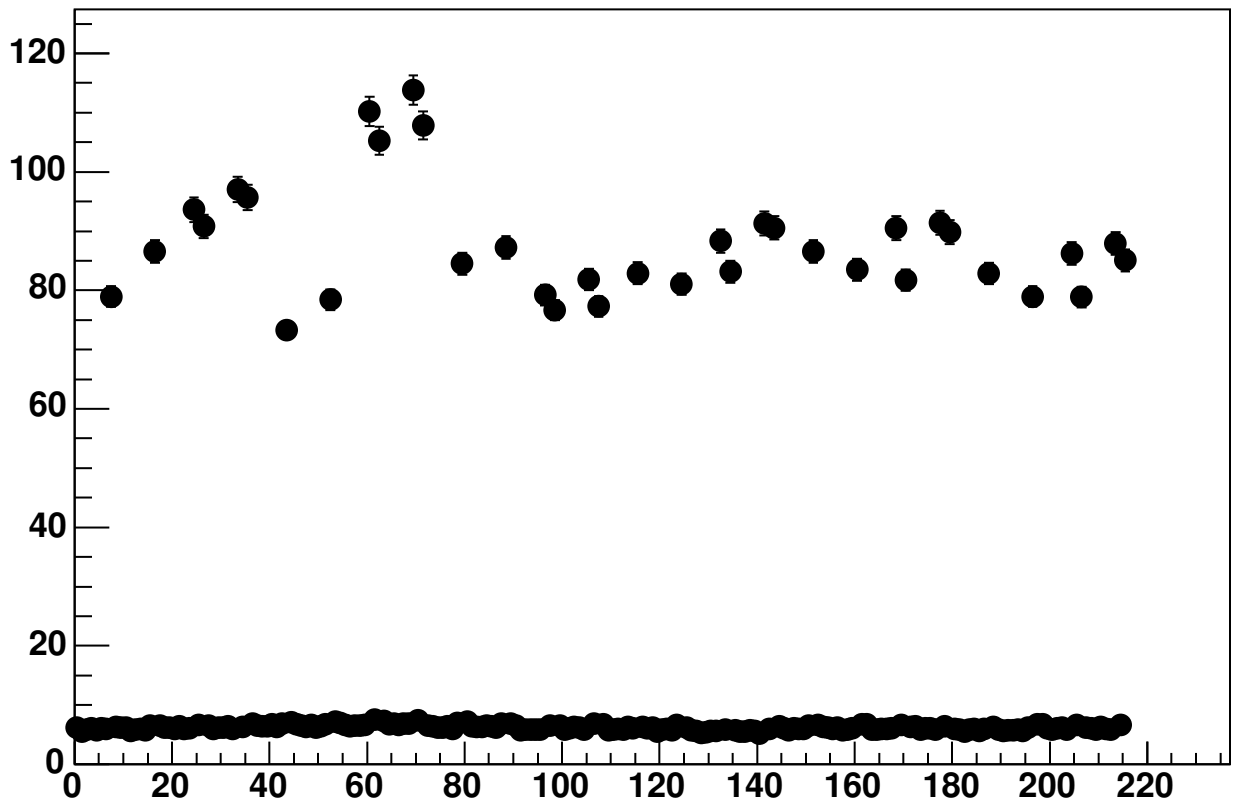
Enable 5, Hold=30, DAC=34816, ADC Noise vs 18*Chip+Chan



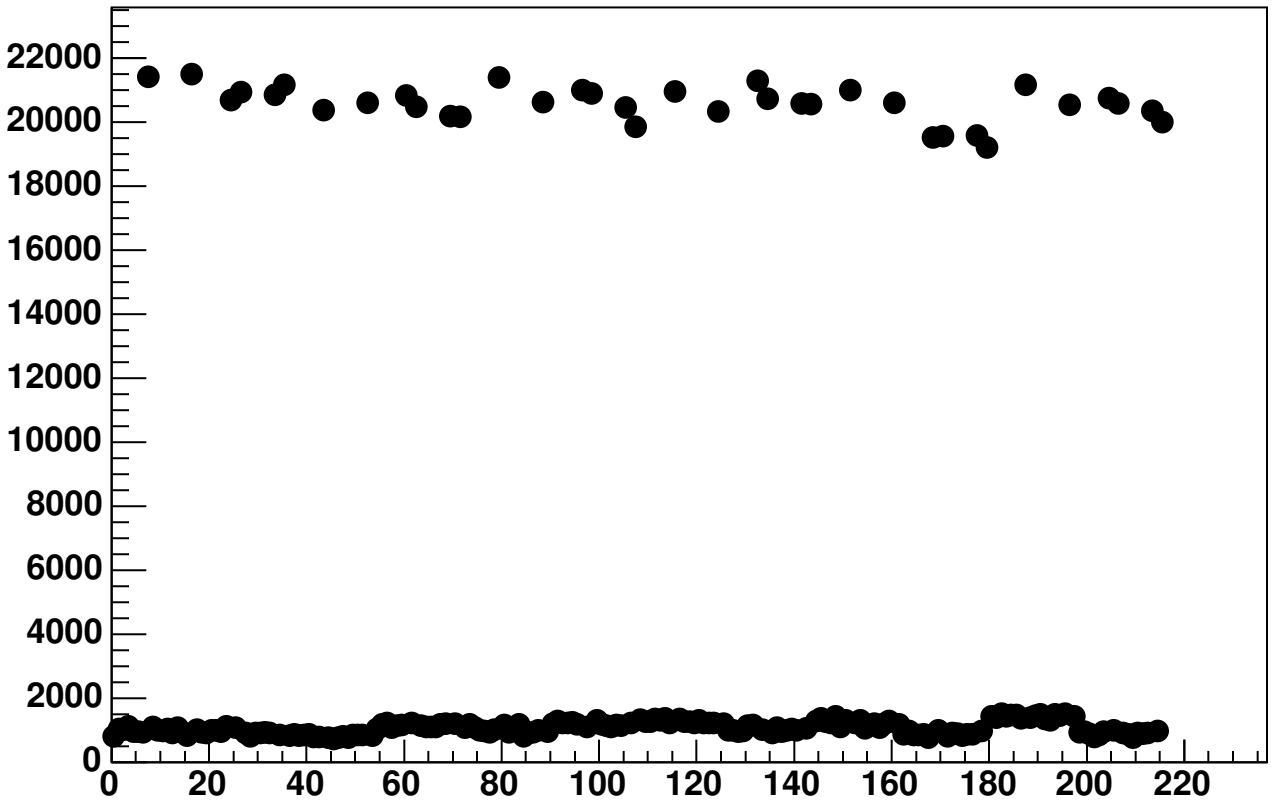
Enable 5, Hold=30, DAC=32768, ADC Mean vs 18*Chip+Chan



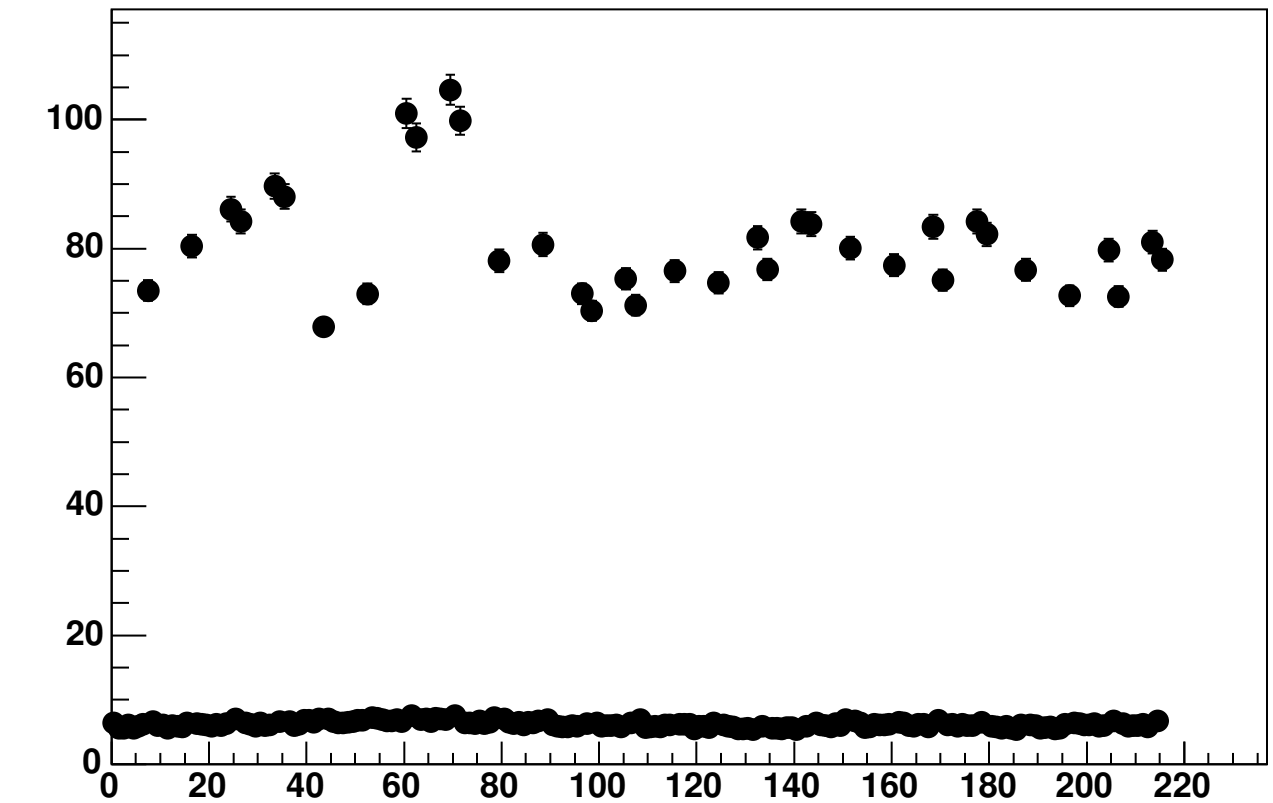
Enable 5, Hold=30, DAC=32768, ADC Noise vs 18*Chip+Chan



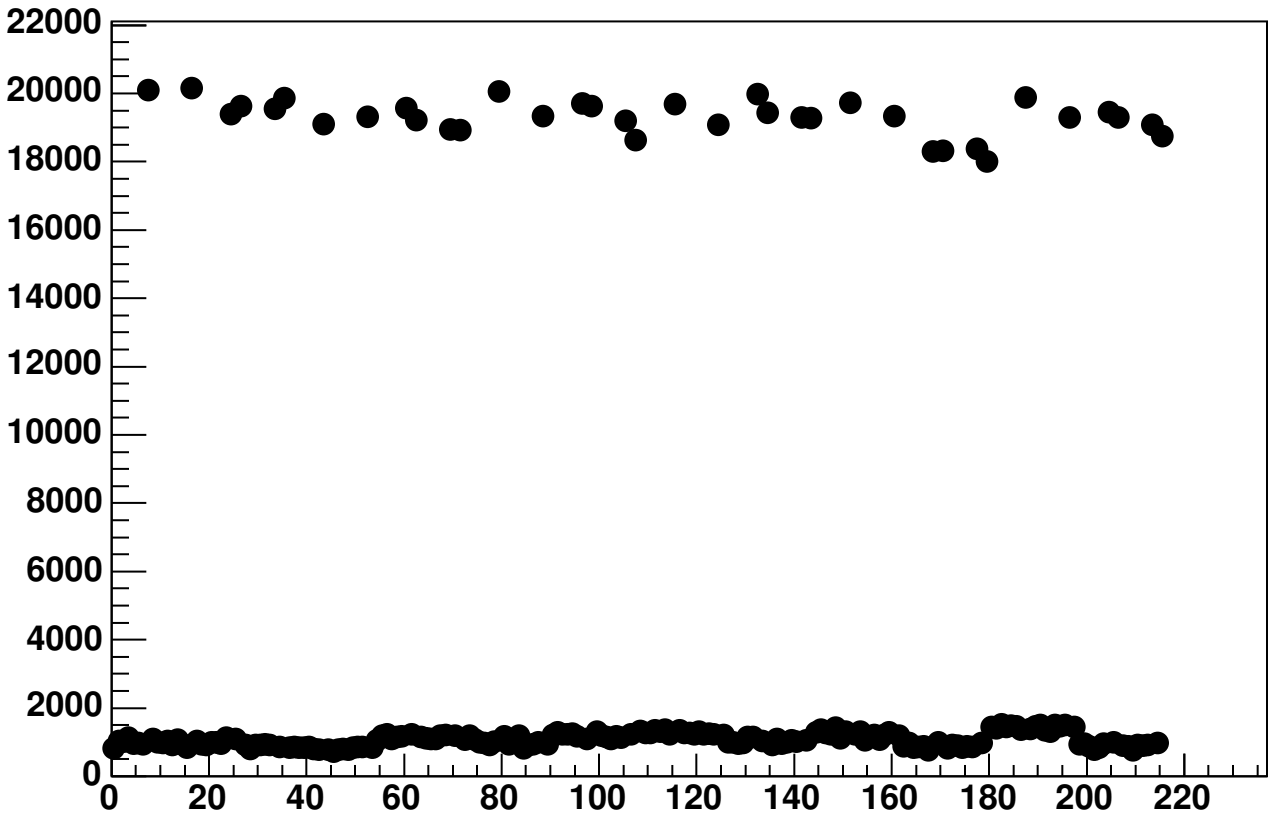
Enable 5, Hold=30, DAC=30720, ADC Mean vs 18*Chip+Chan



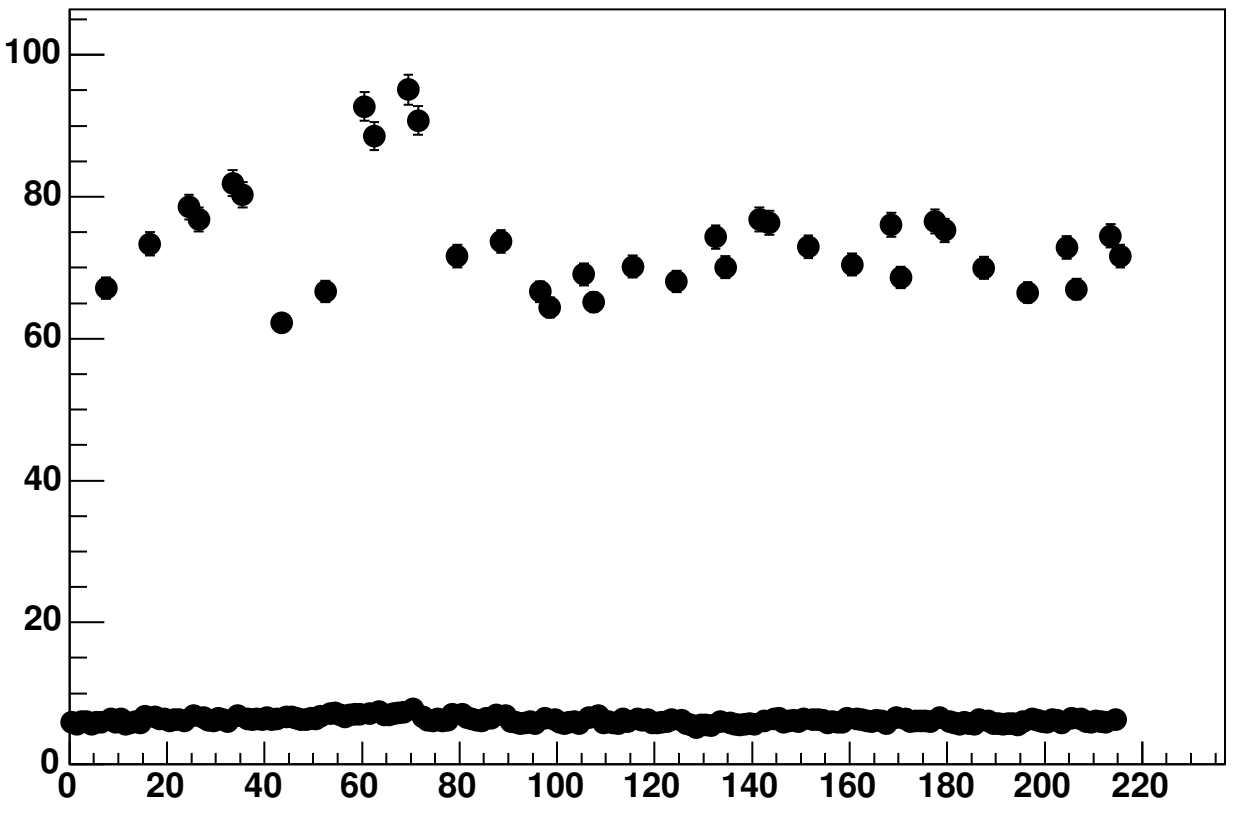
Enable 5, Hold=30, DAC=30720, ADC Noise vs 18*Chip+Chan



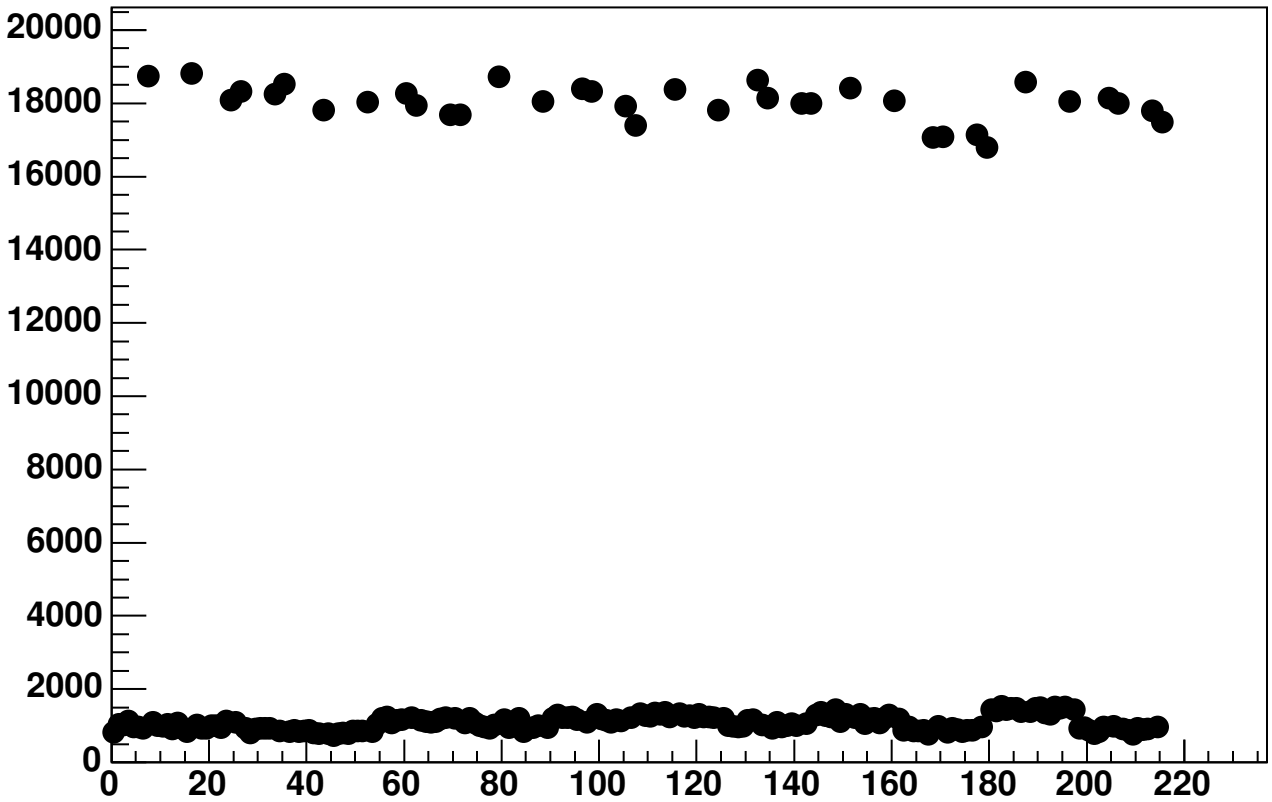
Enable 5, Hold=30, DAC=28672, ADC Mean vs 18*Chip+Chan



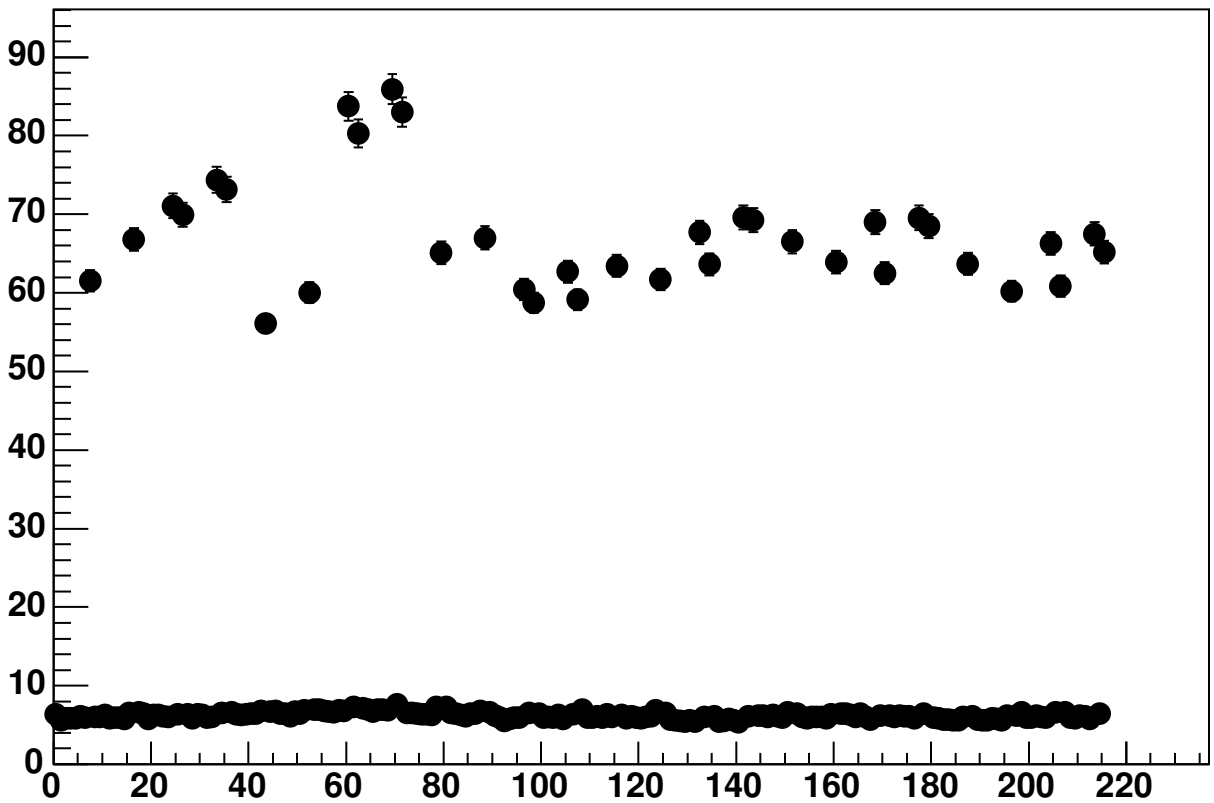
Enable 5, Hold=30, DAC=28672, ADC Noise vs 18*Chip+Chan



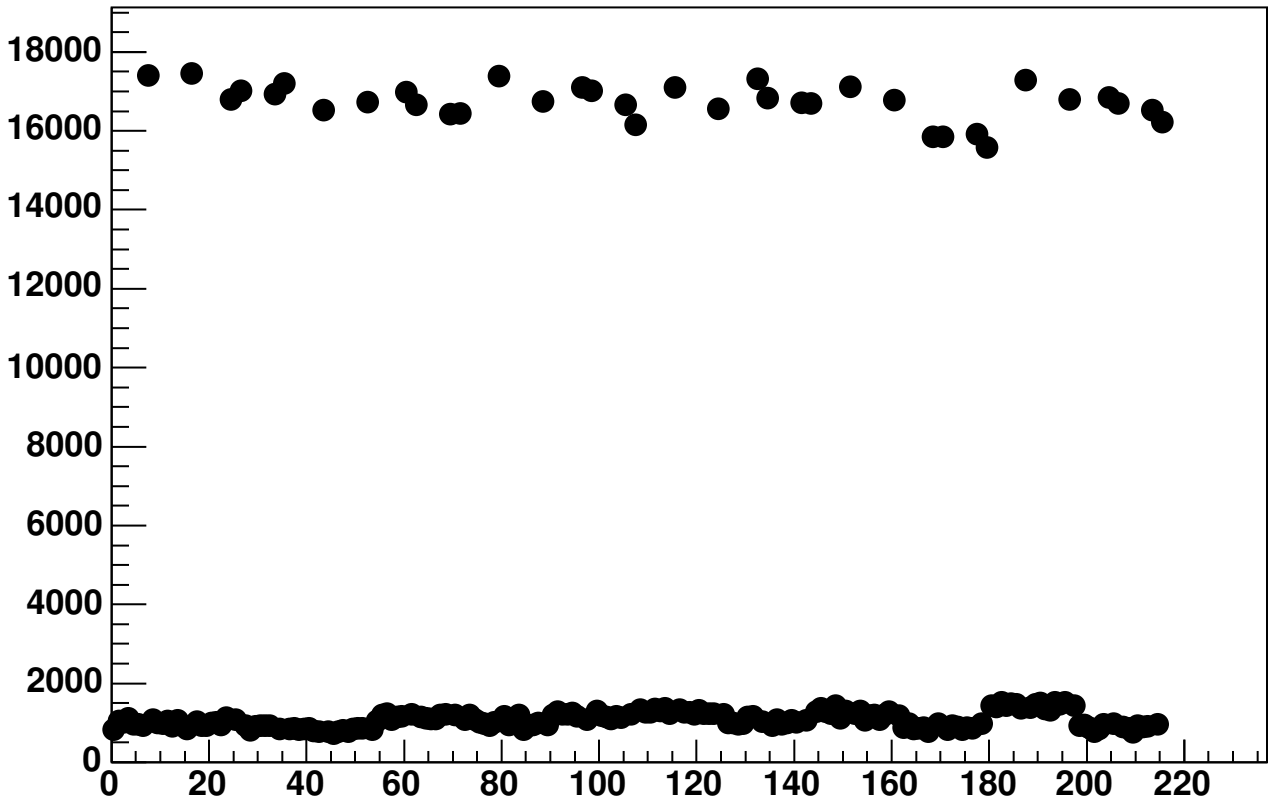
Enable 5, Hold=30, DAC=26624, ADC Mean vs 18*Chip+Chan



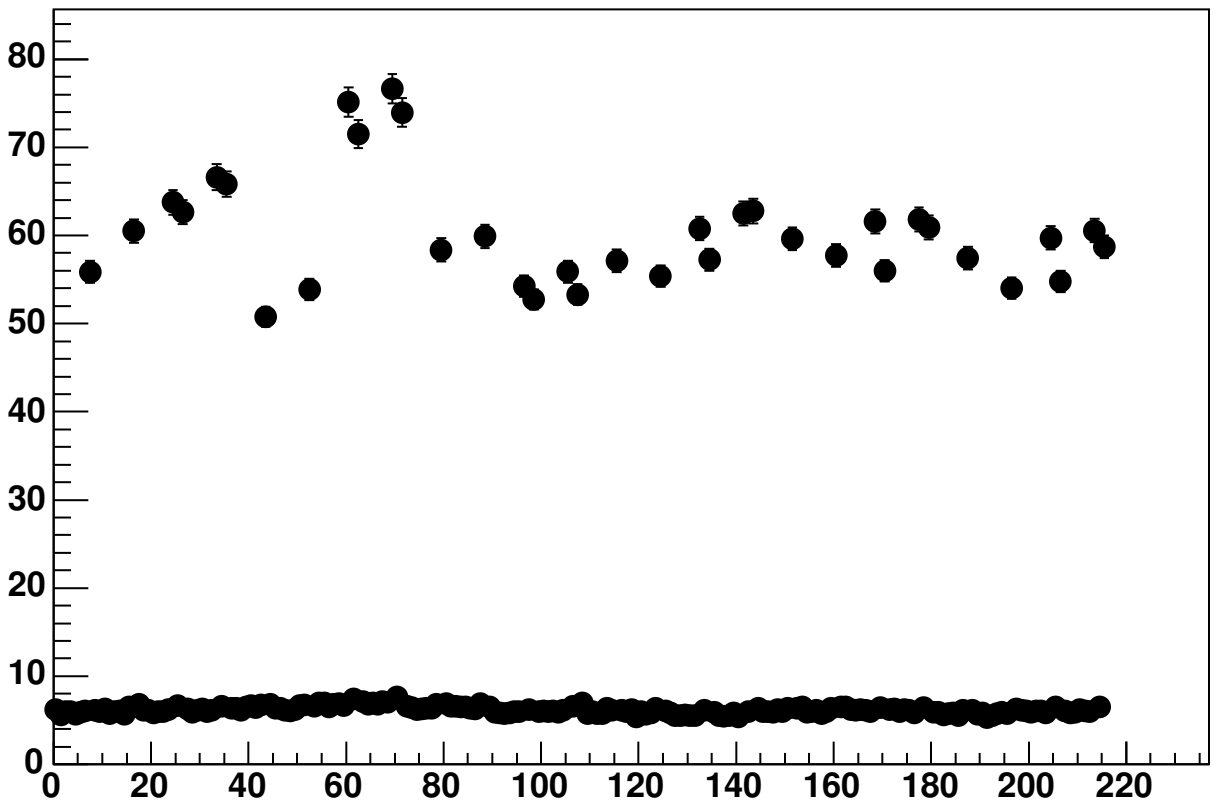
Enable 5, Hold=30, DAC=26624, ADC Noise vs 18*Chip+Chan



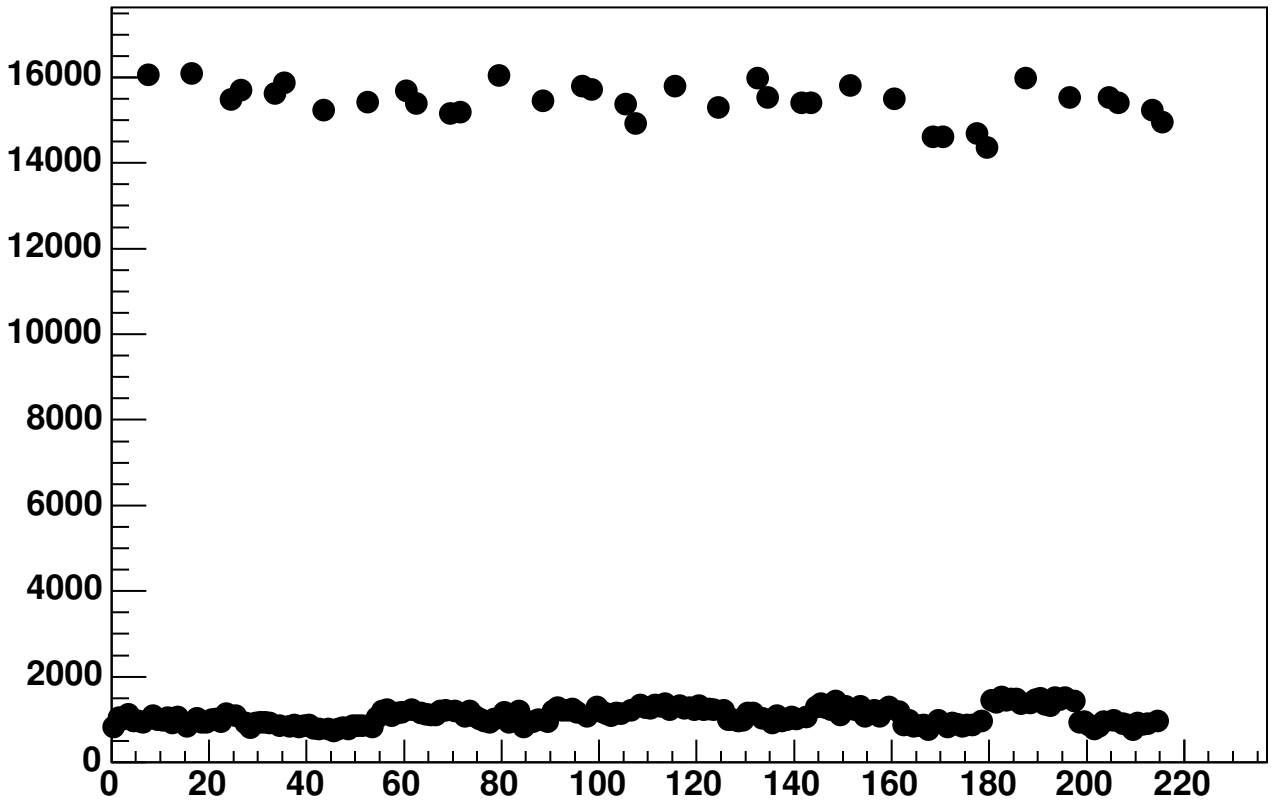
Enable 5, Hold=30, DAC=24576, ADC Mean vs 18*Chip+Chan



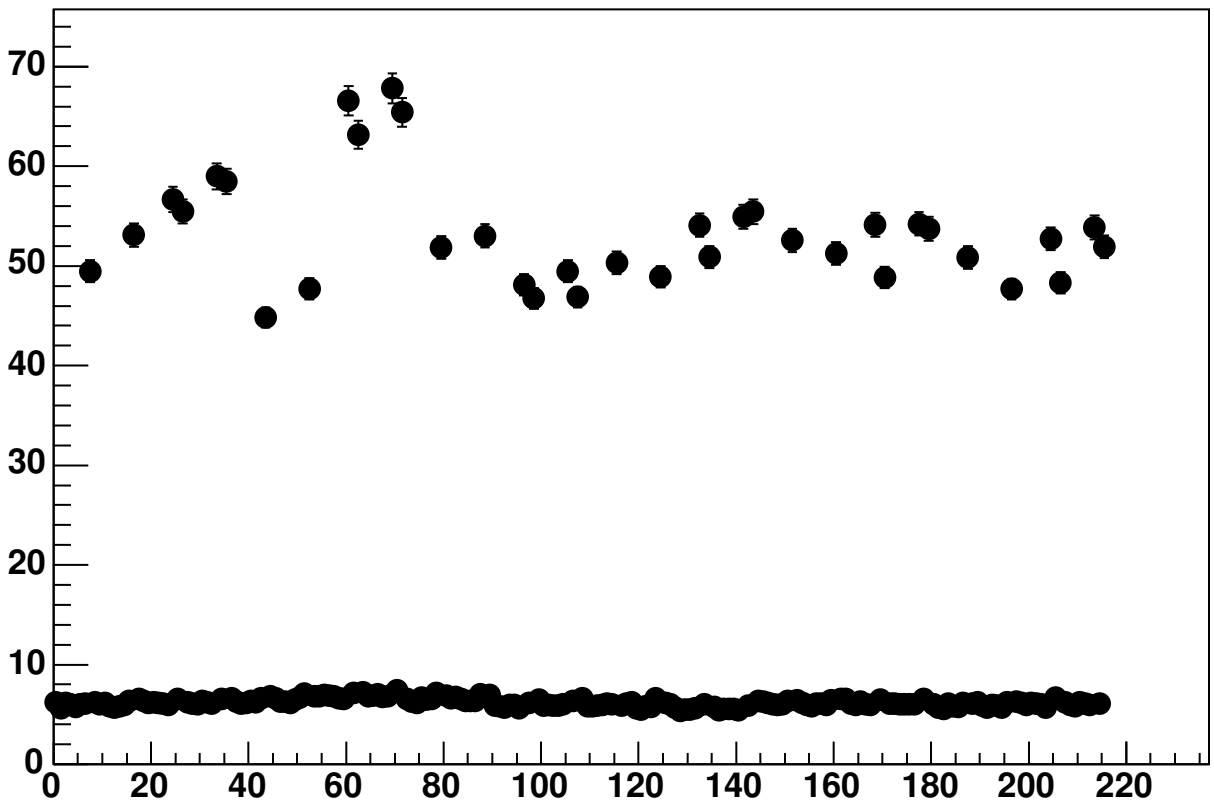
Enable 5, Hold=30, DAC=24576, ADC Noise vs 18*Chip+Chan



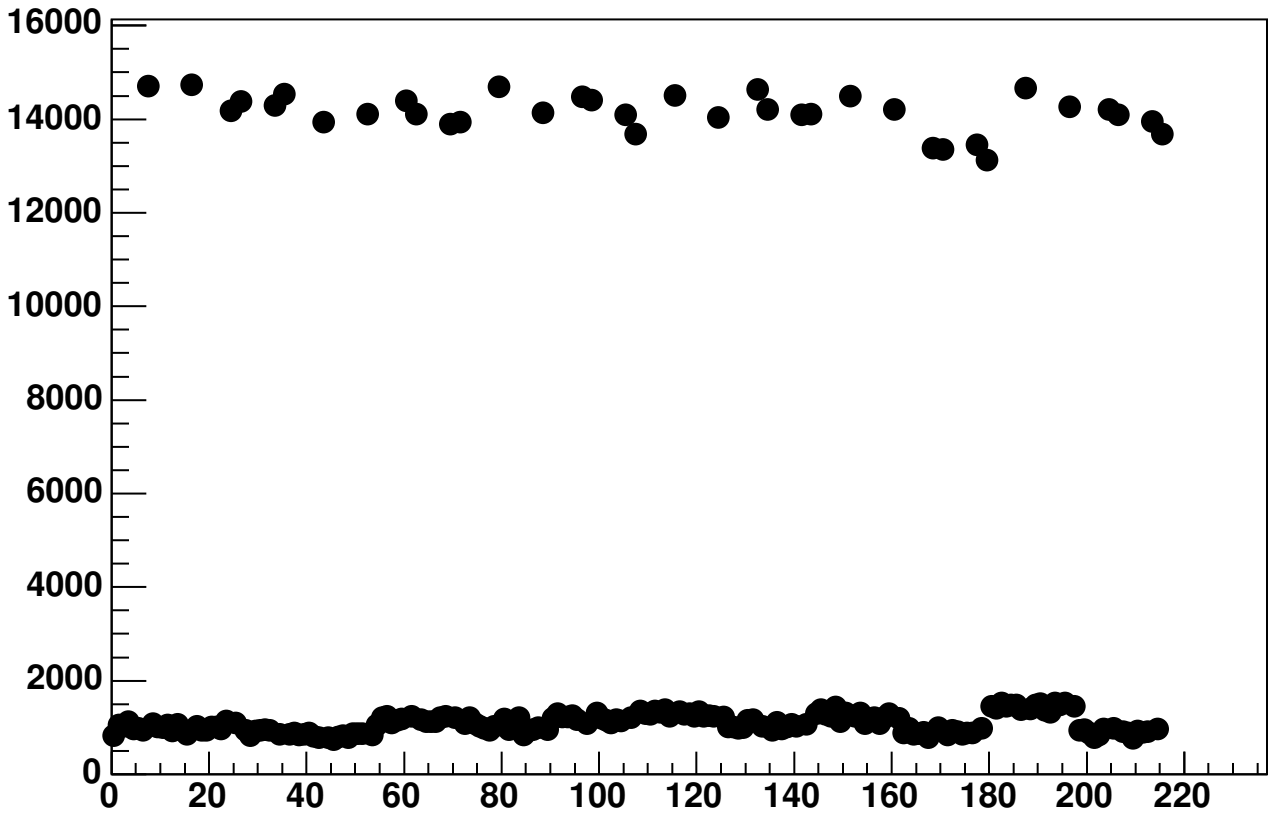
Enable 5, Hold=30, DAC=22528, ADC Mean vs 18*Chip+Chan



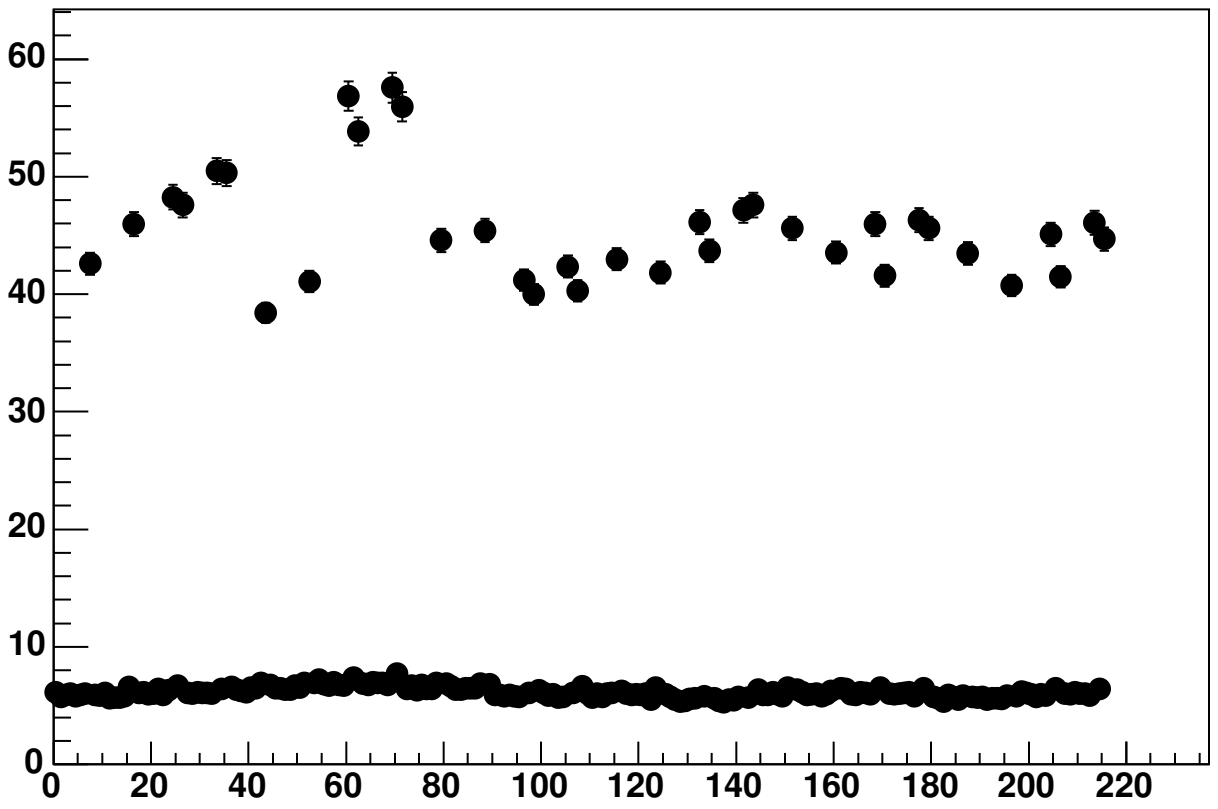
Enable 5, Hold=30, DAC=22528, ADC Noise vs 18*Chip+Chan



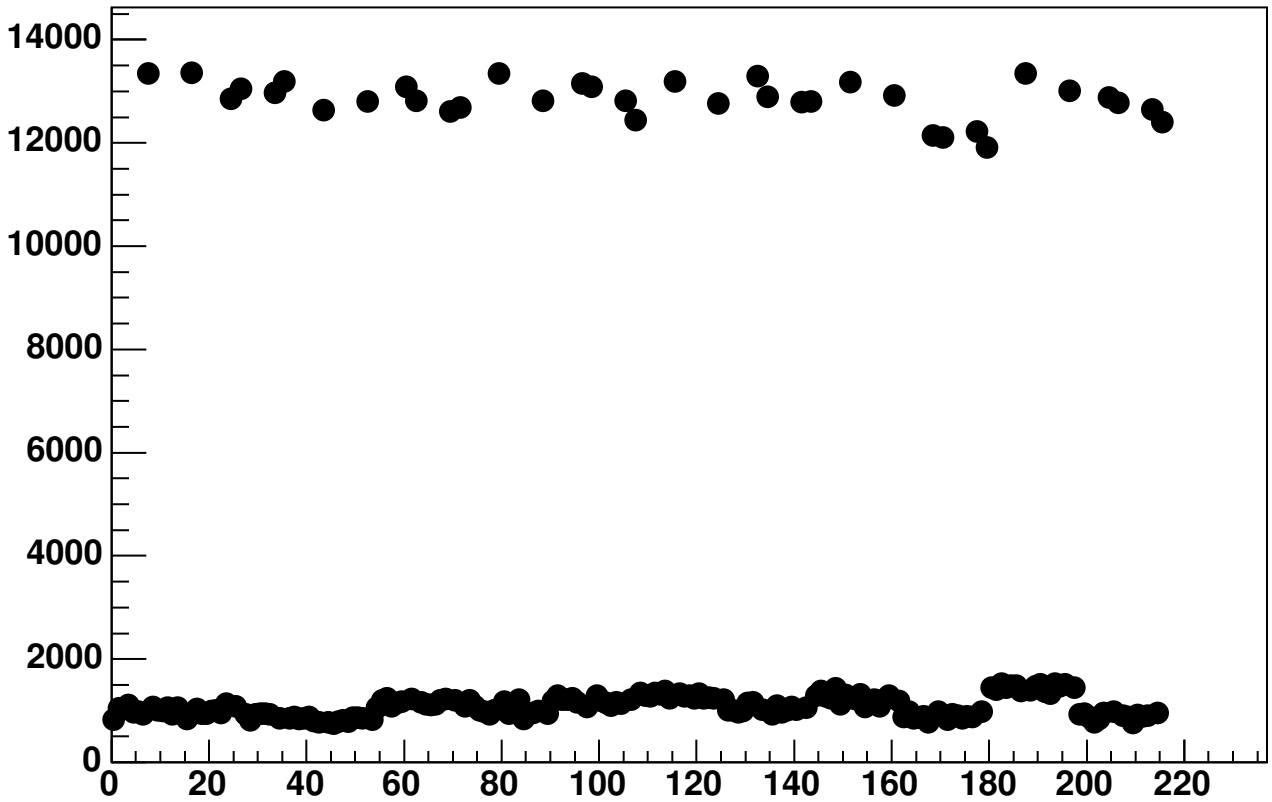
Enable 5, Hold=30, DAC=20480, ADC Mean vs 18*Chip+Chan



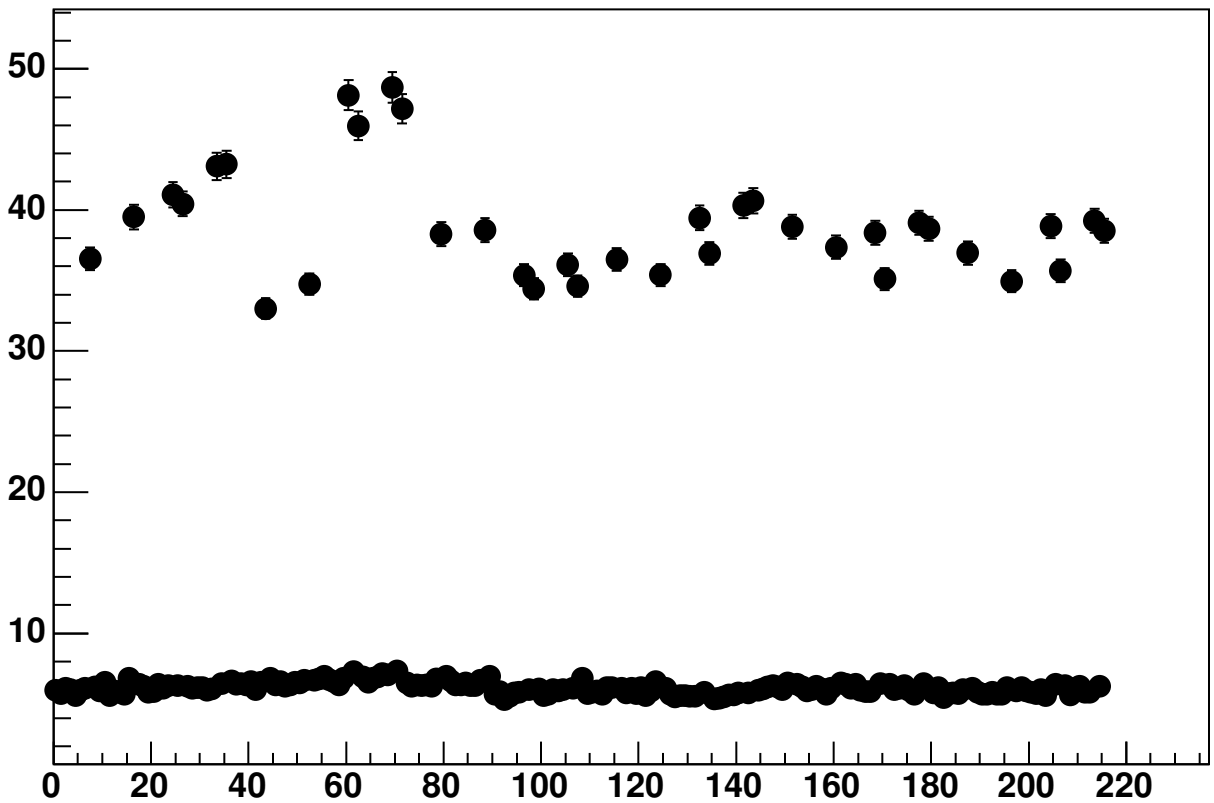
Enable 5, Hold=30, DAC=20480, ADC Noise vs 18*Chip+Chan



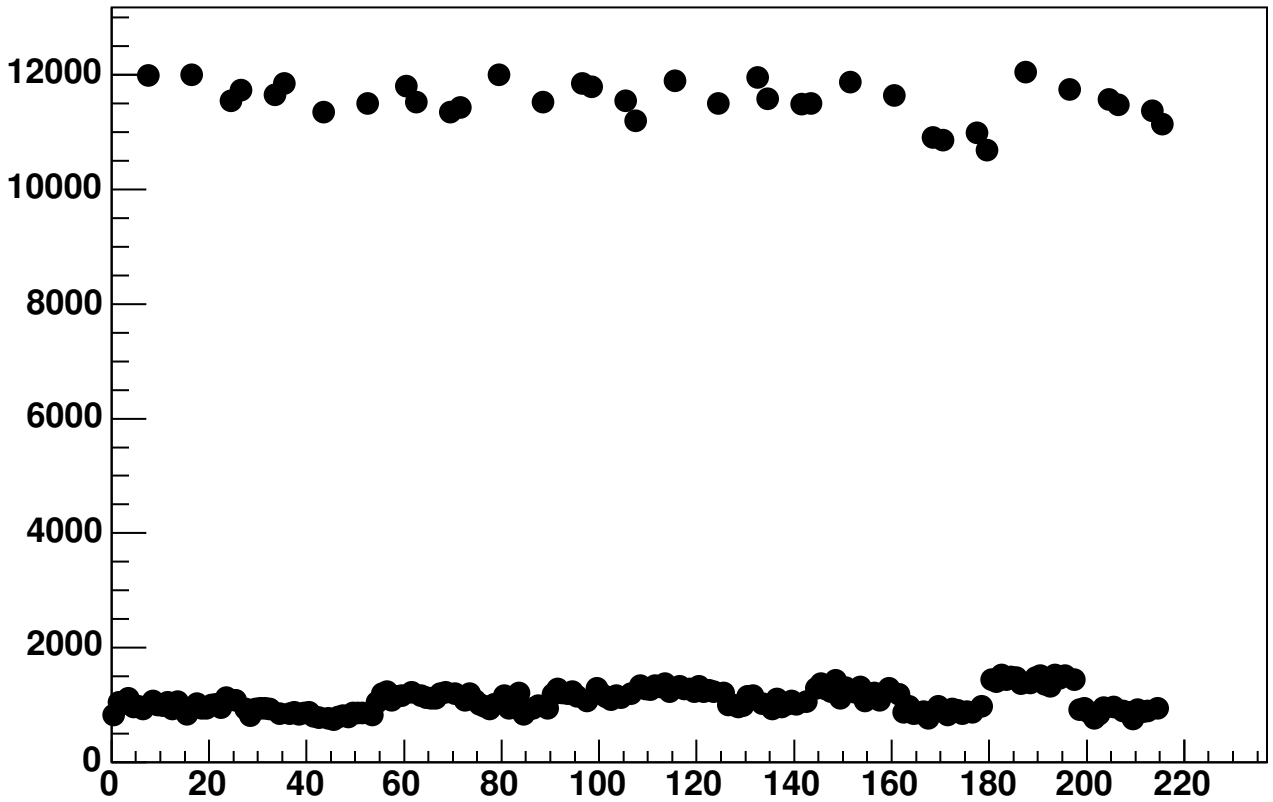
Enable 5, Hold=30, DAC=18432, ADC Mean vs 18*Chip+Chan



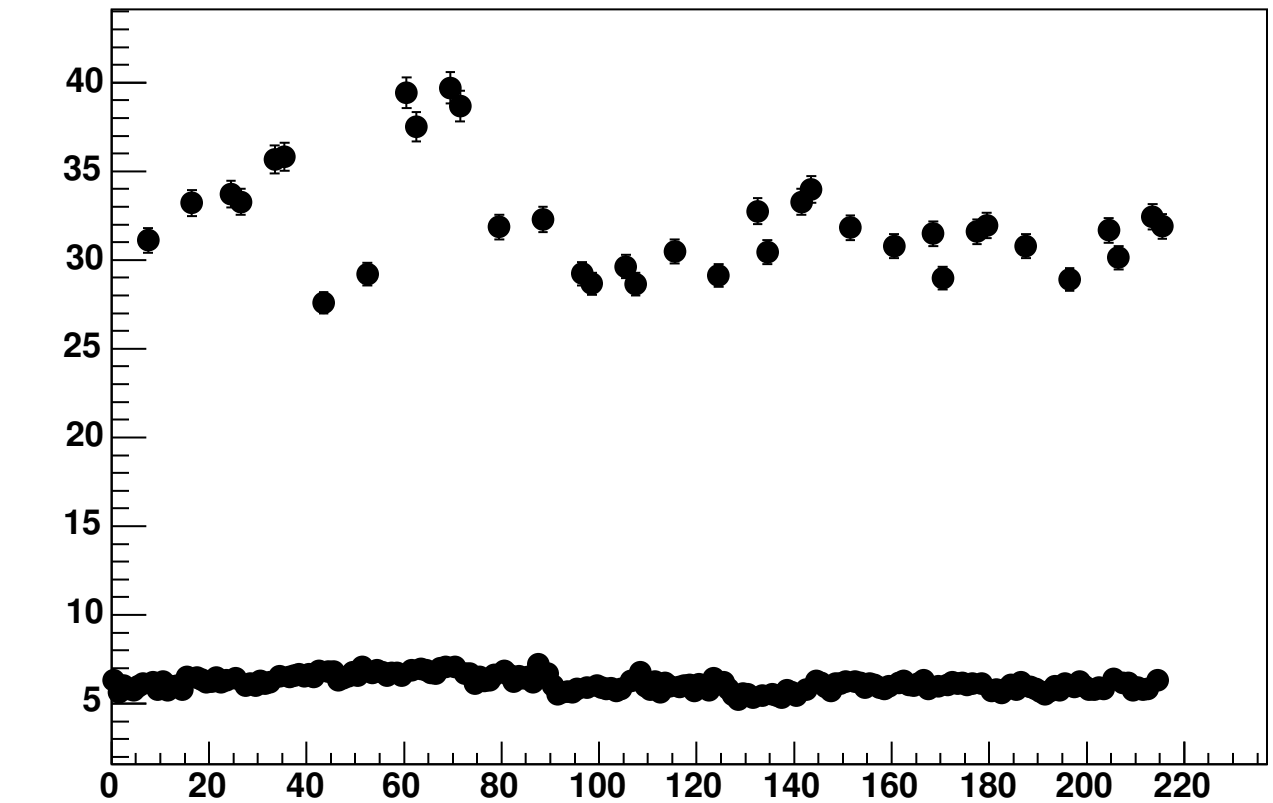
Enable 5, Hold=30, DAC=18432, ADC Noise vs 18*Chip+Chan



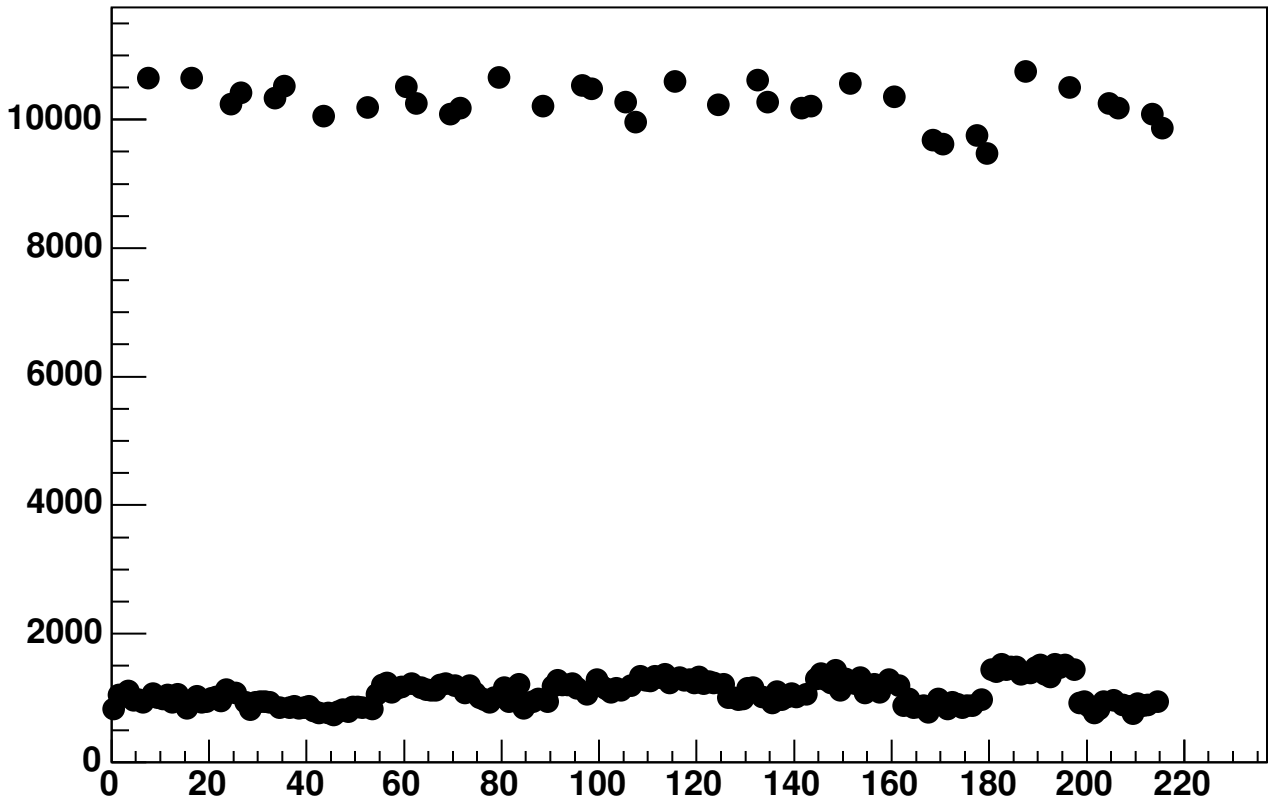
Enable 5, Hold=30, DAC=16384, ADC Mean vs 18*Chip+Chan



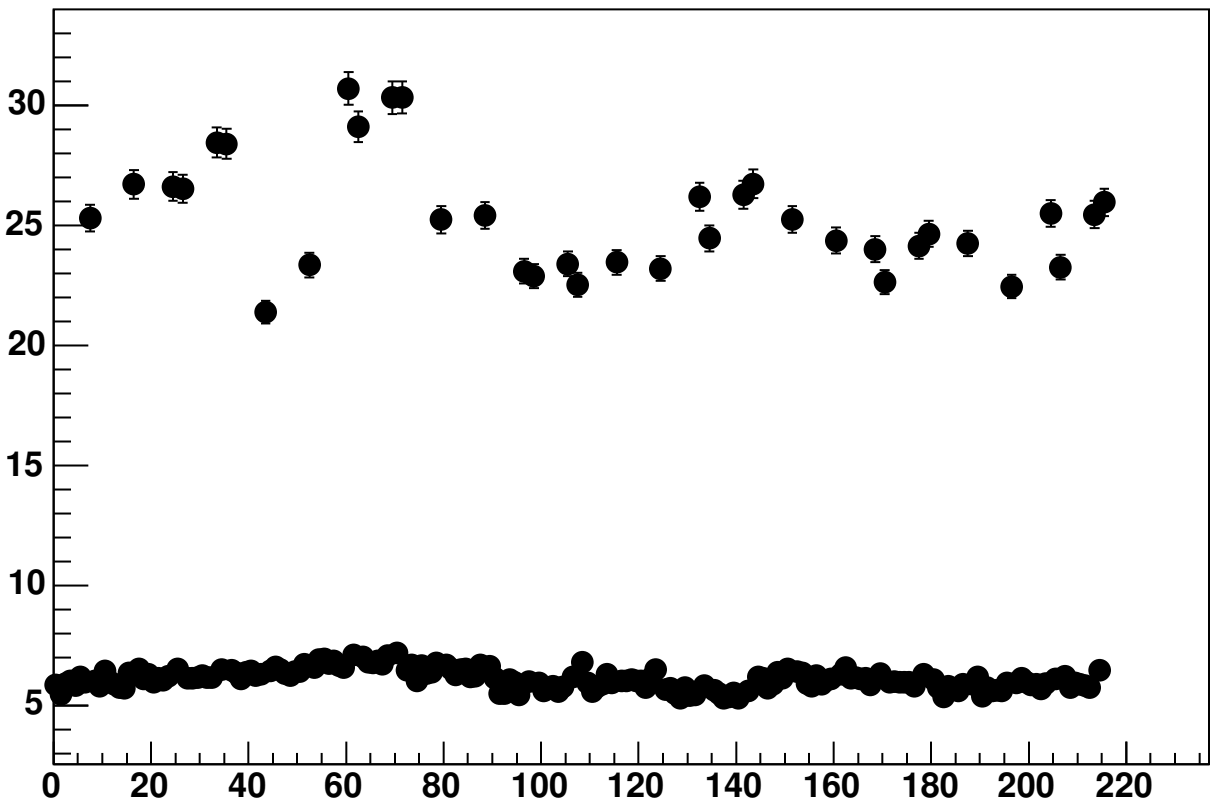
Enable 5, Hold=30, DAC=16384, ADC Noise vs 18*Chip+Chan



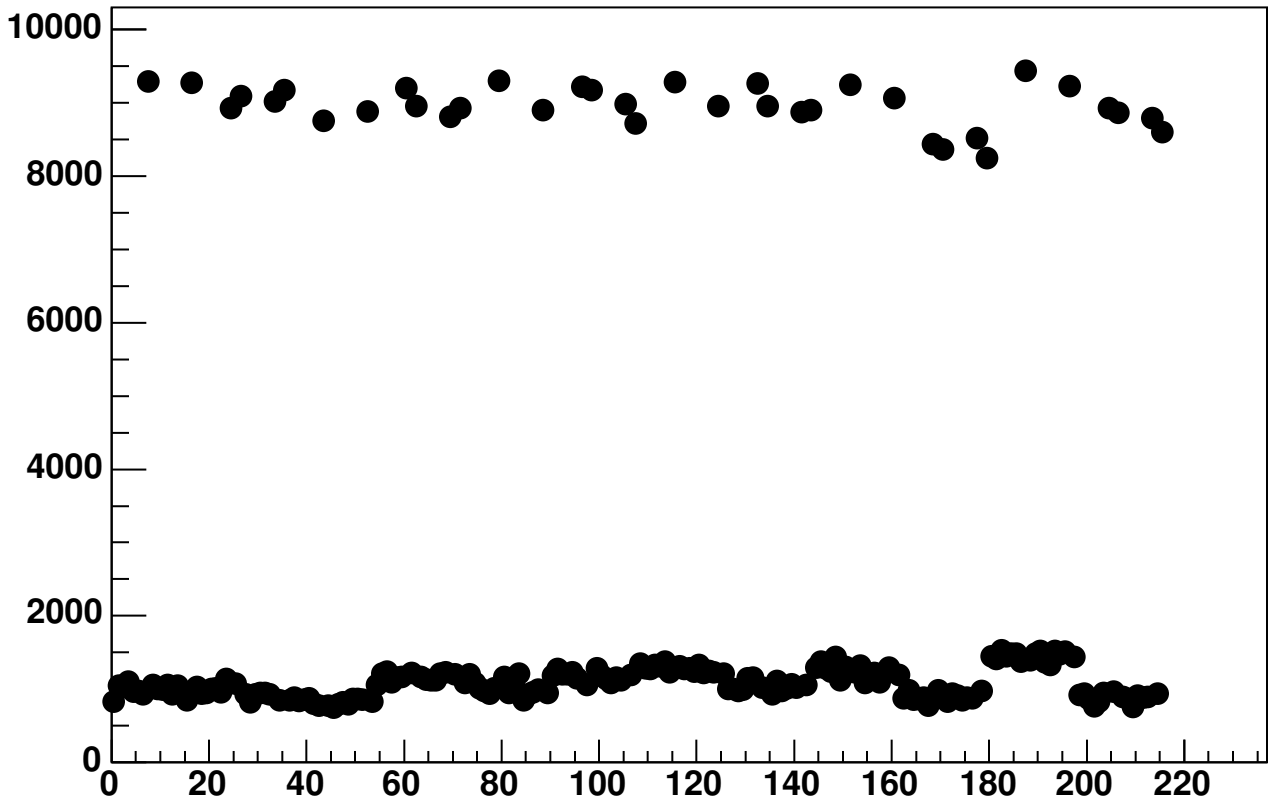
Enable 5, Hold=30, DAC=14336, ADC Mean vs 18*Chip+Chan



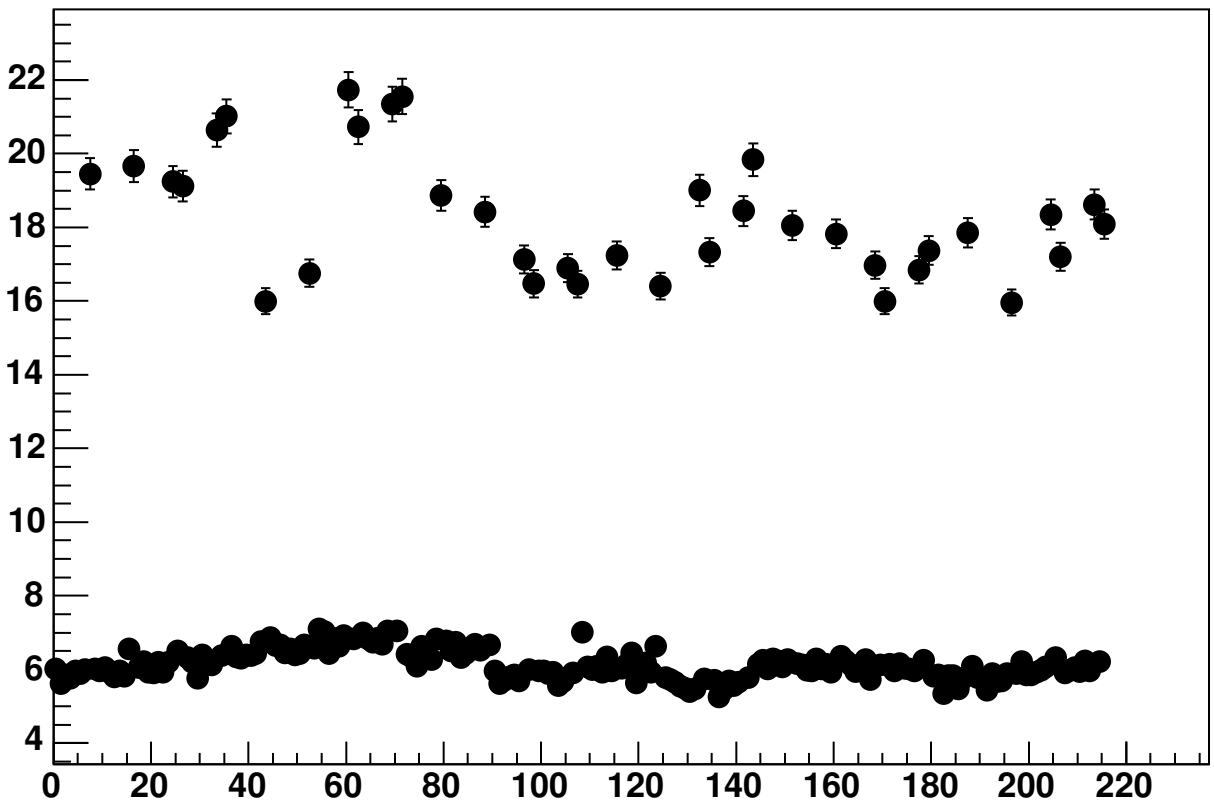
Enable 5, Hold=30, DAC=14336, ADC Noise vs 18*Chip+Chan



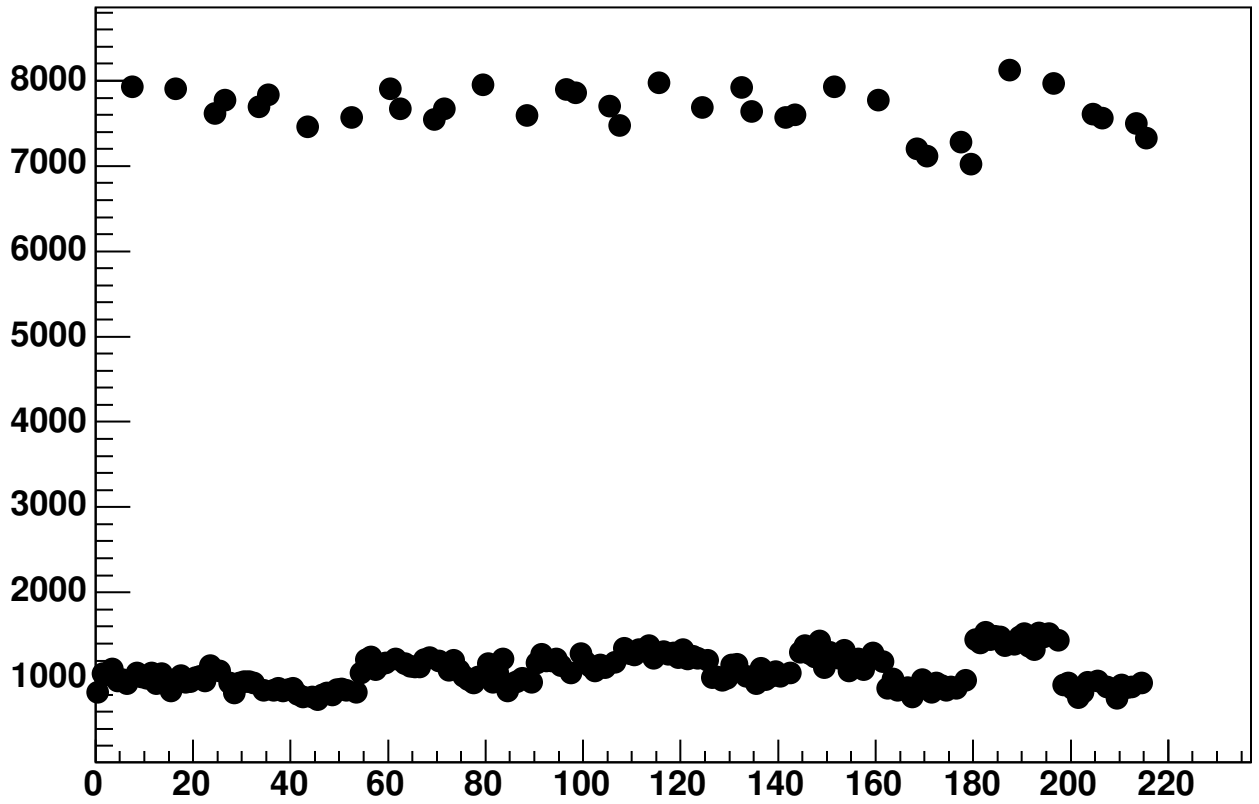
Enable 5, Hold=30, DAC=12288, ADC Mean vs 18*Chip+Chan



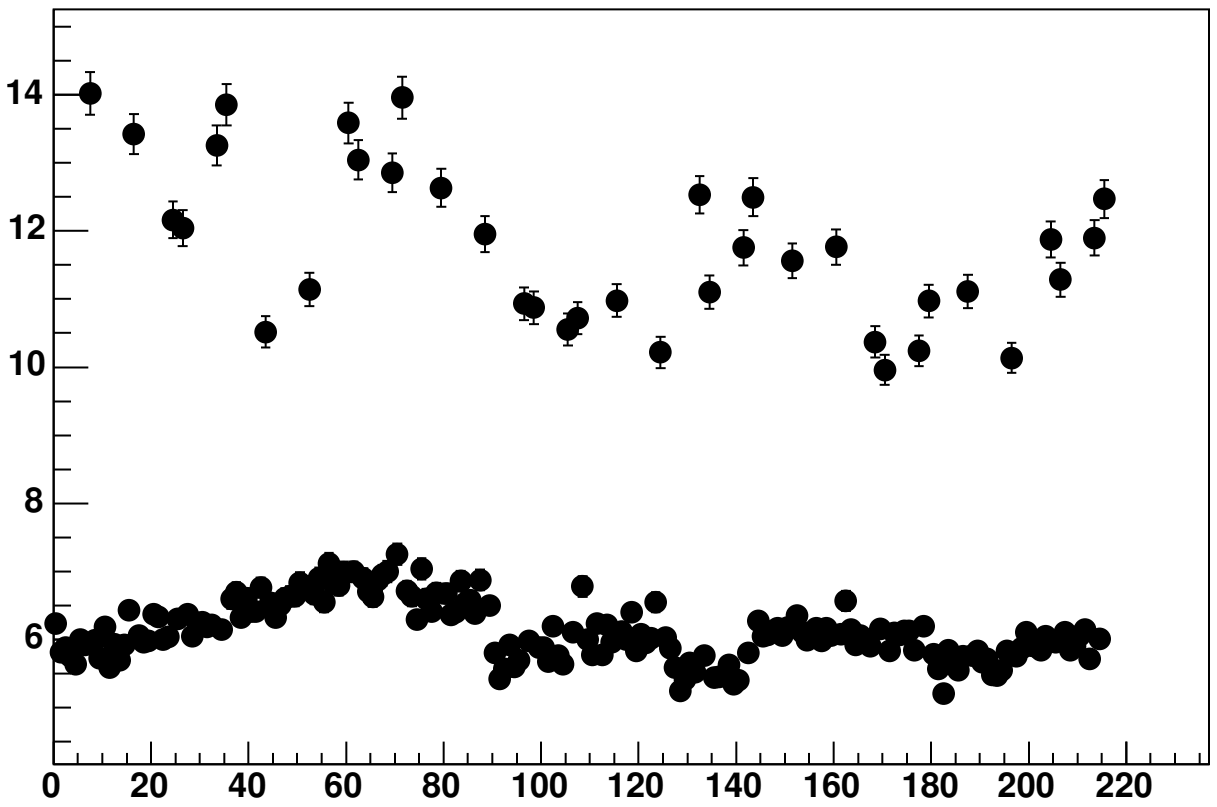
Enable 5, Hold=30, DAC=12288, ADC Noise vs 18*Chip+Chan



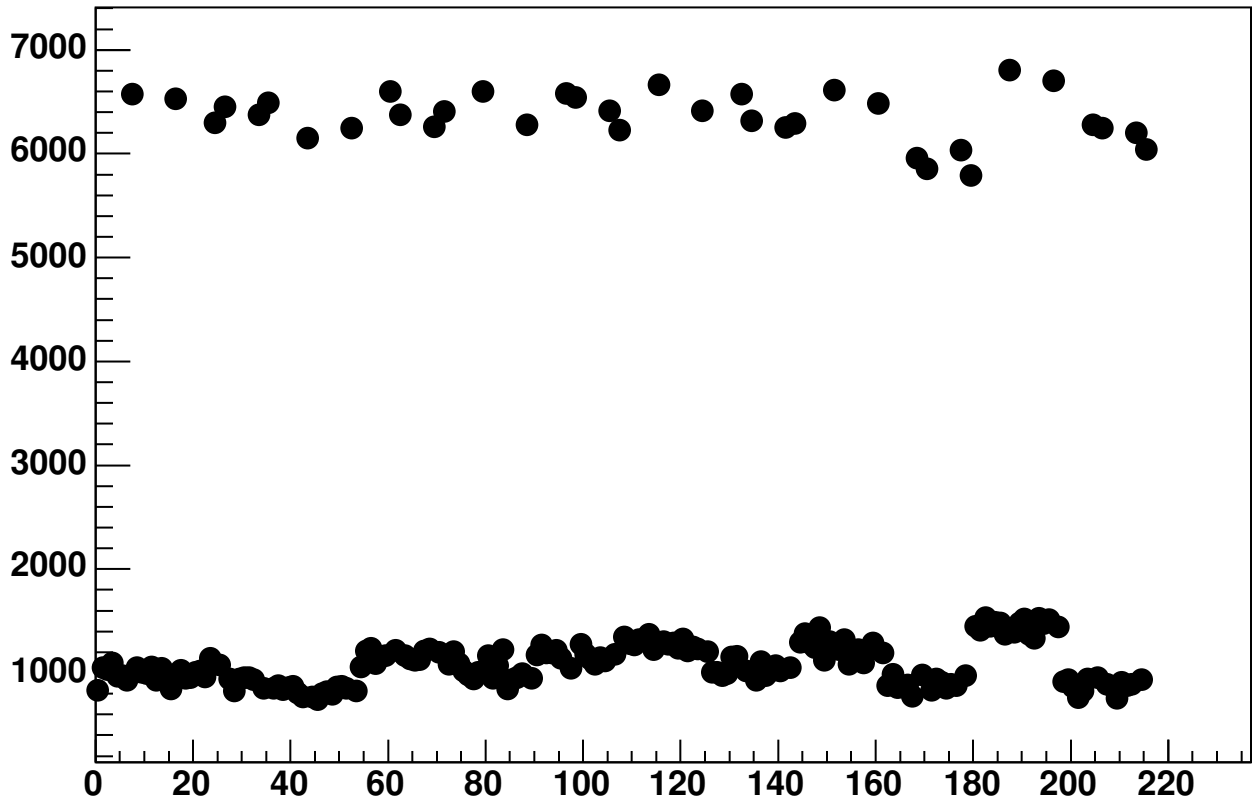
Enable 5, Hold=30, DAC=10240, ADC Mean vs 18*Chip+Chan



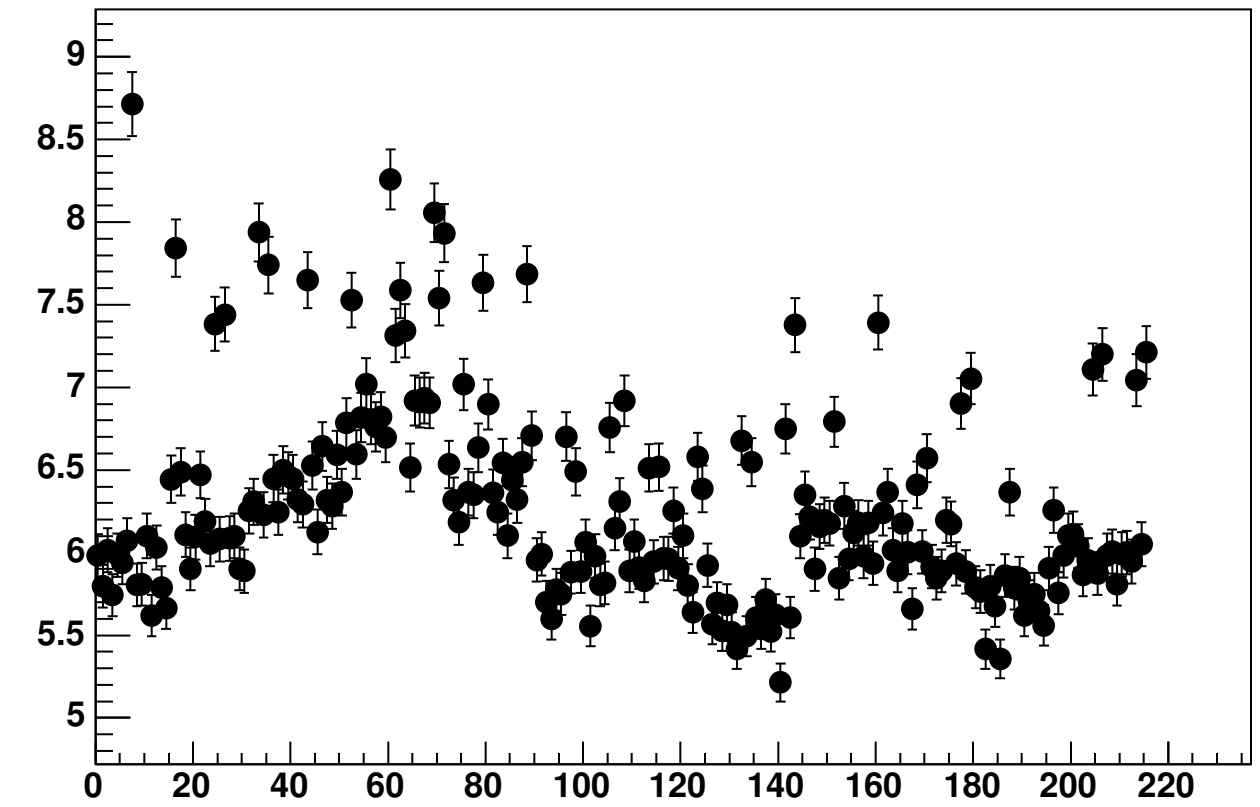
Enable 5, Hold=30, DAC=10240, ADC Noise vs 18*Chip+Chan



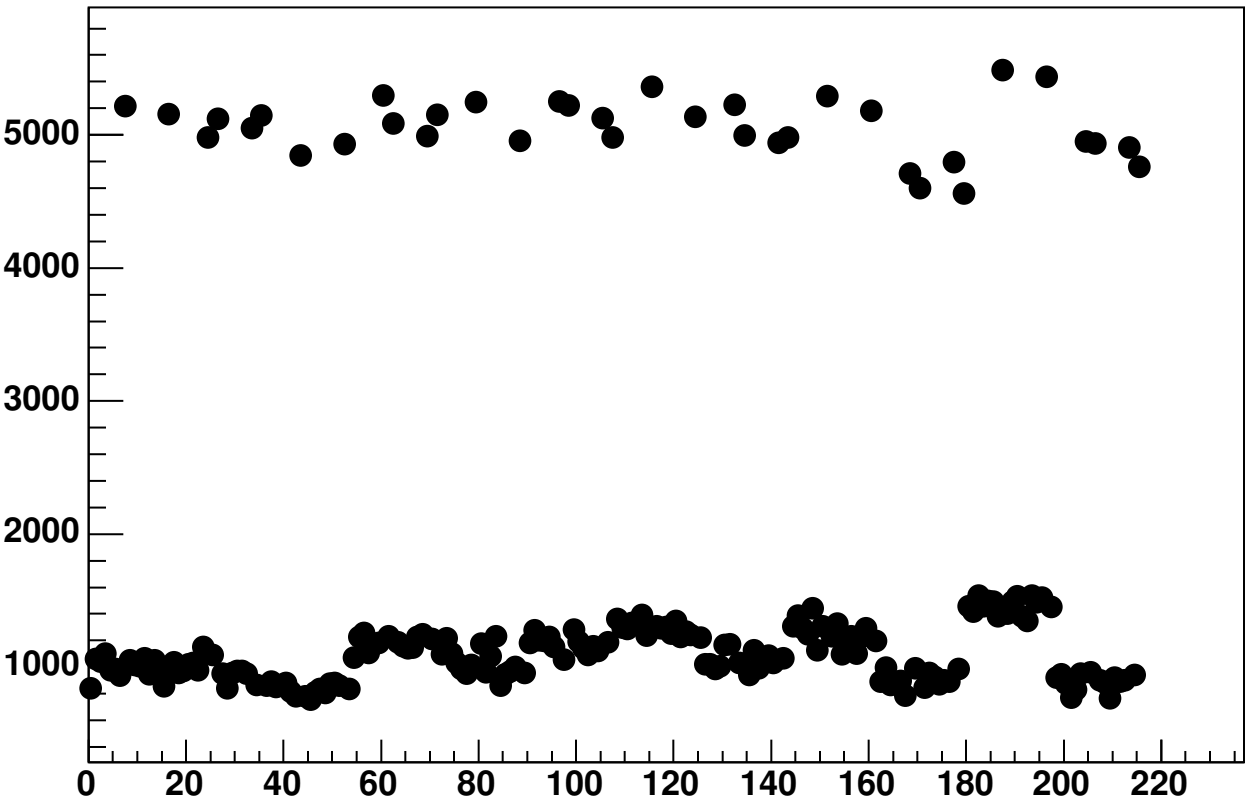
Enable 5, Hold=30, DAC=8192, ADC Mean vs 18*Chip+Chan



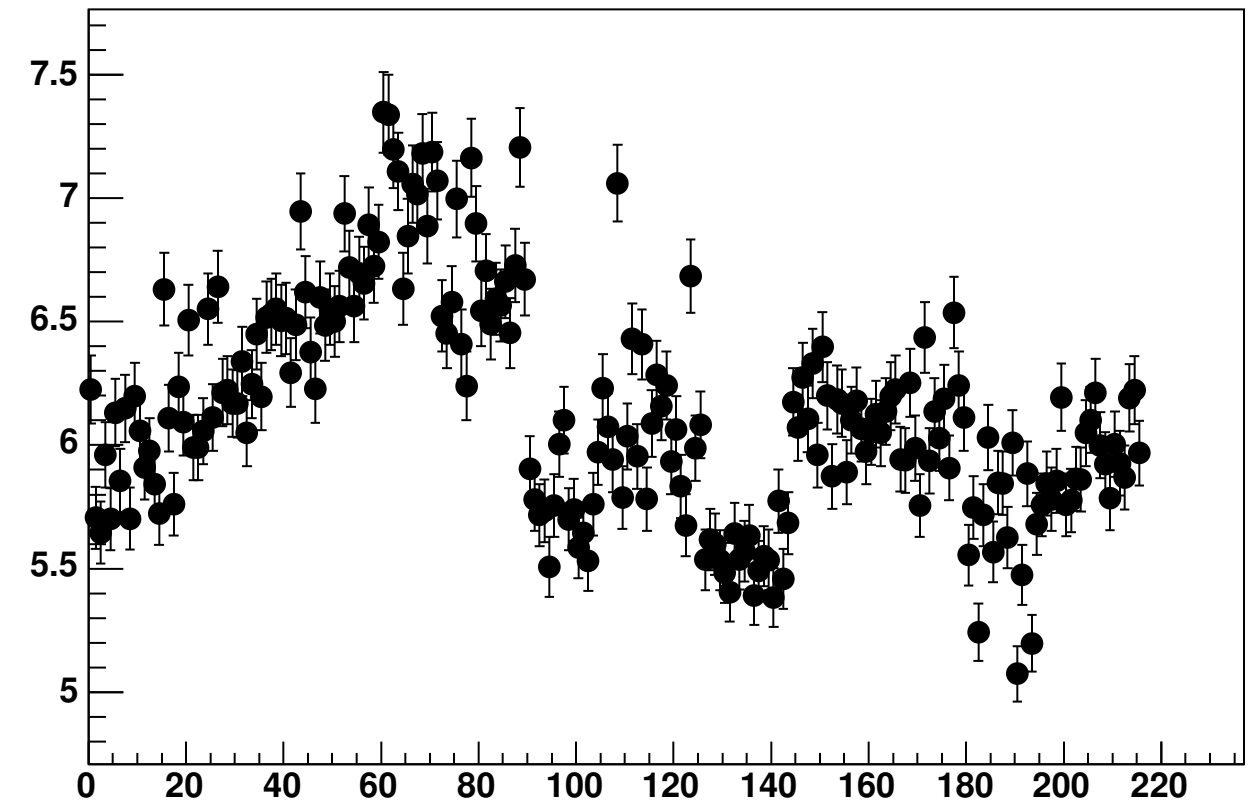
Enable 5, Hold=30, DAC=8192, ADC Noise vs 18*Chip+Chan



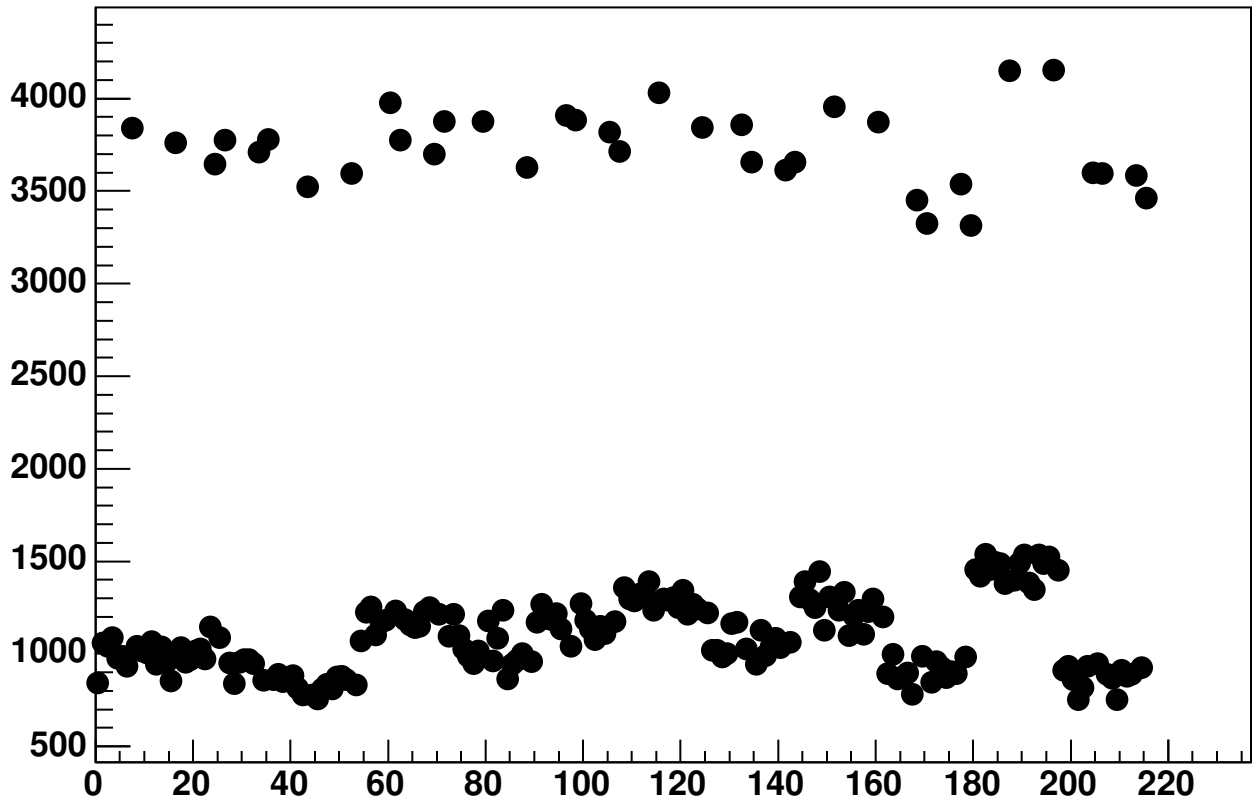
Enable 5, Hold=30, DAC=6144, ADC Mean vs 18*Chip+Chan



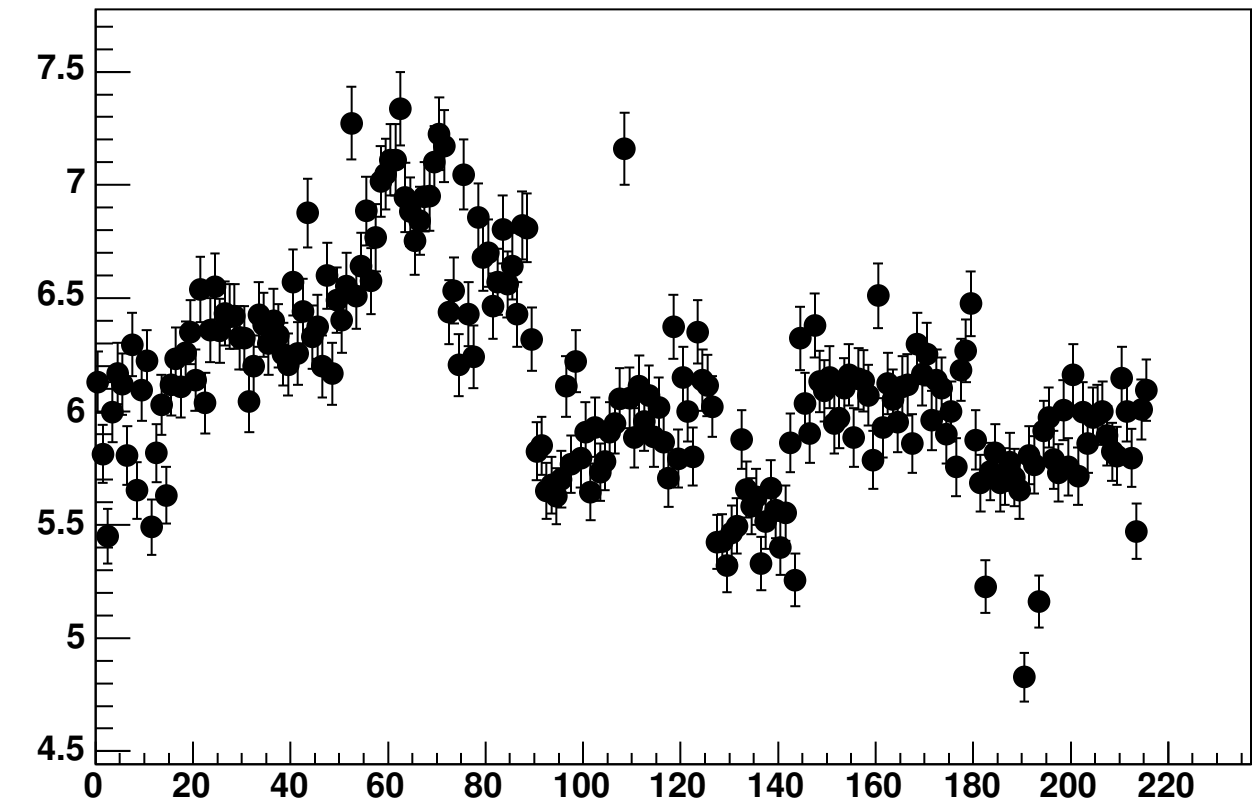
Enable 5, Hold=30, DAC=6144, ADC Noise vs 18*Chip+Chan



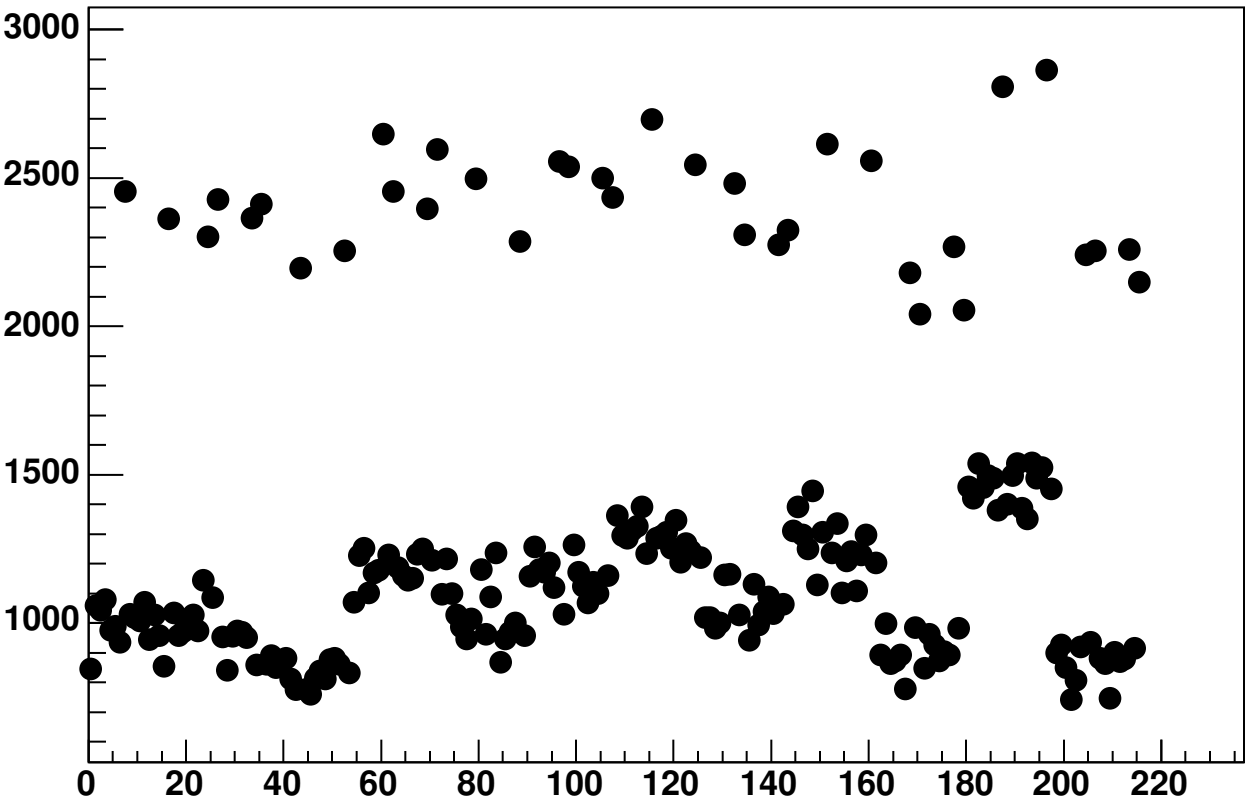
Enable 5, Hold=30, DAC=4096, ADC Mean vs 18*Chip+Chan



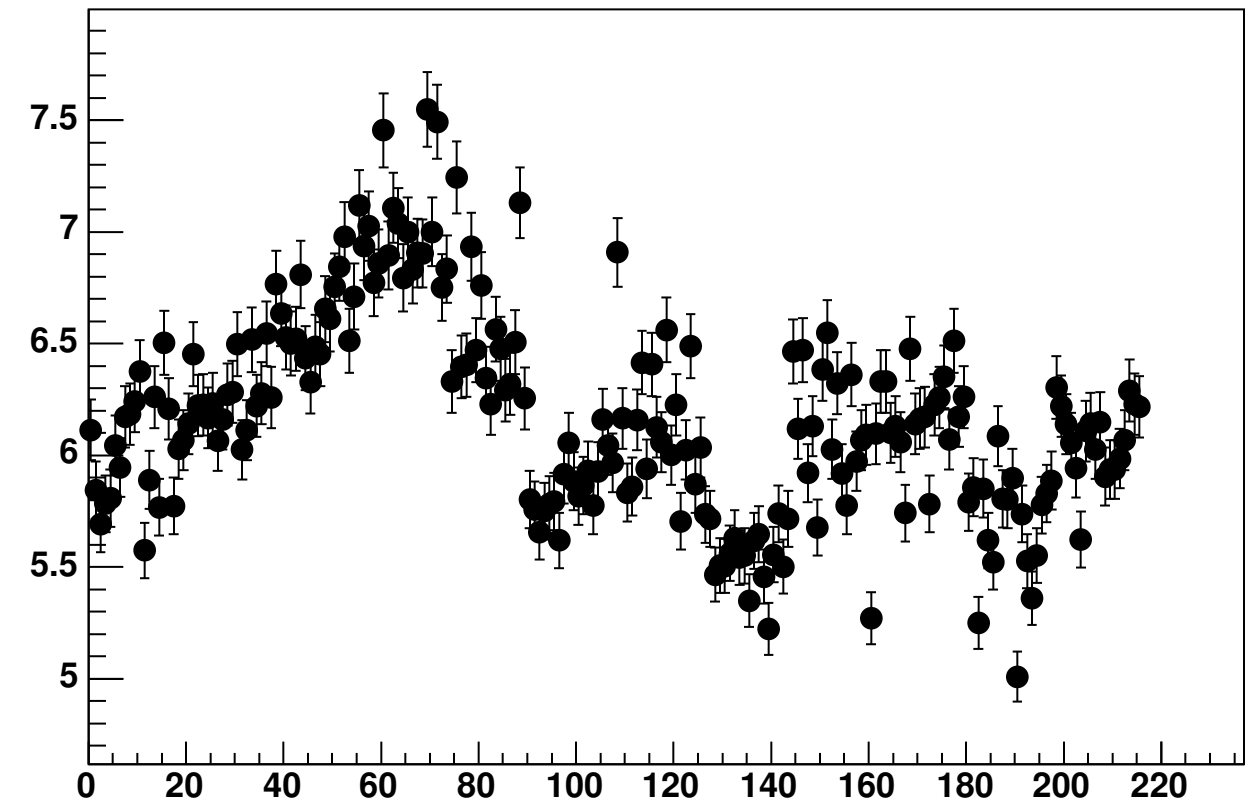
Enable 5, Hold=30, DAC=4096, ADC Noise vs 18*Chip+Chan

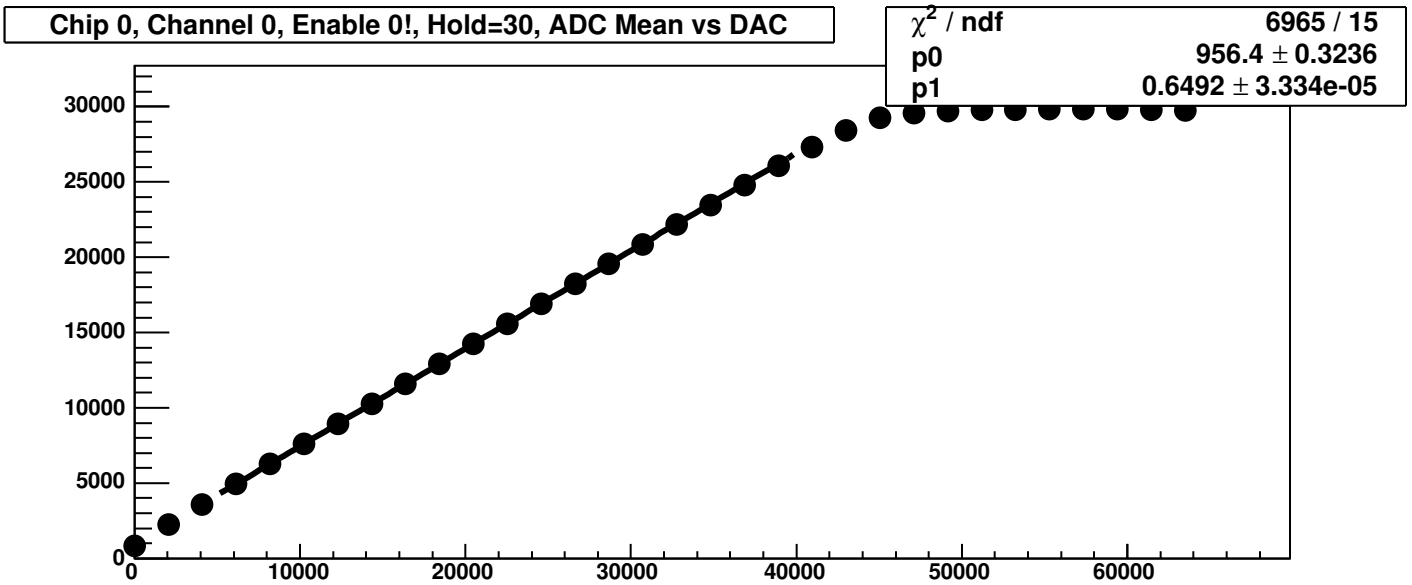


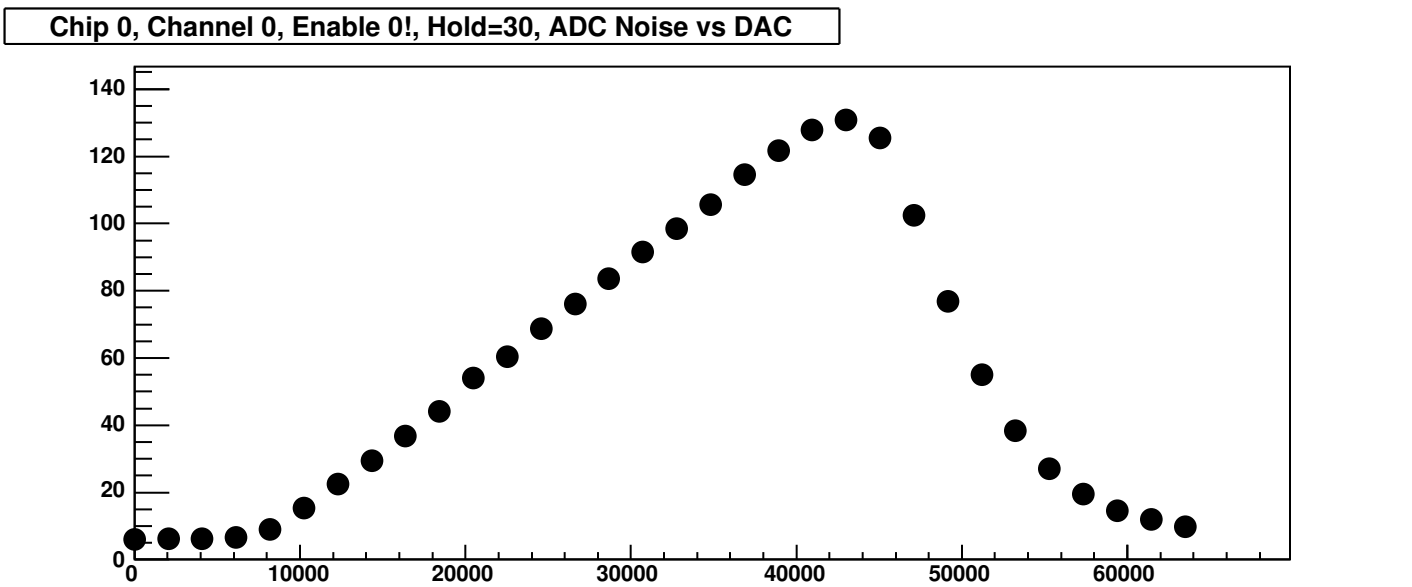
Enable 5, Hold=30, DAC=2048, ADC Mean vs 18*Chip+Chan

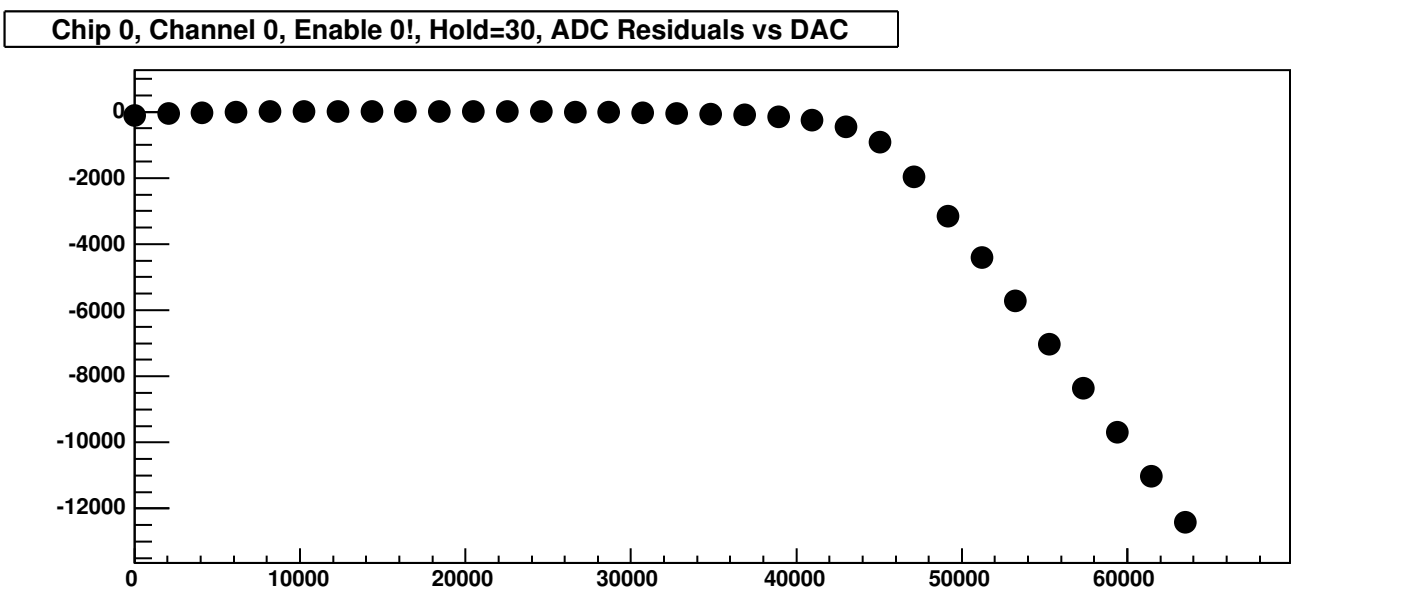


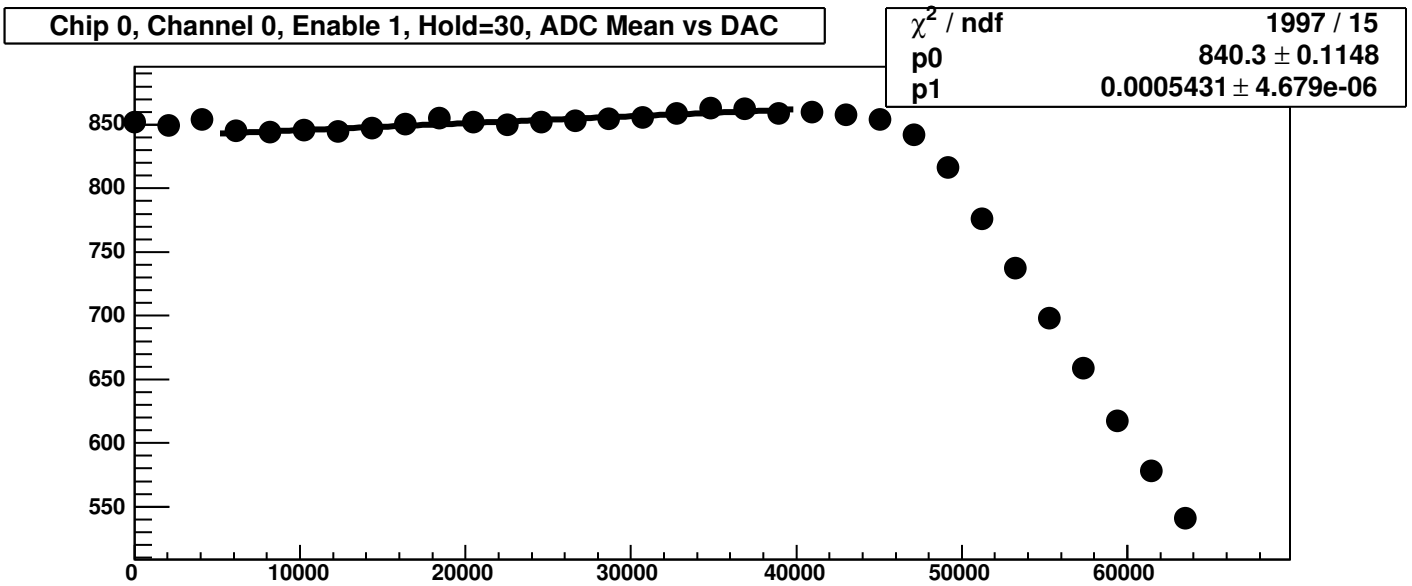
Enable 5, Hold=30, DAC=2048, ADC Noise vs 18*Chip+Chan

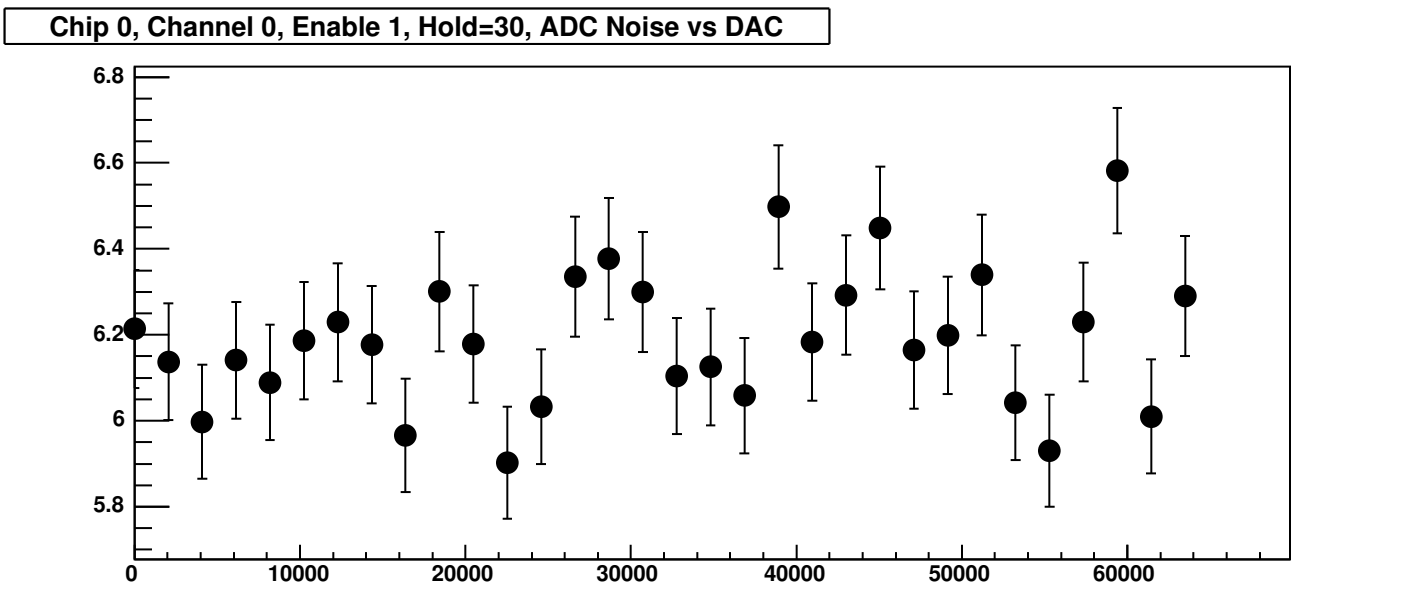


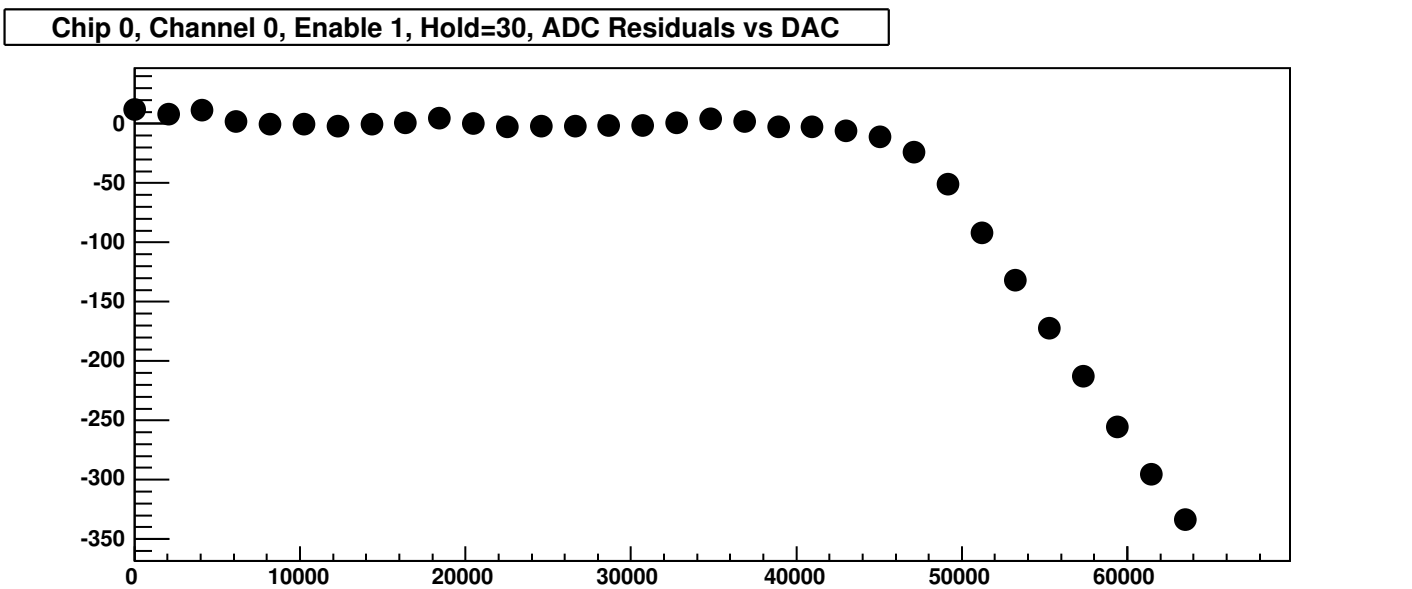

Chip 0, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 6965 / 15
p0 956.4 ± 0.3236
p1 0.6492 ± 3.334e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

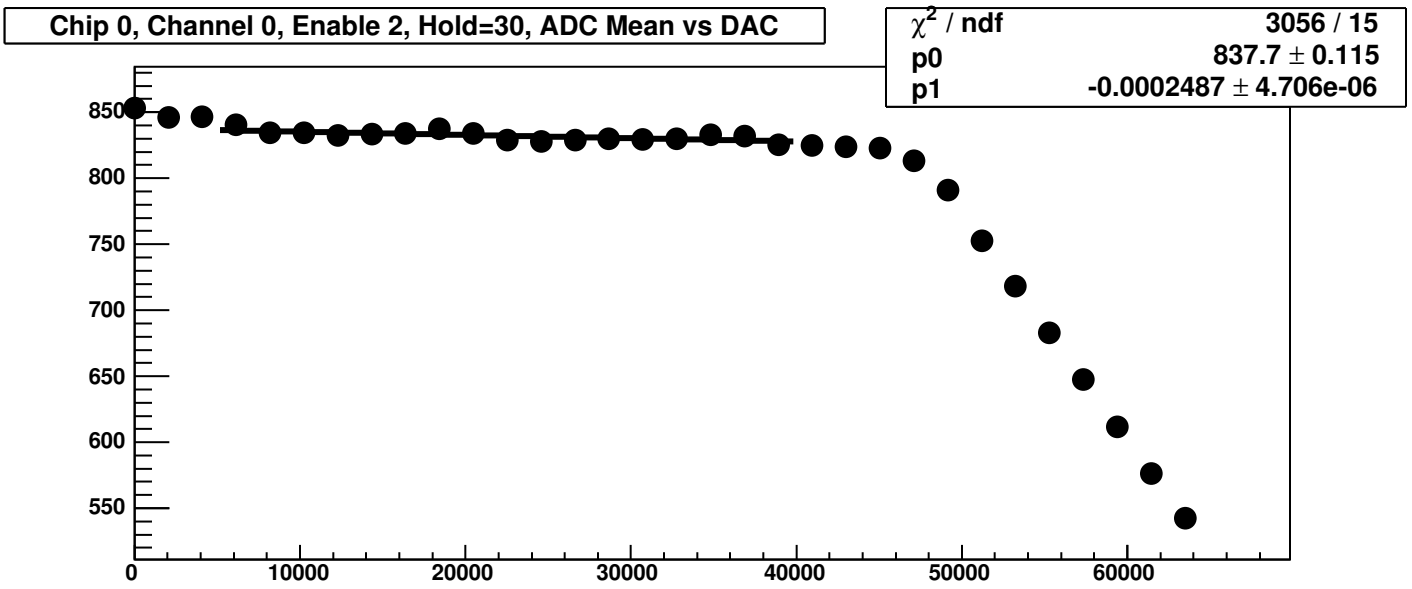

Chip 0, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

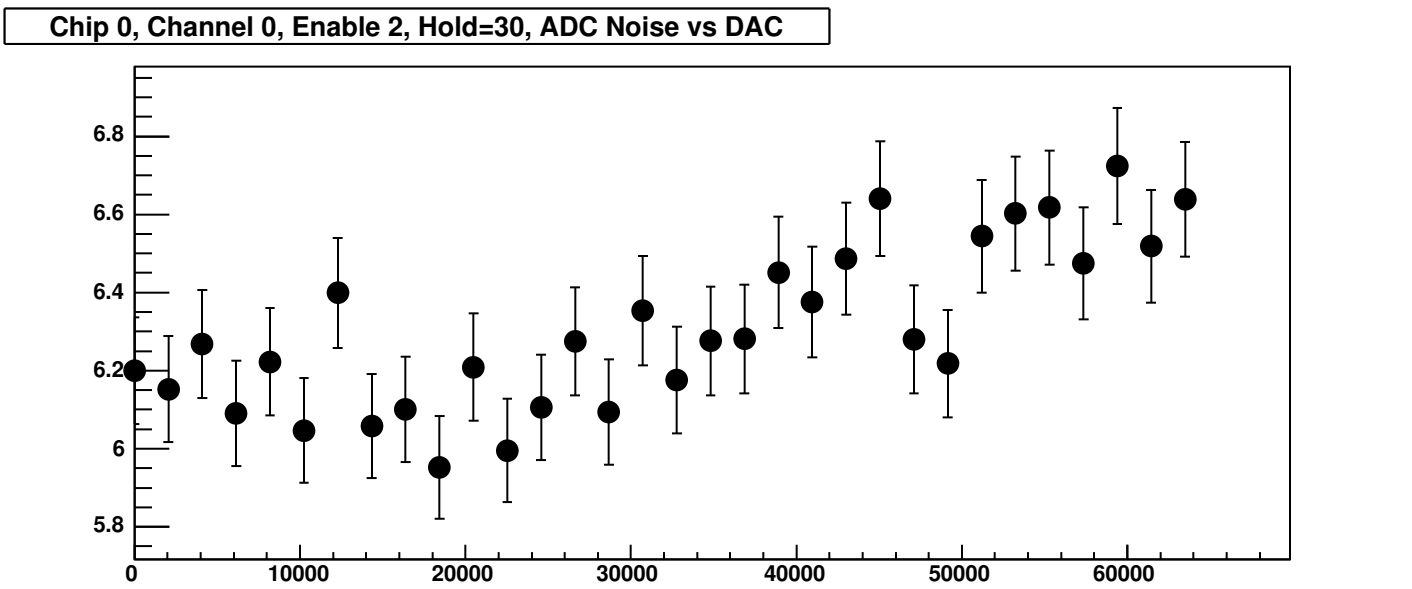

Chip 0, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

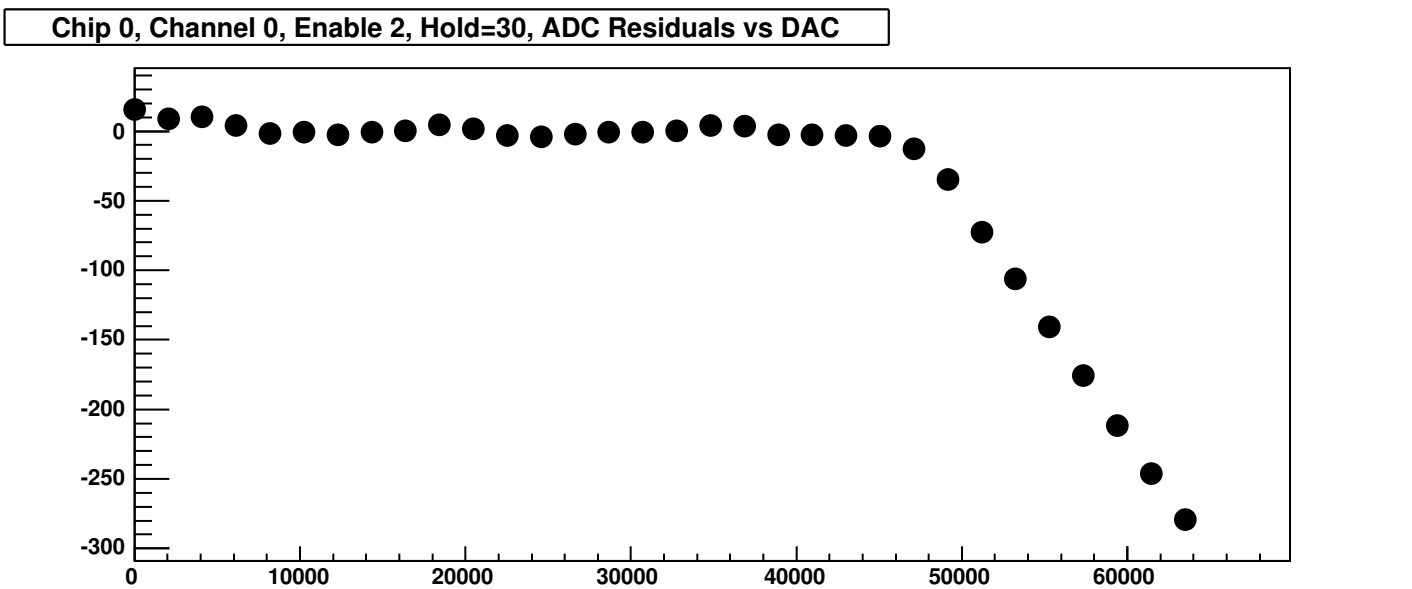

Chip 0, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1997 / 15
p0 840.3 ± 0.1148
p1 0.0005431 ± 4.679e-06
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

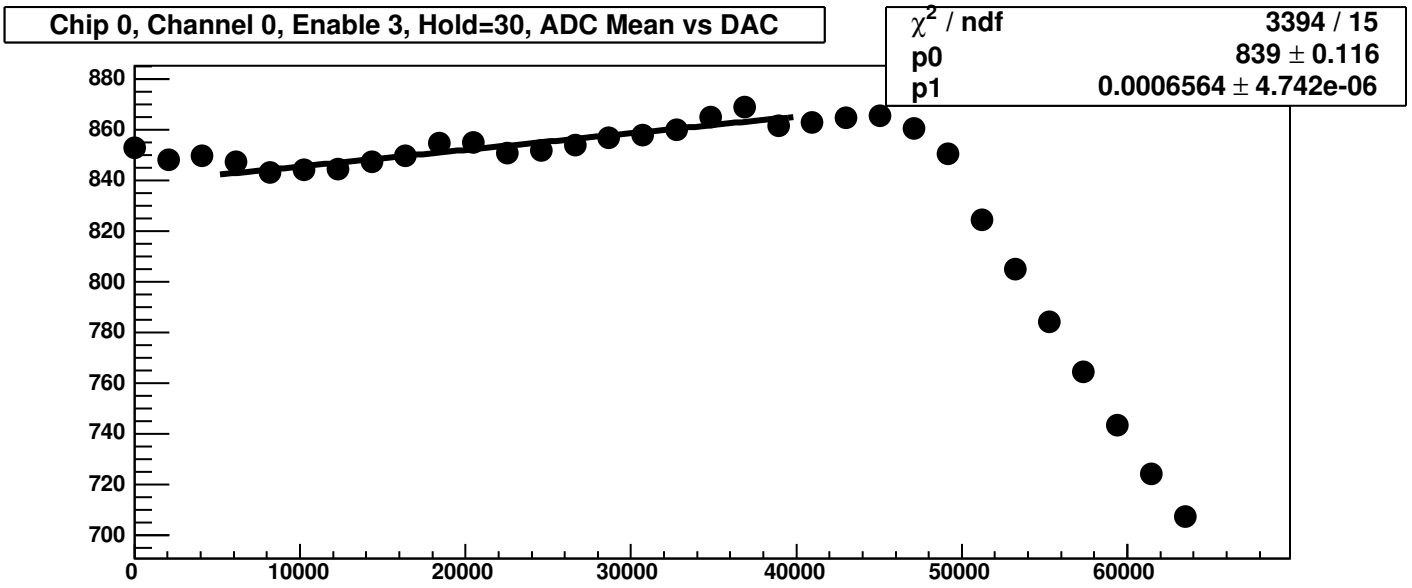

Chip 0, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

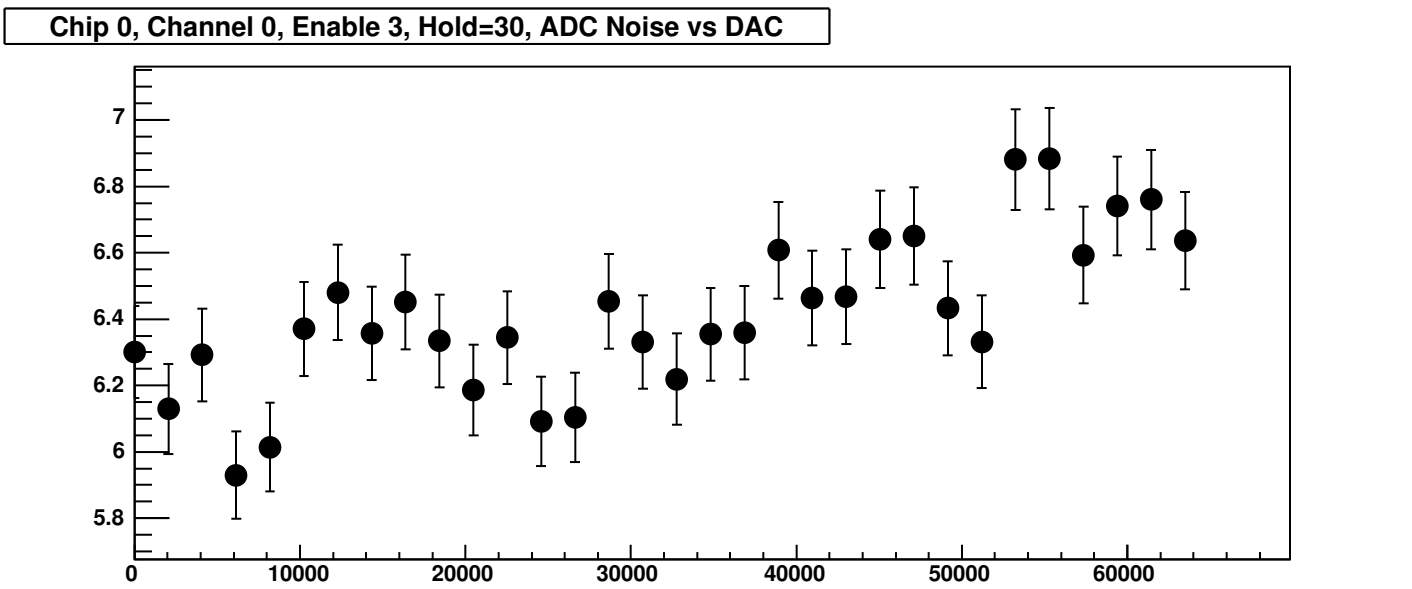

Chip 0, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

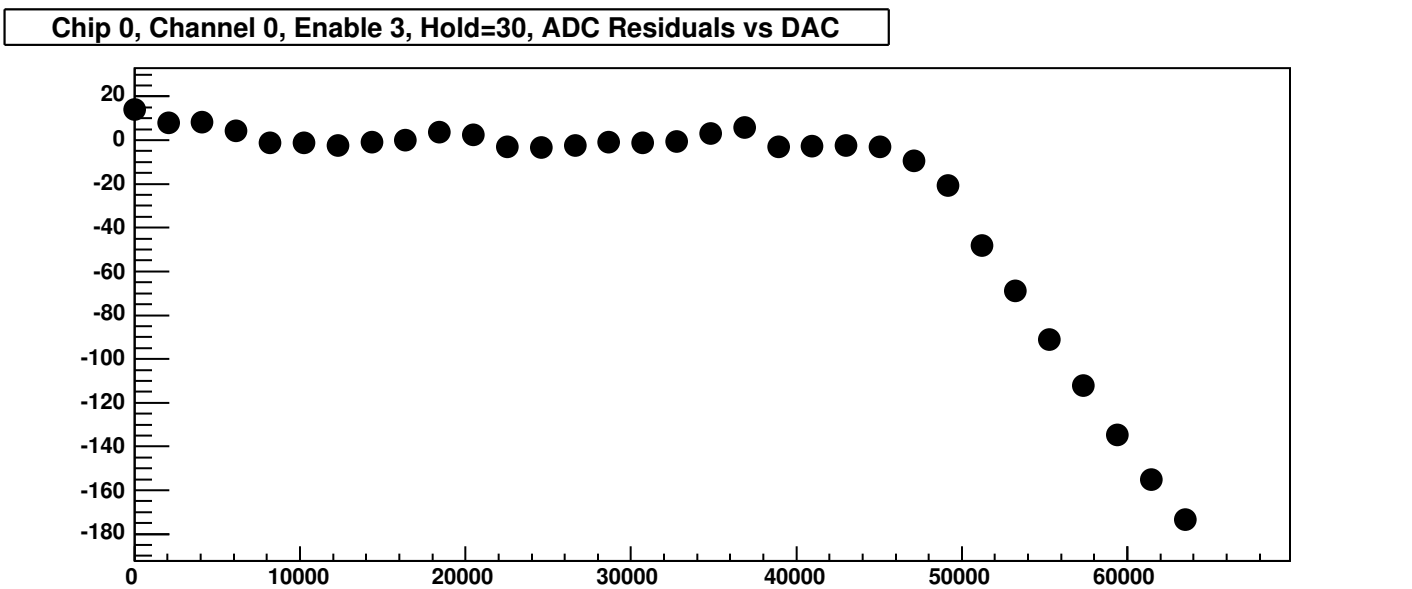

Chip 0, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3056 / 15
p0 837.7 ± 0.115
p1 -0.0002487 ± 4.706e-06
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

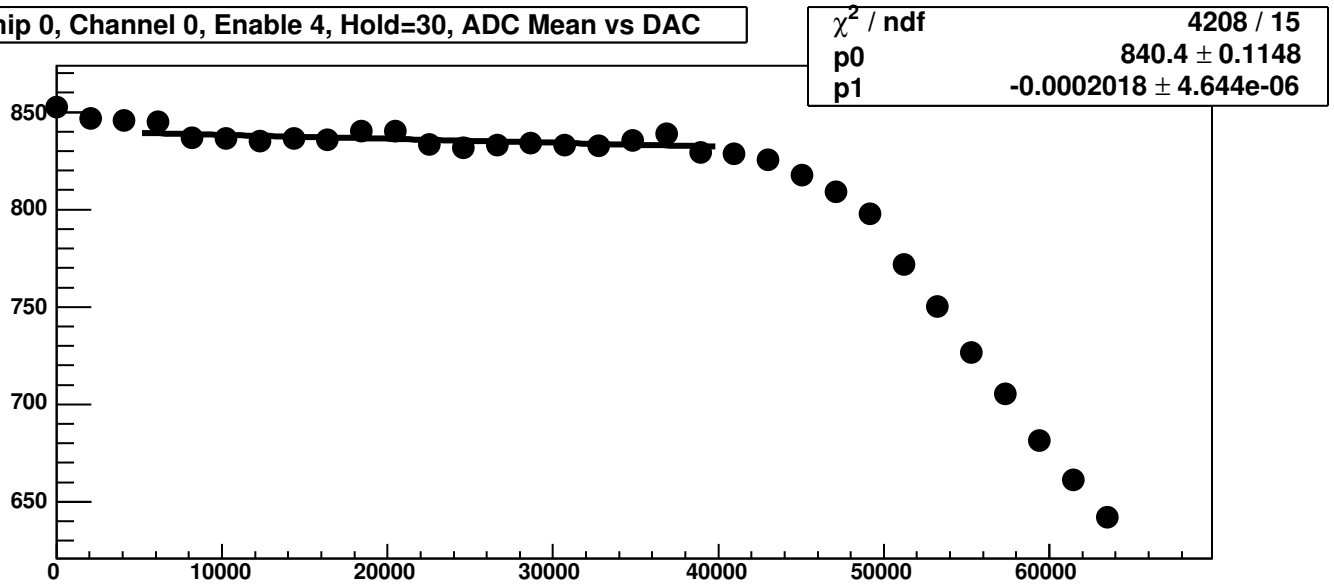

Chip 0, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3394 / 15
p0 839 ± 0.116
p1 0.0006564 ± 4.742e-06
880
860
840
820
800
780
760
740
720
700
0
10000
20000
30000
40000
50000
60000

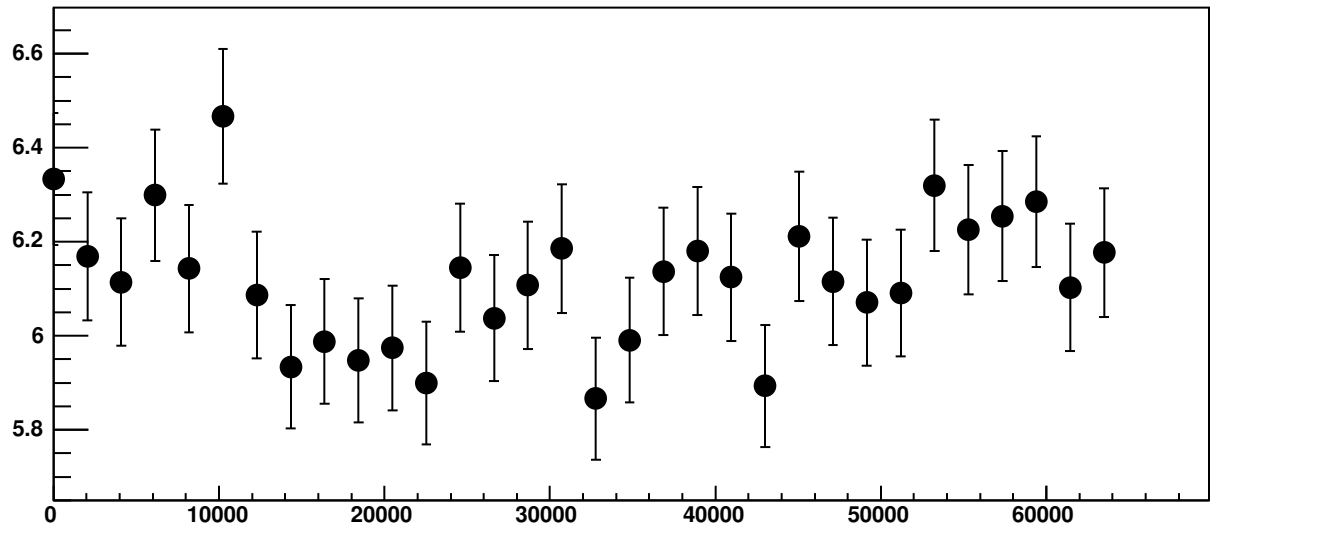

Chip 0, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

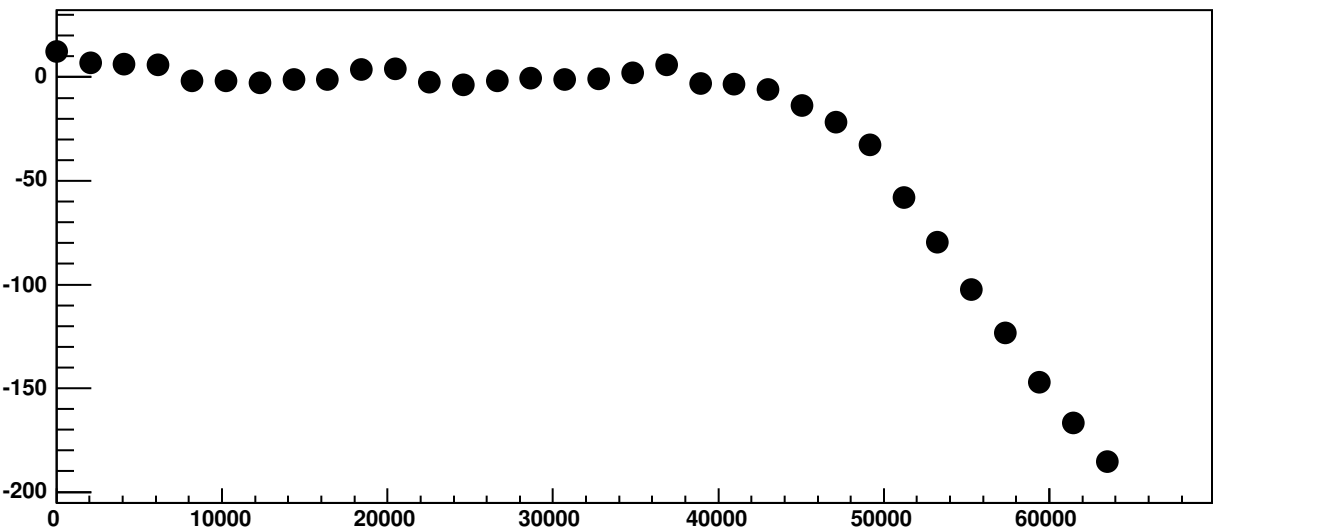
Chip 0, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC

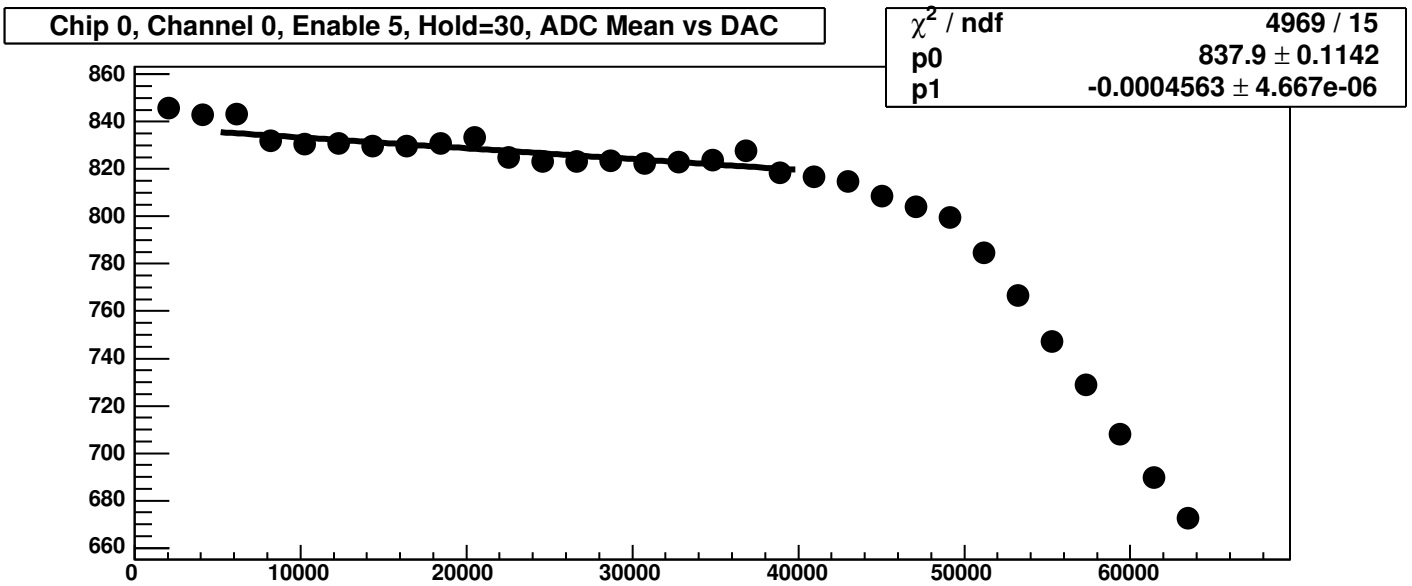


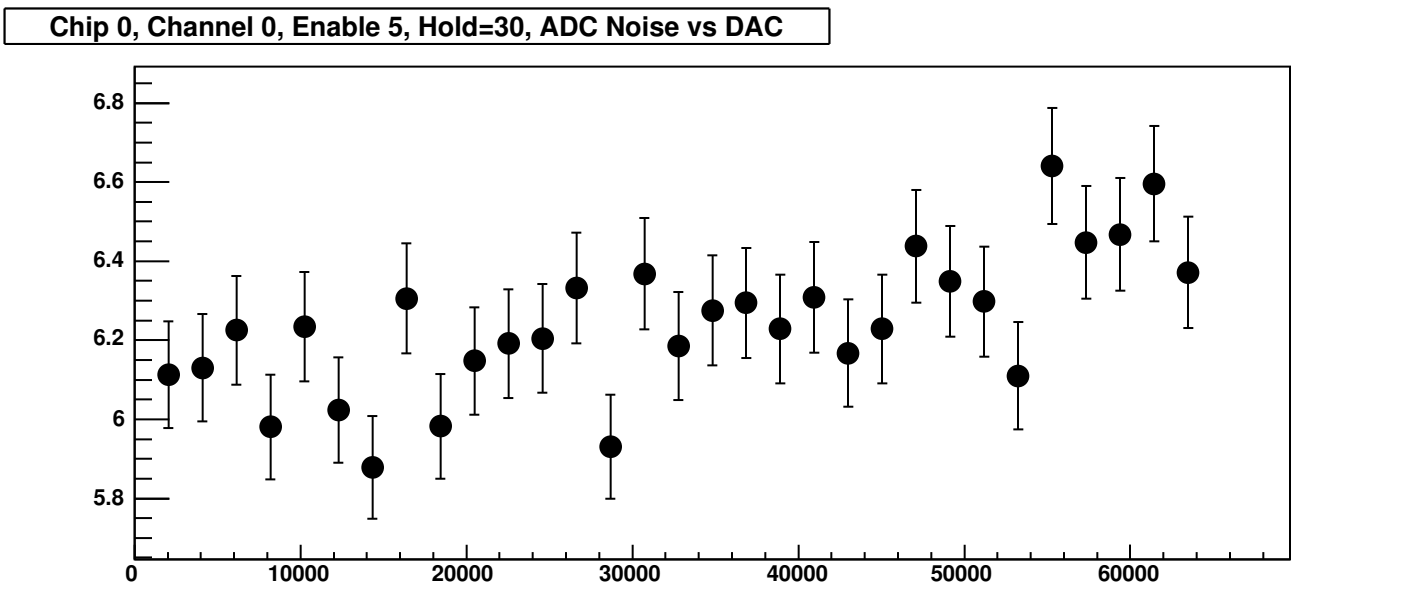
Chip 0, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC

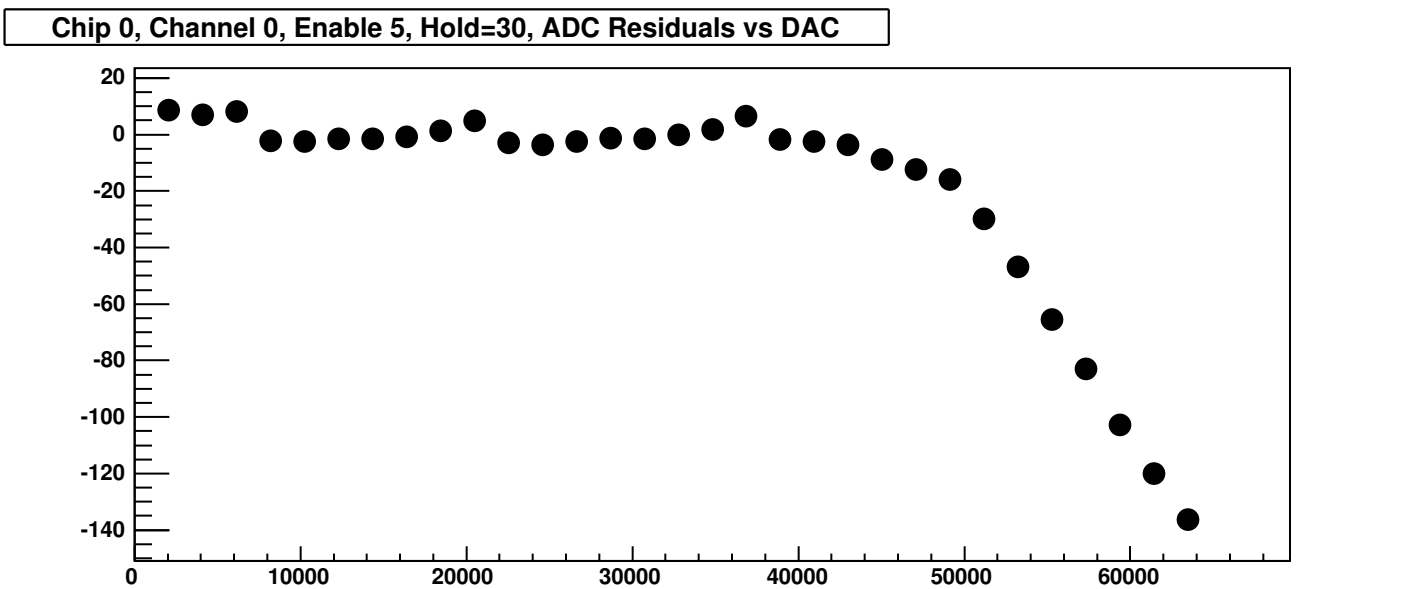


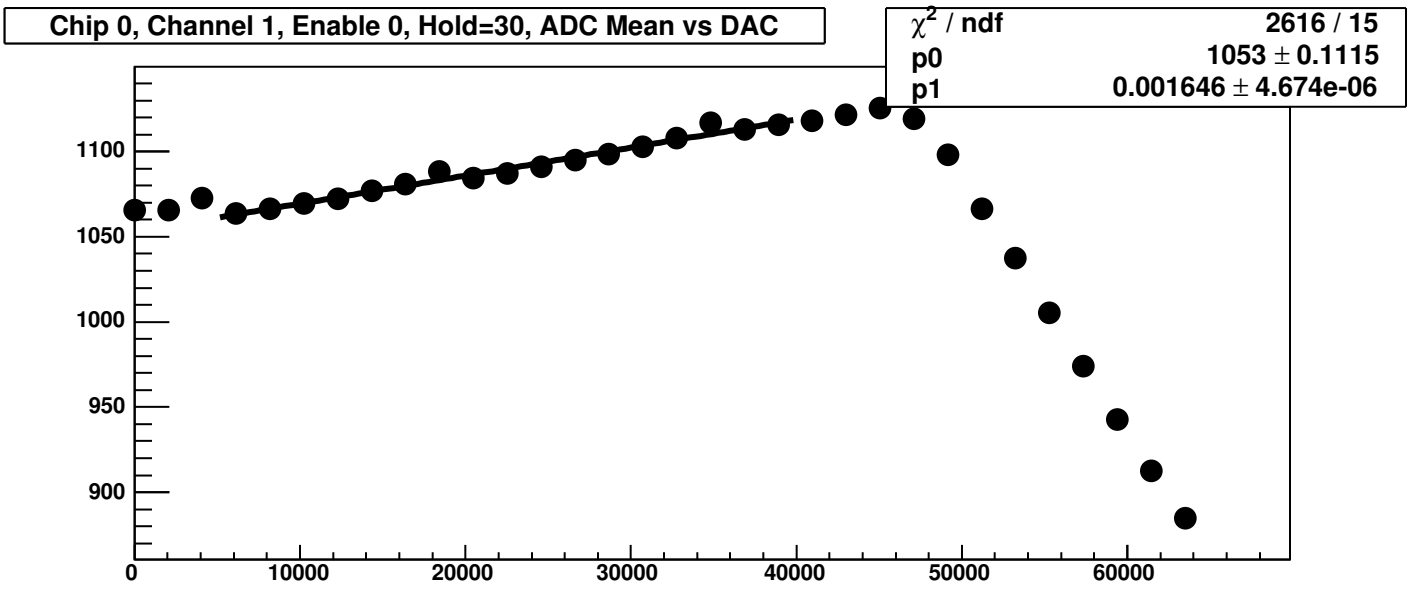
Chip 0, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC

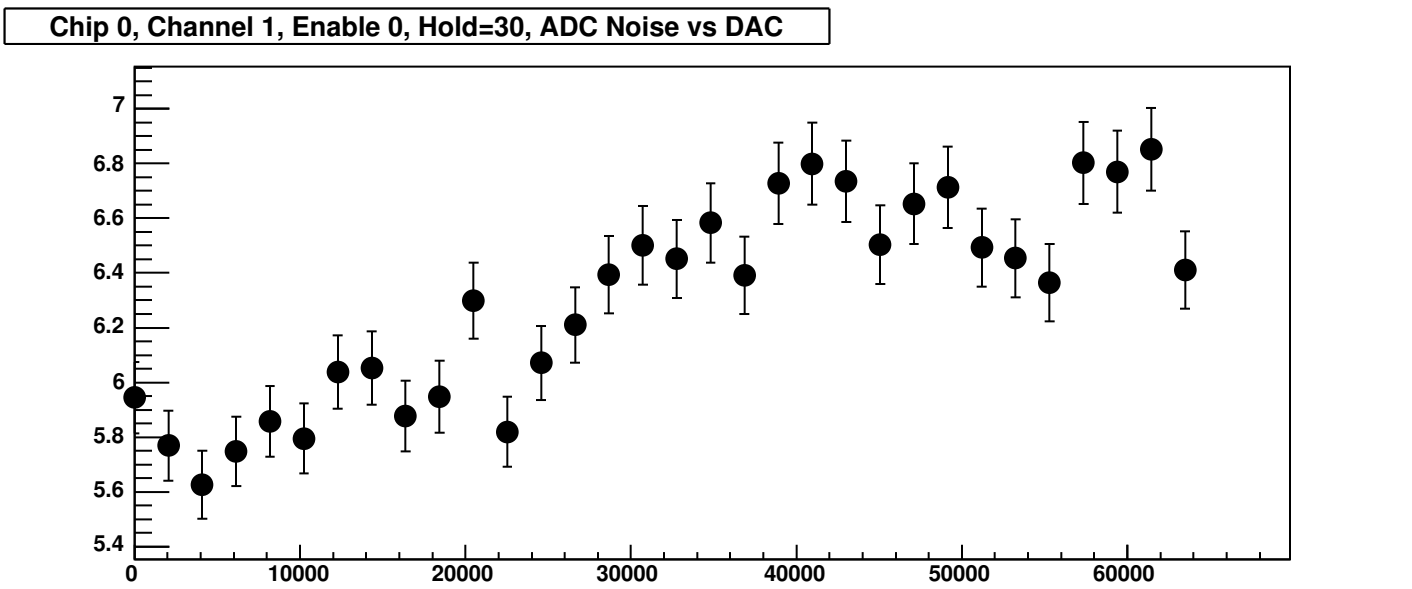


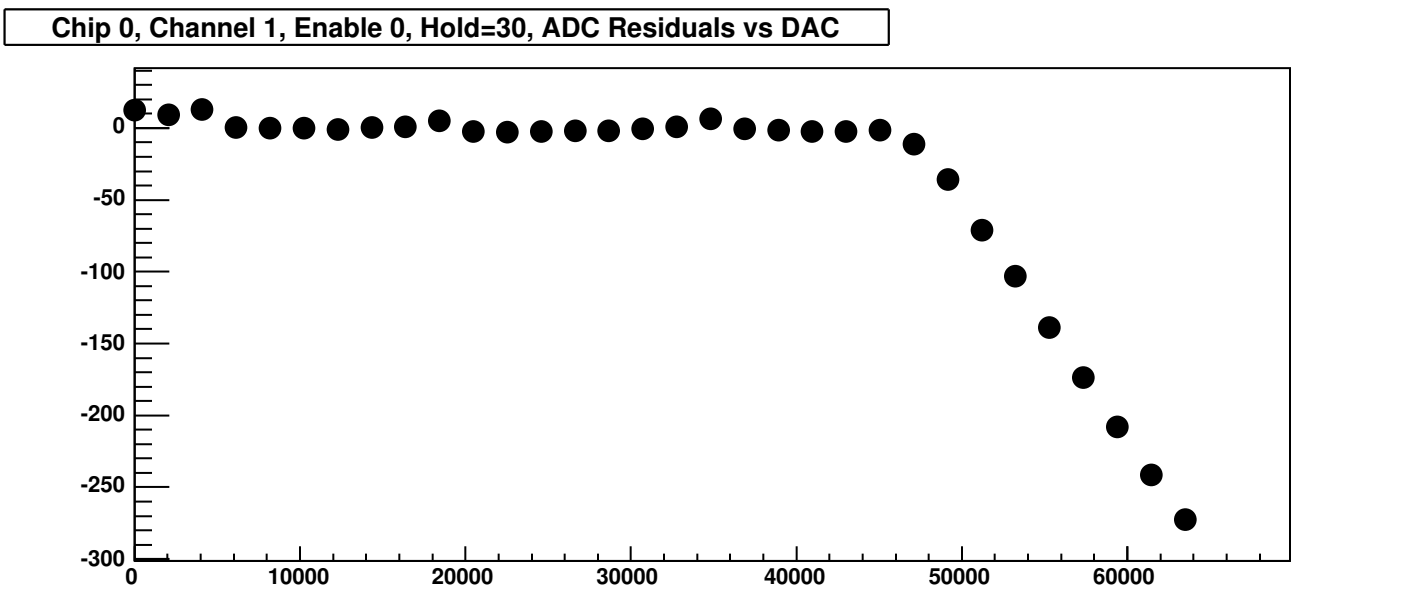

Chip 0, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4969 / 15
p0 837.9 ± 0.1142
p1 -0.0004563 ± 4.667e-06
860
840
820
800
780
760
740
720
700
680
660
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

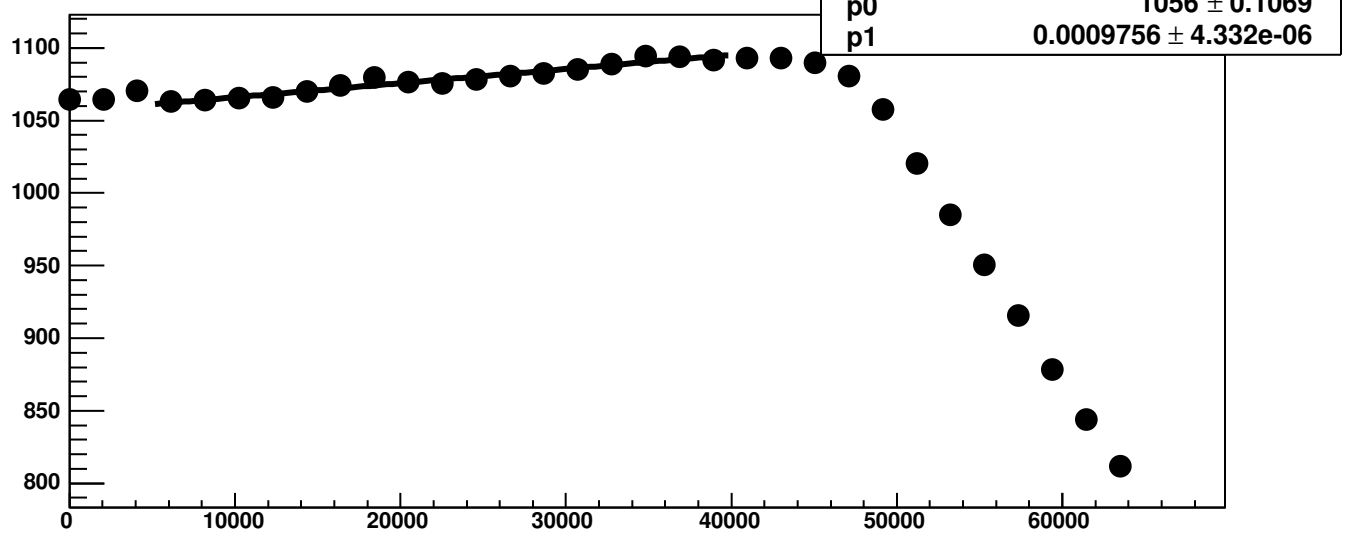

Chip 0, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2616 / 15
p0 1053 ± 0.1115
p1 0.001646 ± 4.674e-06
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

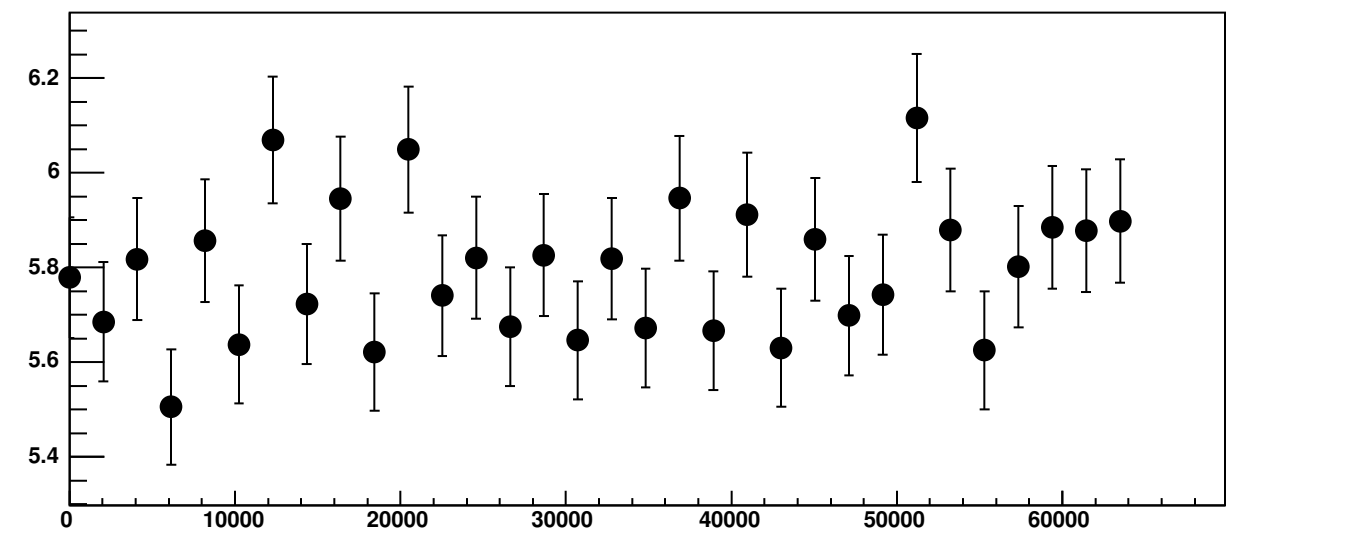

Chip 0, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

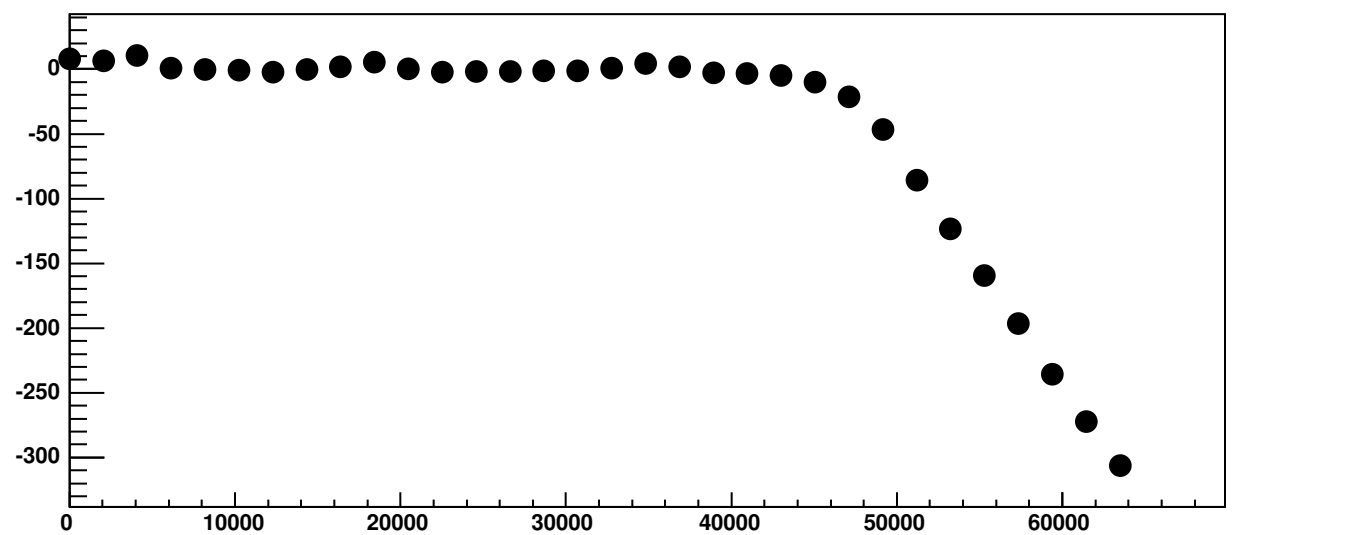
Chip 0, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

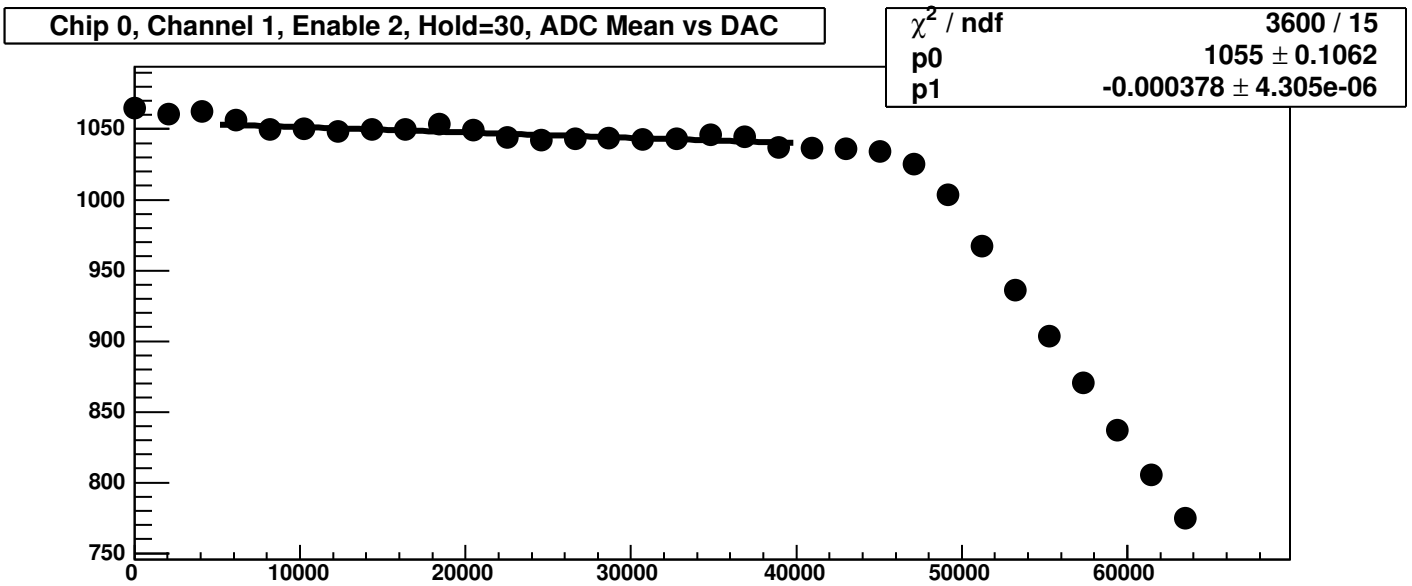


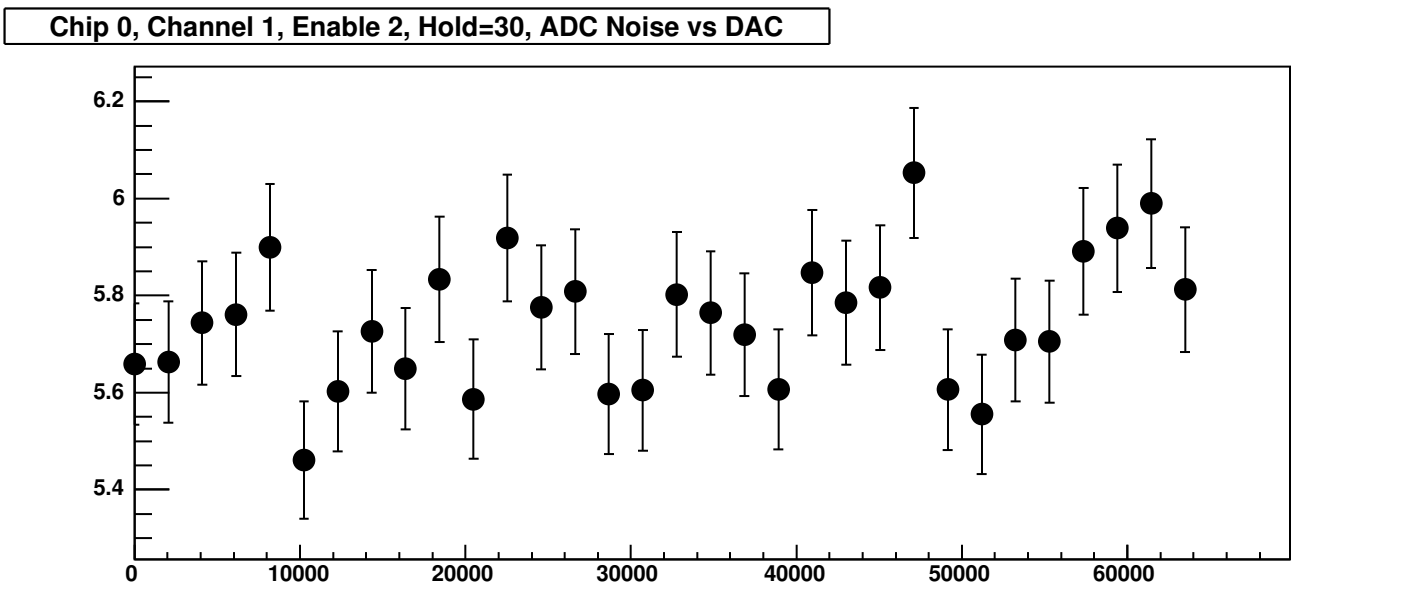
Chip 0, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC

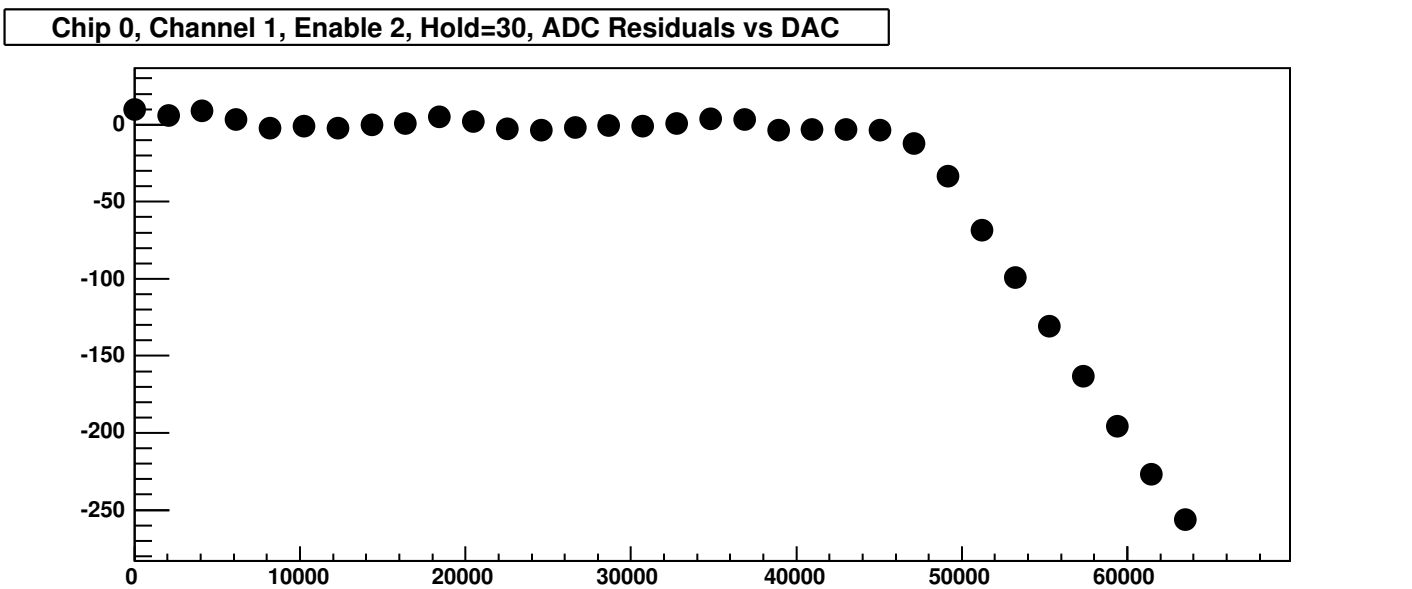


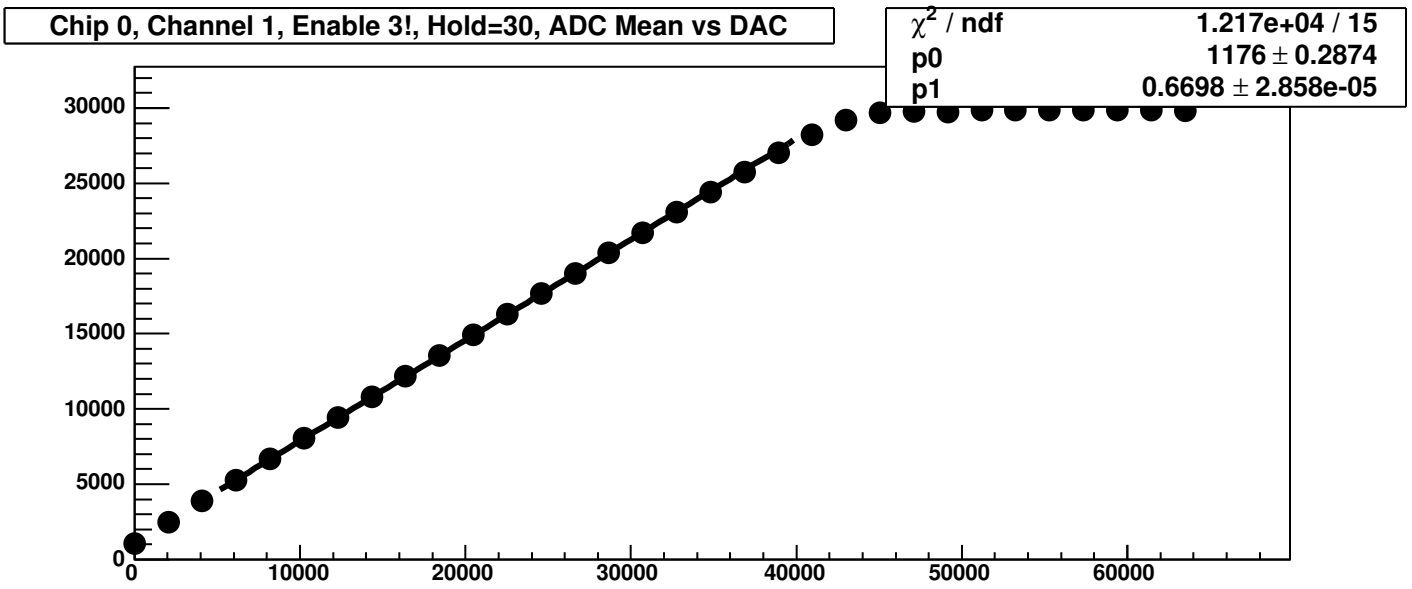
Chip 0, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

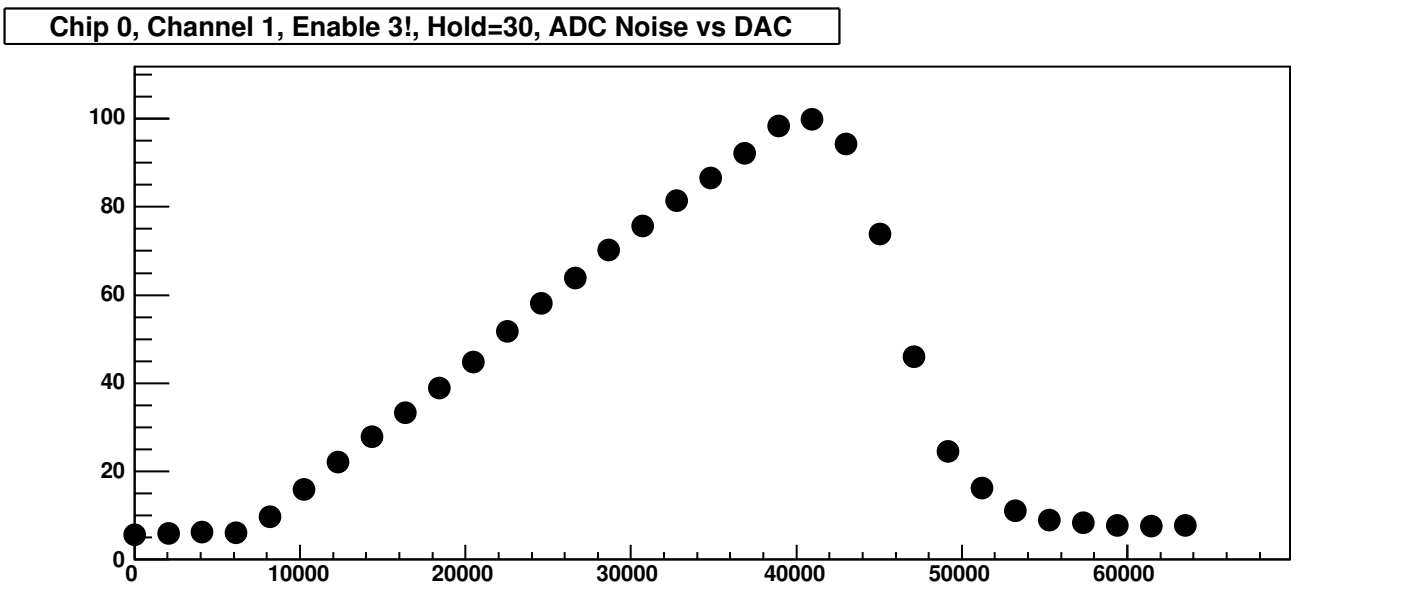


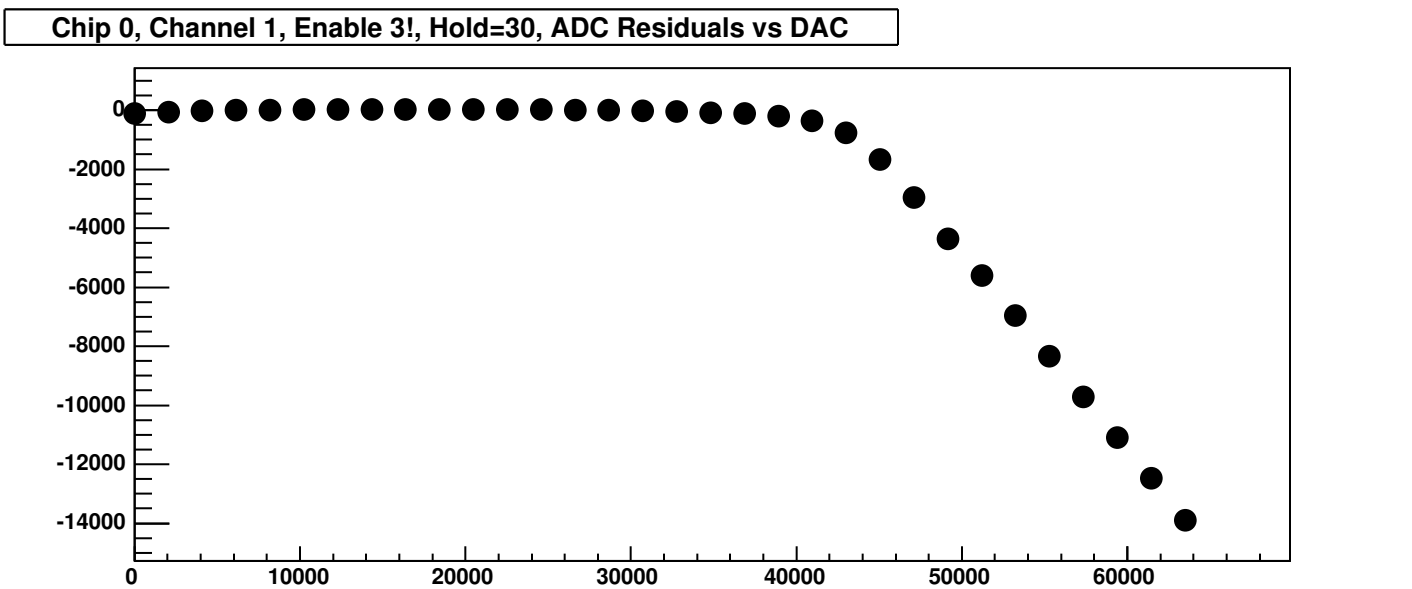

Chip 0, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3600 / 15
p0 1055 ± 0.1062
p1 -0.000378 ± 4.305e-06
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

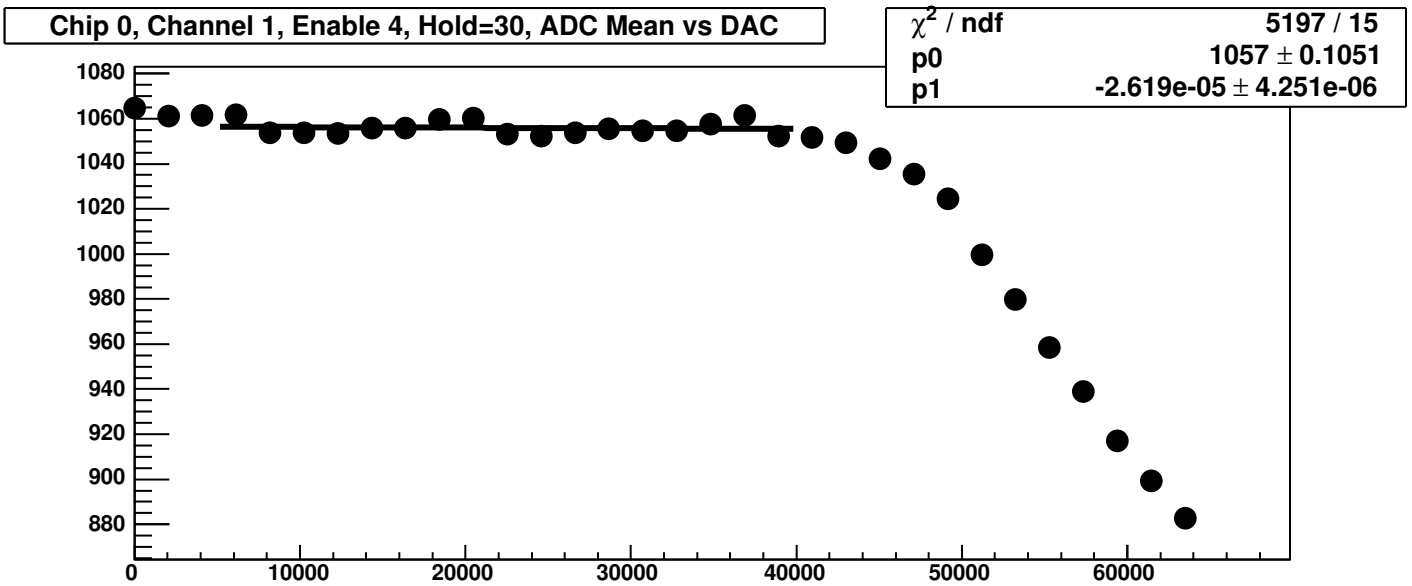

Chip 0, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

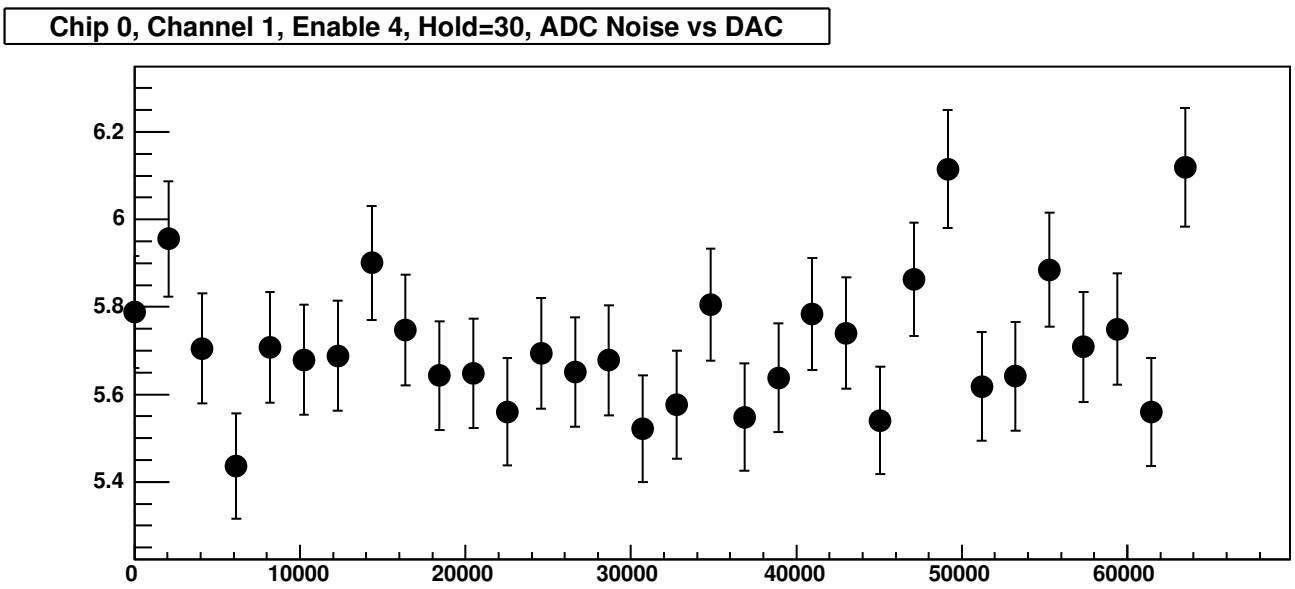

Chip 0, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

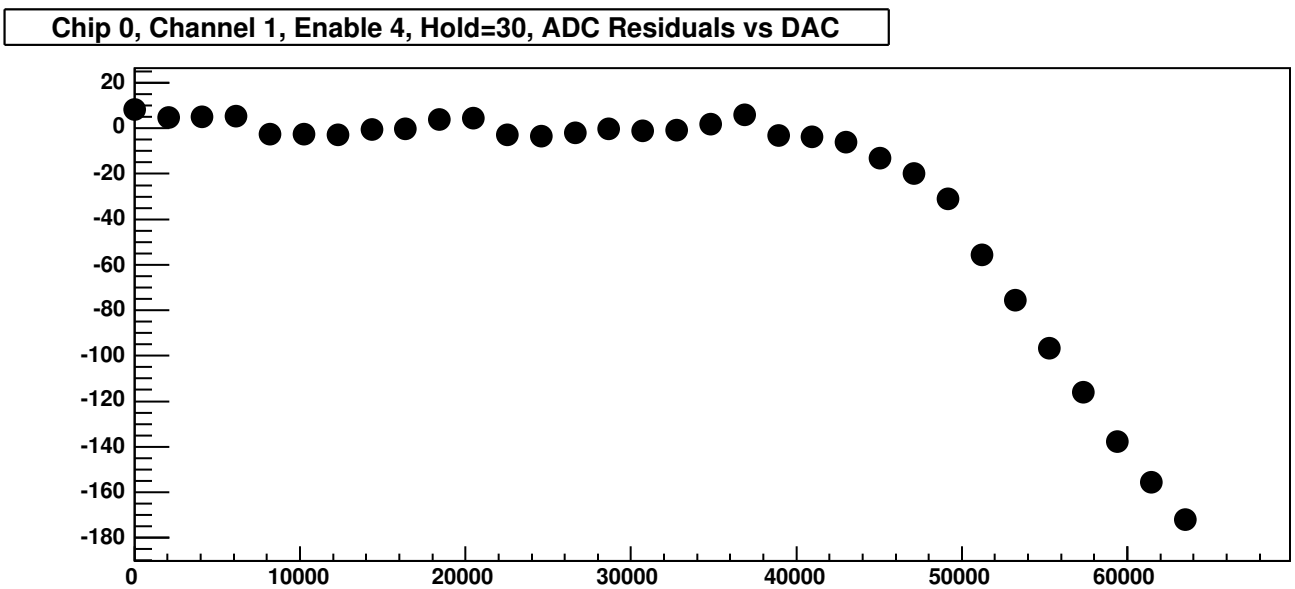

Chip 0, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.217e+04 / 15
p0 1176 ± 0.2874
p1 0.6698 ± 2.858e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

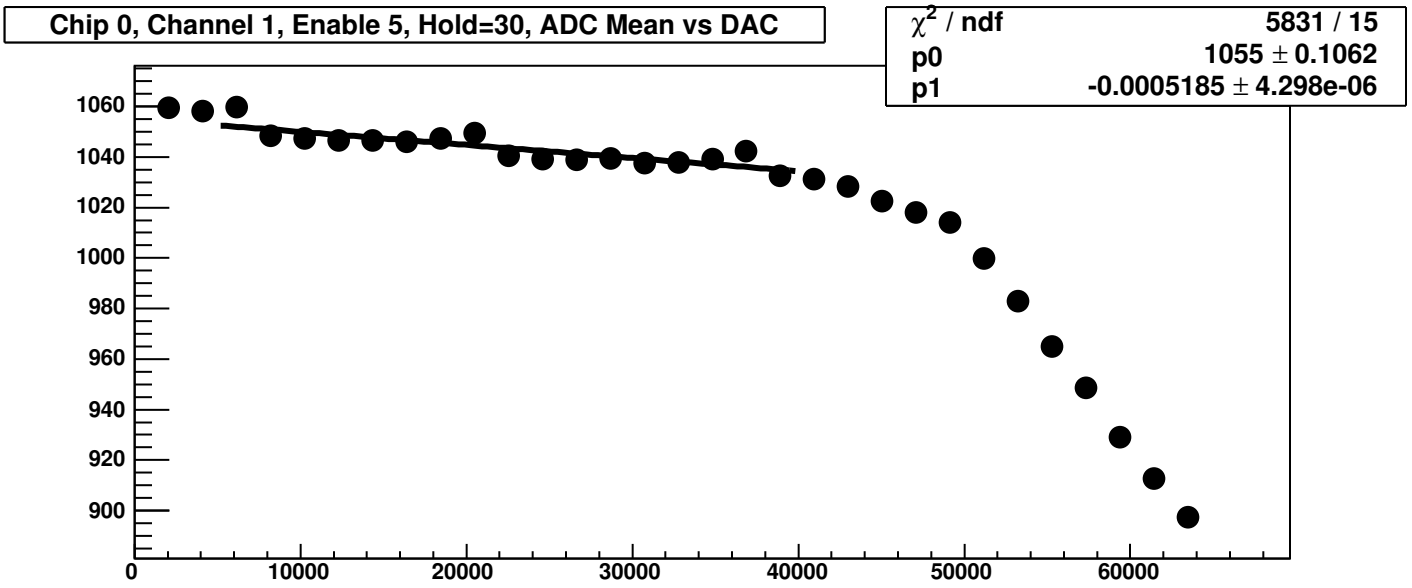

Chip 0, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

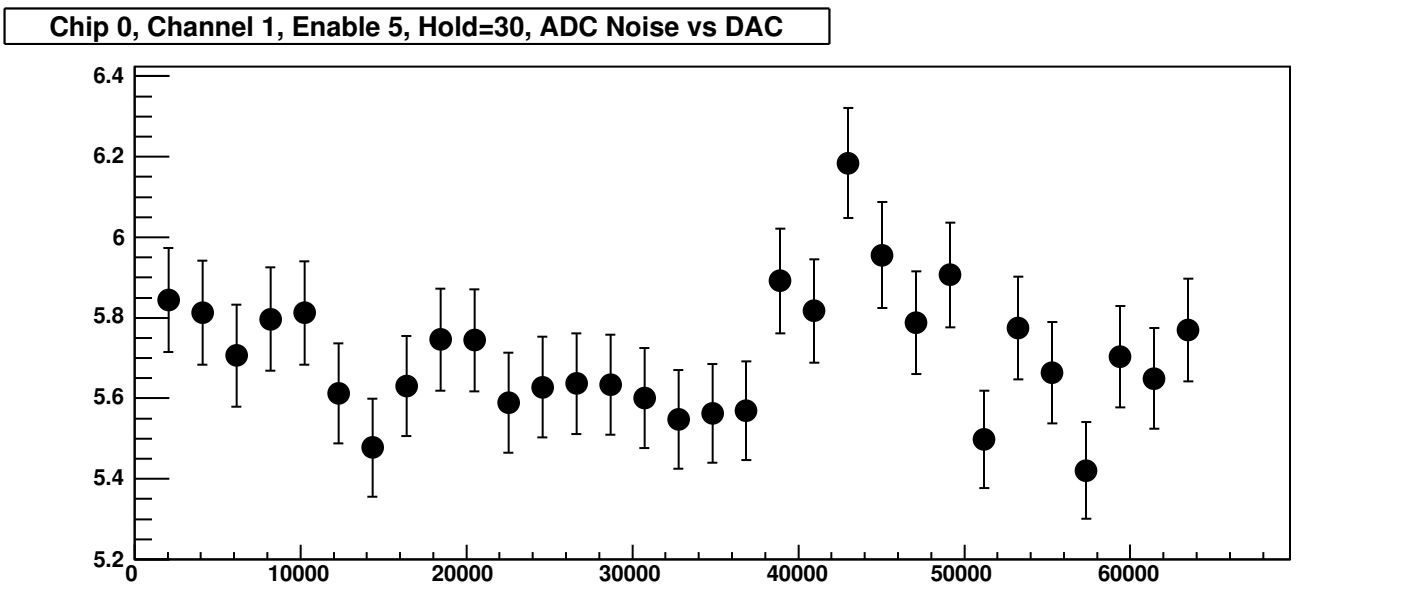

Chip 0, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

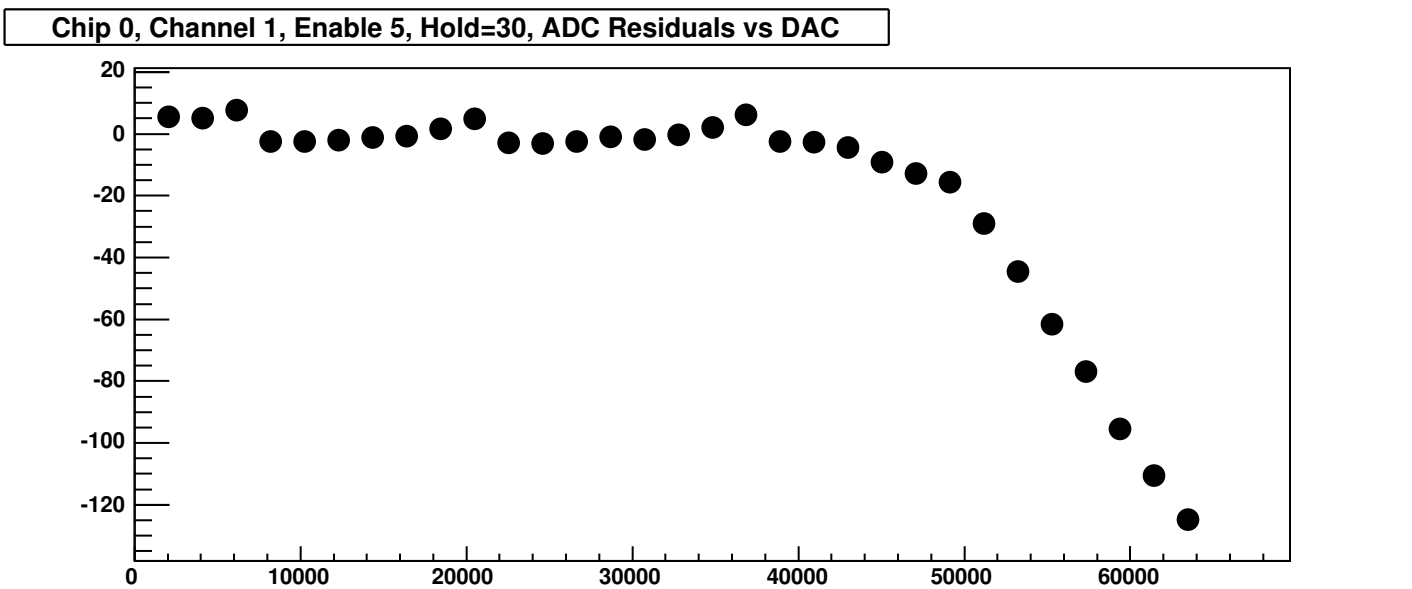

Chip 0, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5197 / 15
p0 1057 ± 0.1051
p1 -2.619e-05 ± 4.251e-06
1080
1060
1040
1020
1000
980
960
940
920
900
880
0
10000
20000
30000
40000
50000
60000

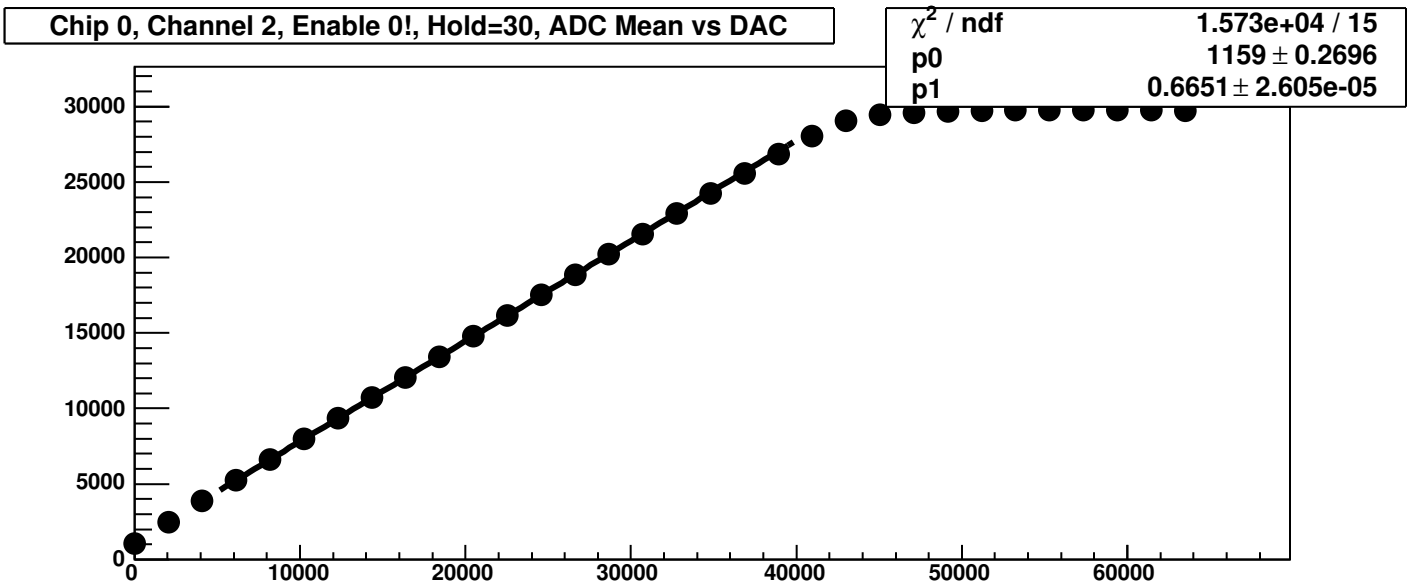

Chip 0, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

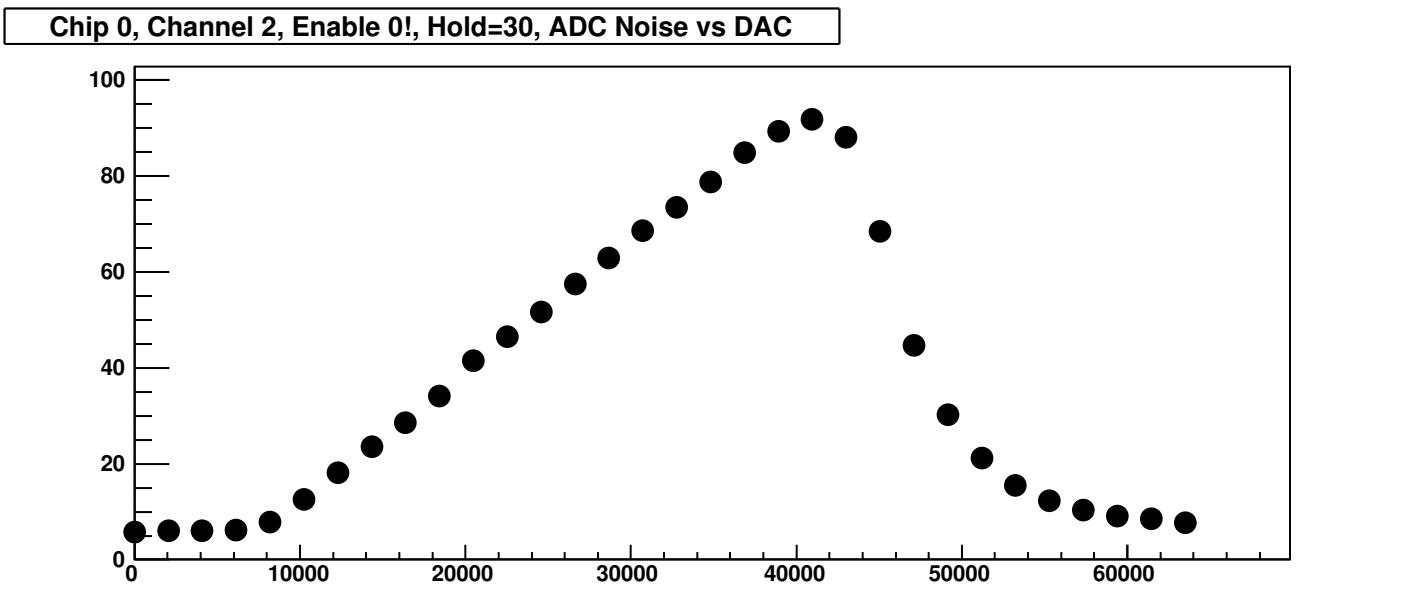

Chip 0, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

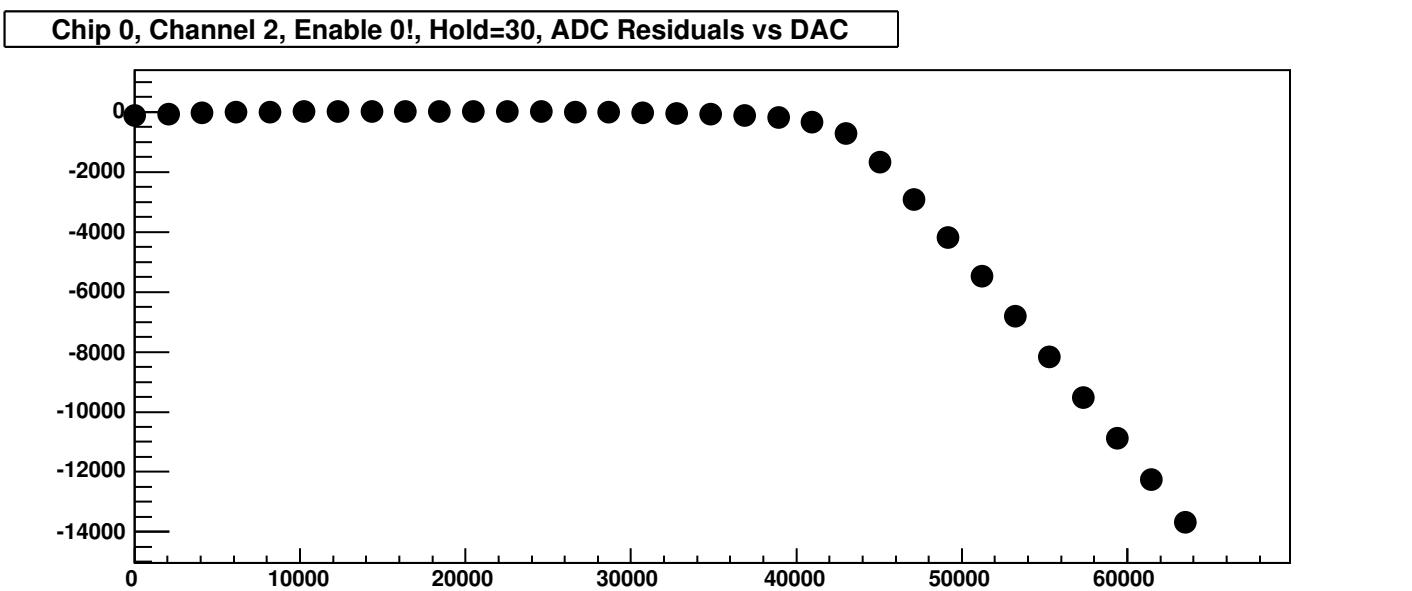

Chip 0, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5831 / 15
p0 1055 ± 0.1062
p1 -0.0005185 ± 4.298e-06
1060
1040
1020
1000
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000

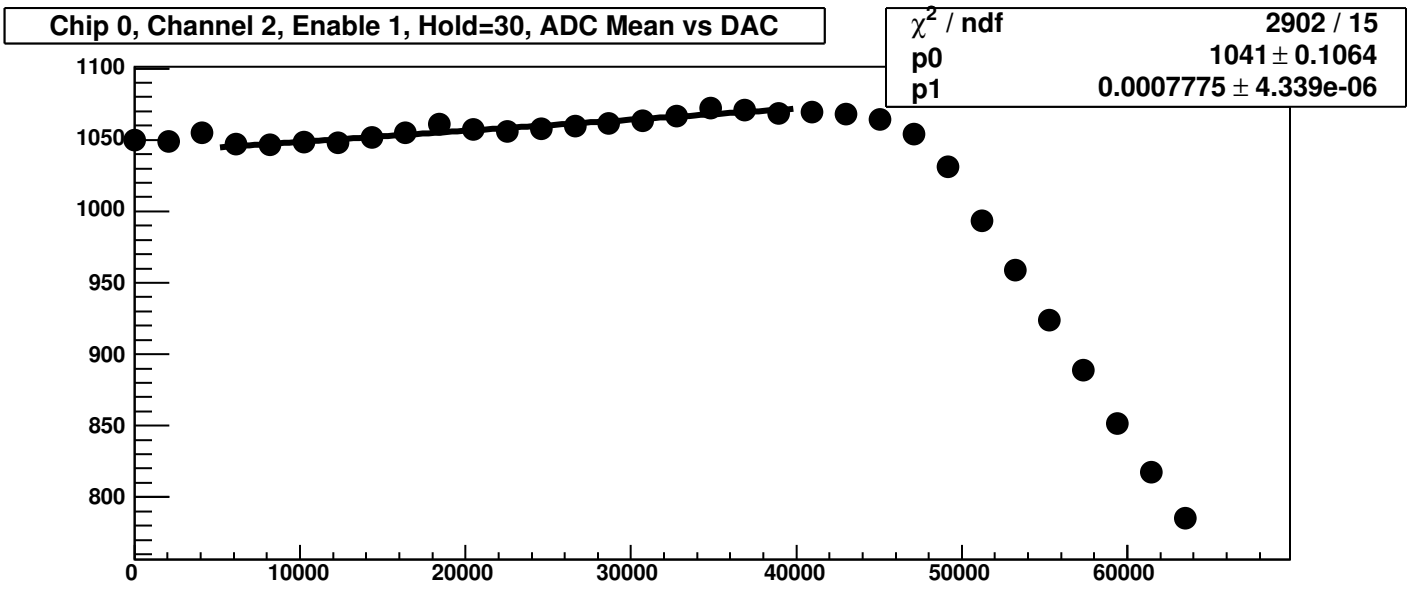

Chip 0, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

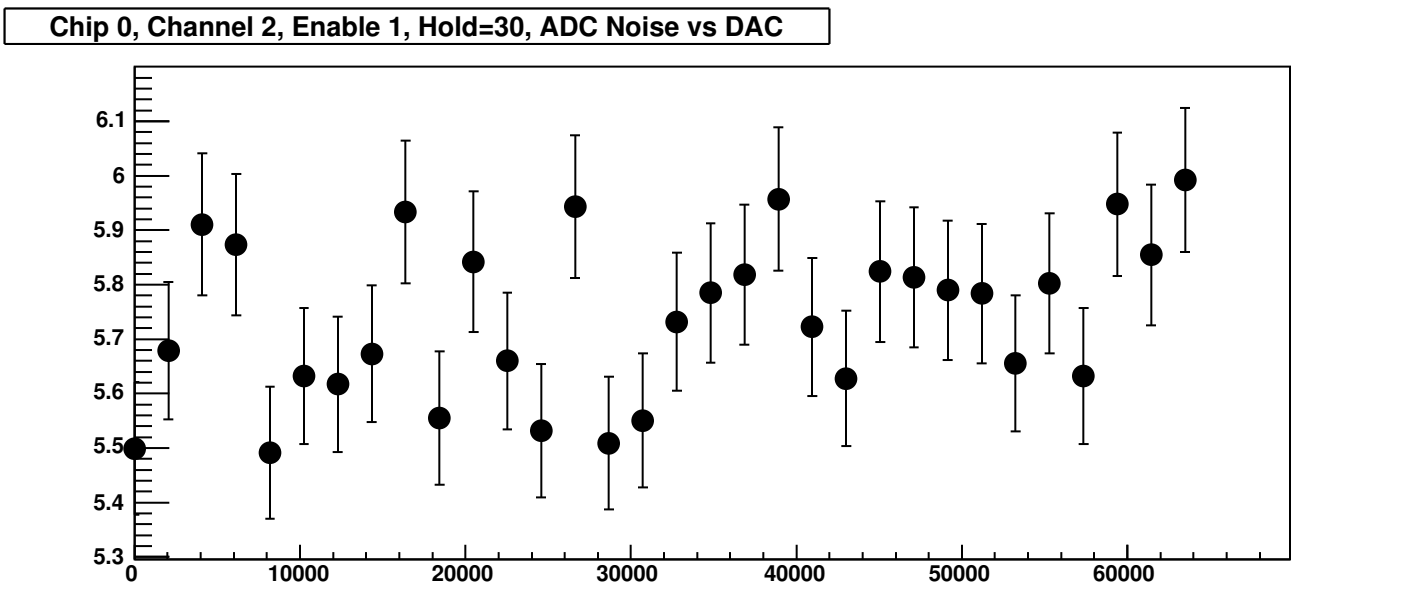

Chip 0, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

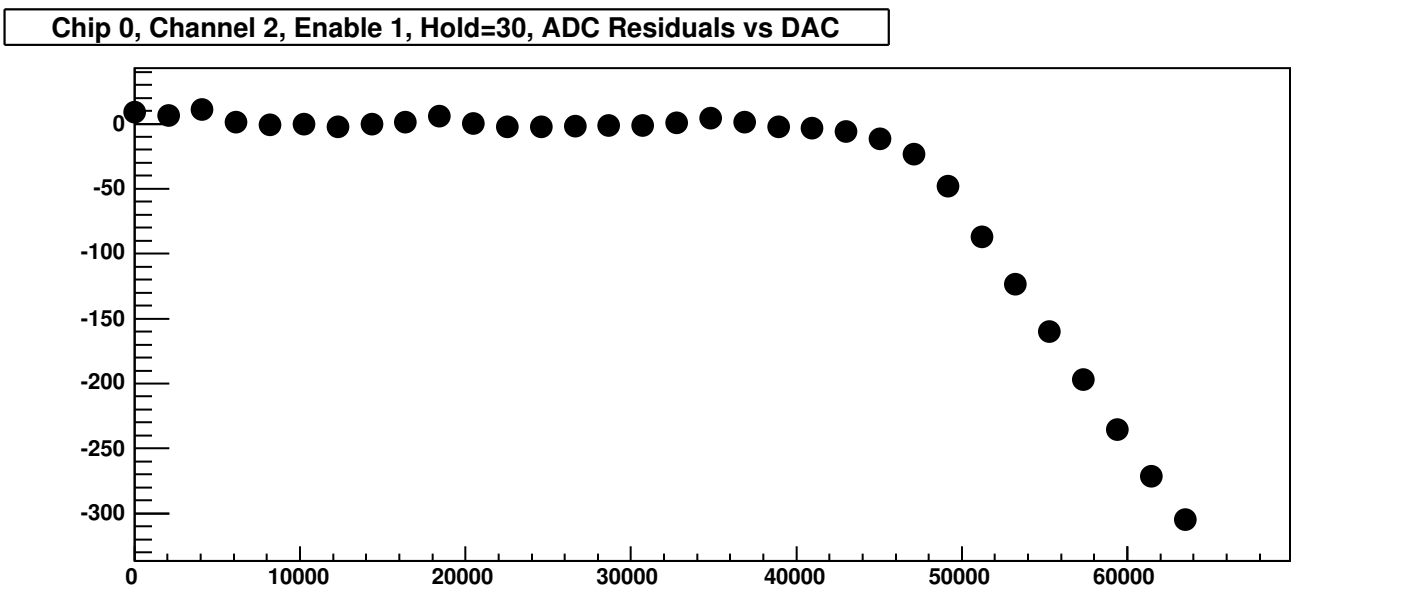

Chip 0, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.573e+04 / 15
p0 1159 ± 0.2696
p1 0.6651 ± 2.605e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

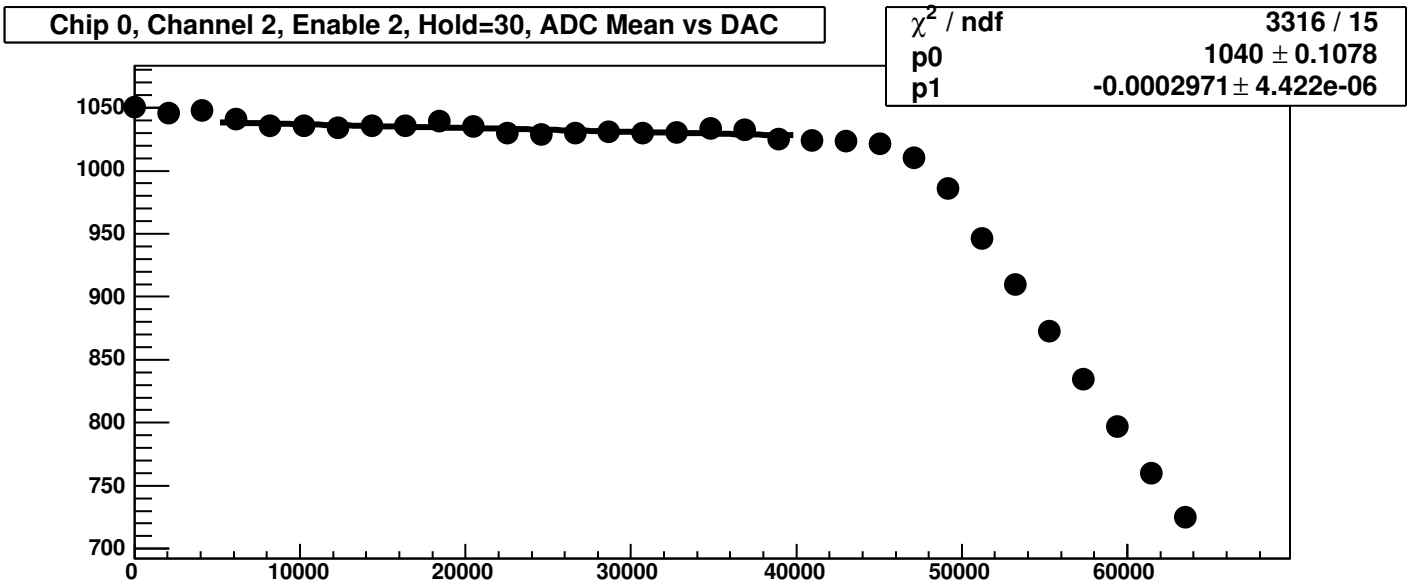

Chip 0, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

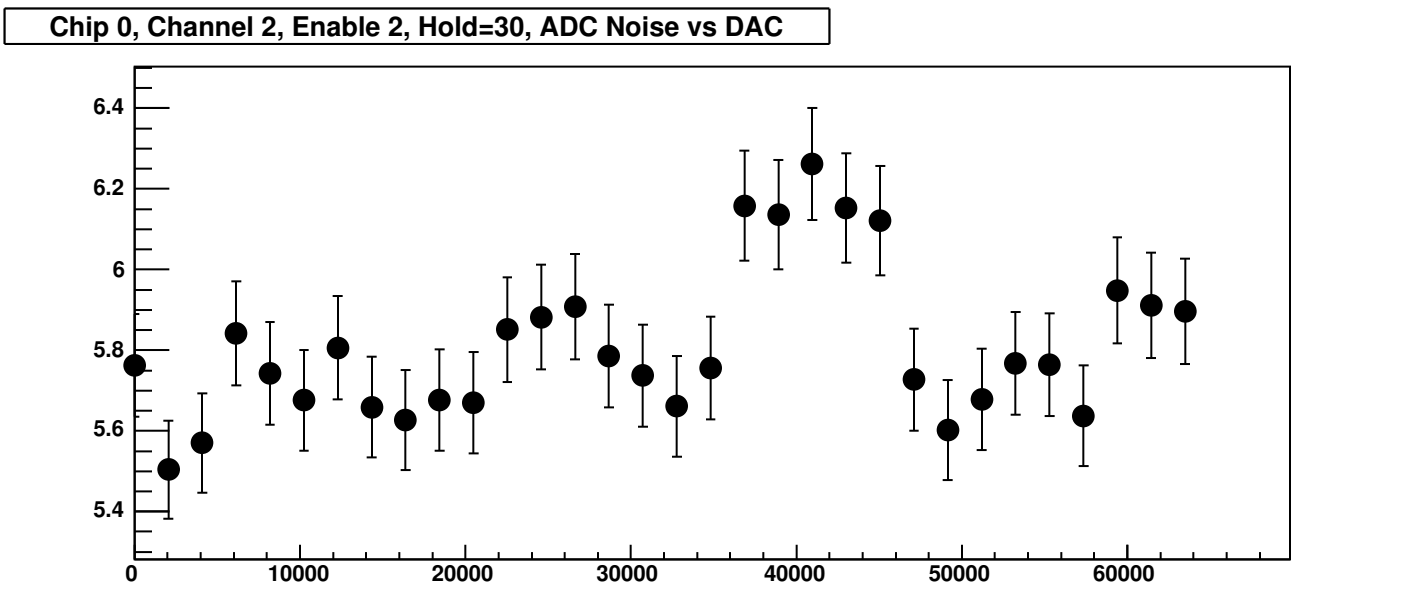

Chip 0, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

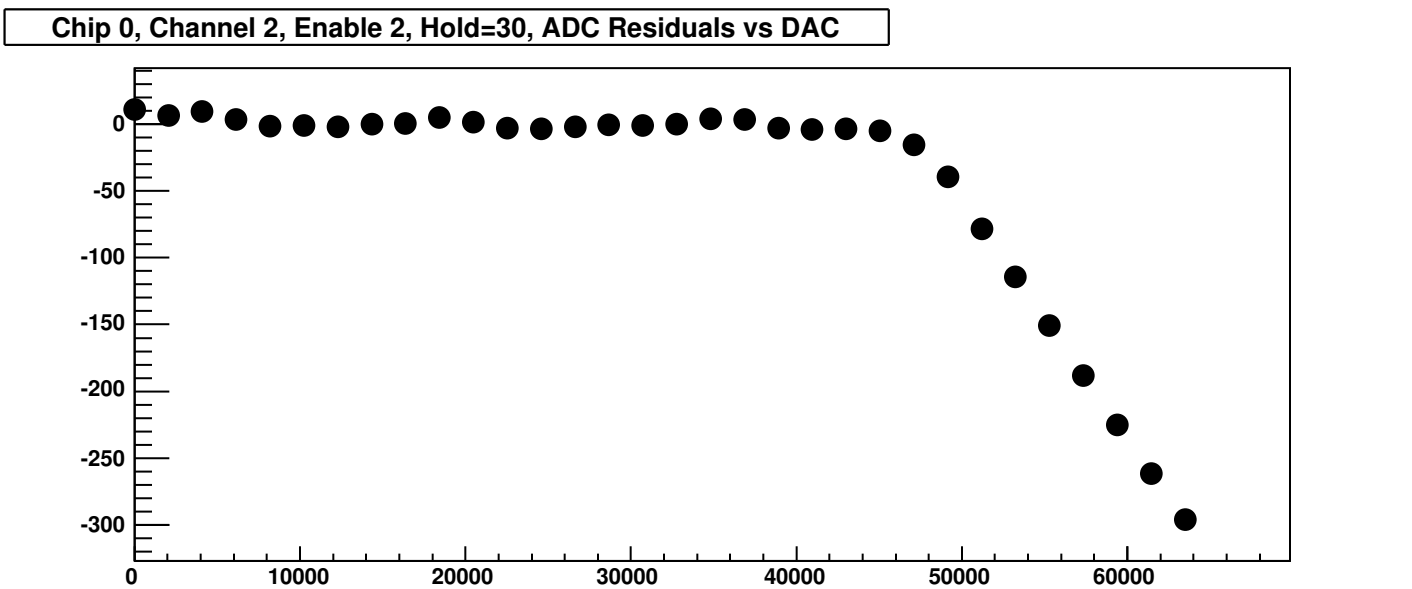

Chip 0, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2902 / 15
p0 1041 ± 0.1064
p1 0.0007775 ± 4.339e-06
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

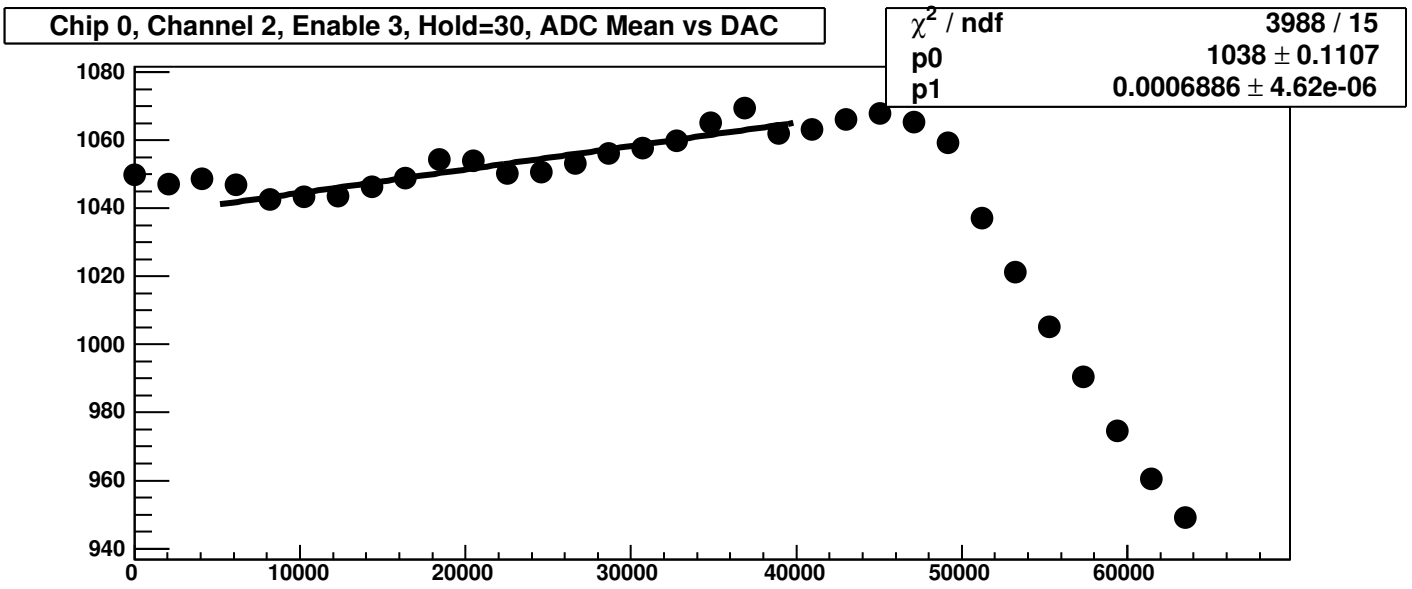

Chip 0, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
0
10000
20000
30000
40000
50000
60000

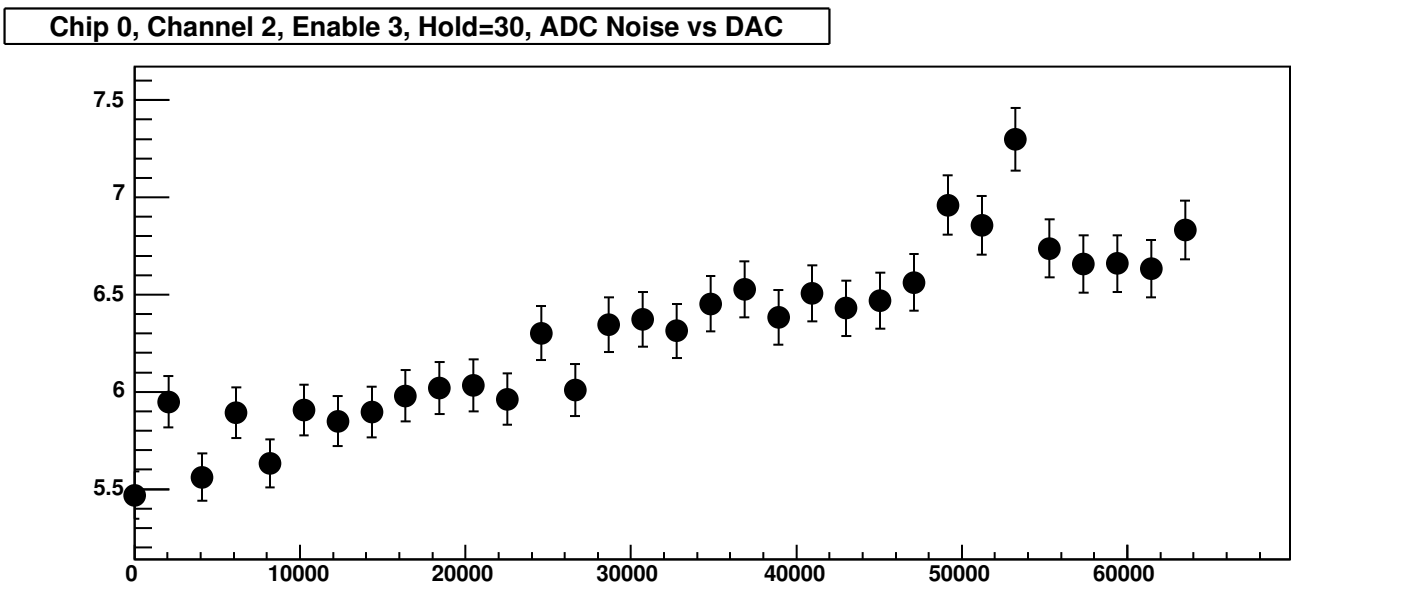

Chip 0, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

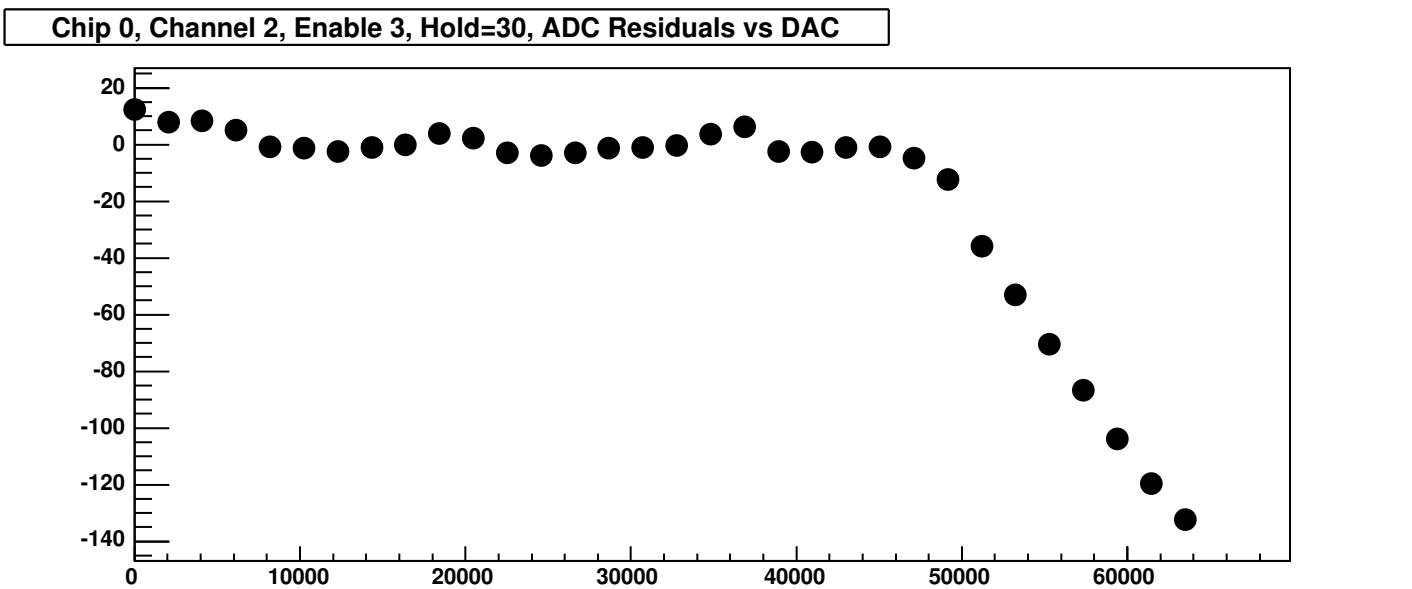

Chip 0, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3316 / 15
p0 1040 ± 0.1078
p1 -0.0002971 ± 4.422e-06
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

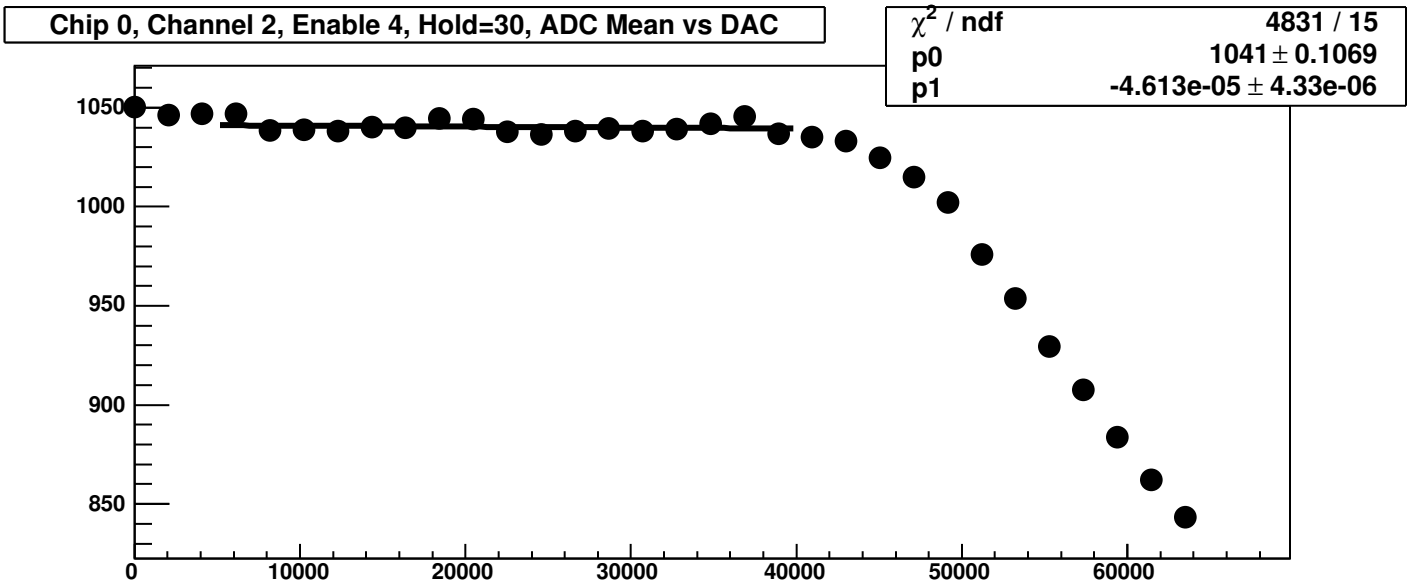

Chip 0, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

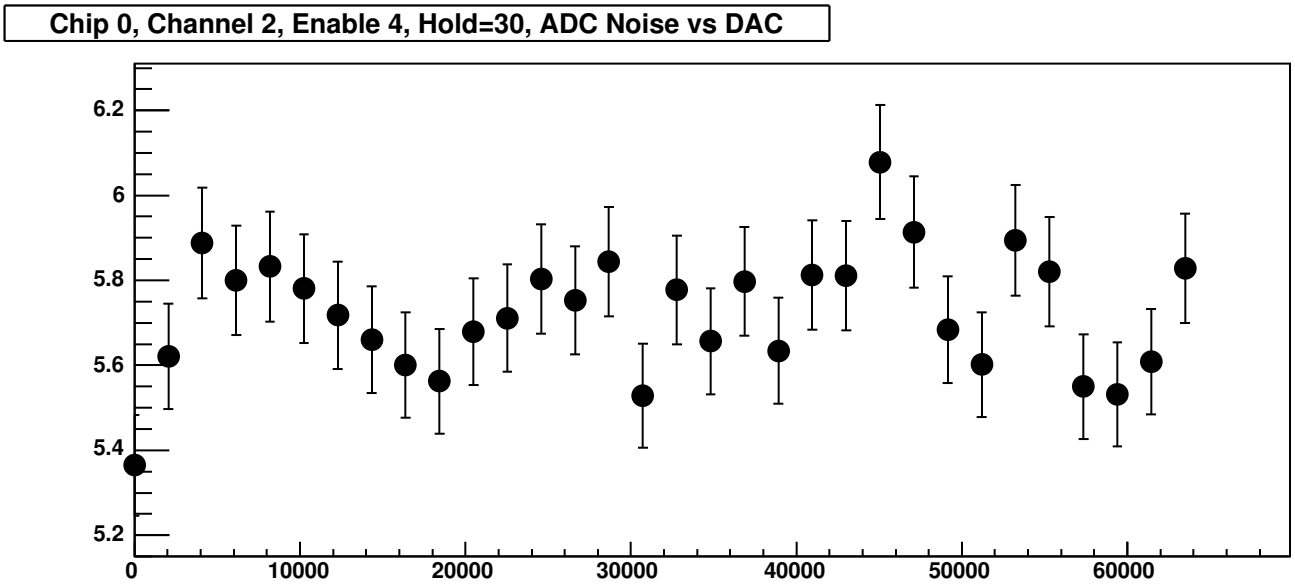

Chip 0, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

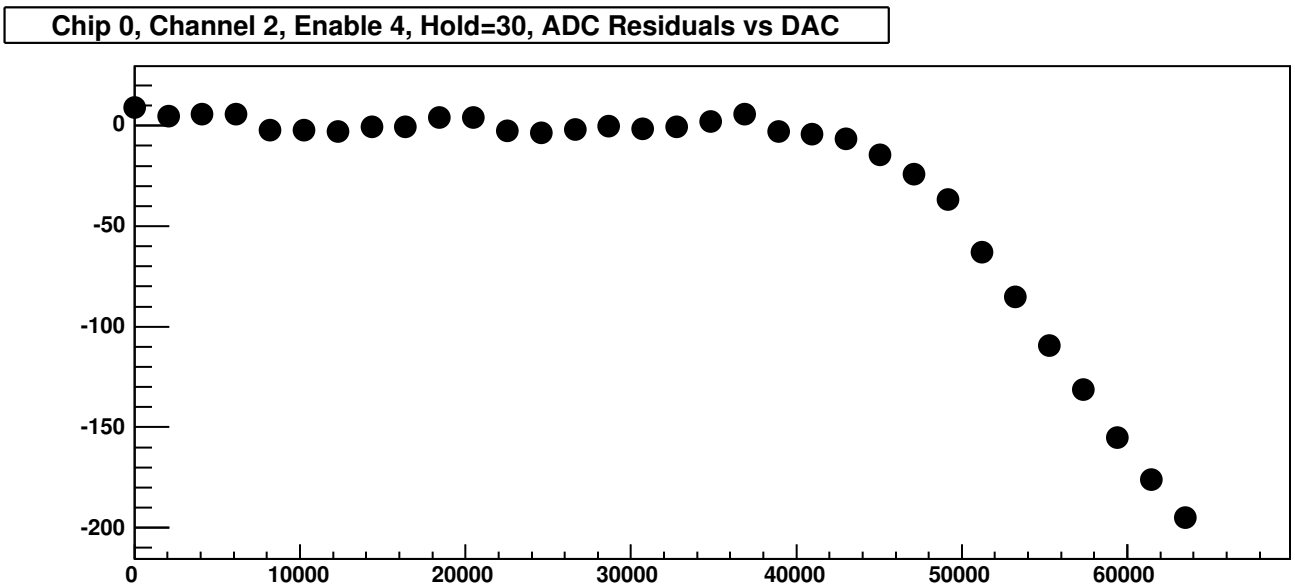

Chip 0, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3988 / 15
p0 1038 ± 0.1107
p1 0.0006886 ± 4.62e-06
1080
1060
1040
1020
1000
980
960
940
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000

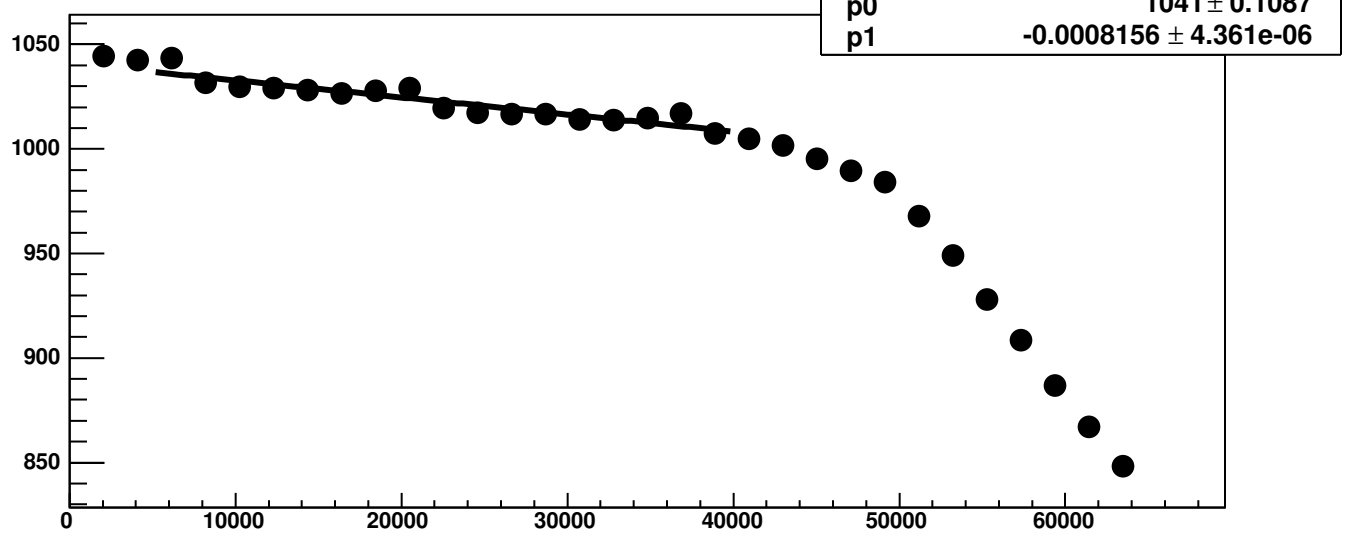

Chip 0, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4831 / 15
p0 1041 ± 0.1069
p1 -4.613e-05 ± 4.33e-06
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

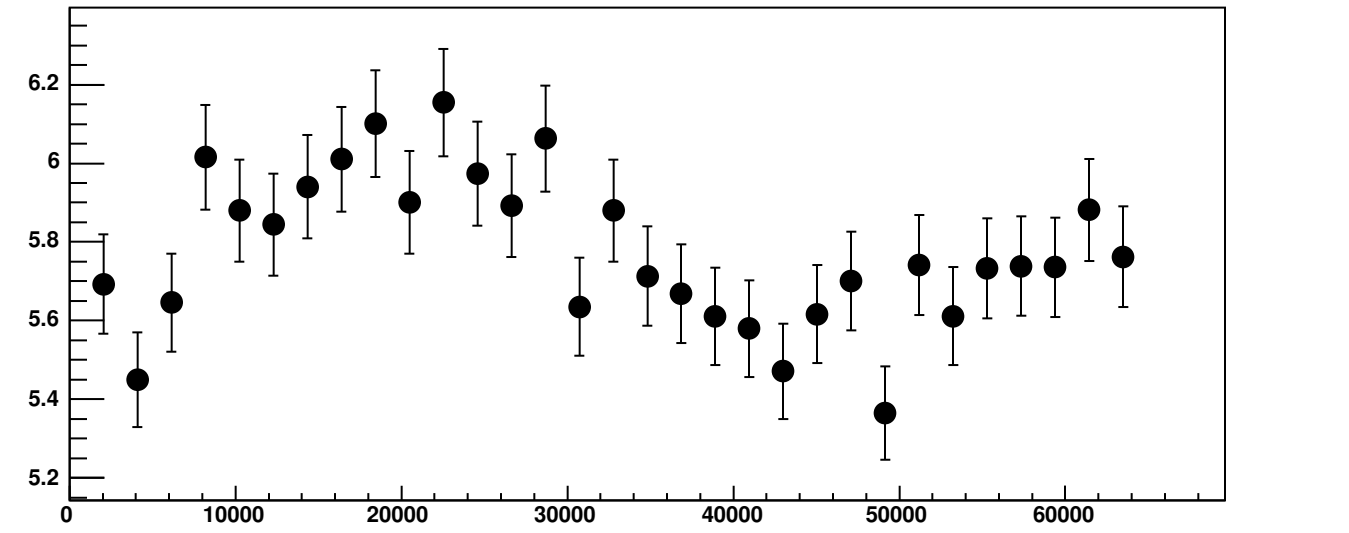

Chip 0, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

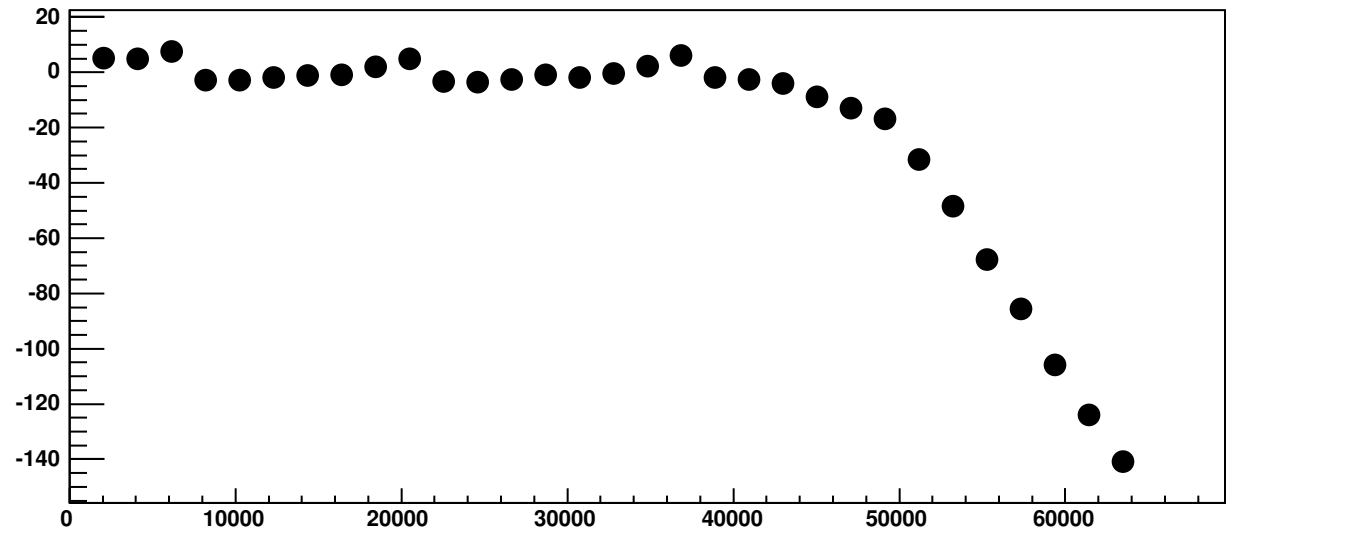
Chip 0, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC

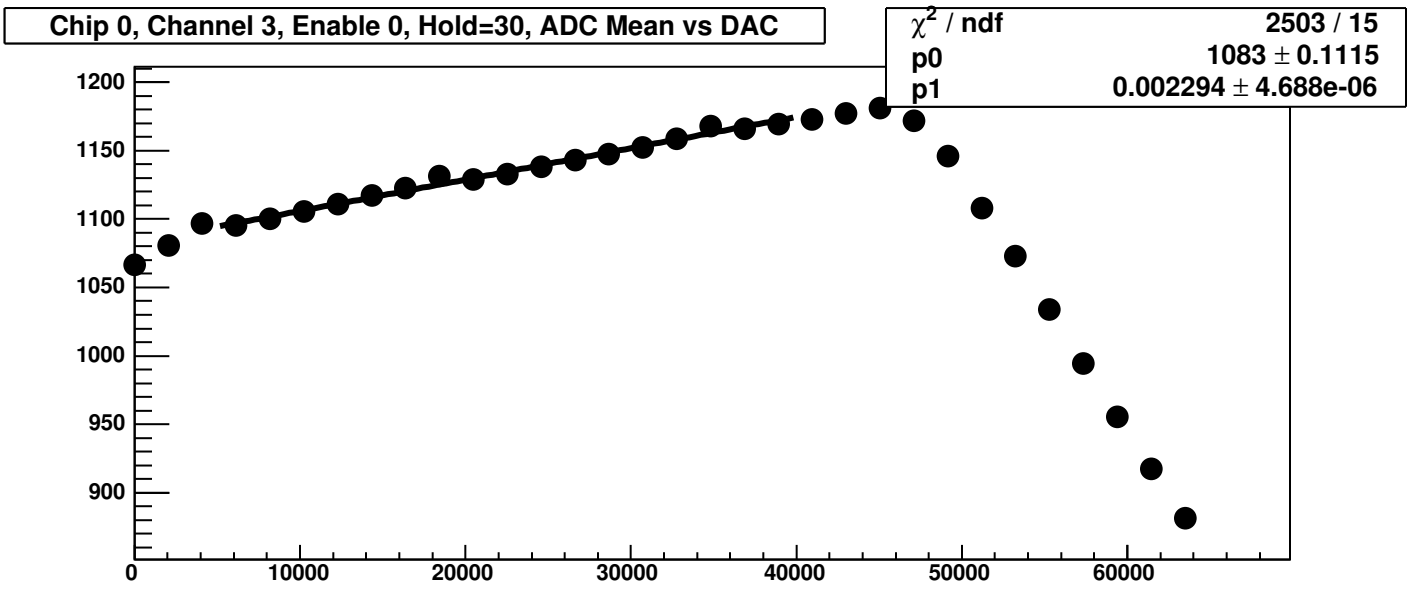


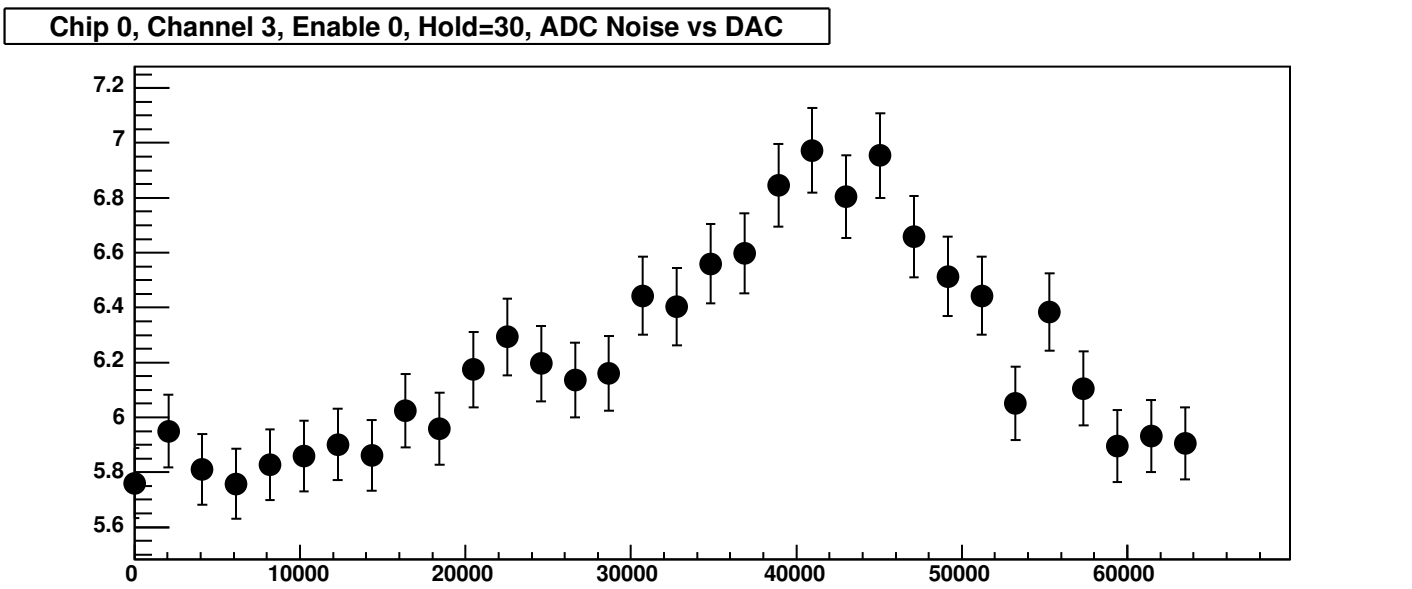
Chip 0, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

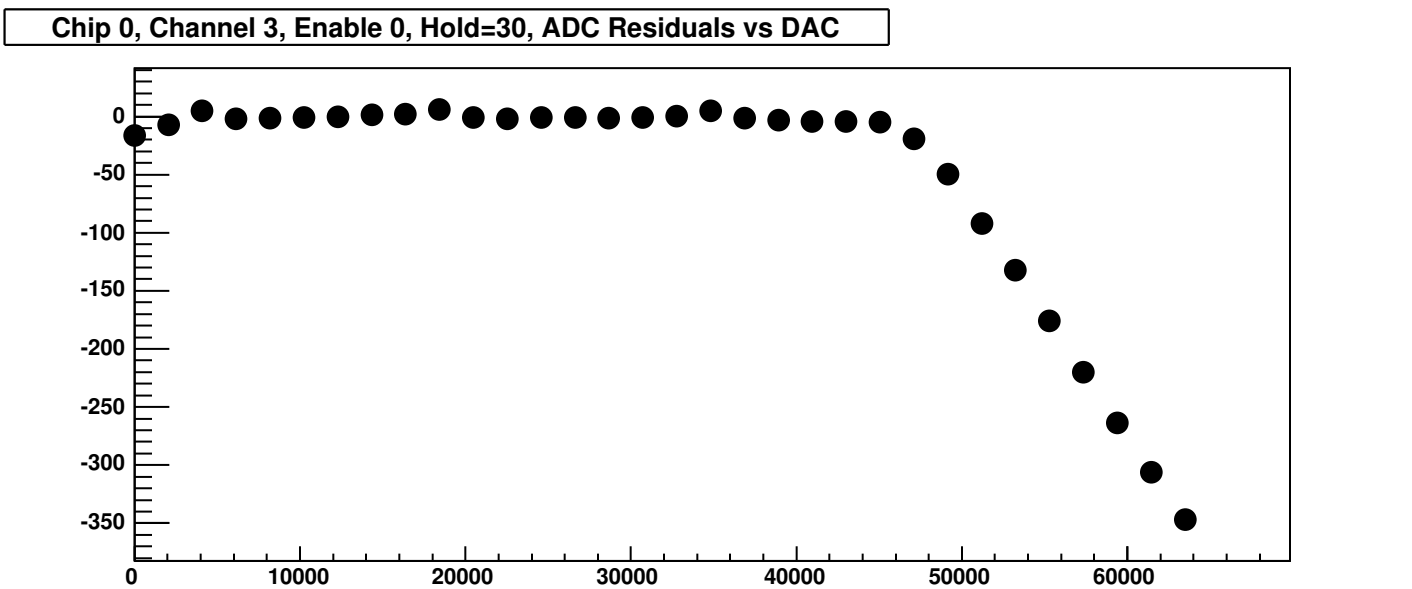


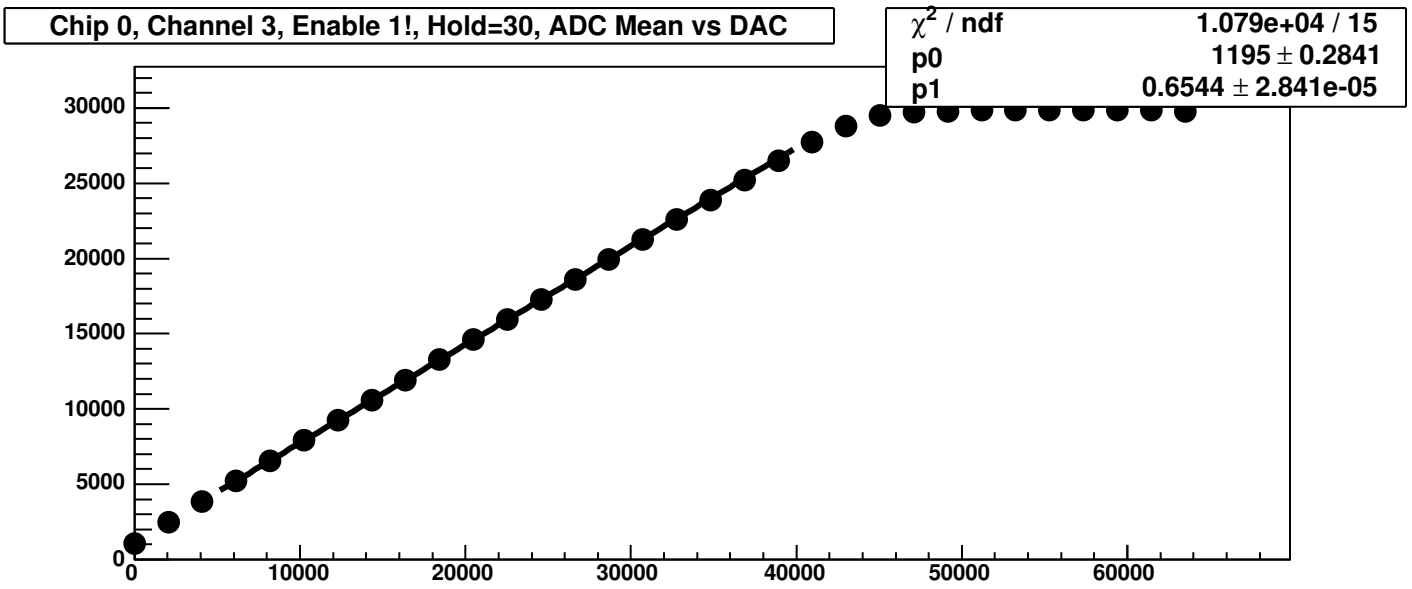
Chip 0, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC

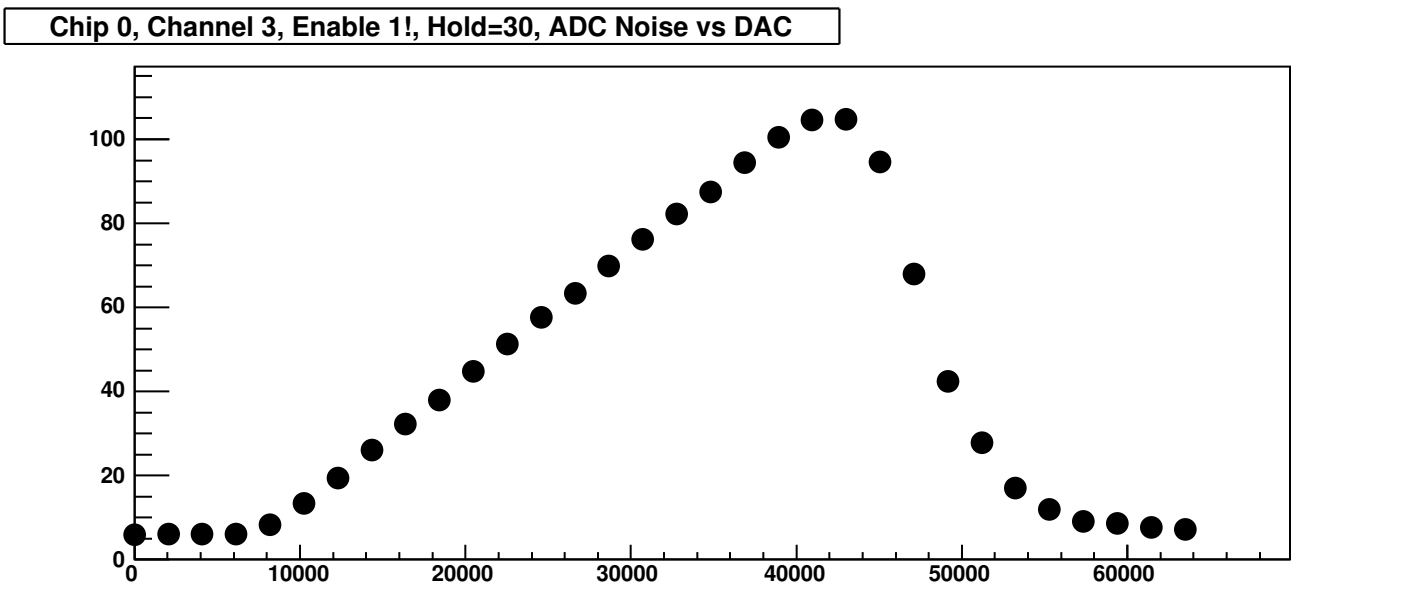


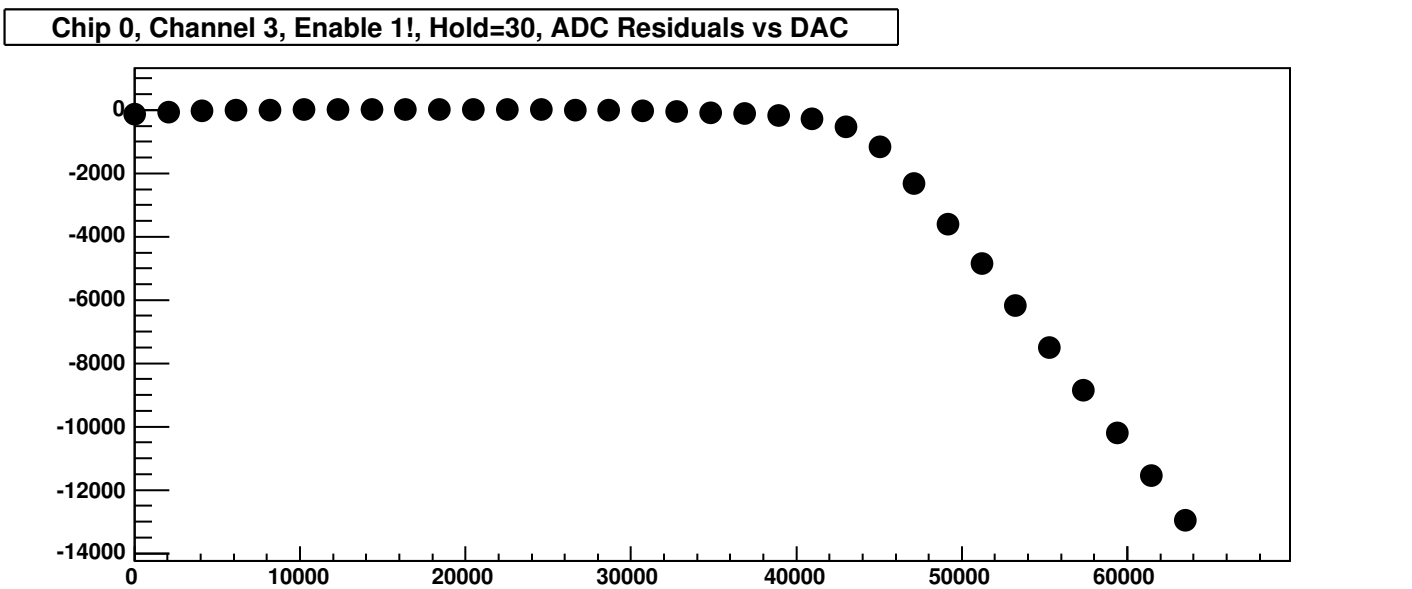

Chip 0, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2503 / 15
p0 1083 ± 0.1115
p1 0.002294 ± 4.688e-06
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

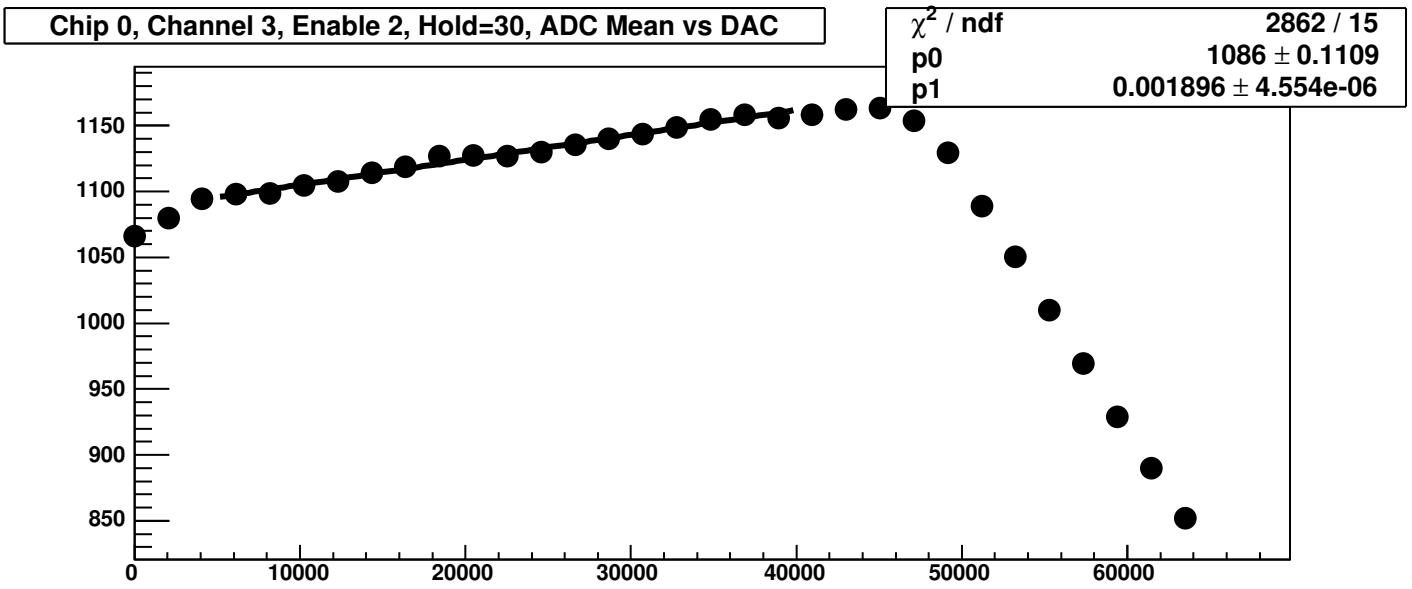

Chip 0, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

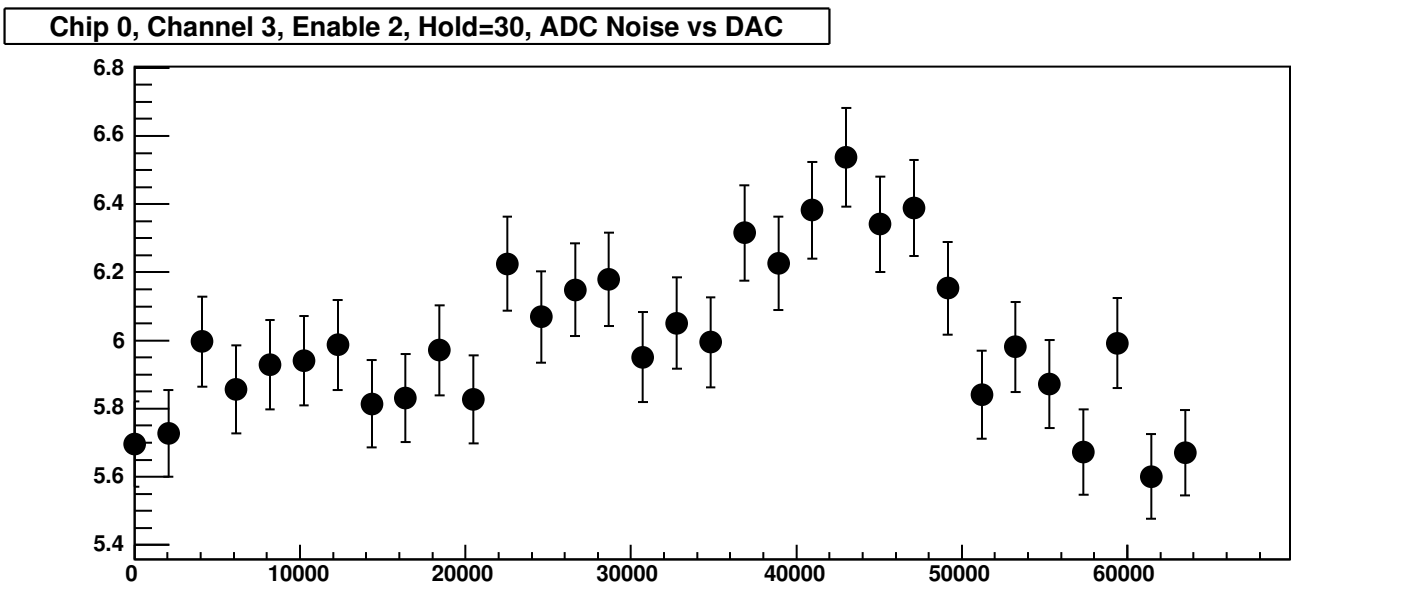

Chip 0, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

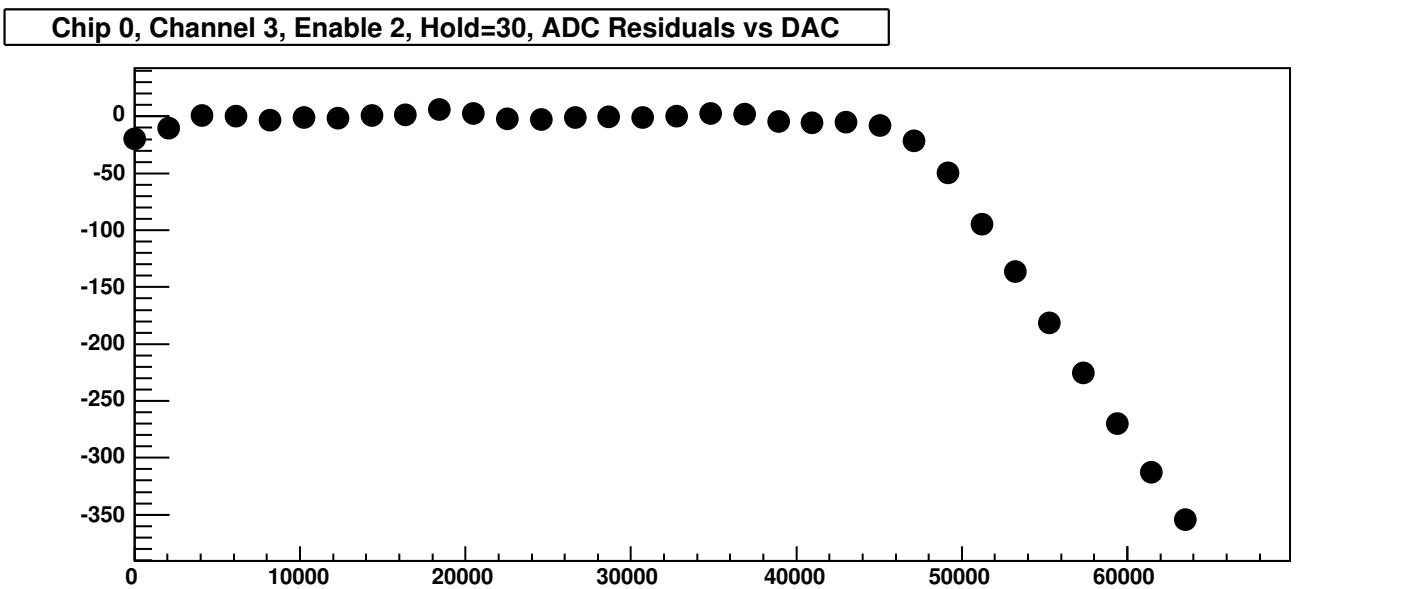

Chip 0, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.079e+04 / 15
p0 1195 ± 0.2841
p1 0.6544 ± 2.841e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

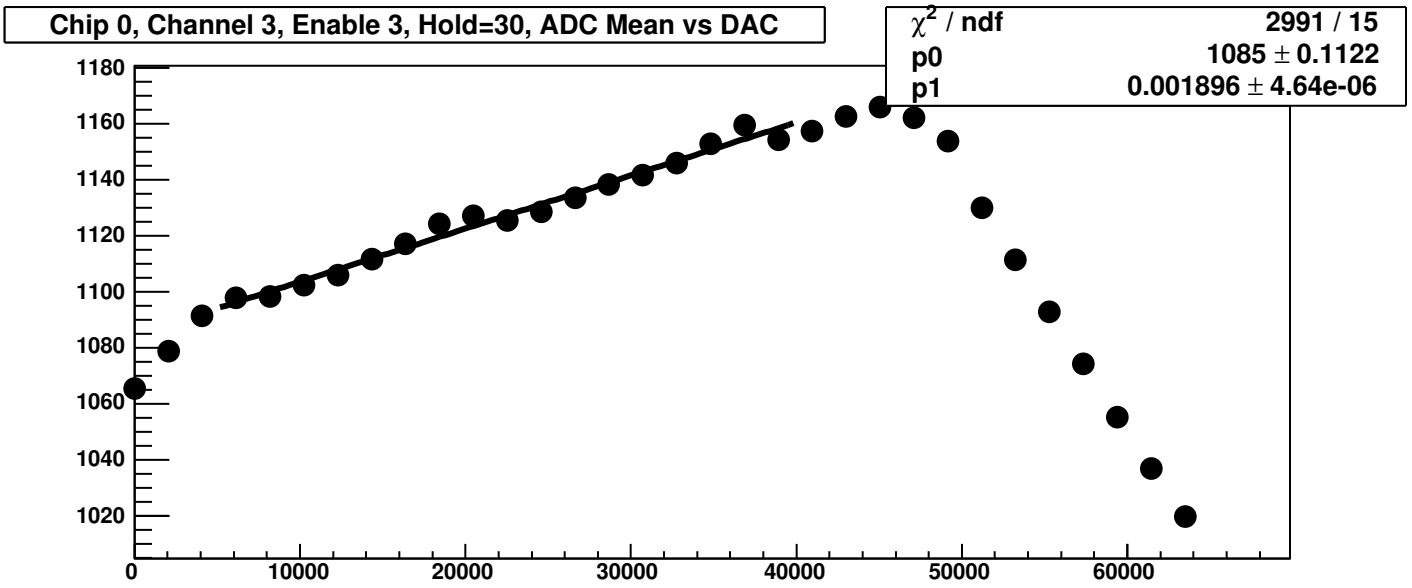

Chip 0, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

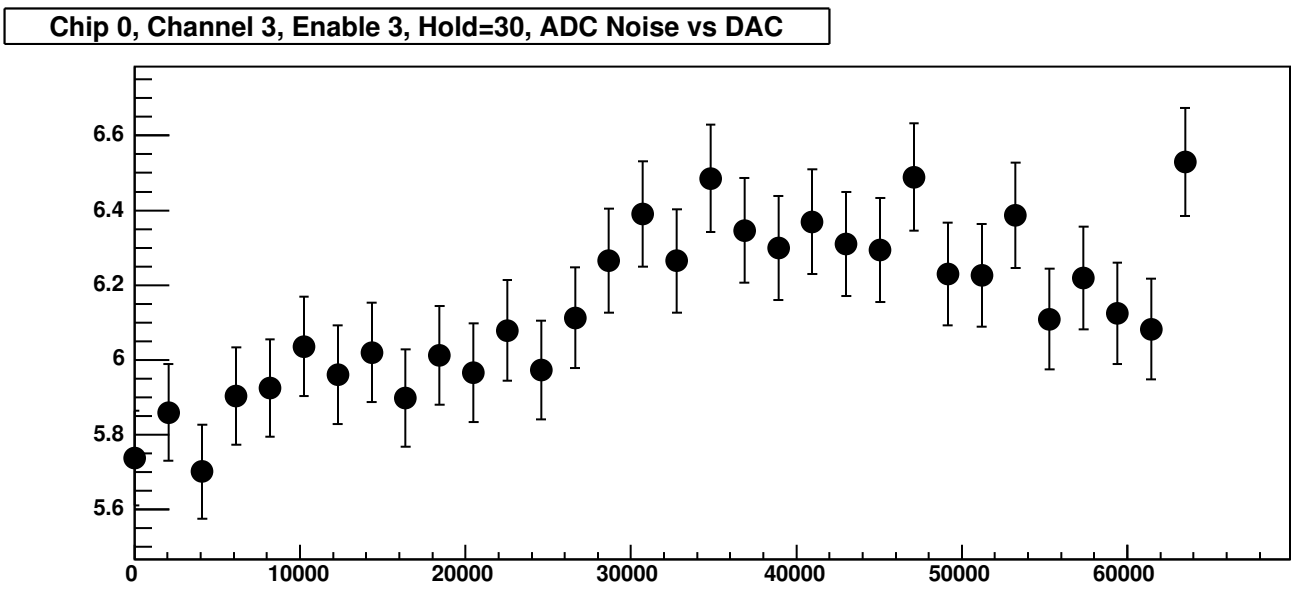

Chip 0, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

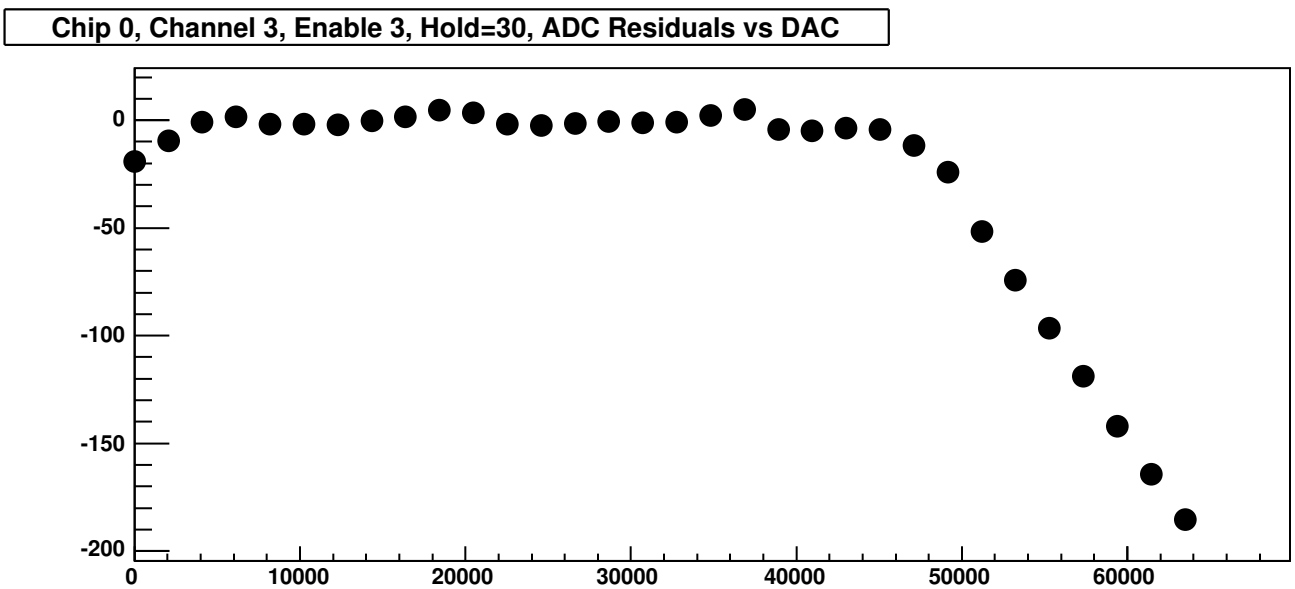

Chip 0, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2862 / 15
p0 1086 ± 0.1109
p1 0.001896 ± 4.554e-06
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

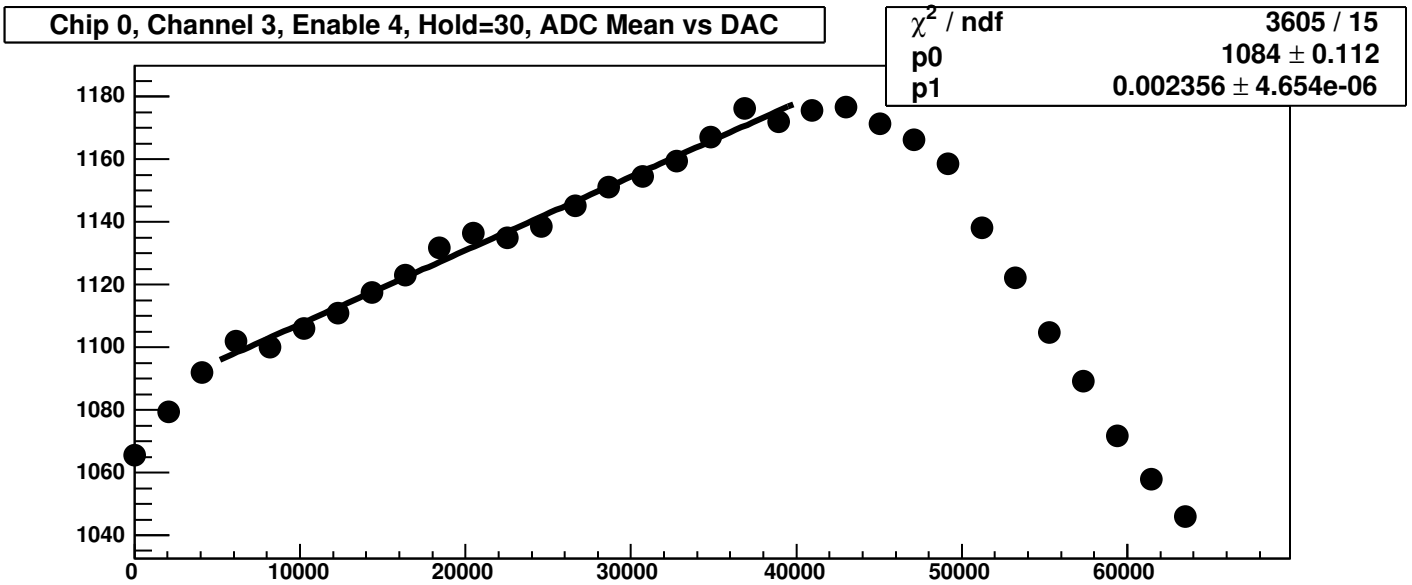

Chip 0, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

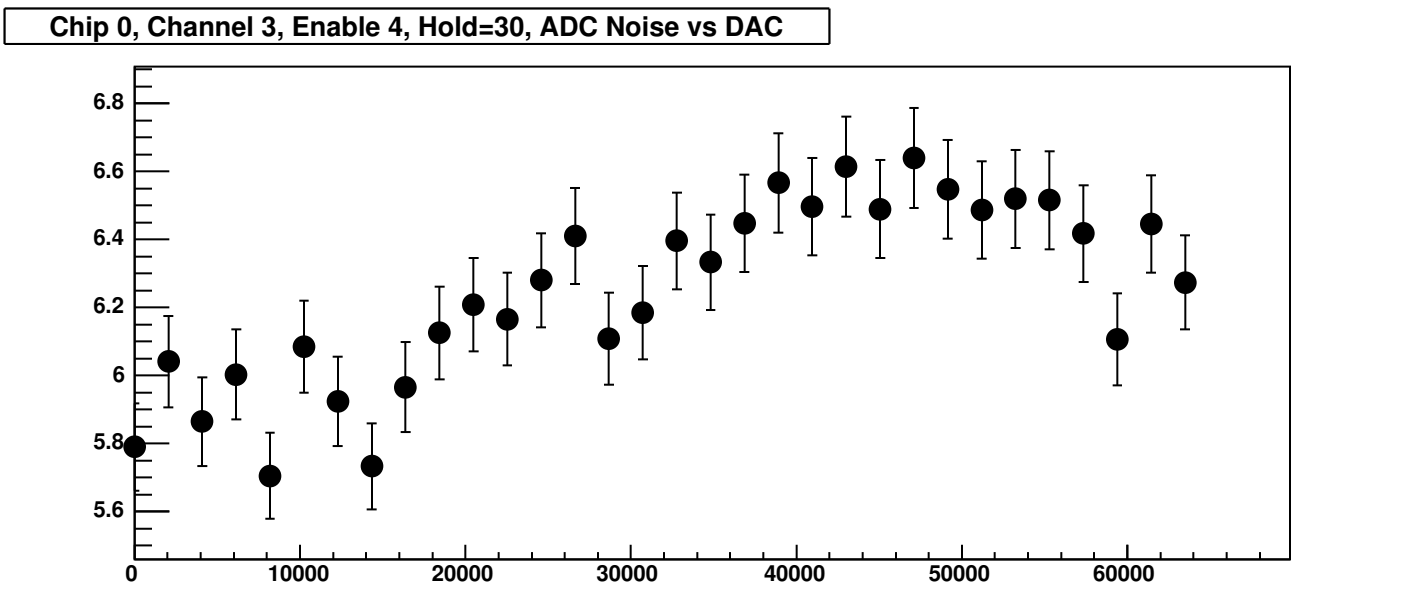

Chip 0, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

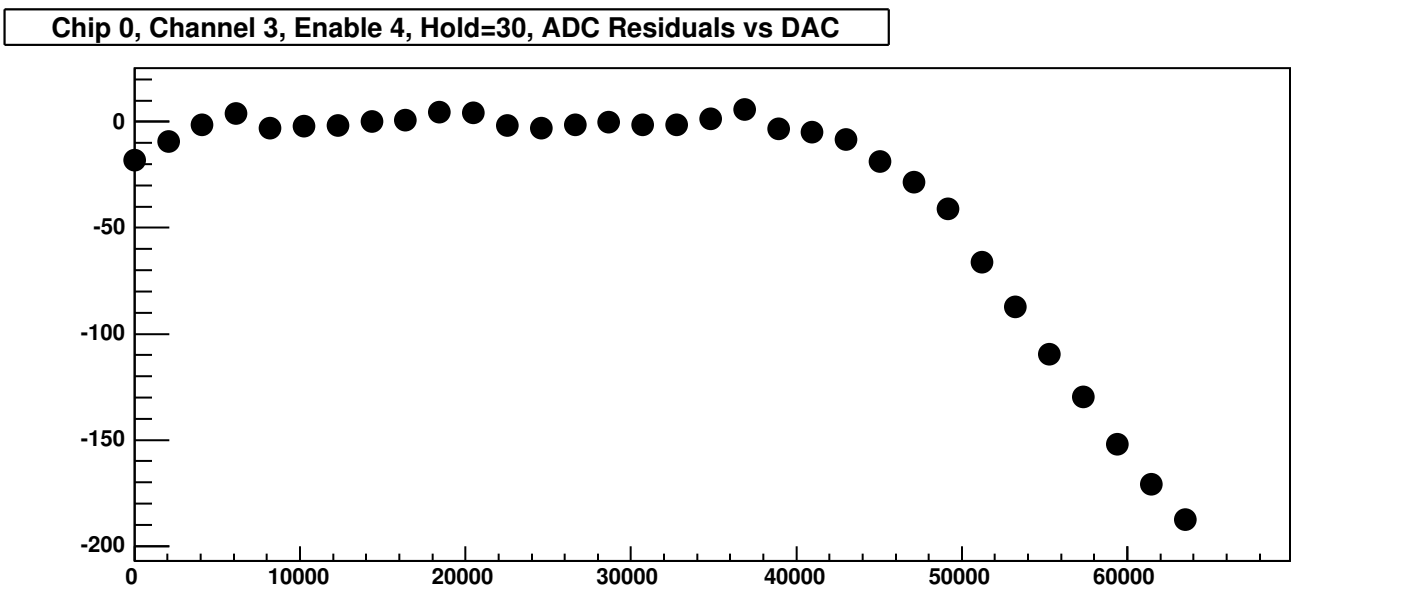

Chip 0, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2991 / 15
p0 1085 ± 0.1122
p1 0.001896 ± 4.64e-06
1180
1160
1140
1120
1100
1080
1060
1040
1020
0
10000
20000
30000
40000
50000
60000

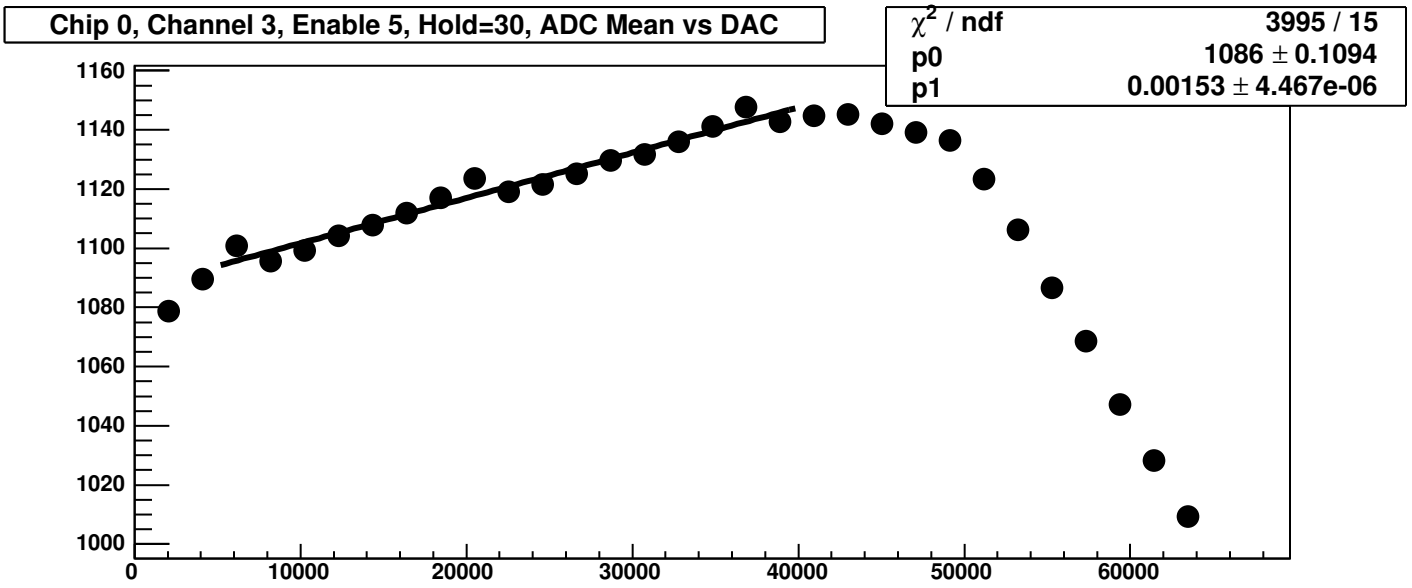

Chip 0, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

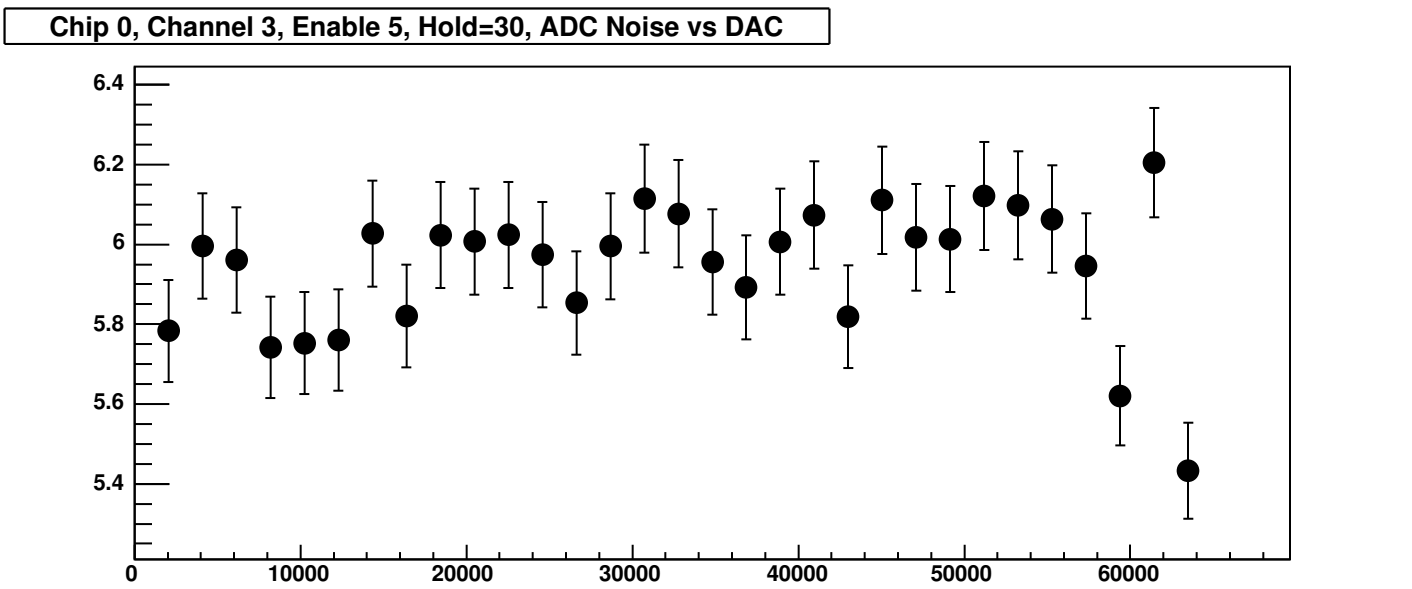

Chip 0, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

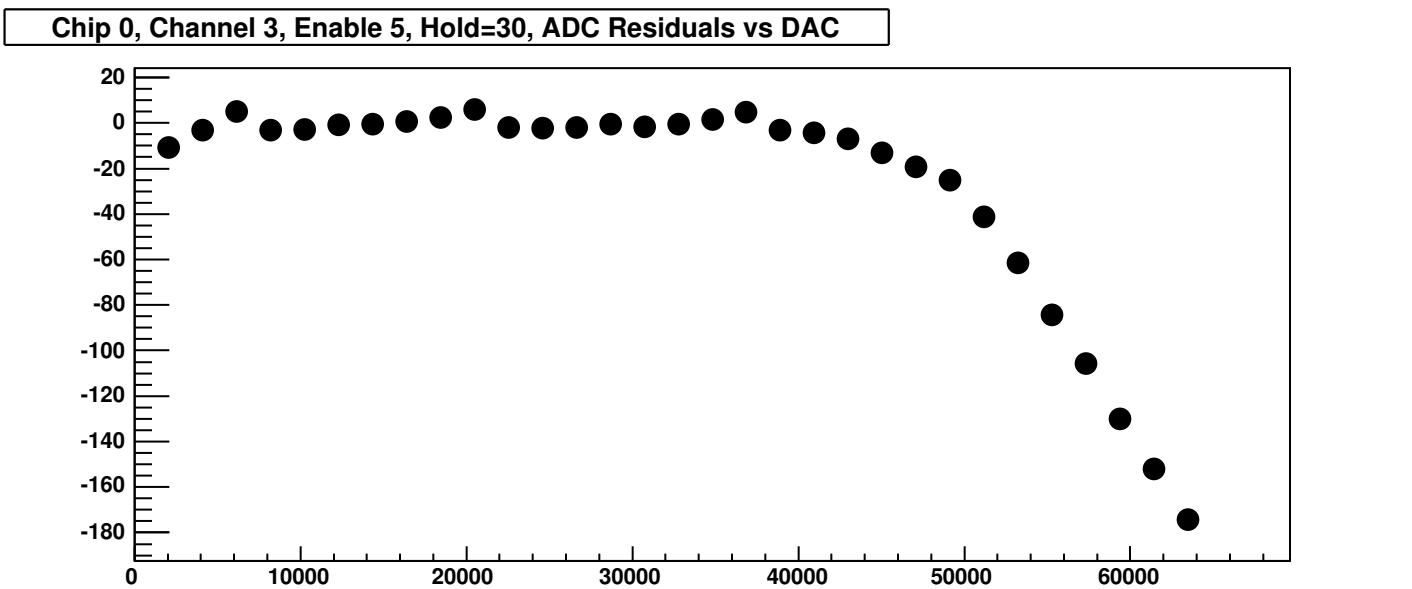

Chip 0, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 3605 / 15
p0 1084 ± 0.112
p1 0.002356 ± 4.654e-06
1180
1160
1140
1120
1100
1080
1060
1040
0
10000
20000
30000
40000
50000
60000

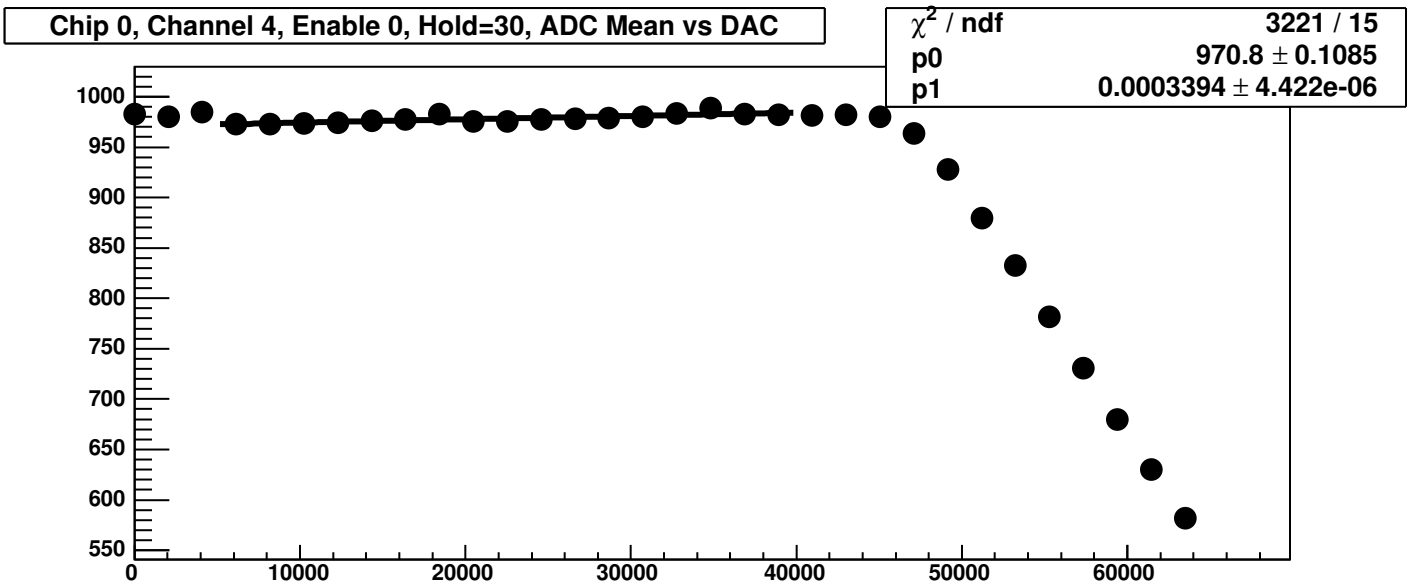

Chip 0, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

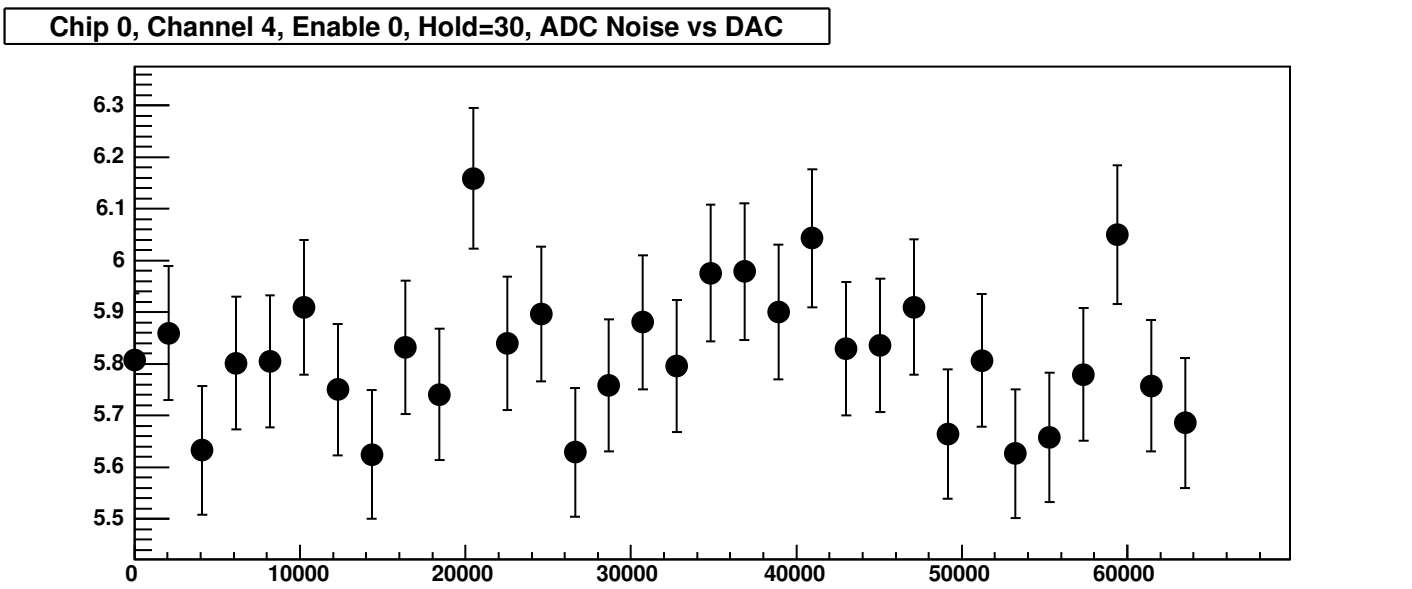

Chip 0, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

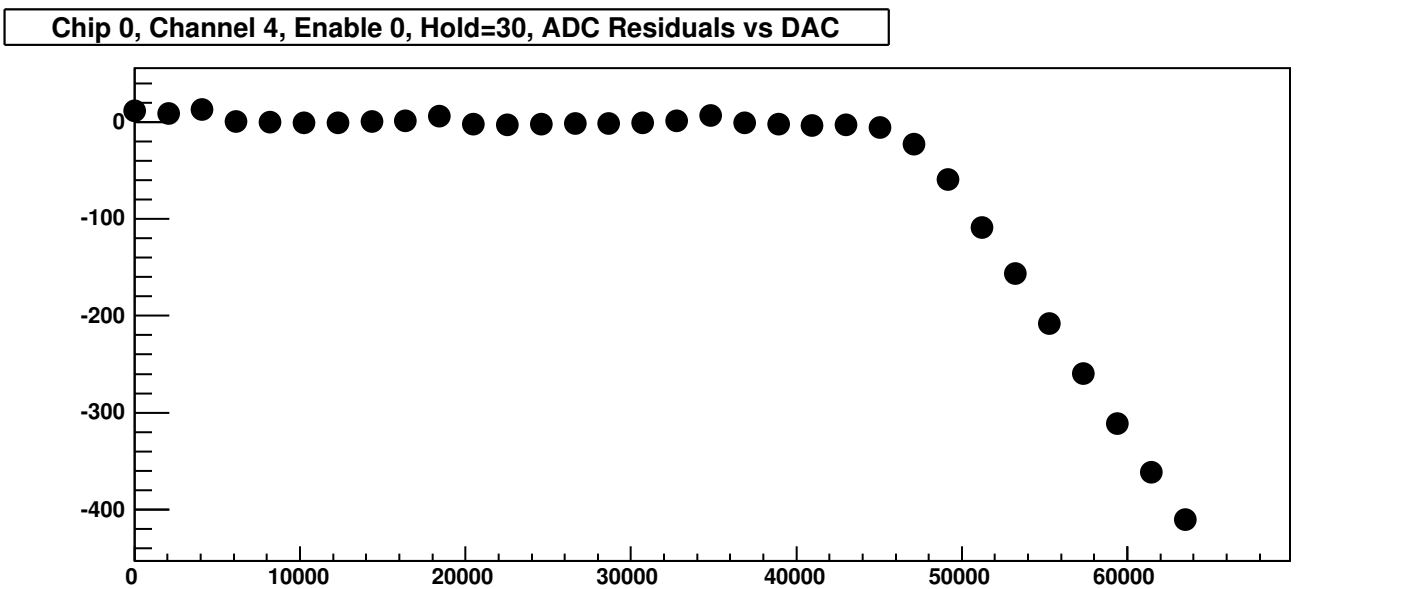

Chip 0, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3995 / 15
p0 1086 ± 0.1094
p1 0.00153 ± 4.467e-06
1160
1140
1120
1100
1080
1060
1040
1020
1000
0
10000
20000
30000
40000
50000
60000

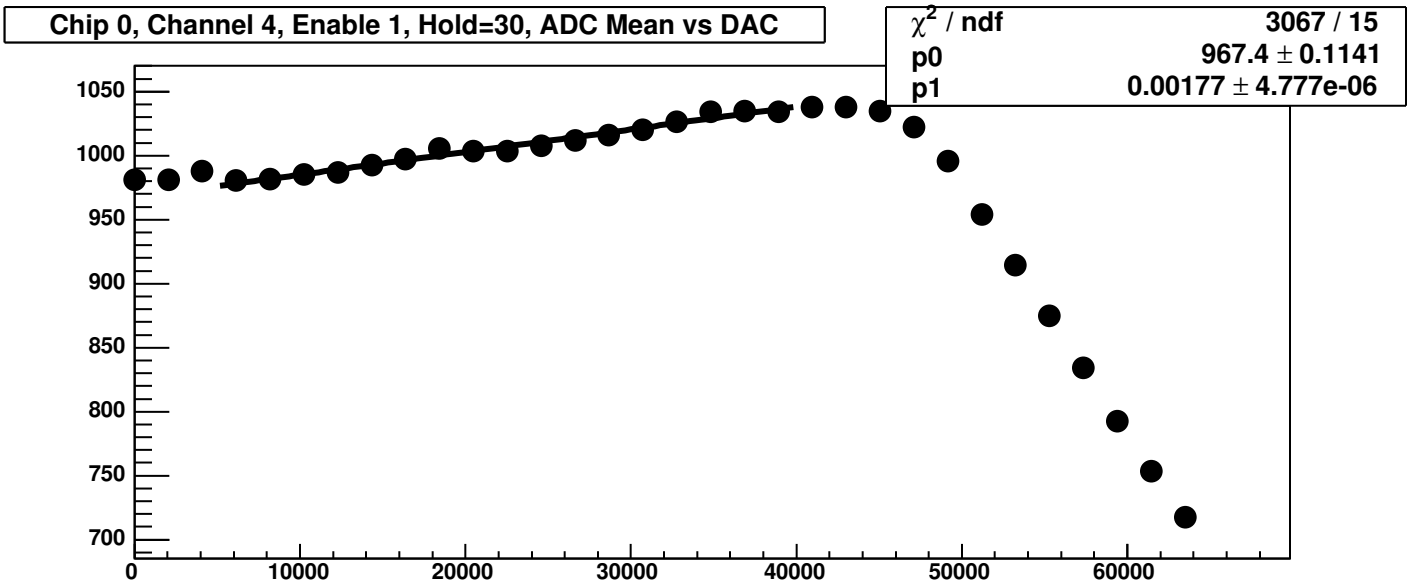

Chip 0, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

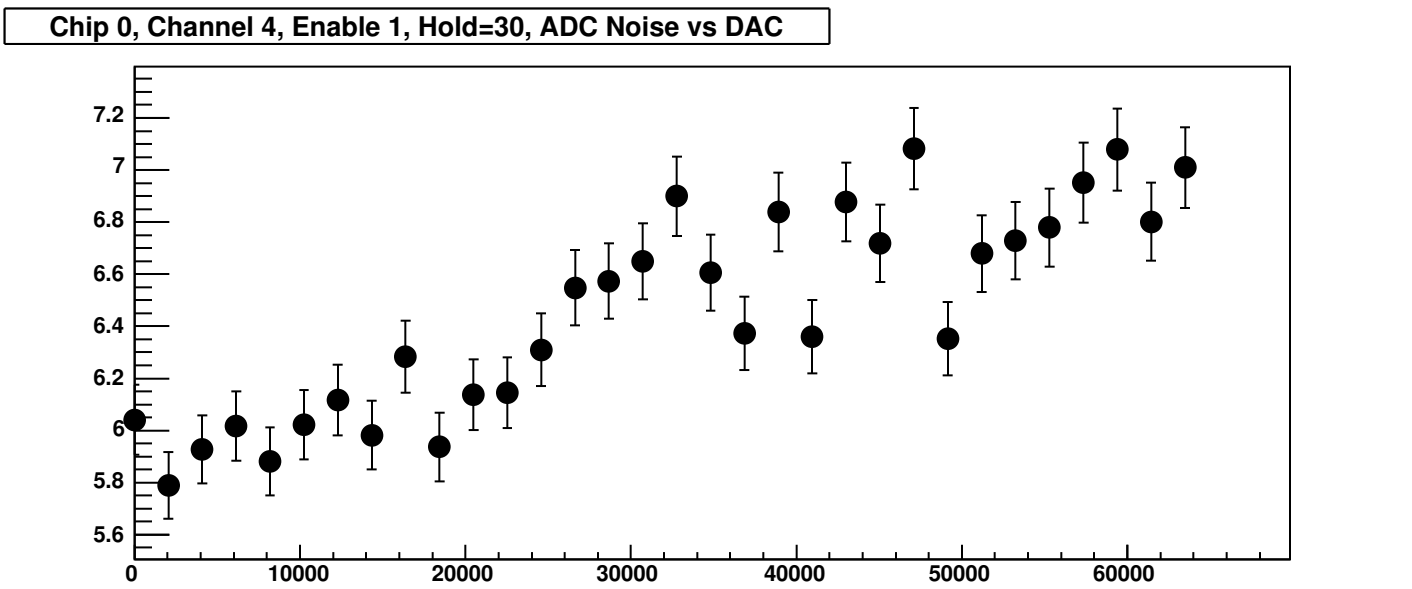

Chip 0, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

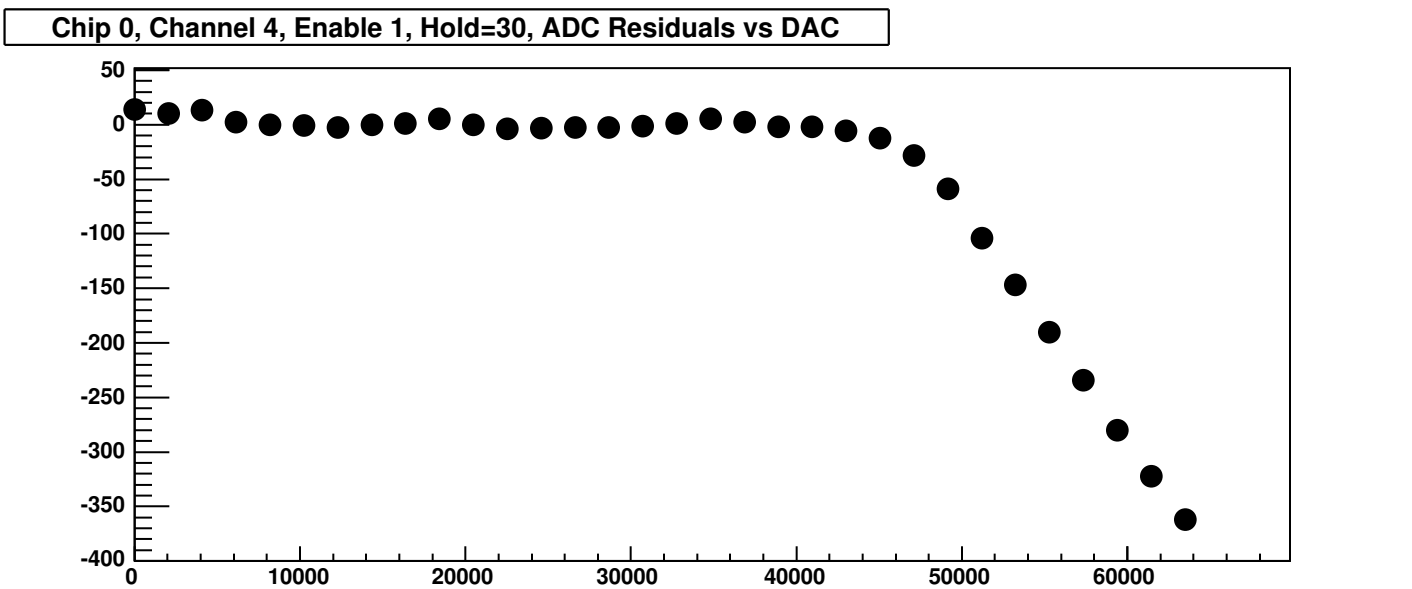

Chip 0, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3221 / 15
p0 970.8 ± 0.1085
p1 0.0003394 ± 4.422e-06
1000
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

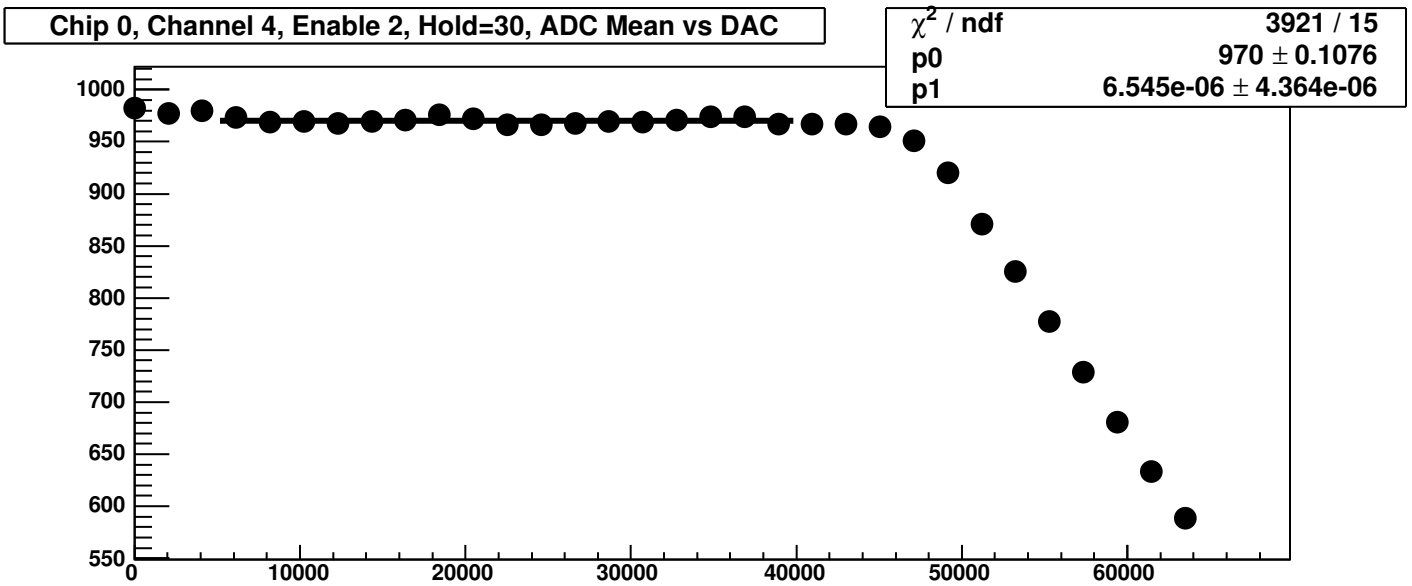

Chip 0, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

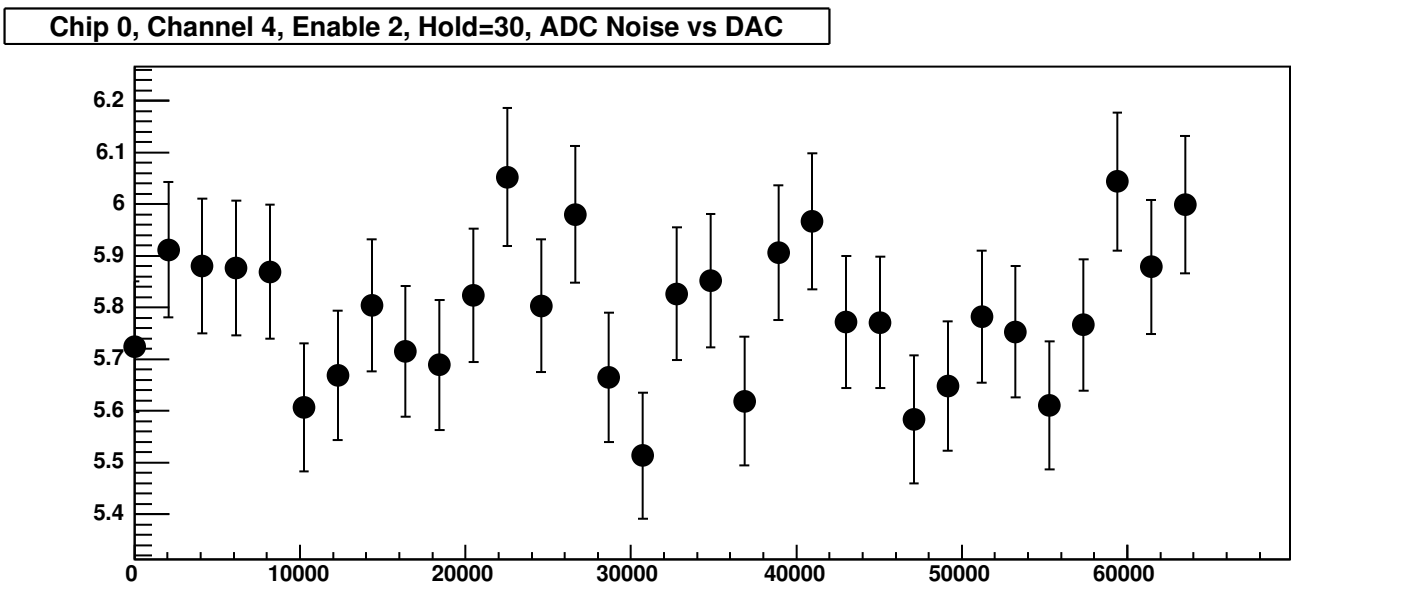

Chip 0, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

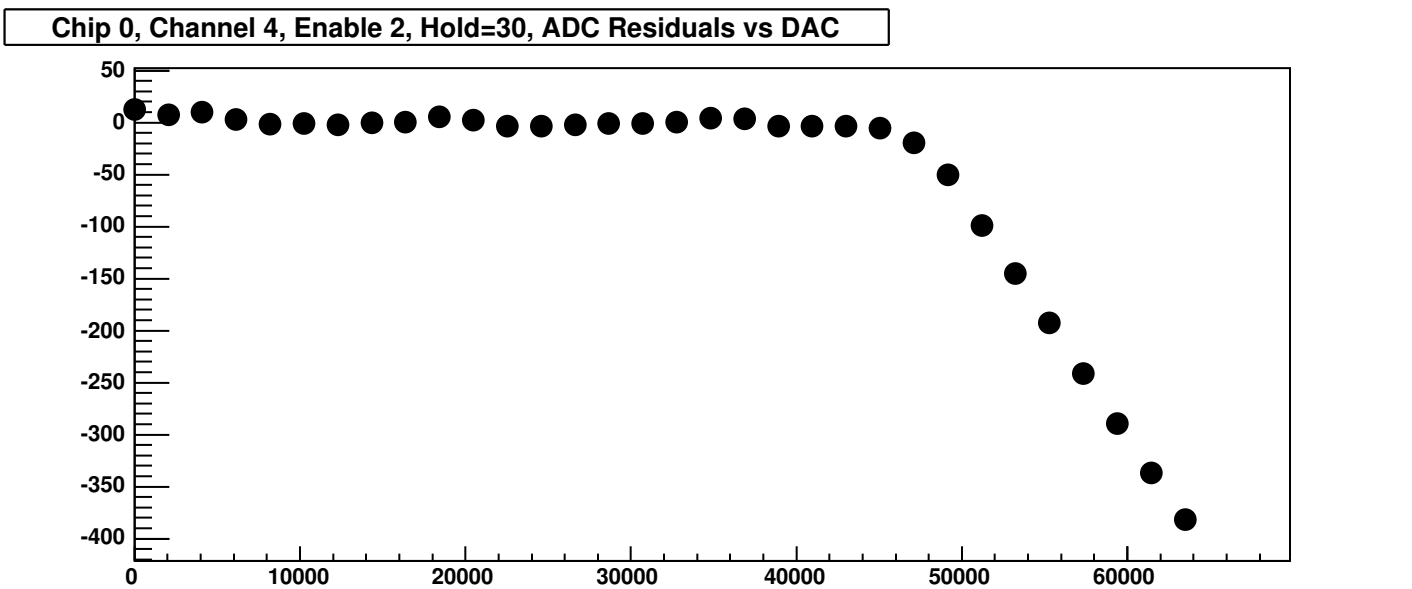

Chip 0, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3067 / 15
p0 967.4 ± 0.1141
p1 0.00177 ± 4.777e-06
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

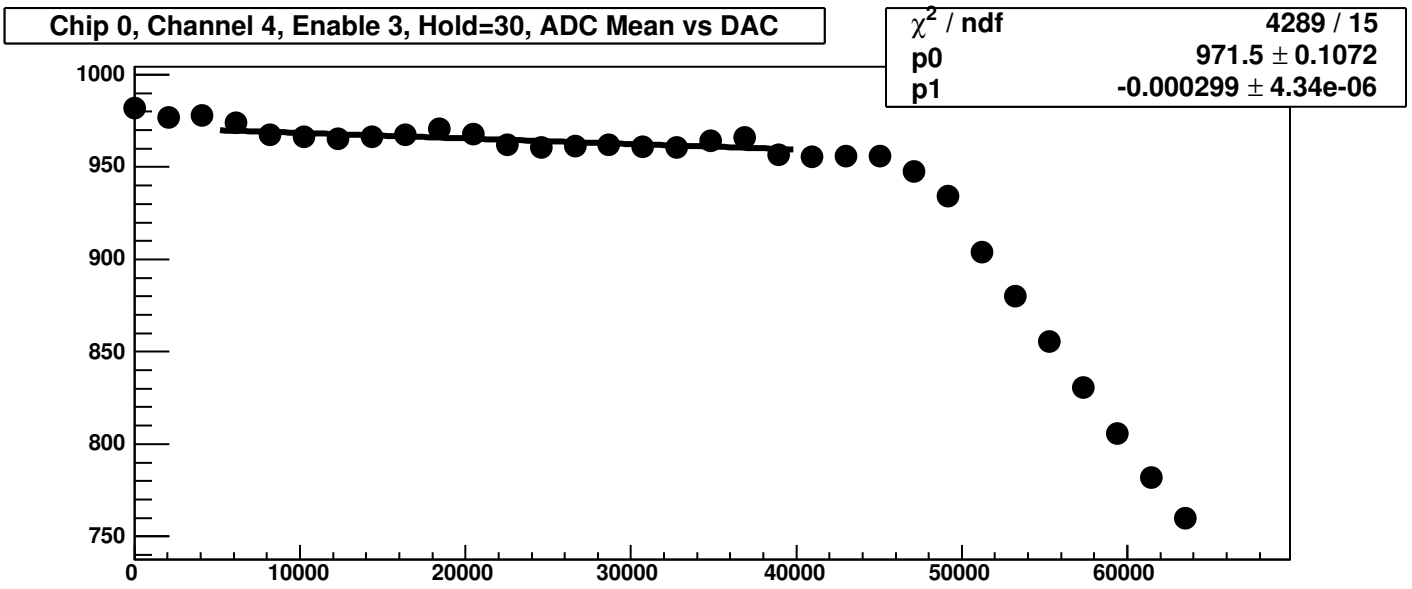

Chip 0, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

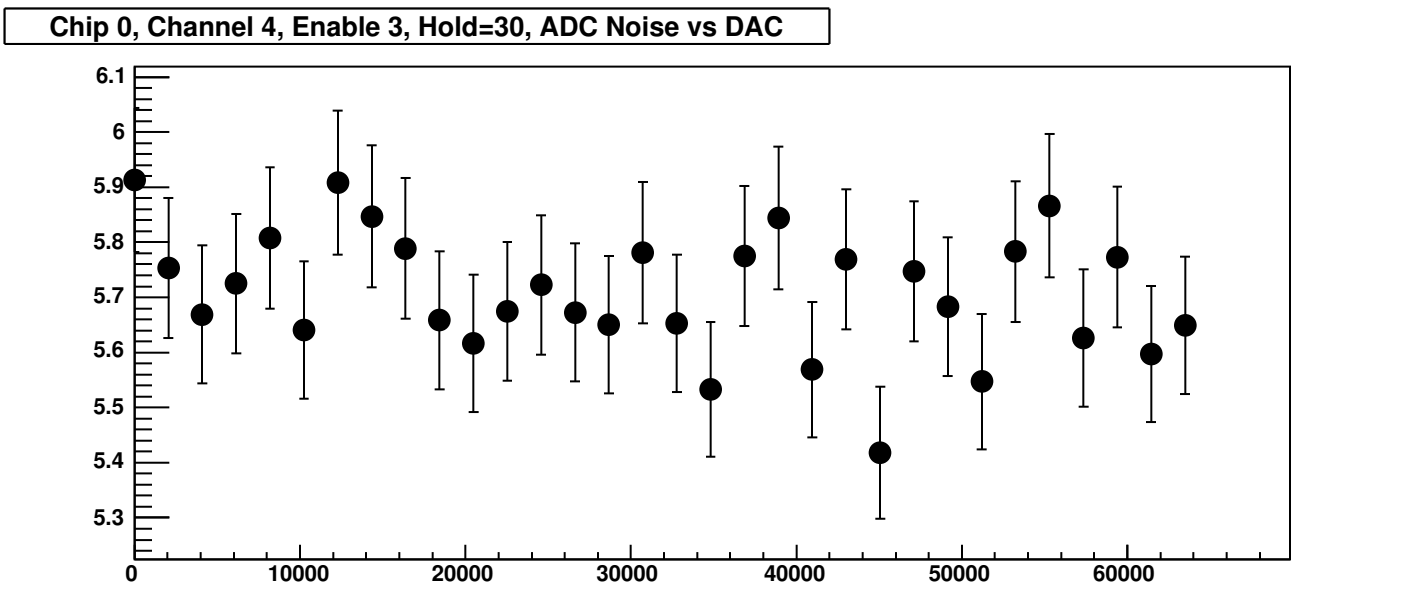

Chip 0, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

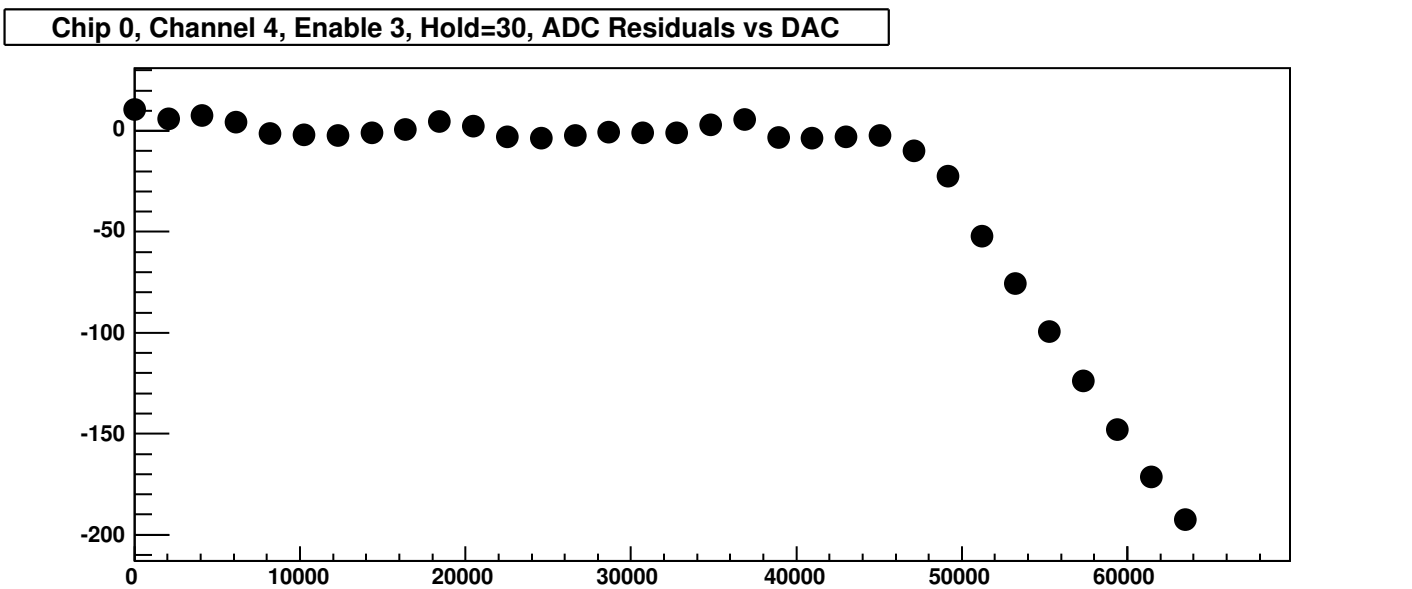

Chip 0, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3921 / 15
p0 970 ± 0.1076
p1 6.545e-06 ± 4.364e-06
1000
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

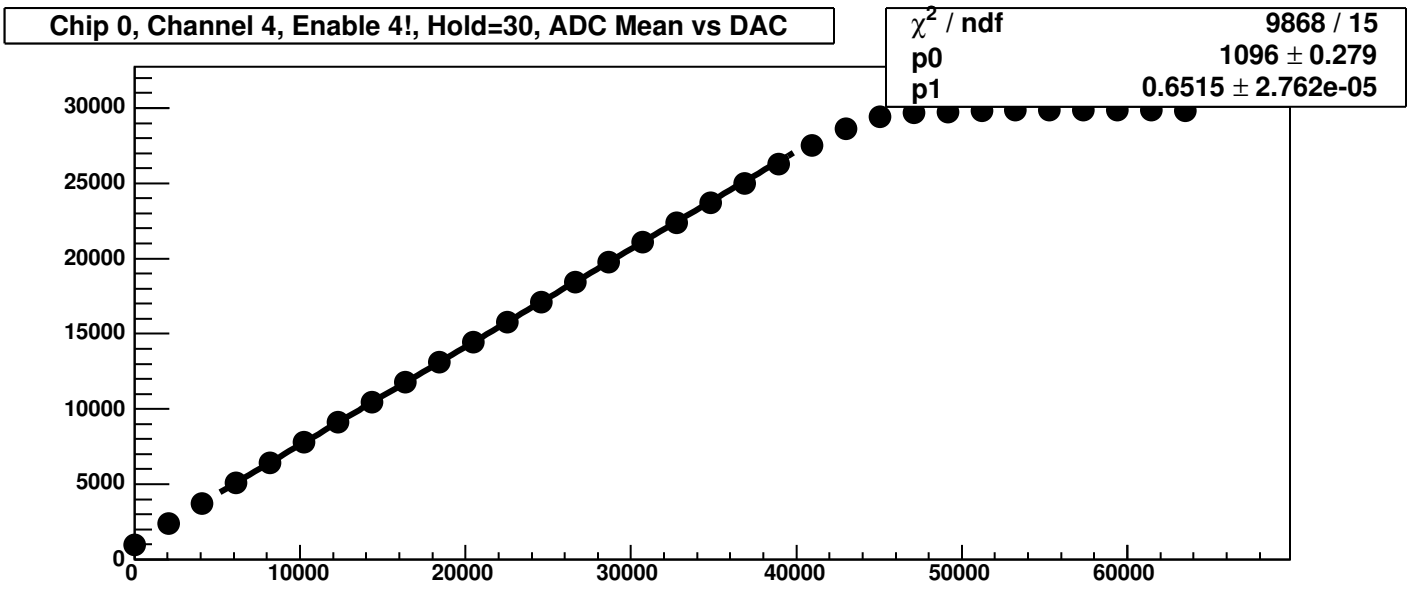

Chip 0, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

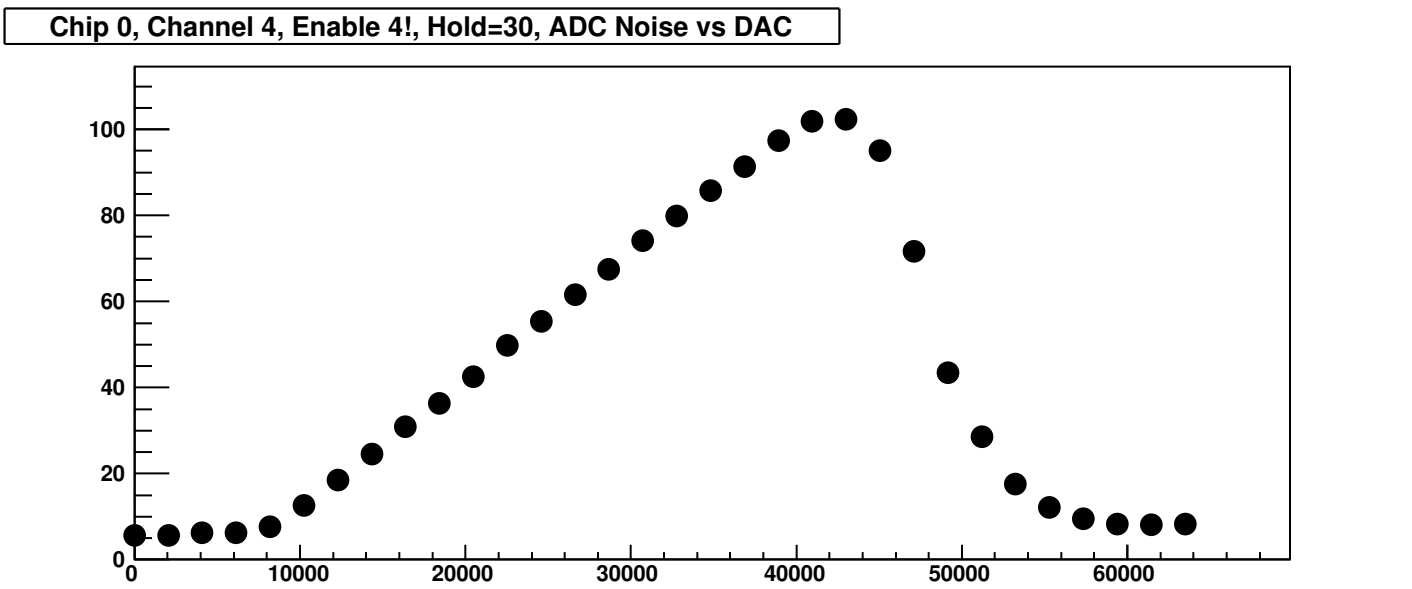

Chip 0, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

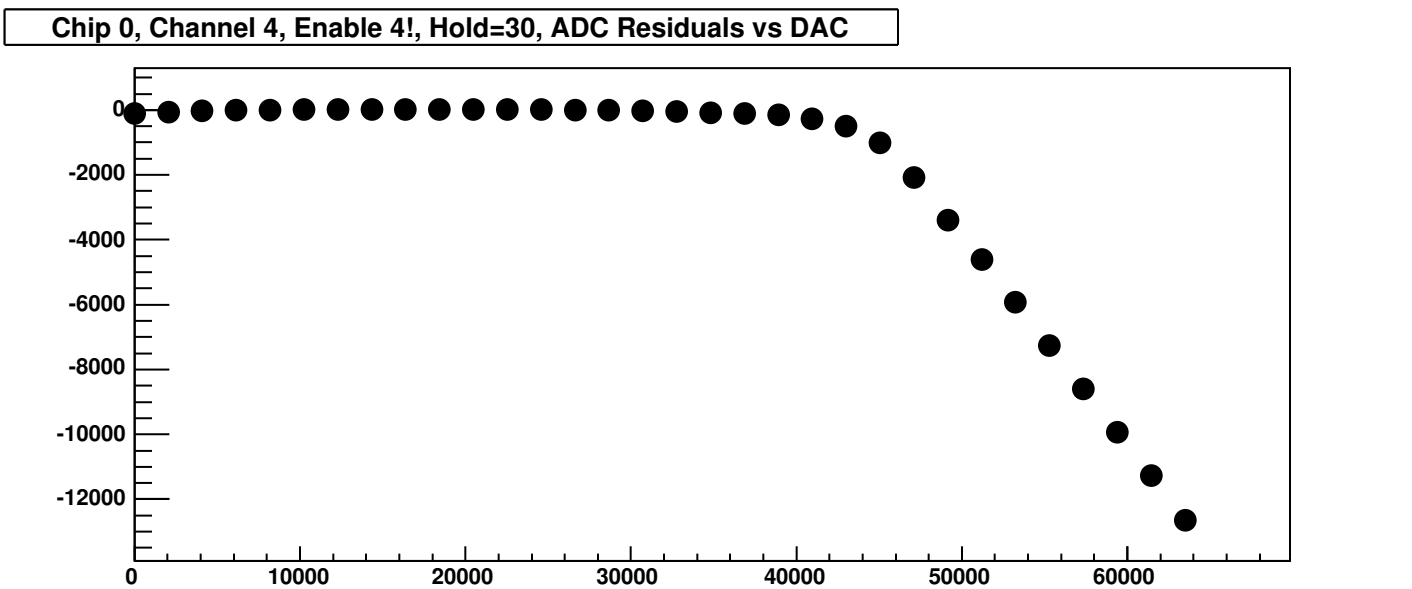

Chip 0, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4289 / 15
p0 971.5 ± 0.1072
p1 -0.000299 ± 4.34e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
0
10000
20000
30000
40000
50000
60000

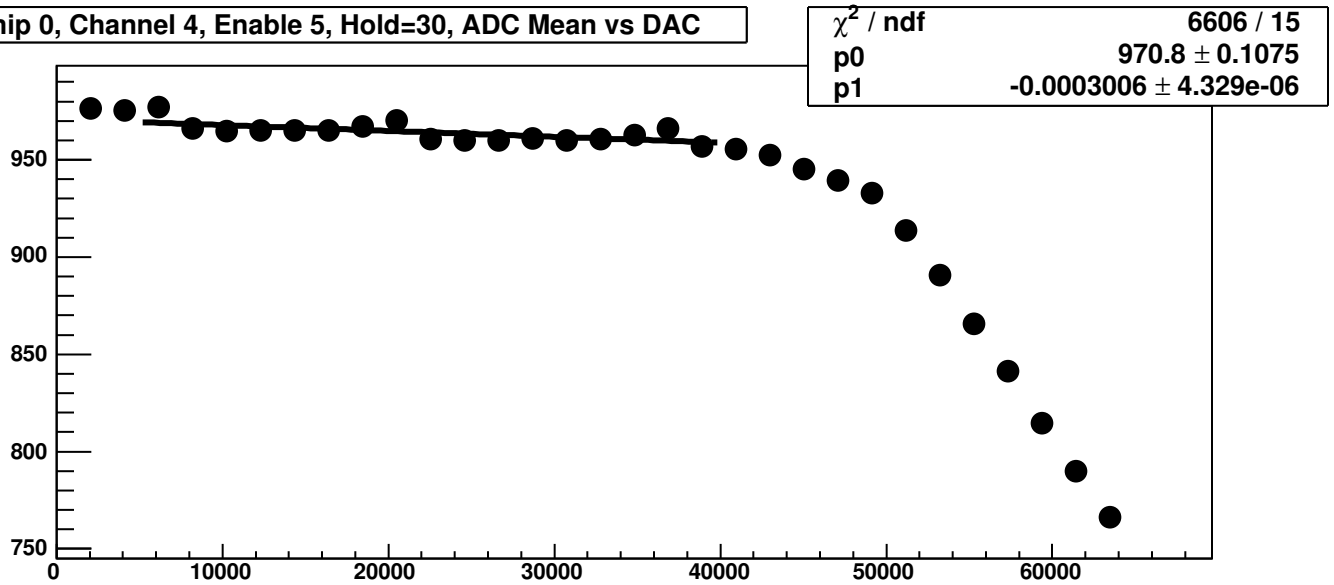

Chip 0, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 9868 / 15
p0 1096 ± 0.279
p1 0.6515 ± 2.762e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

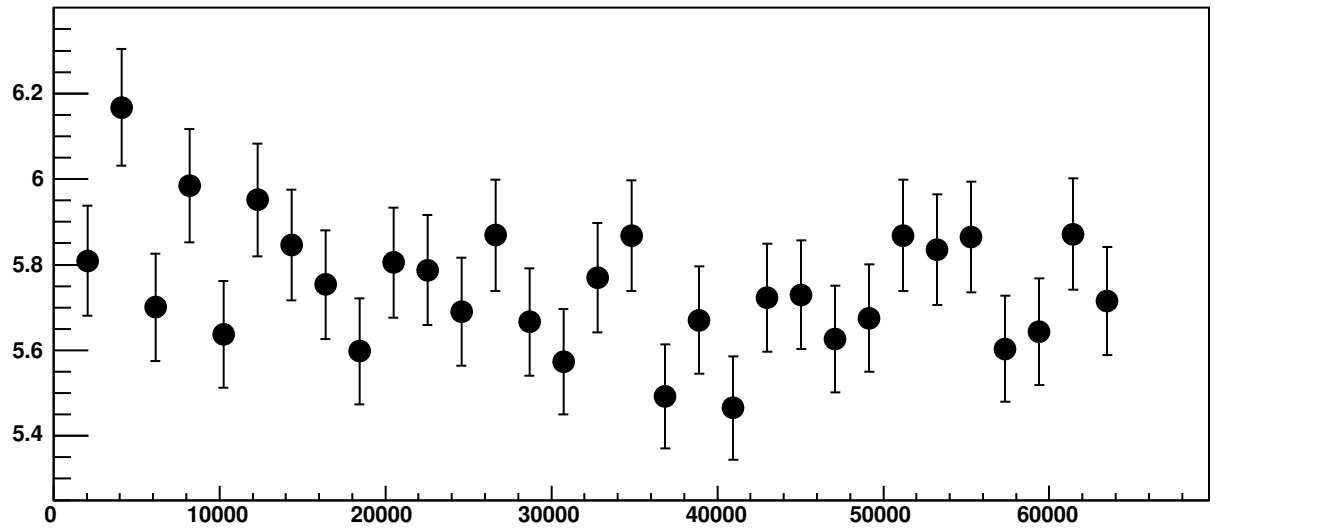

Chip 0, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

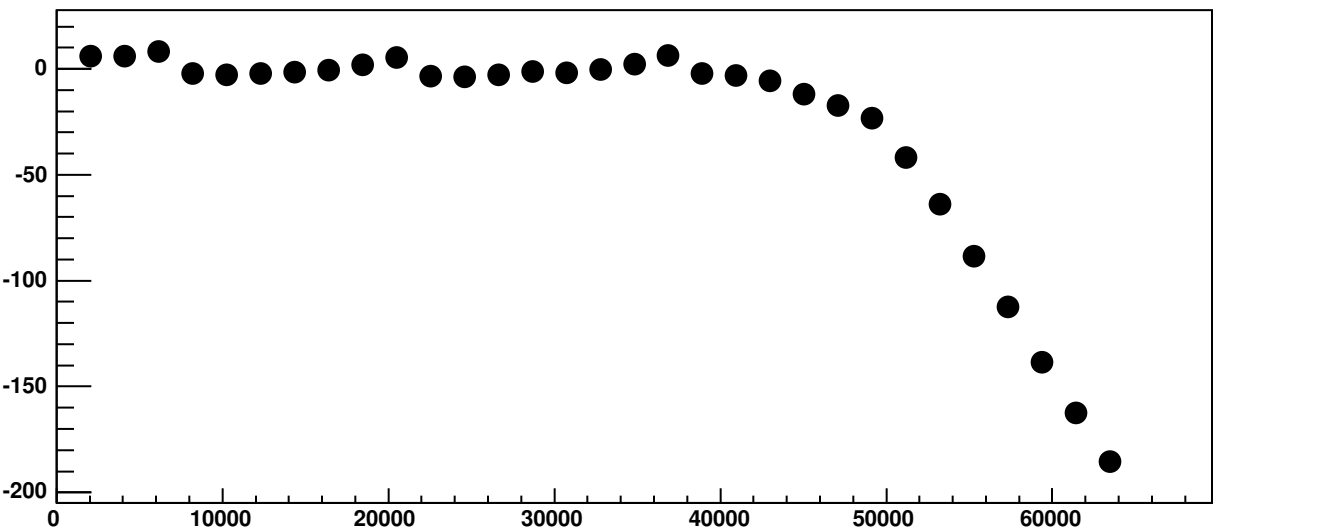
Chip 0, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC

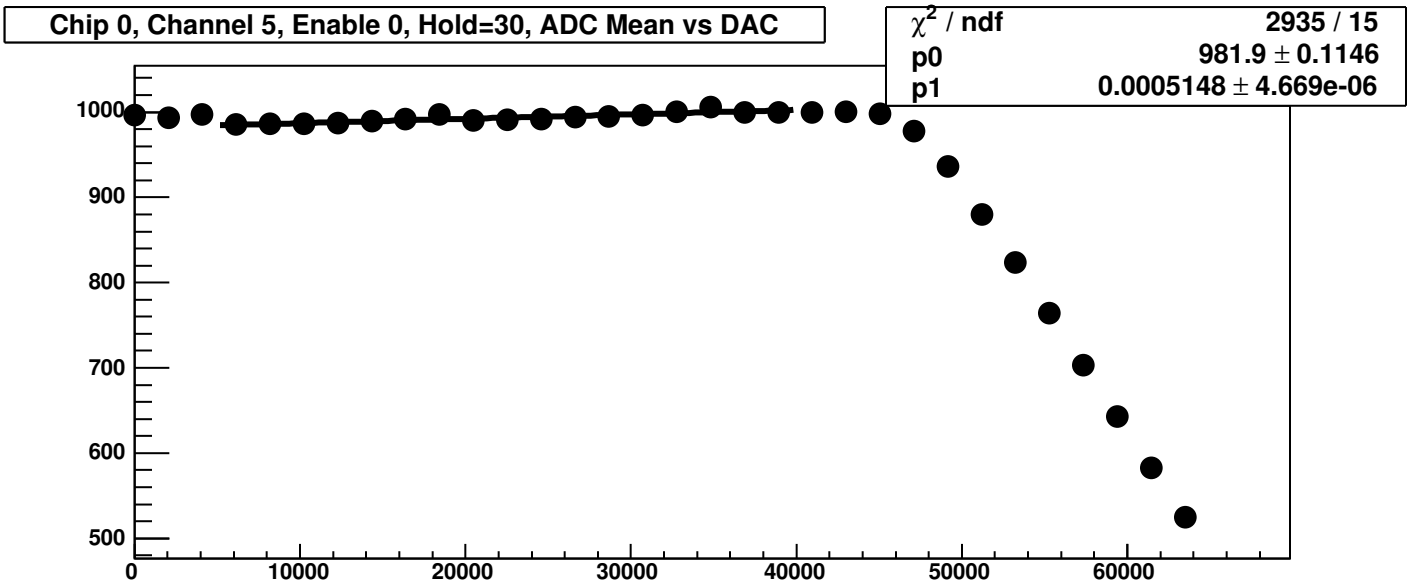


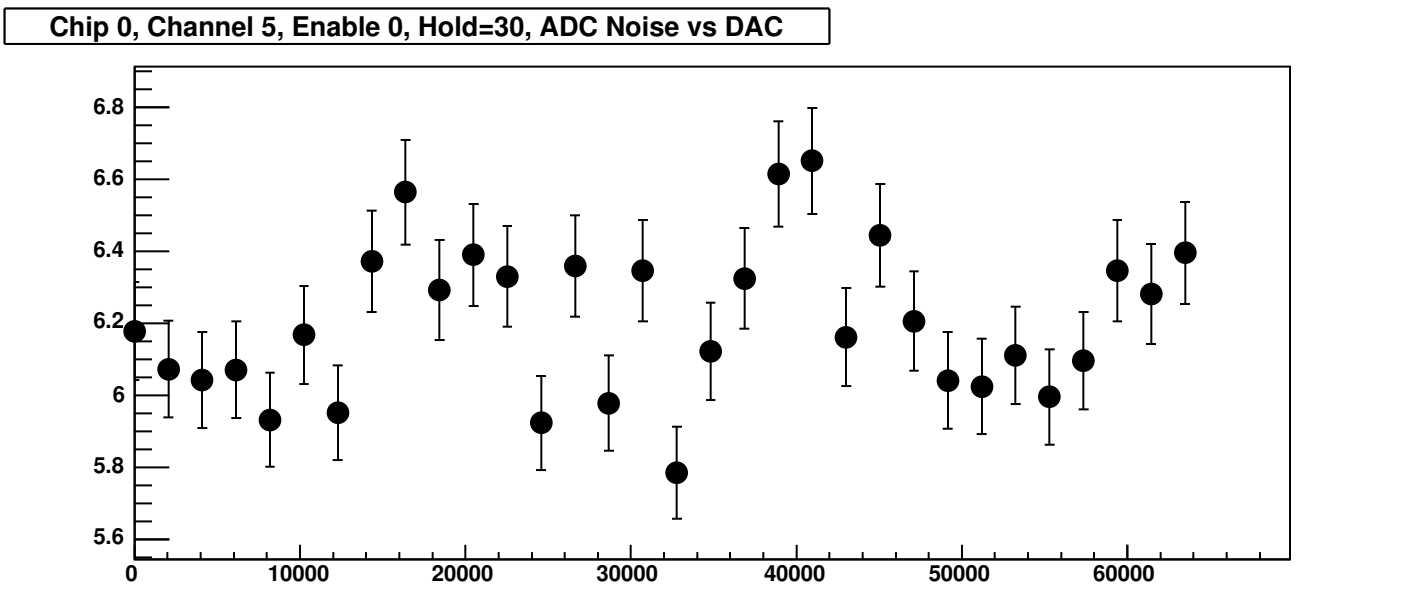
Chip 0, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

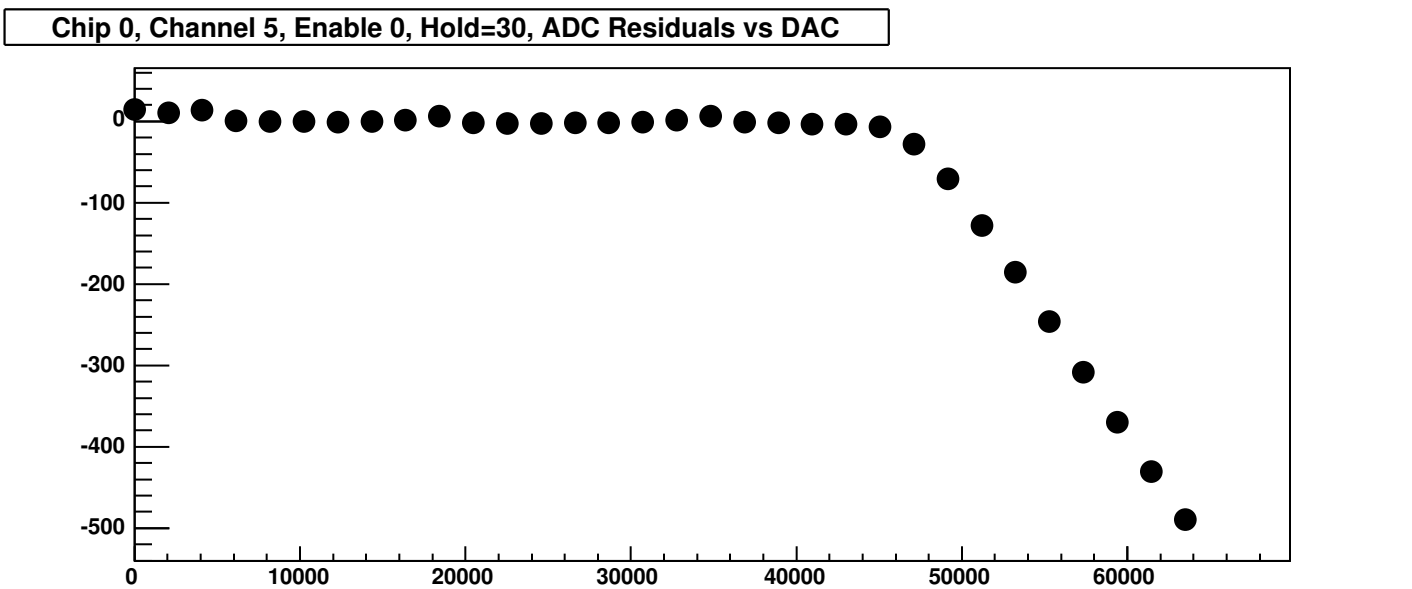


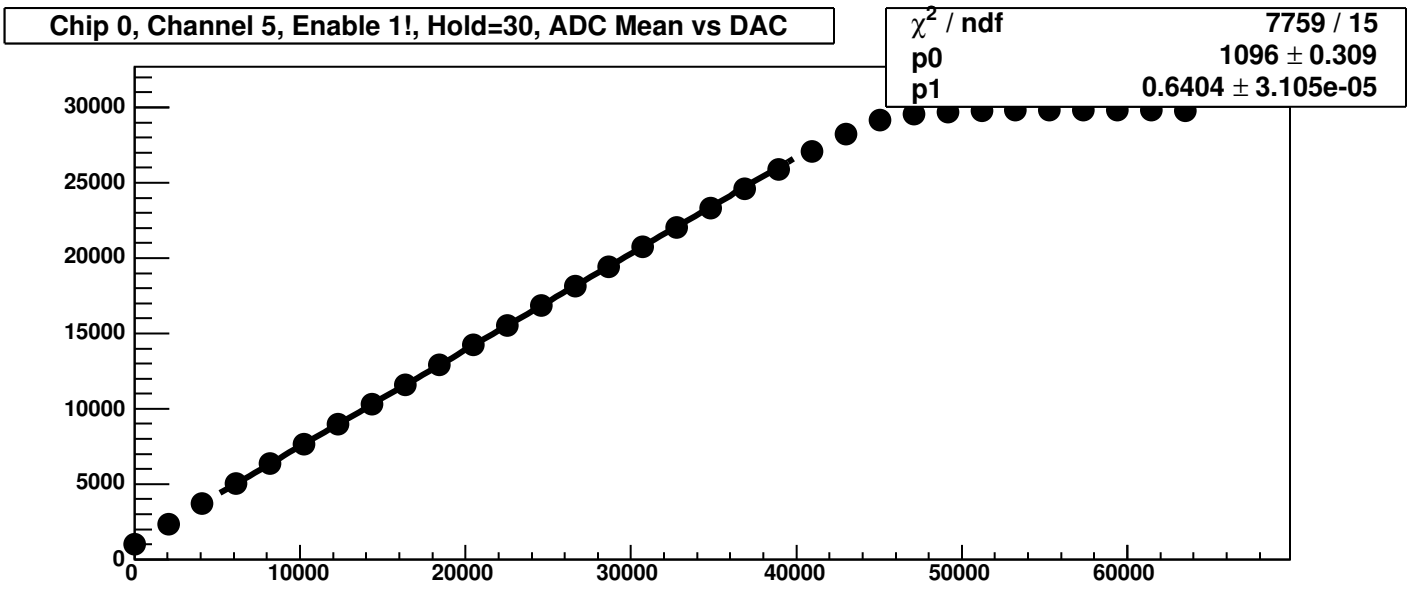
Chip 0, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC

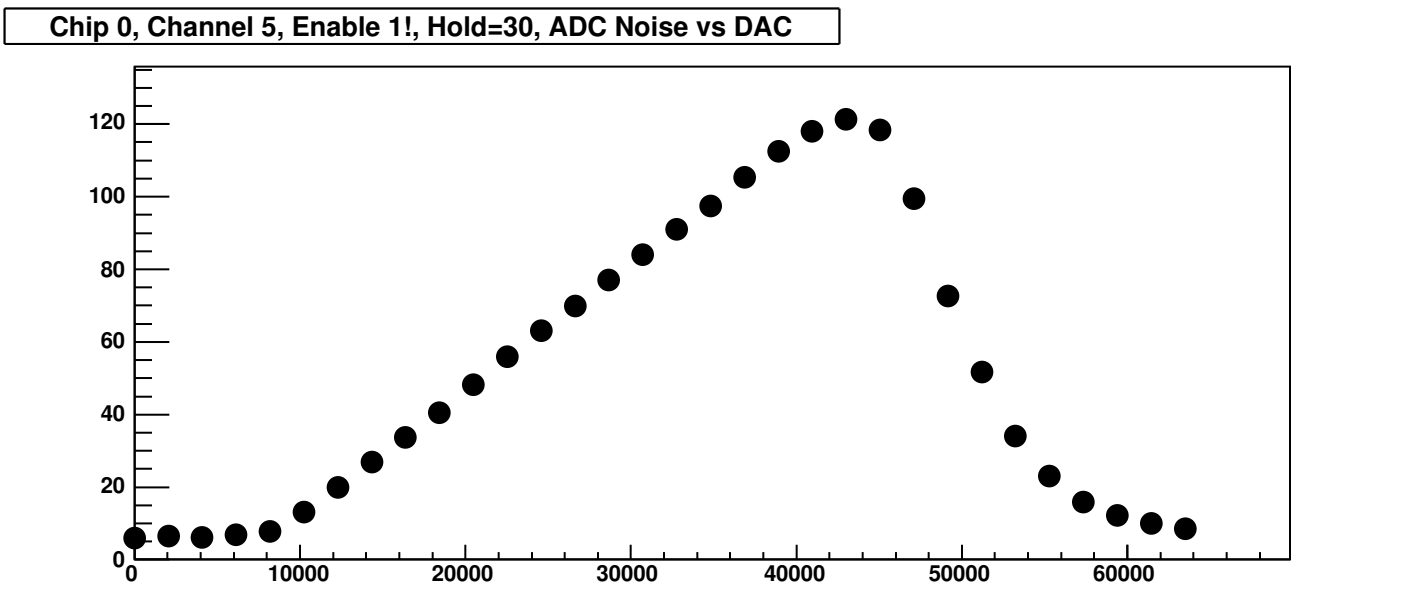


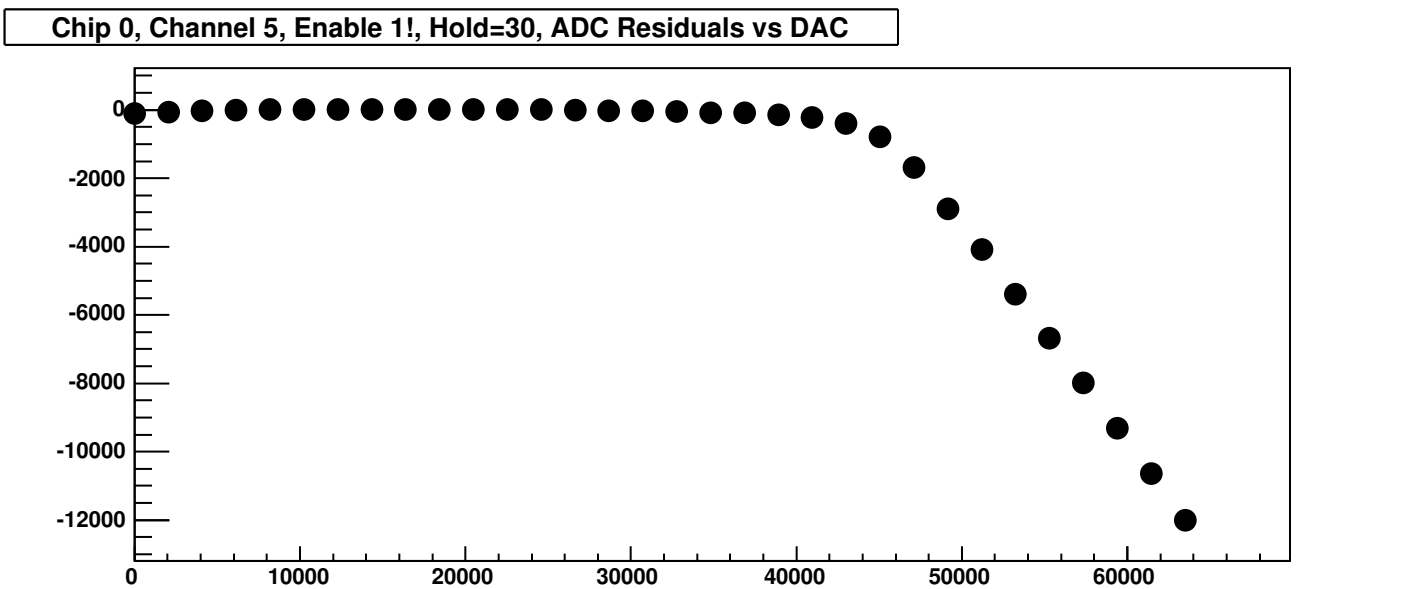

Chip 0, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2935 / 15
p0 981.9 ± 0.1146
p1 0.0005148 ± 4.669e-06
1000
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000

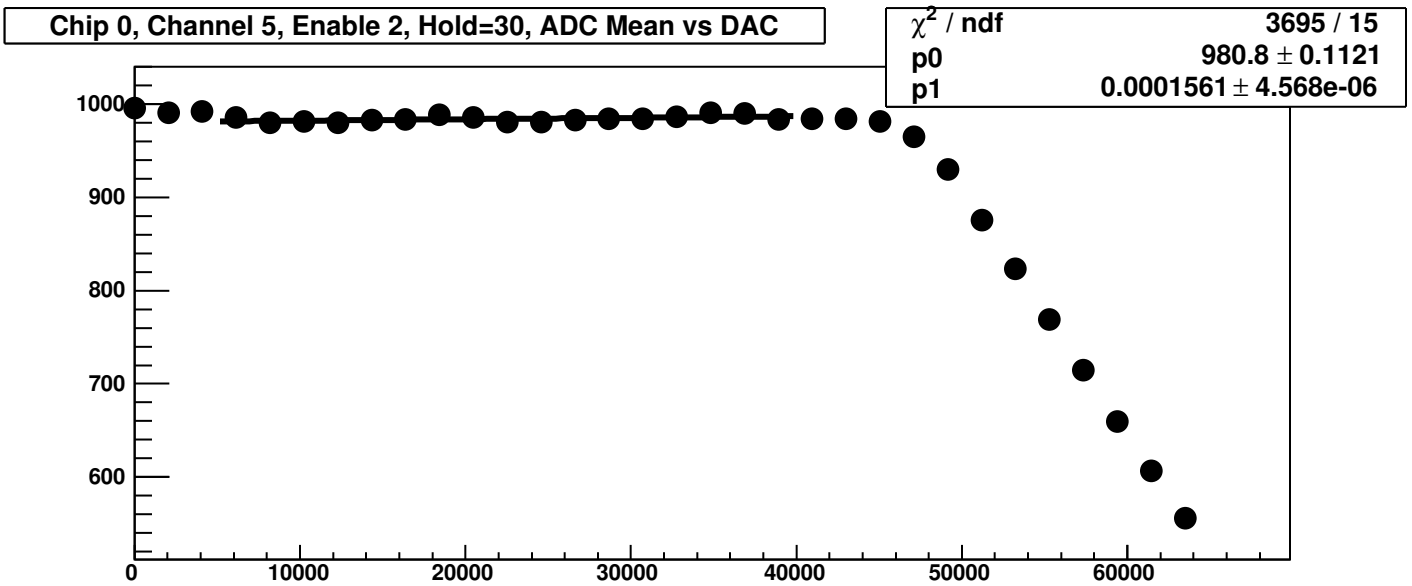

Chip 0, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

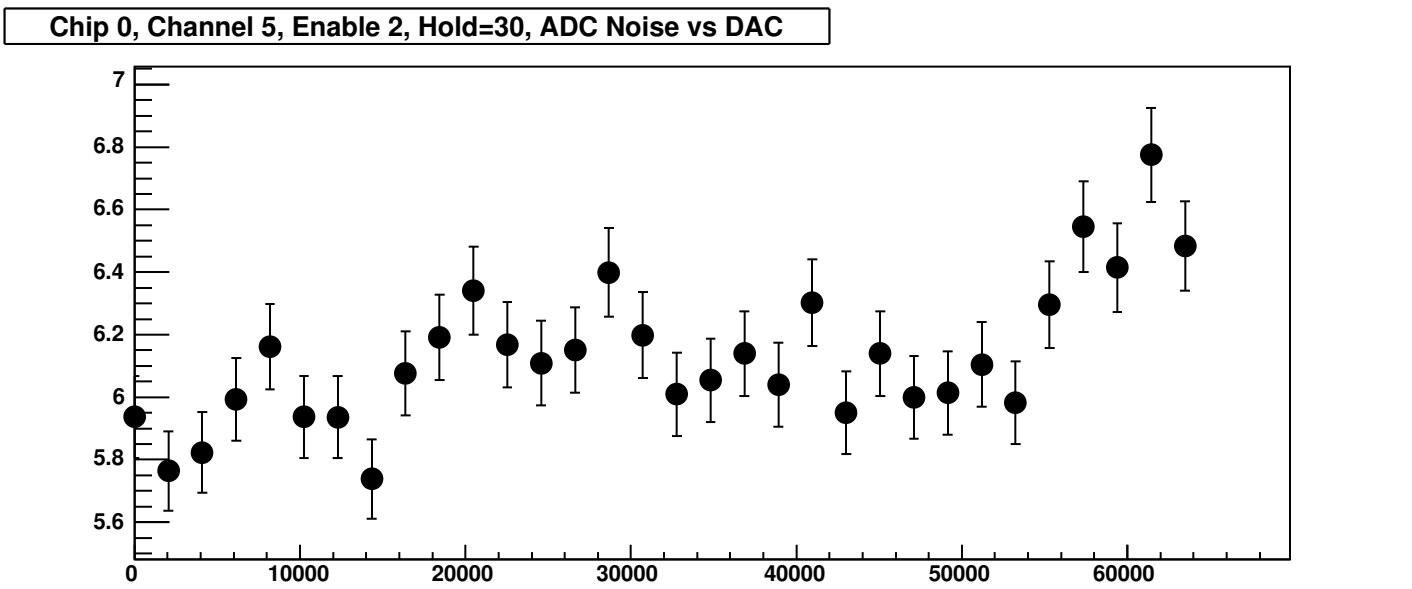

Chip 0, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

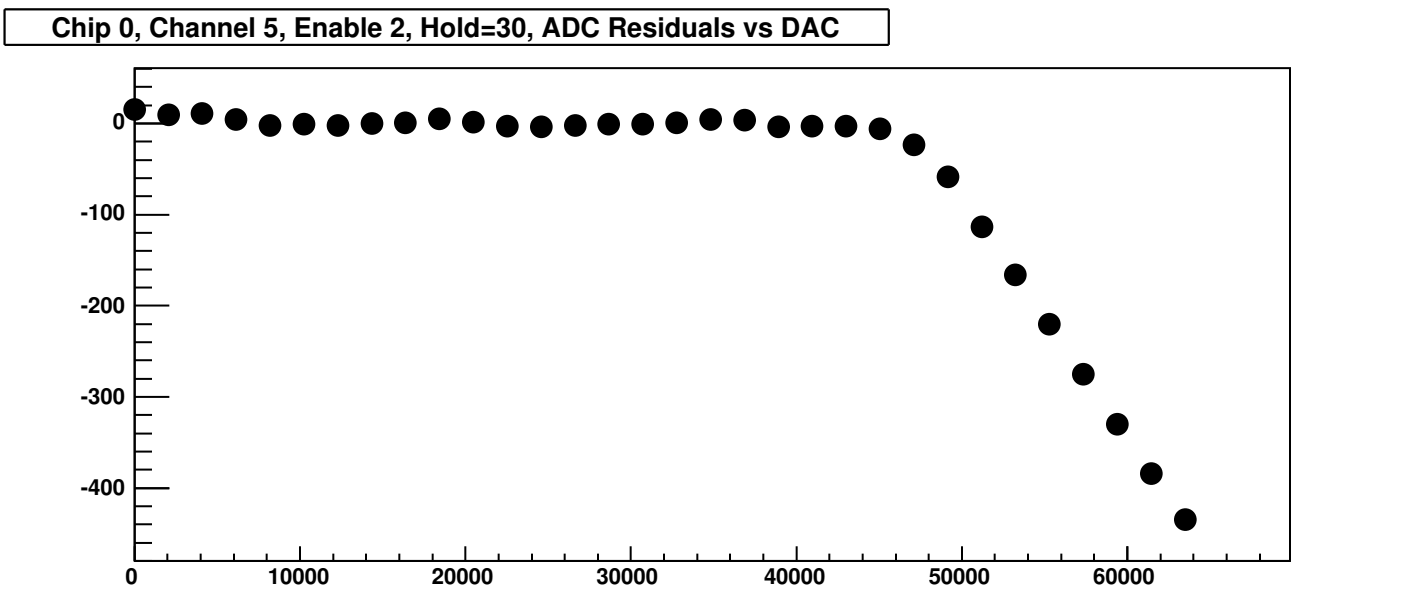

Chip 0, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 7759 / 15
p0 1096 ± 0.309
p1 0.6404 ± 3.105e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

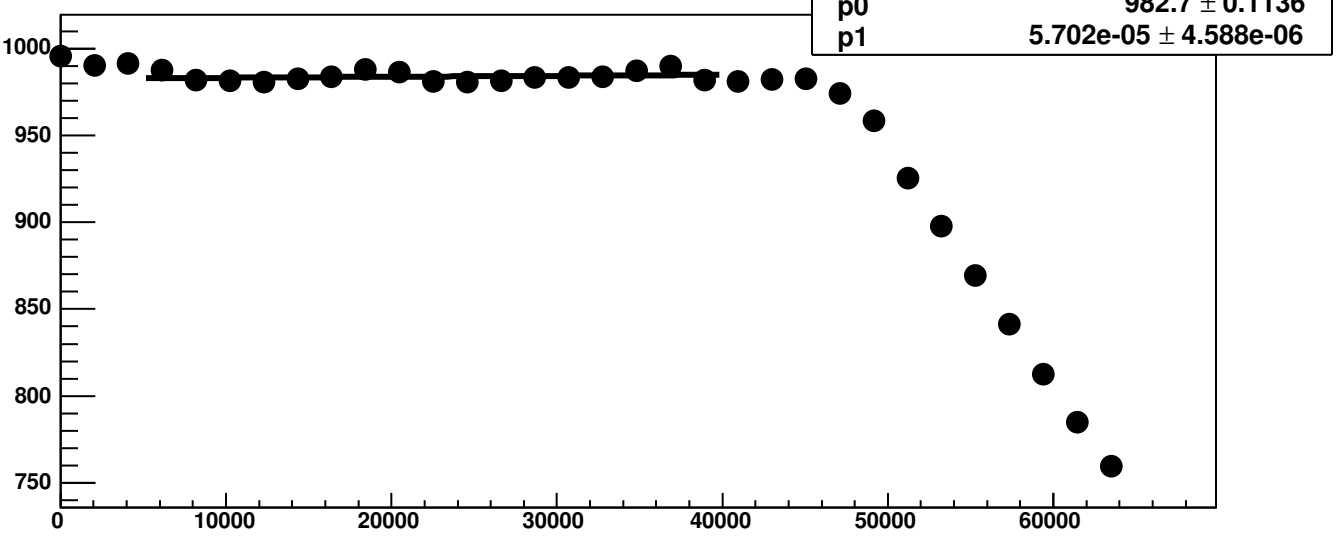

Chip 0, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3695 / 15
p0 980.8 ± 0.1121
p1 0.0001561 ± 4.568e-06
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000

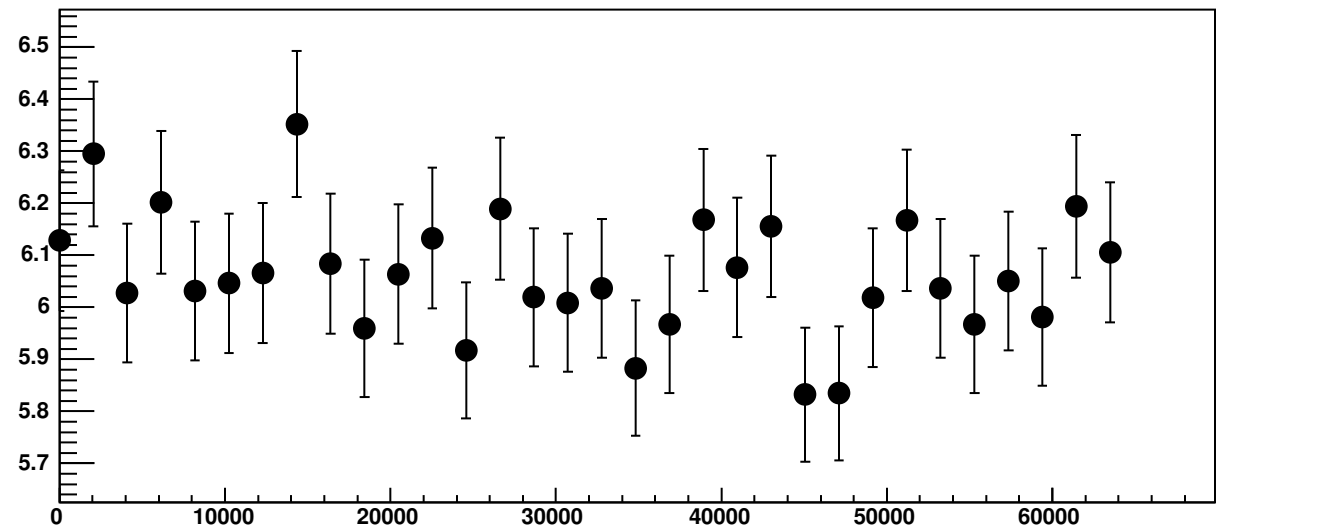

Chip 0, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

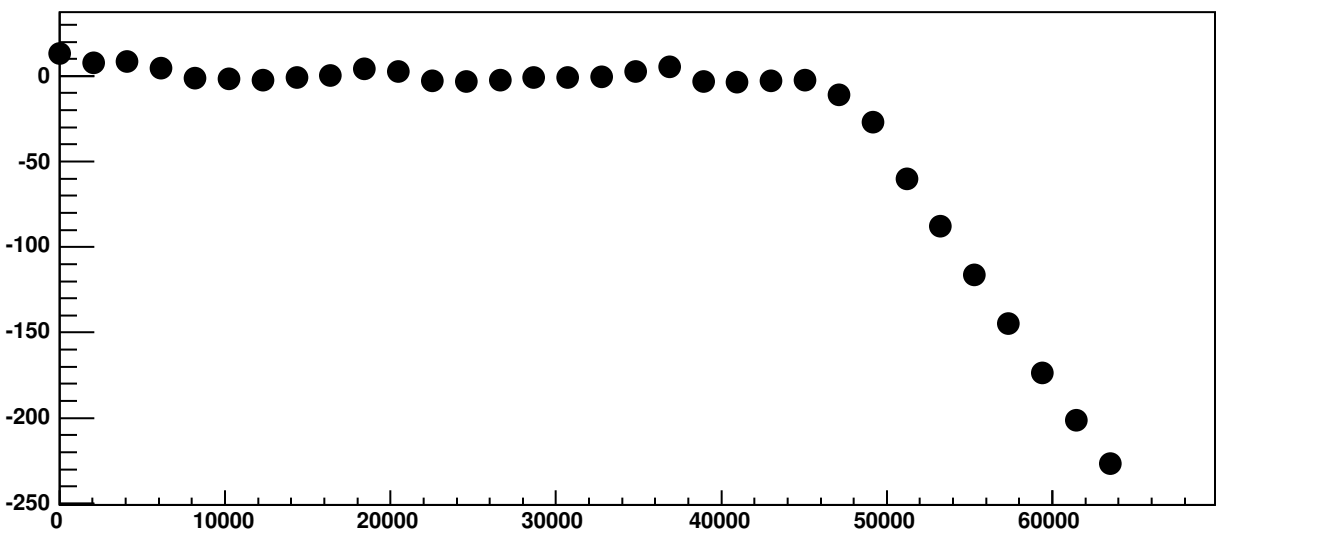
Chip 0, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

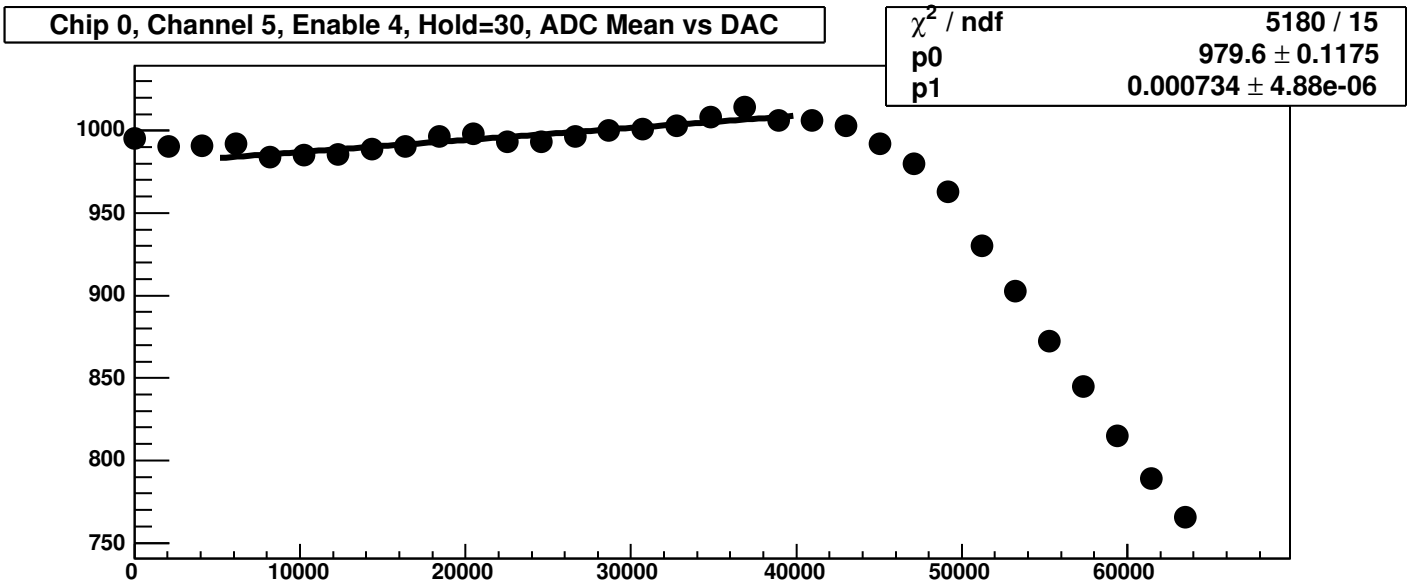


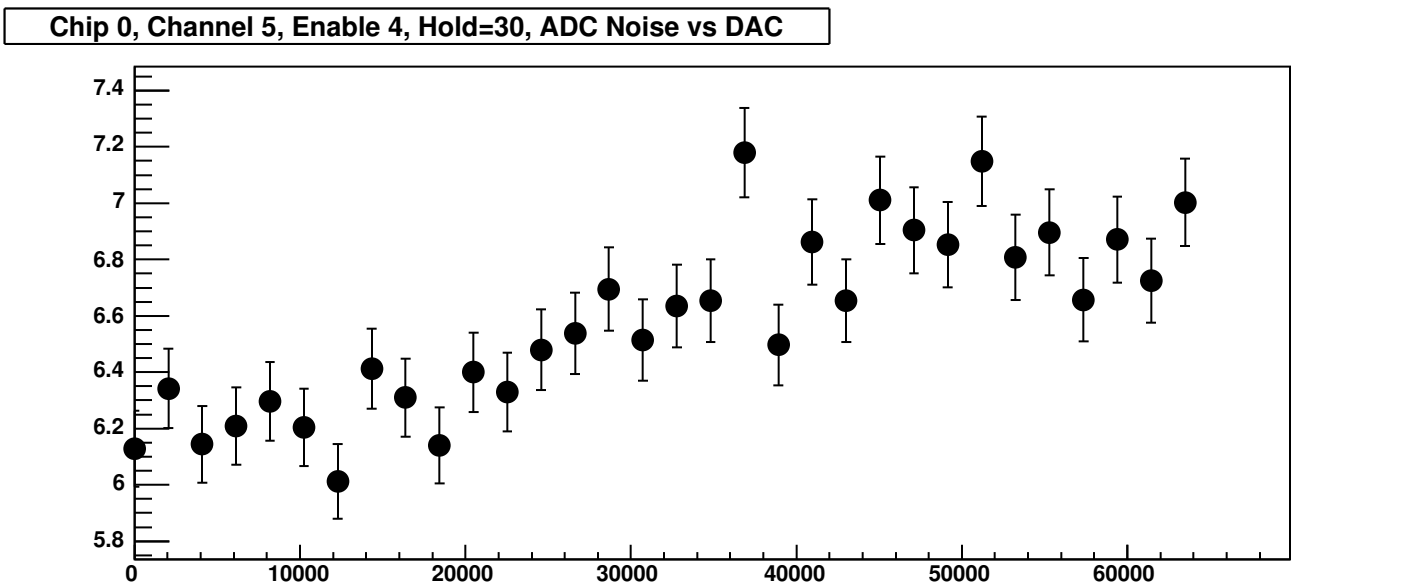
Chip 0, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

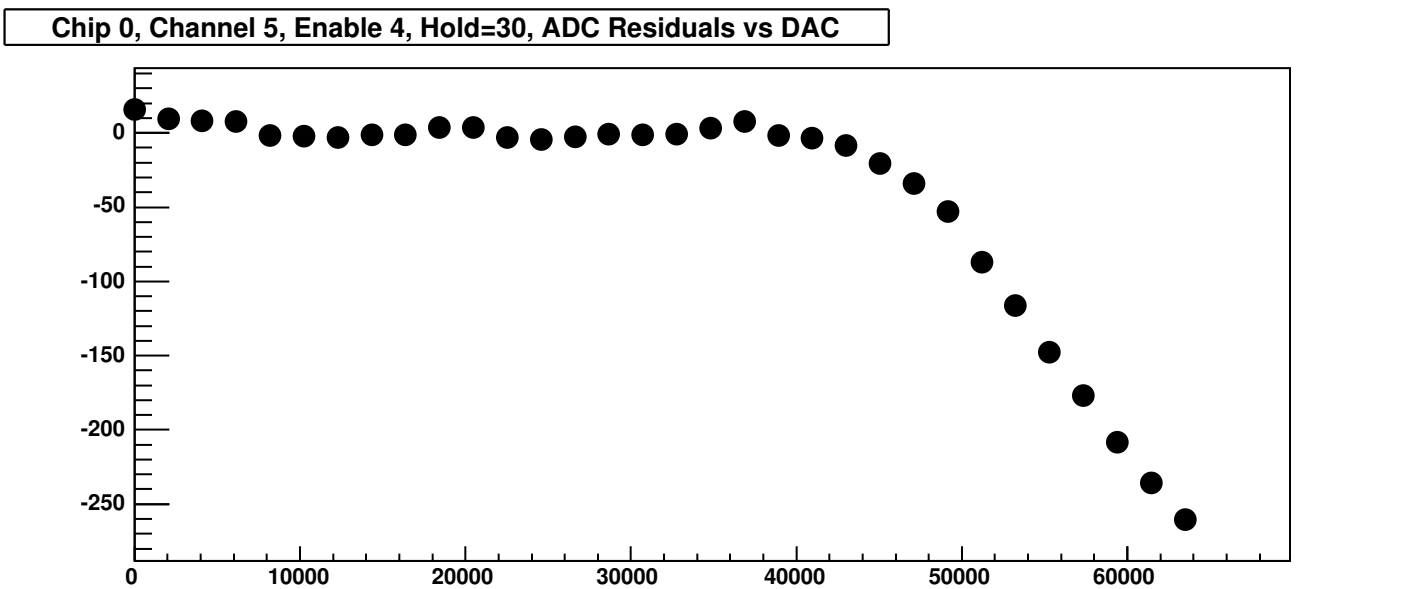


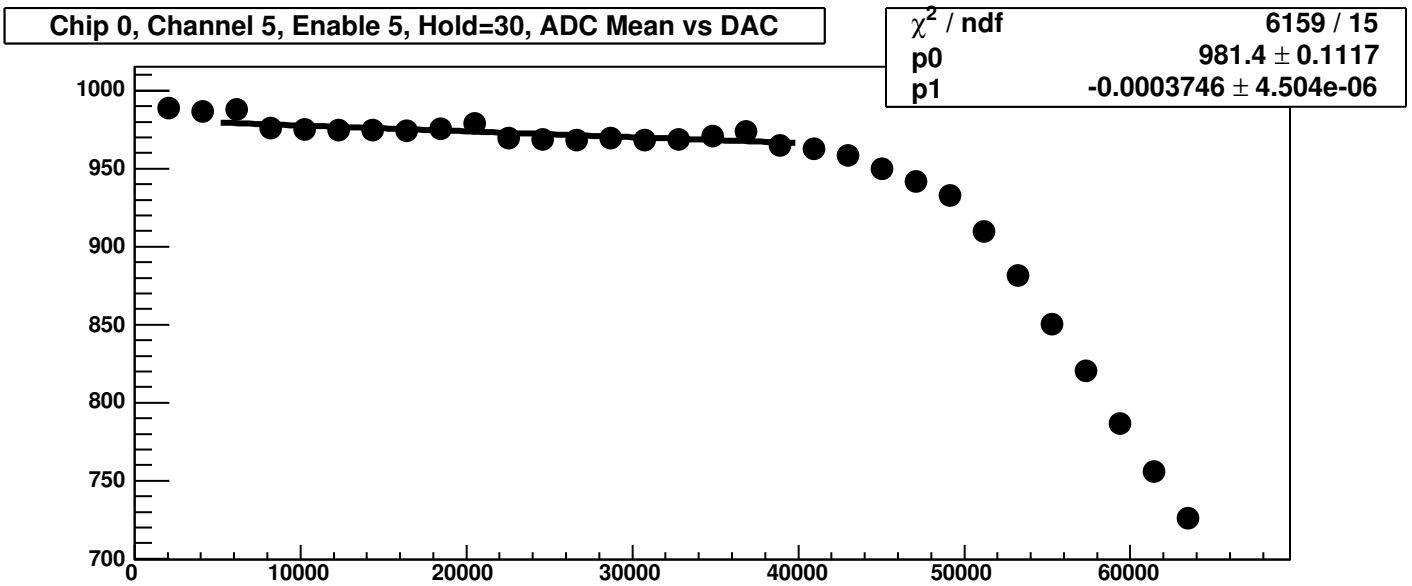
Chip 0, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

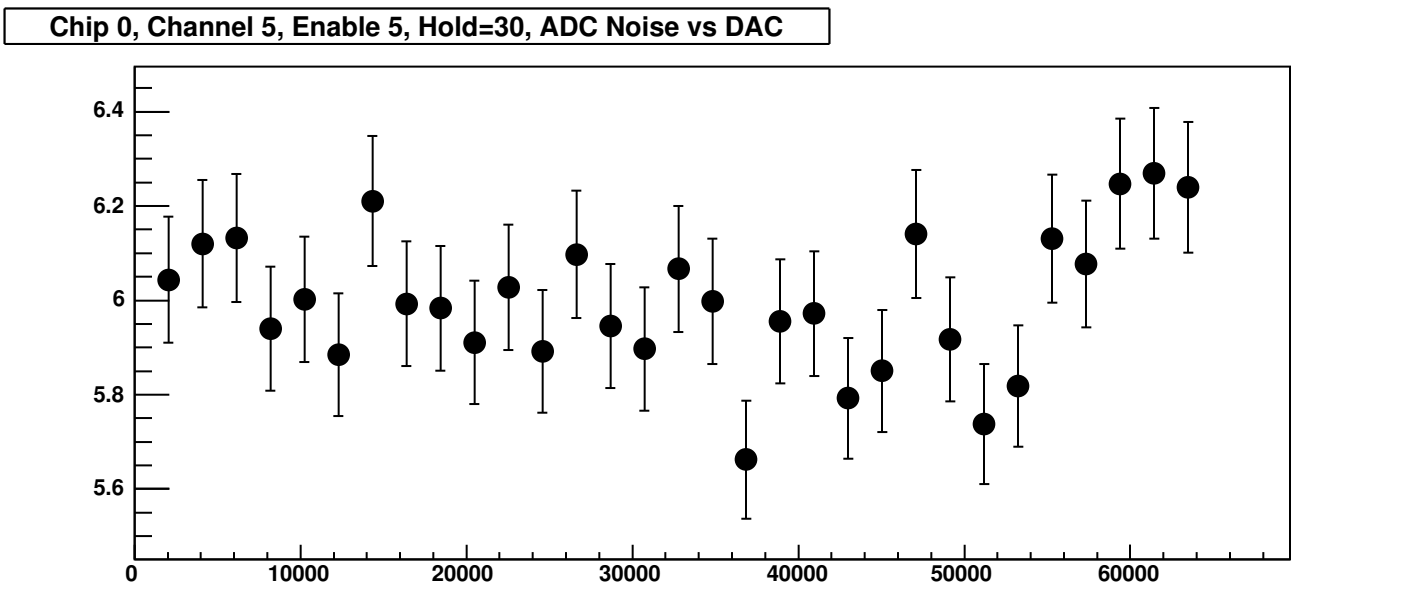


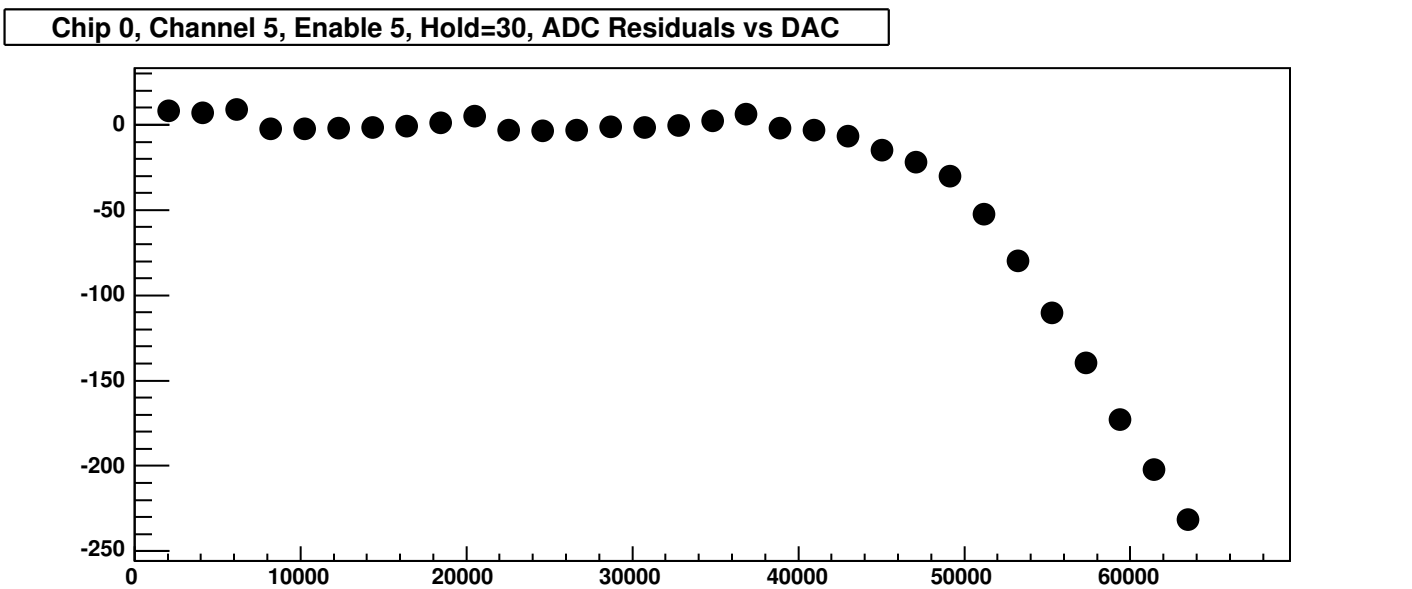

Chip 0, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5180 / 15
p0 979.6 ± 0.1175
p1 0.000734 ± 4.88e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

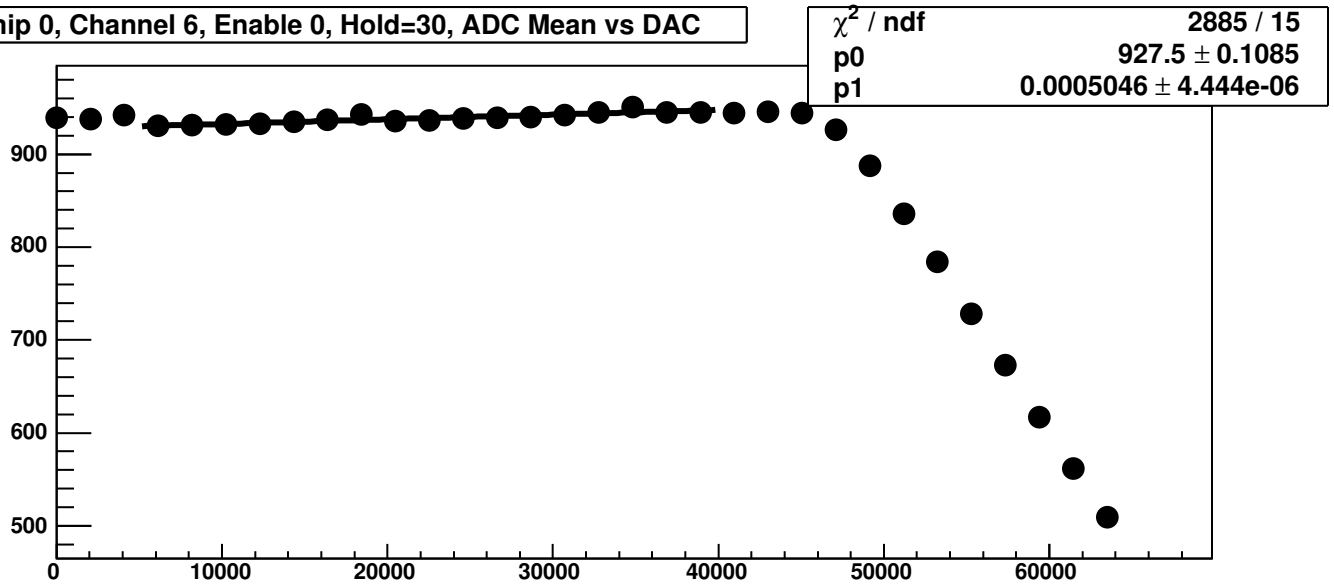

Chip 0, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6159 / 15
p0 981.4 ± 0.1117
p1 -0.0003746 ± 4.504e-06
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

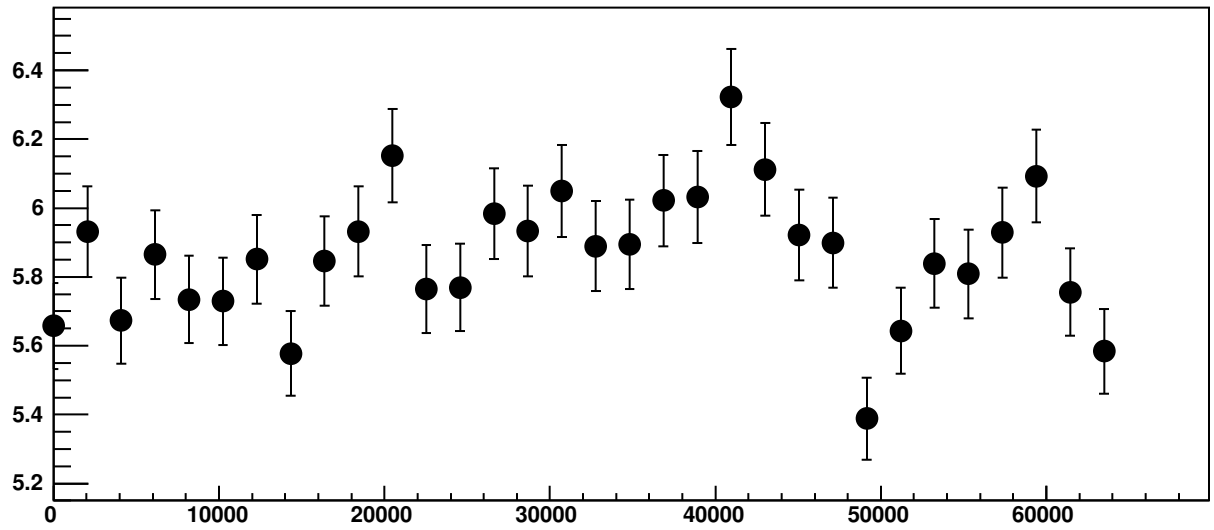

Chip 0, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

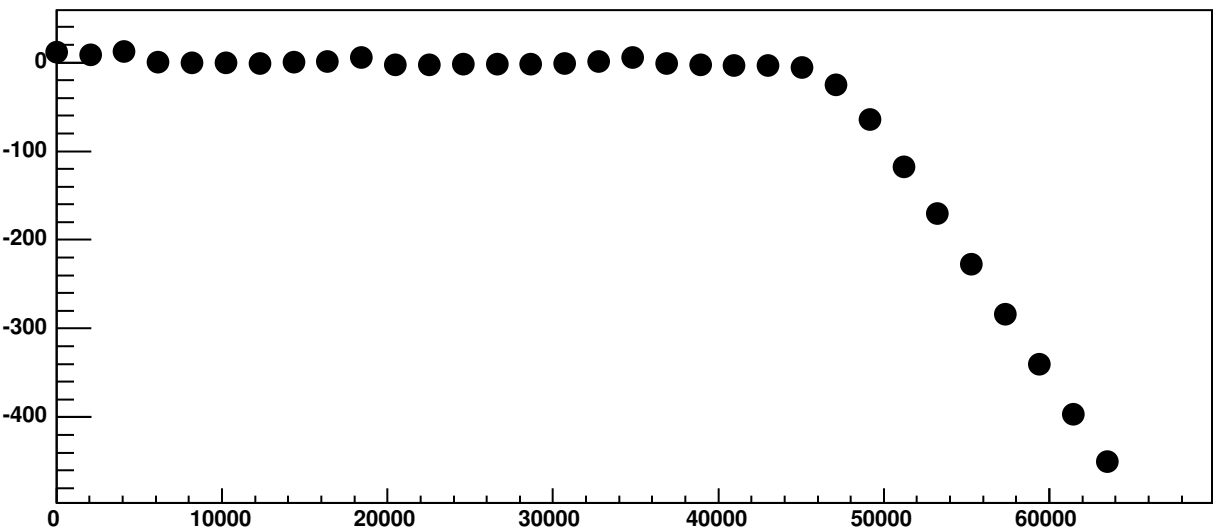
Chip 0, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC

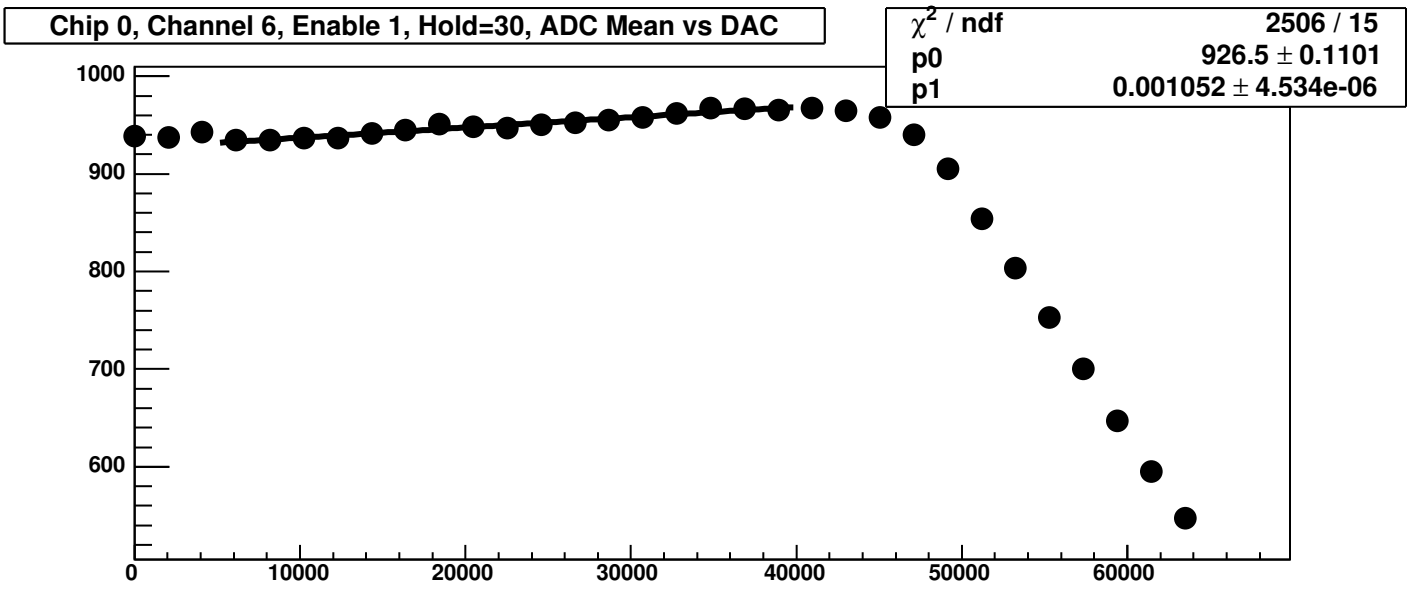


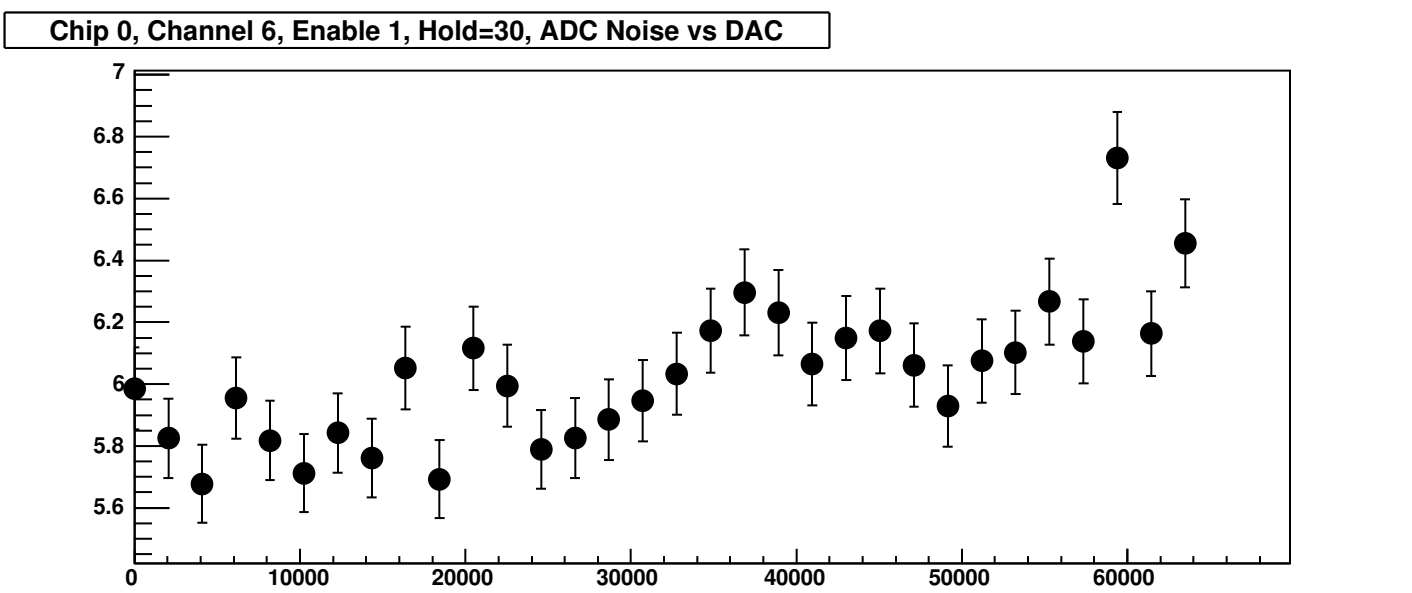
Chip 0, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC

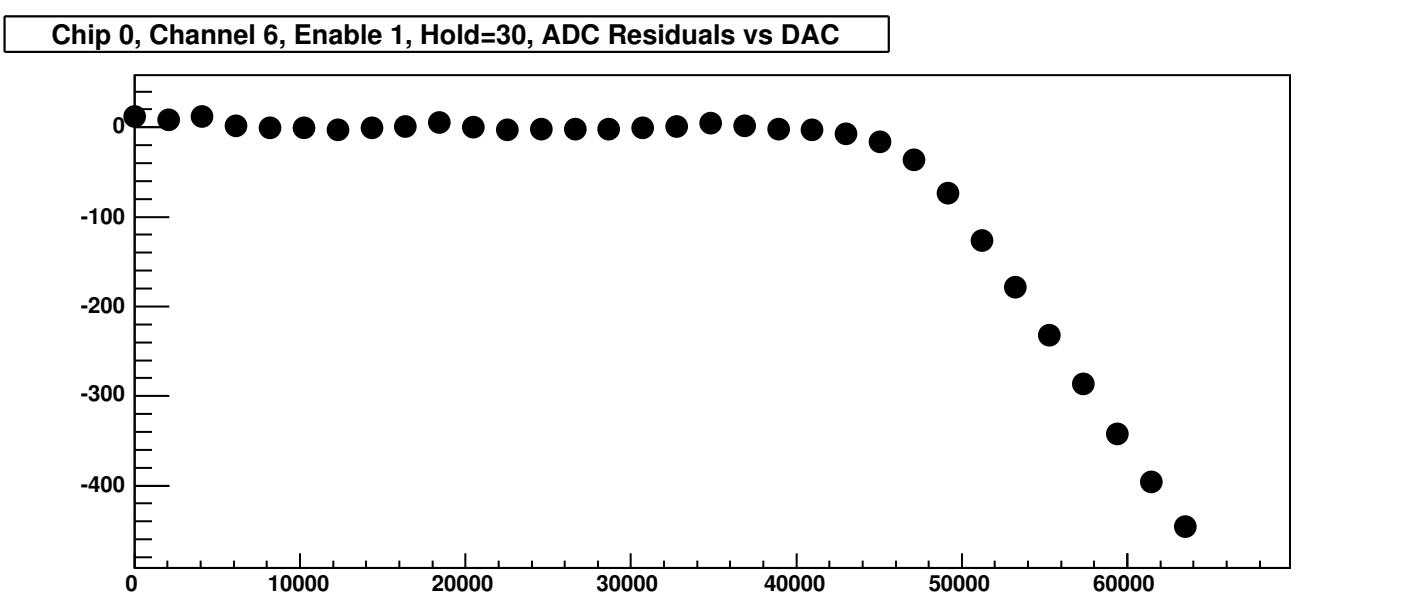


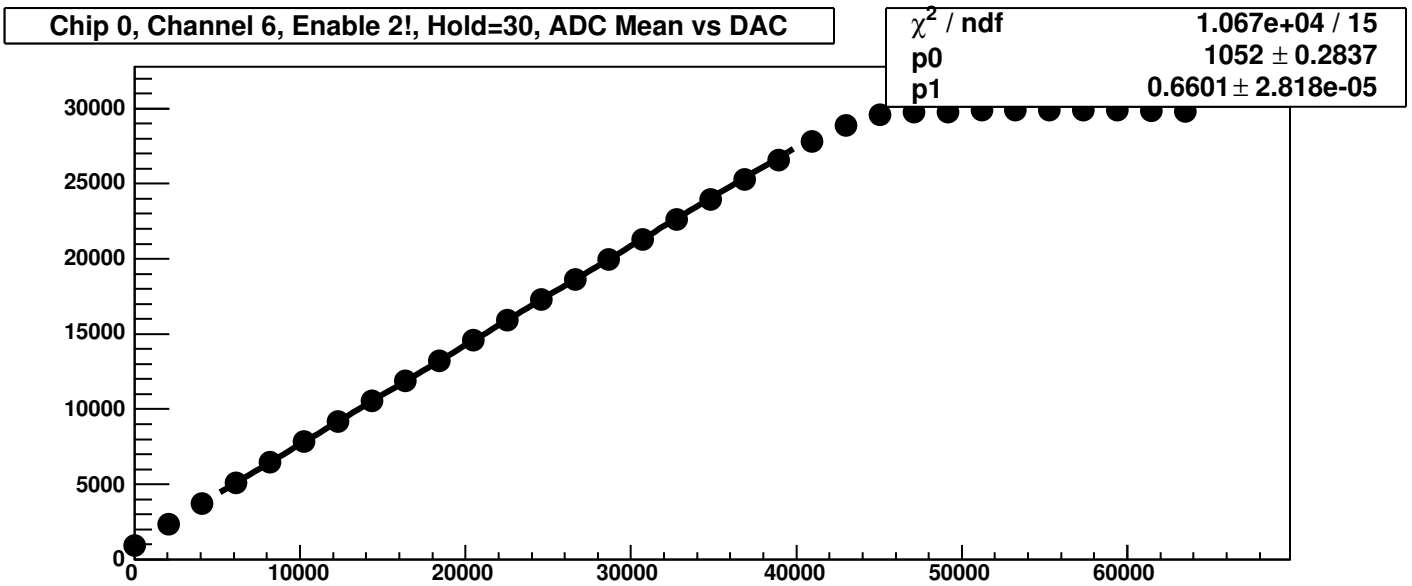
Chip 0, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

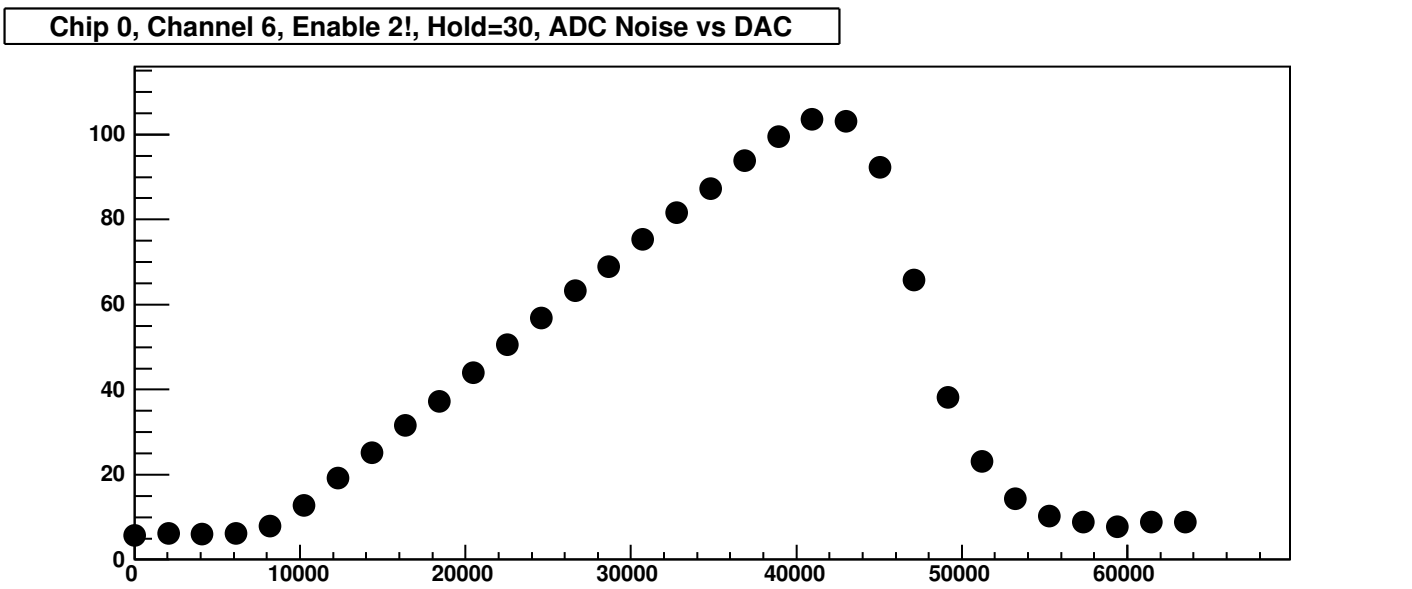


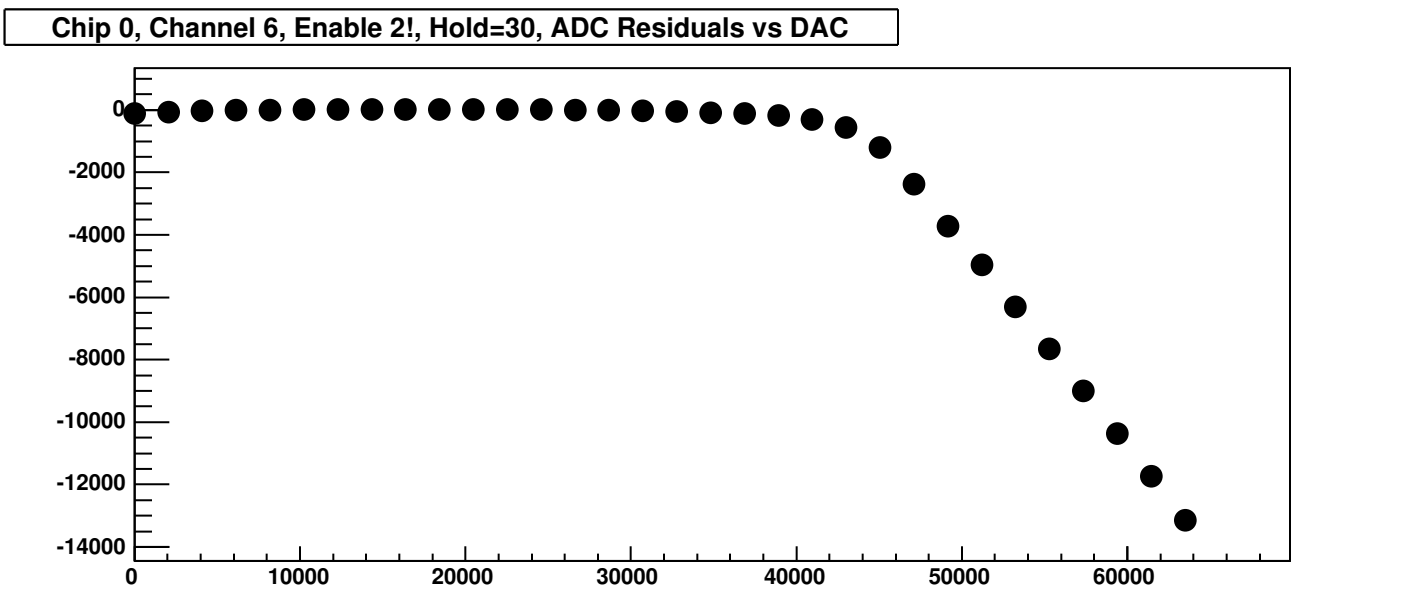

Chip 0, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2506 / 15
p0 926.5 ± 0.1101
p1 0.001052 ± 4.534e-06
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000

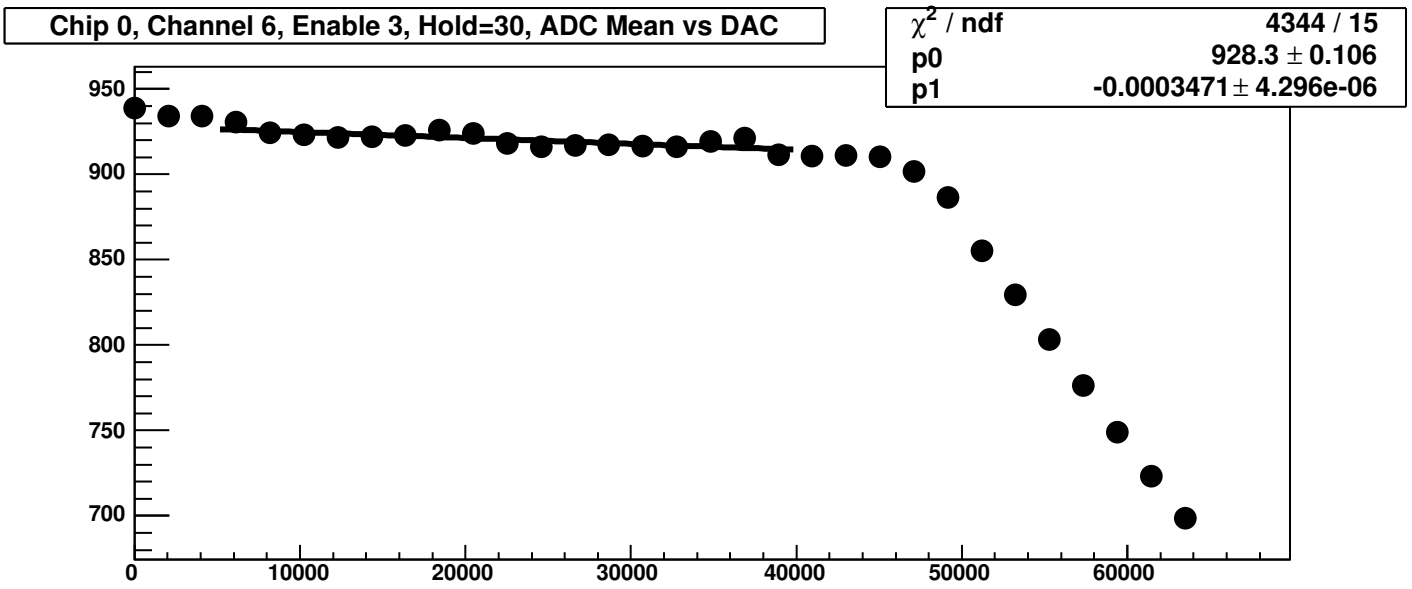

Chip 0, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

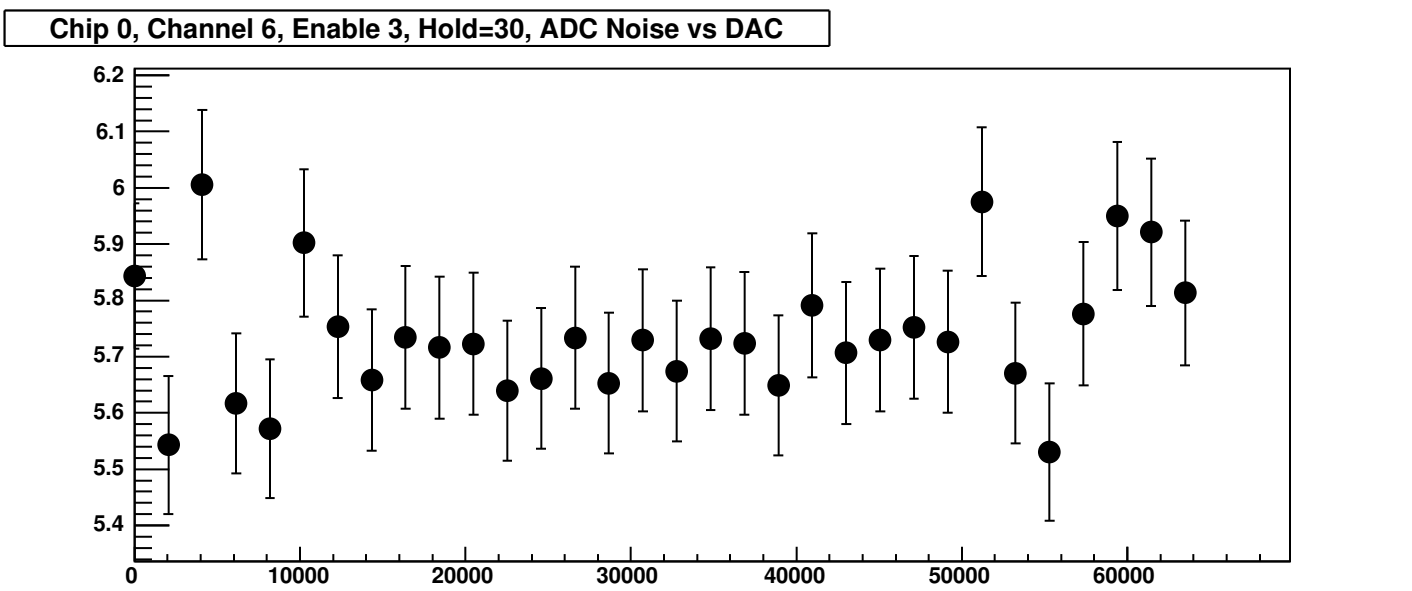

Chip 0, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

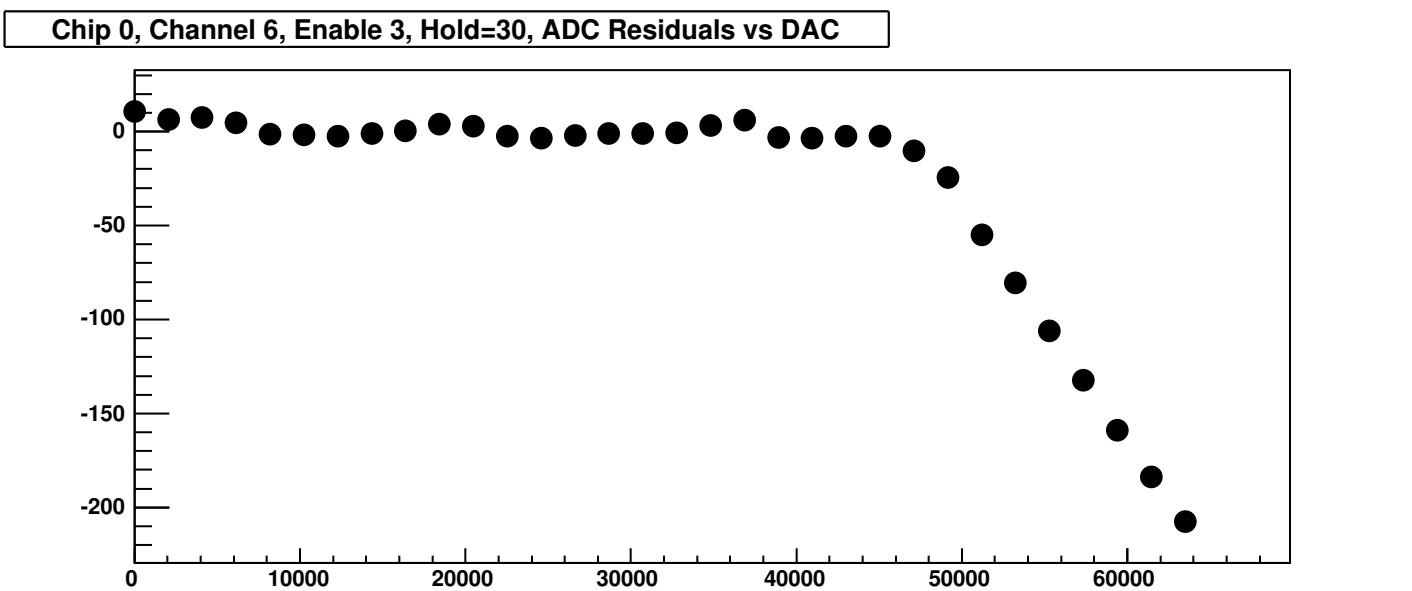

Chip 0, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.067e+04 / 15
p0 1052 ± 0.2837
p1 0.6601 ± 2.818e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

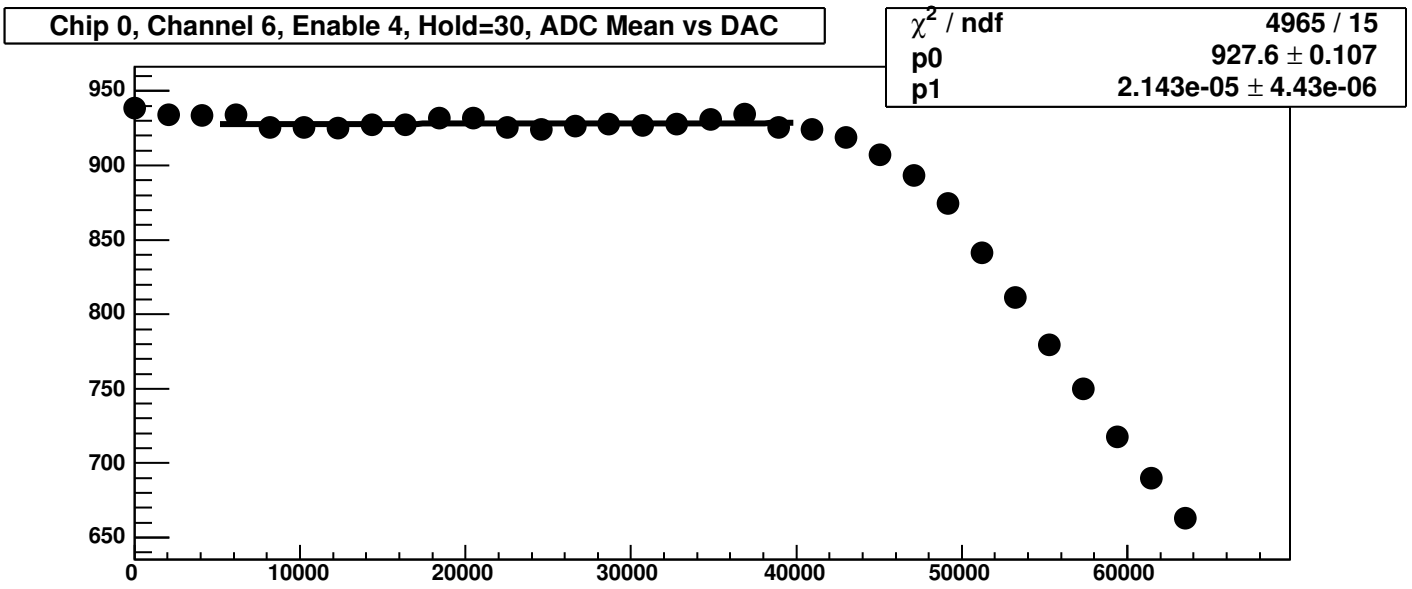

Chip 0, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

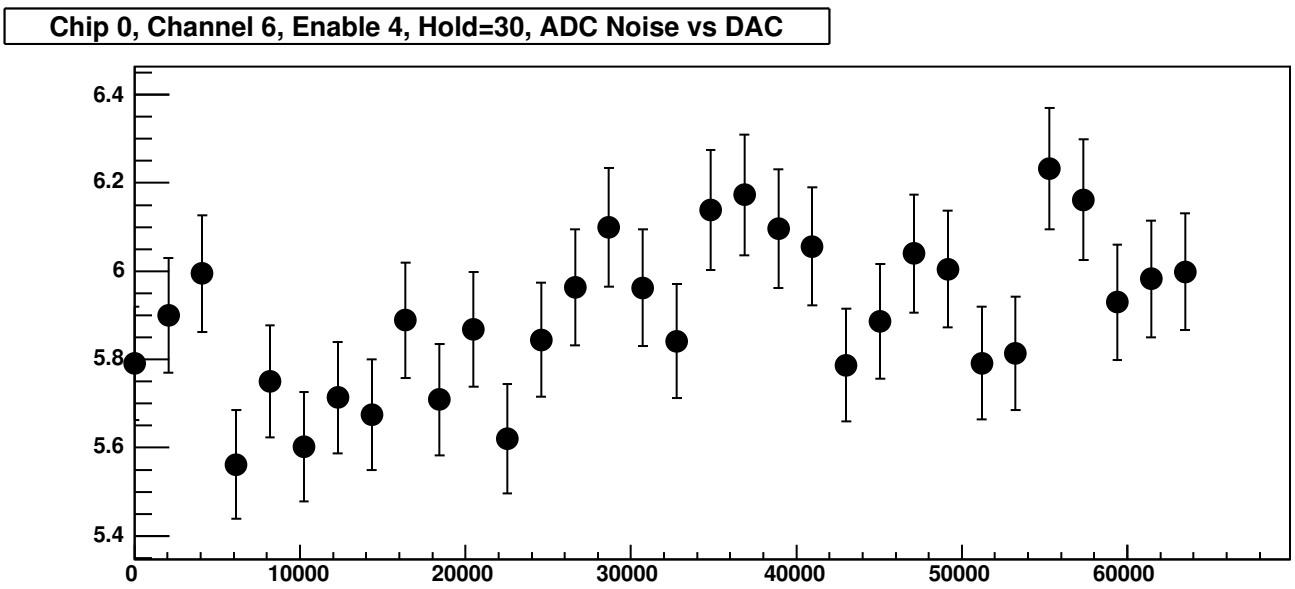

Chip 0, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

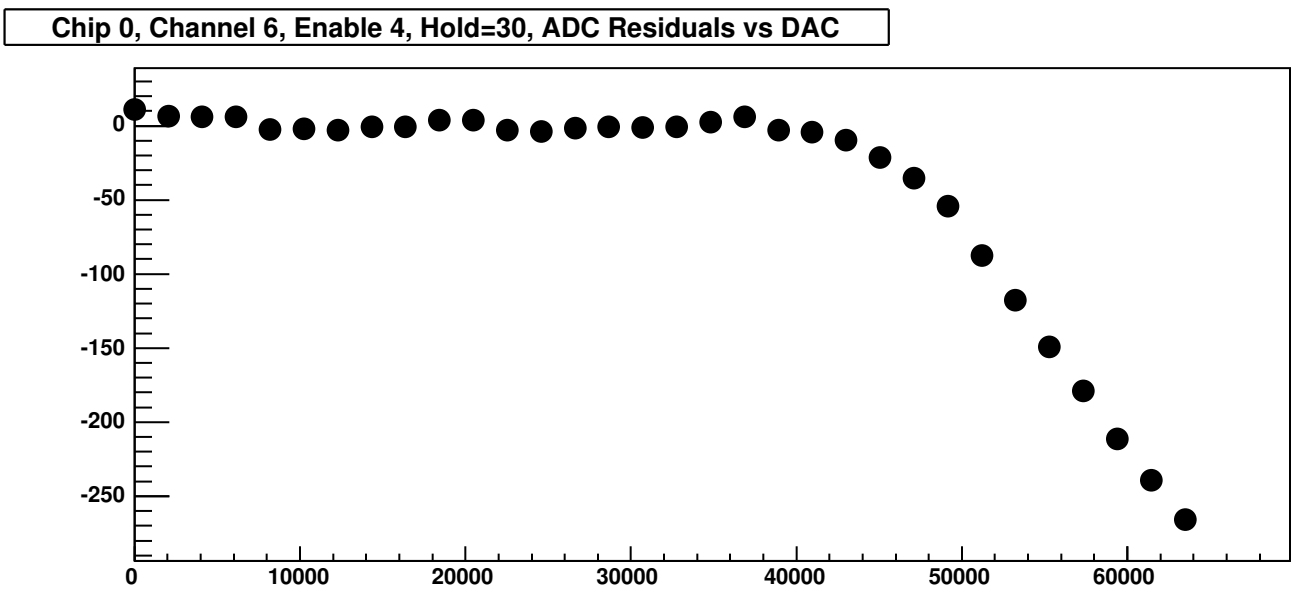

Chip 0, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4344 / 15
p0 928.3 ± 0.106
p1 -0.0003471 ± 4.296e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

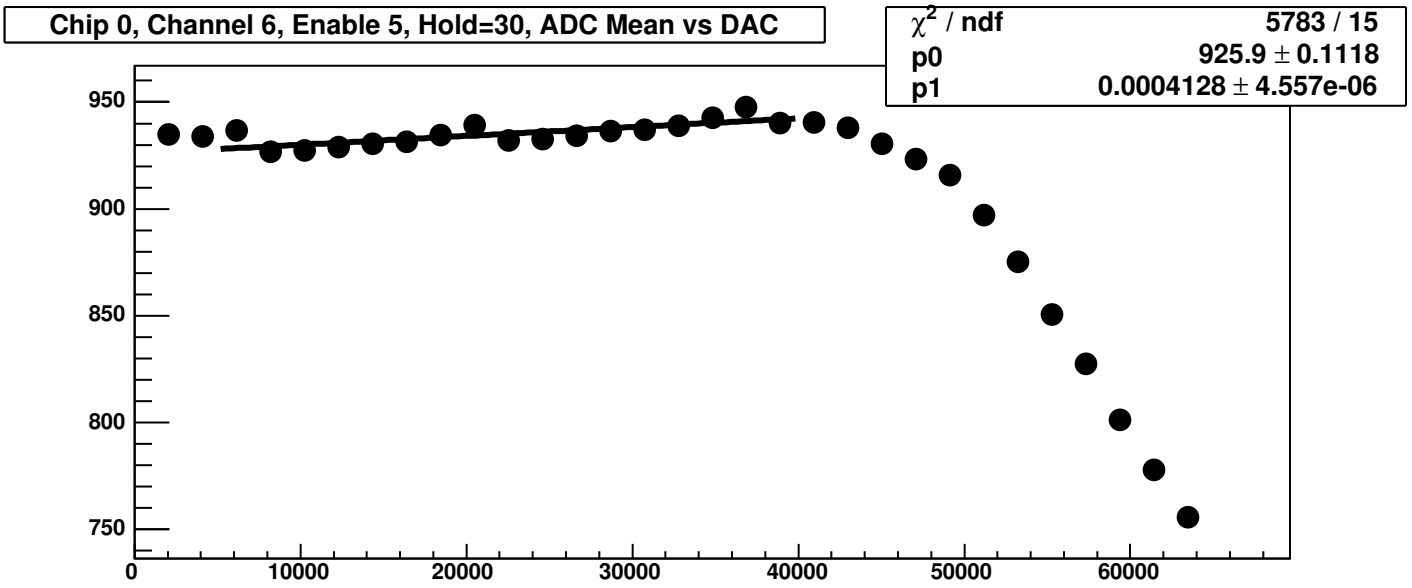

Chip 0, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

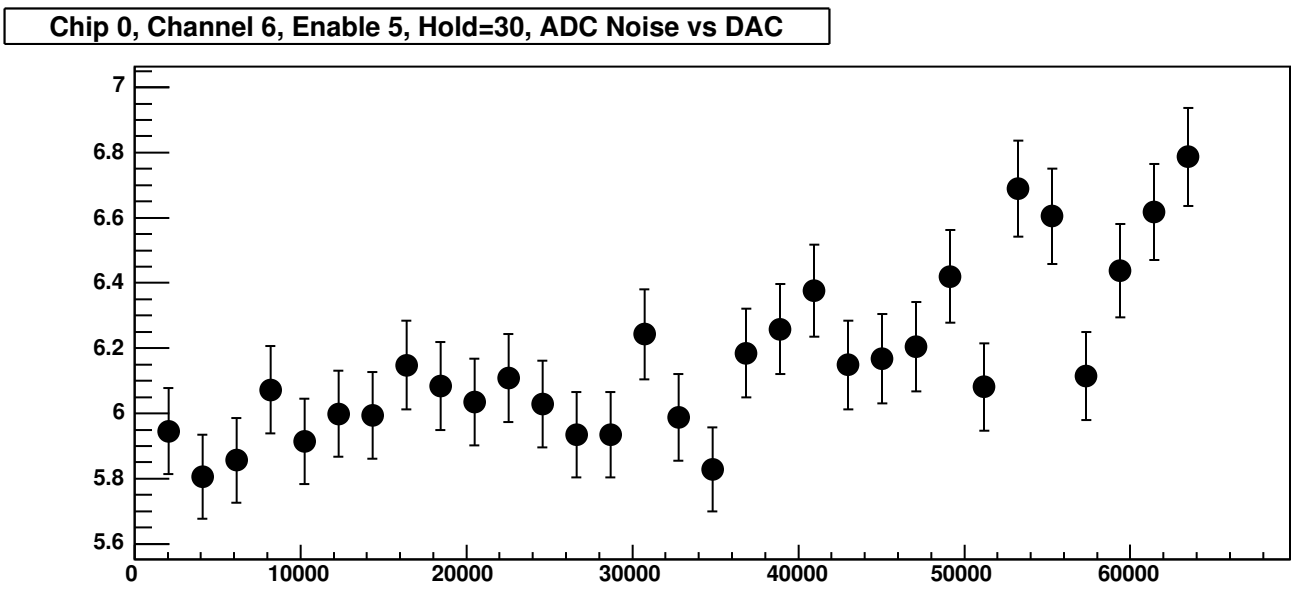

Chip 0, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

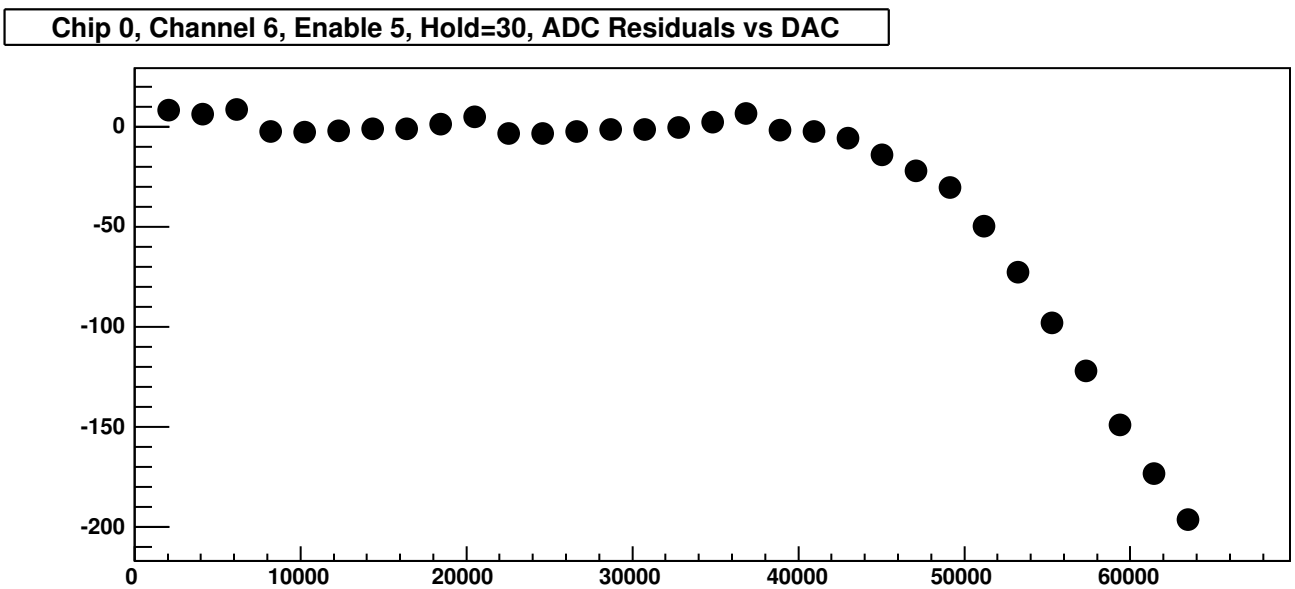

Chip 0, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4965 / 15
p0 927.6 ± 0.107
p1 2.143e-05 ± 4.43e-06
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

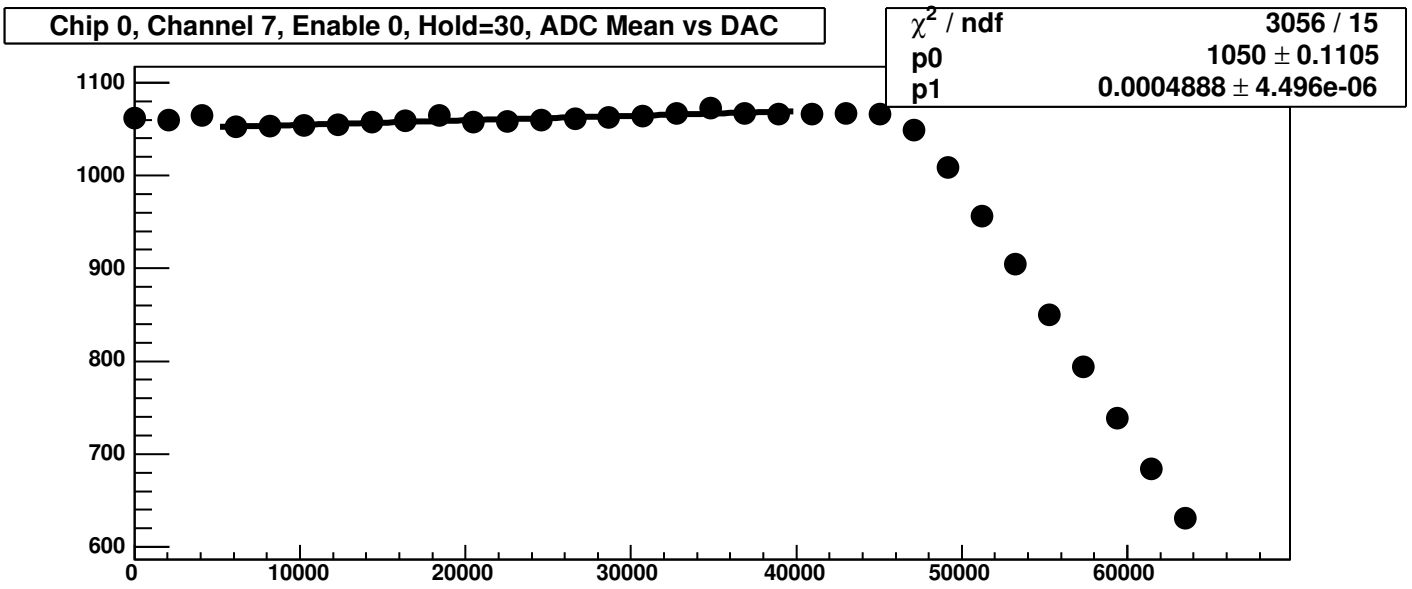

Chip 0, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

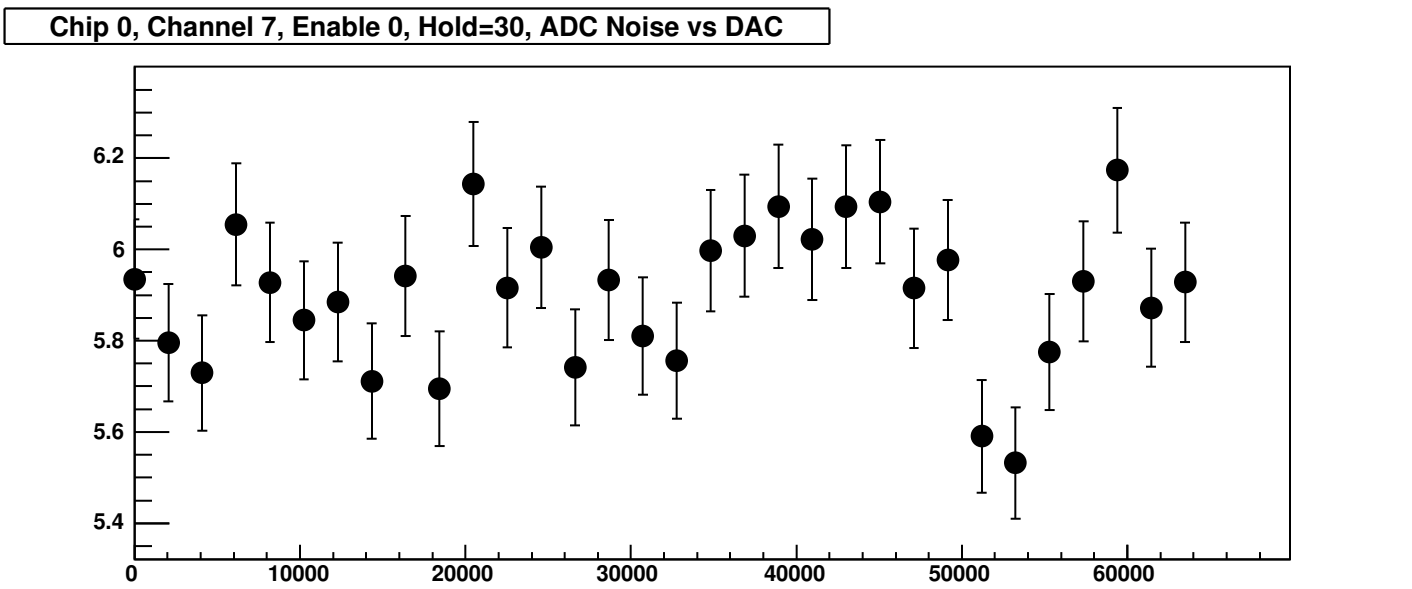

Chip 0, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

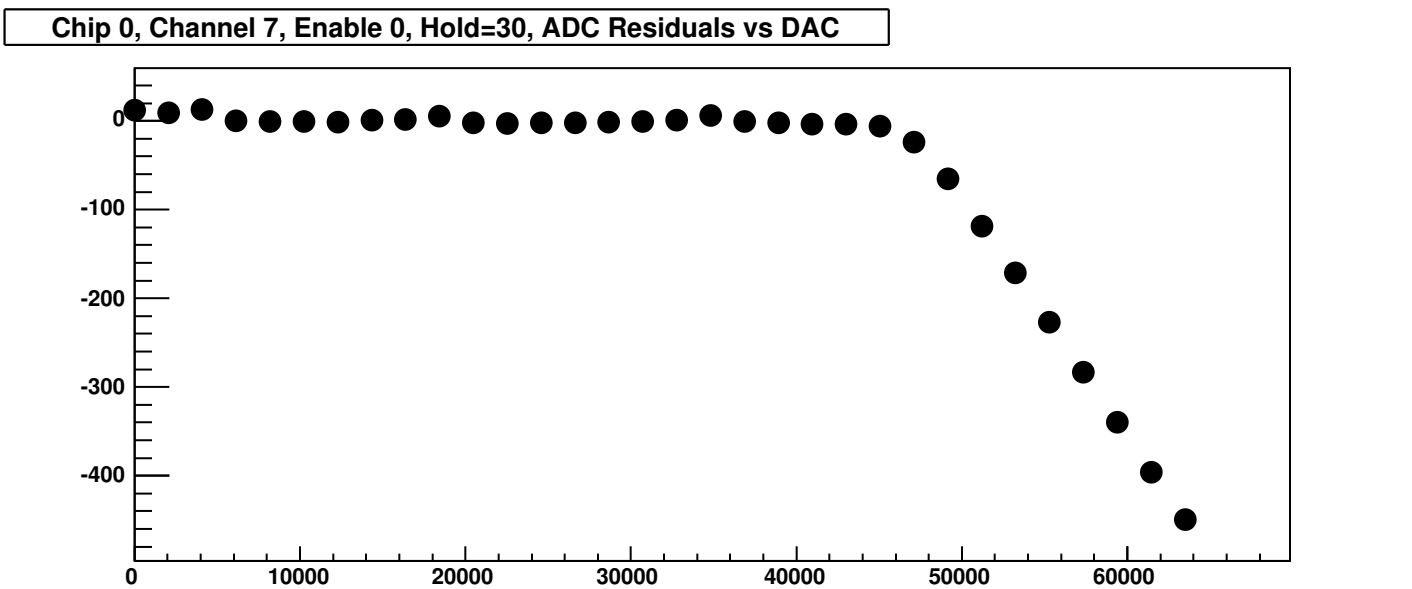

Chip 0, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5783 / 15
p0 925.9 ± 0.1118
p1 0.0004128 ± 4.557e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

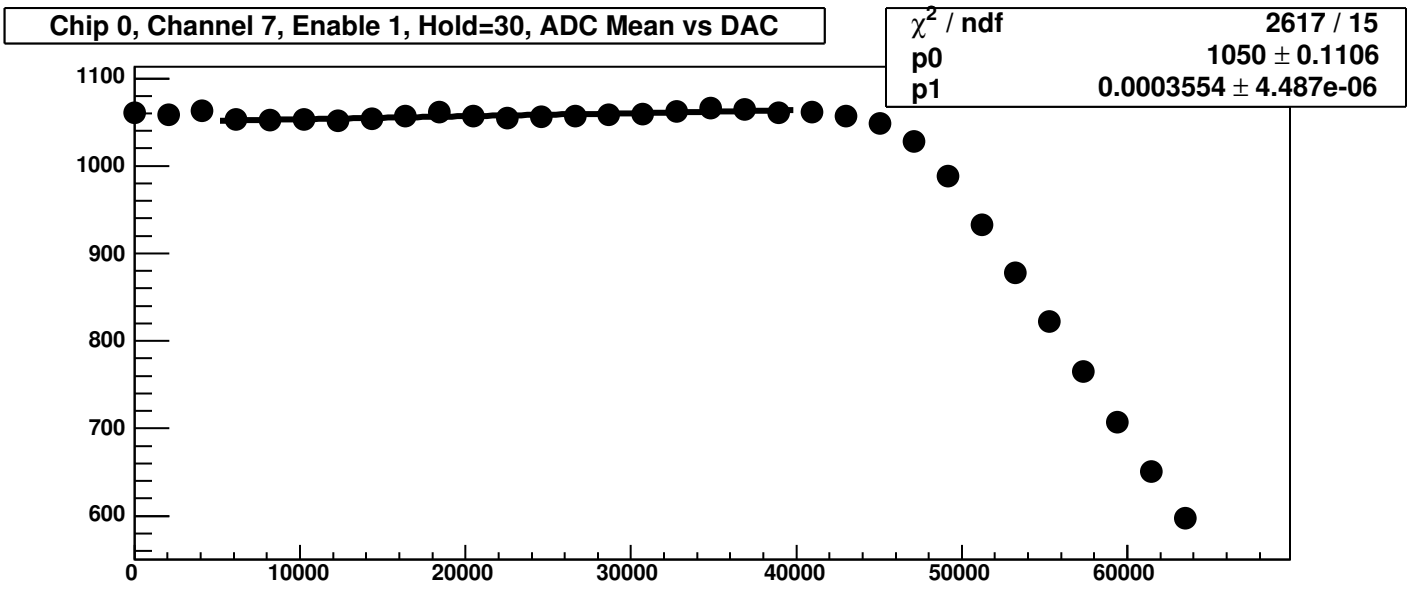

Chip 0, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

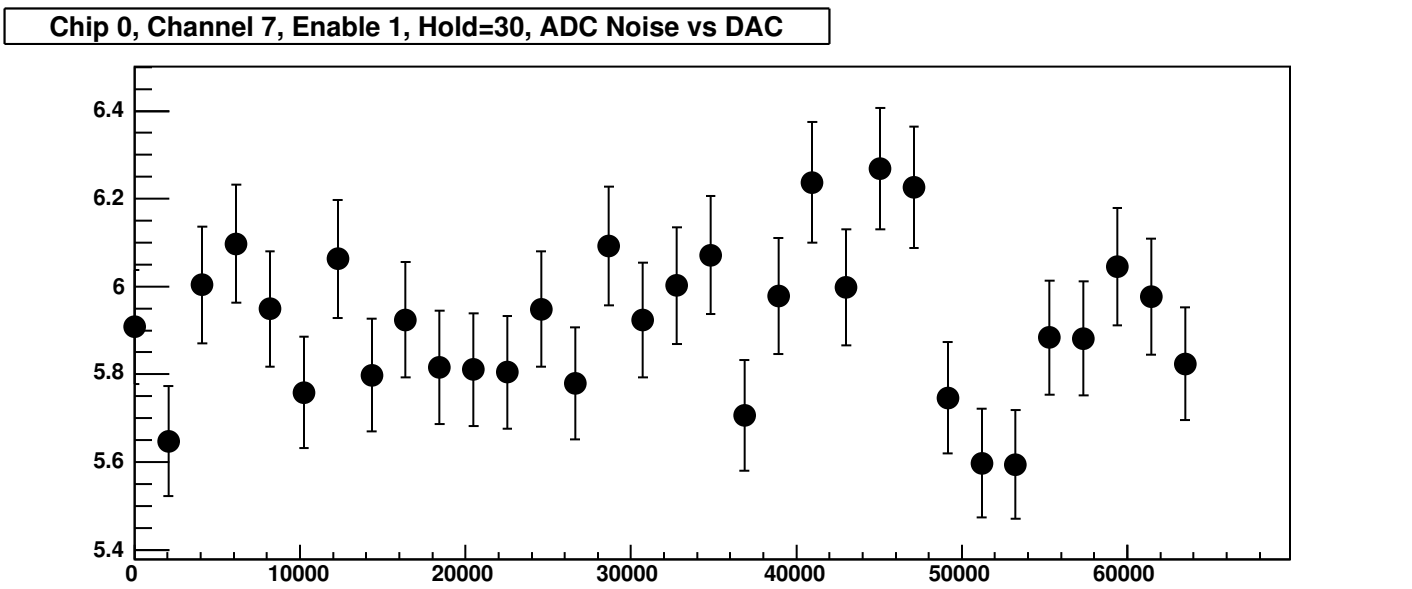

Chip 0, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

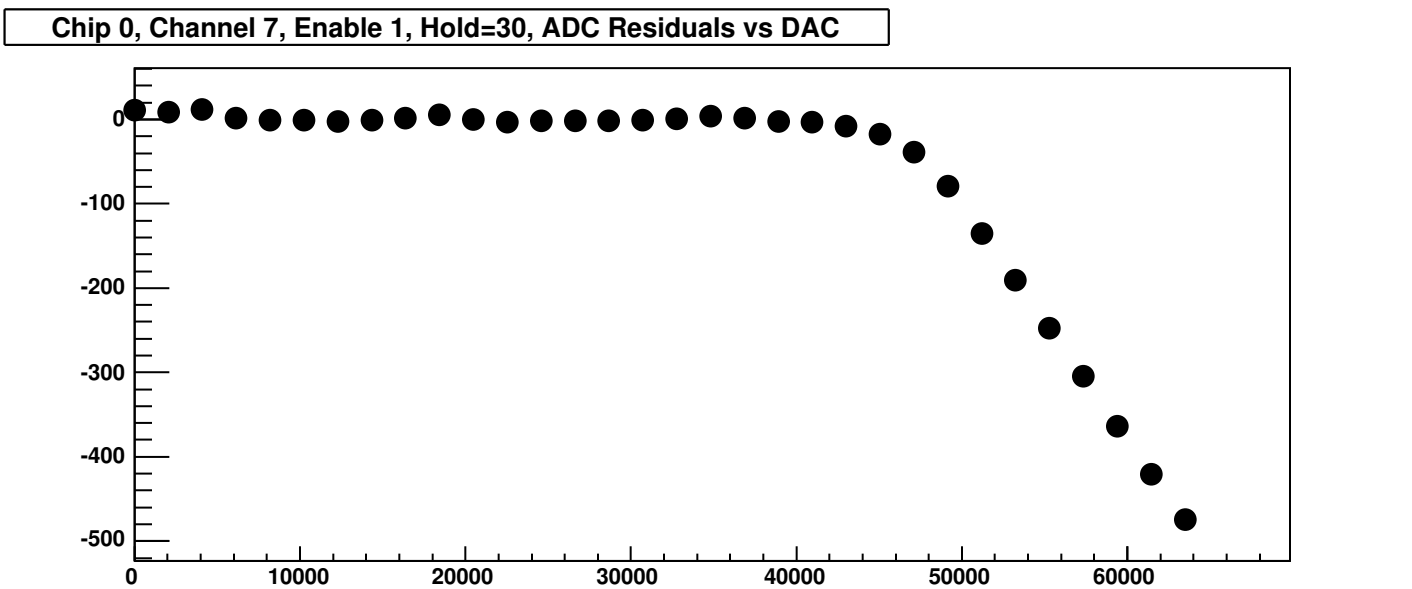

Chip 0, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3056 / 15
p0 1050 ± 0.1105
p1 0.0004888 ± 4.496e-06
1100
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

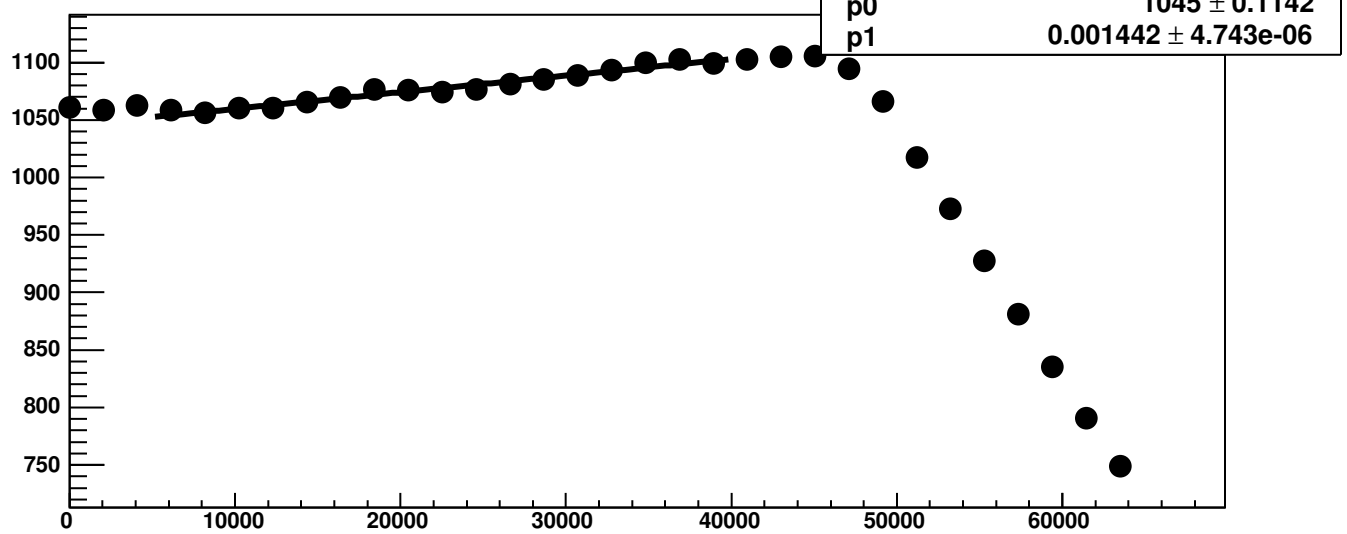

Chip 0, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2617 / 15
p0 1050 ± 0.1106
p1 0.0003554 ± 4.487e-06
1100
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000

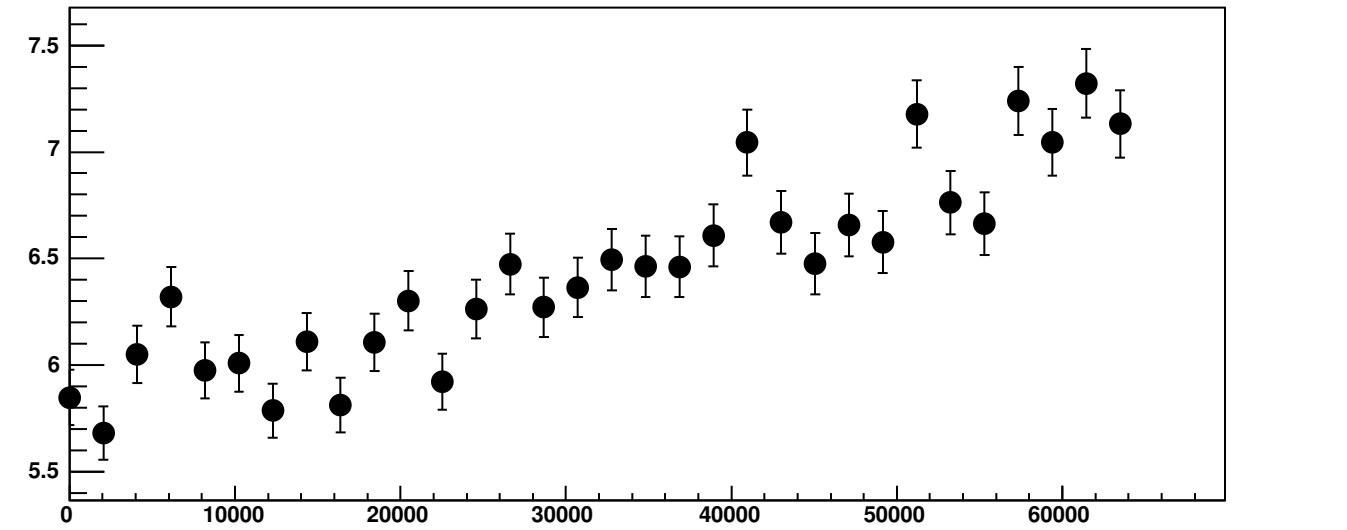

Chip 0, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

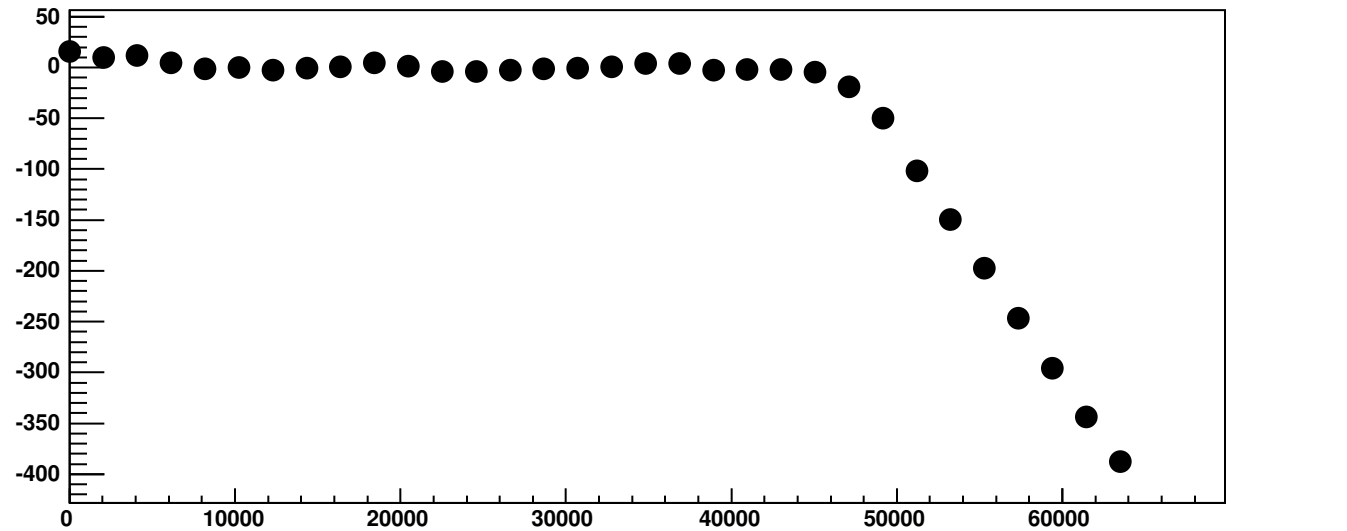
Chip 0, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC



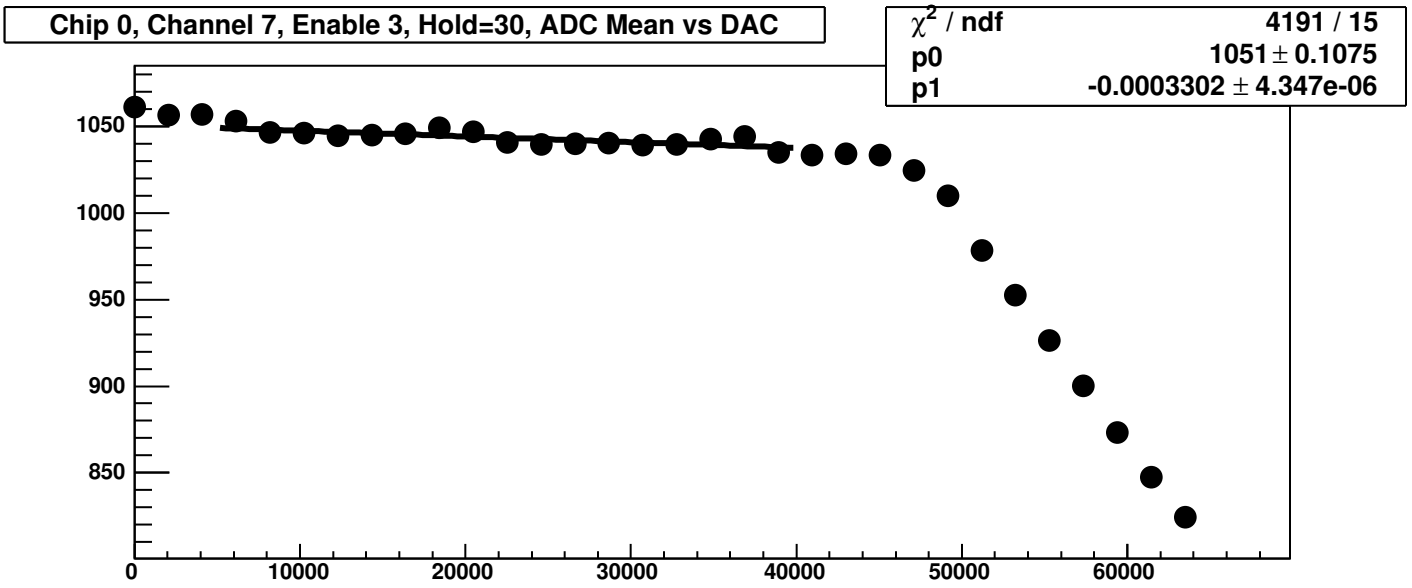
Chip 0, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC



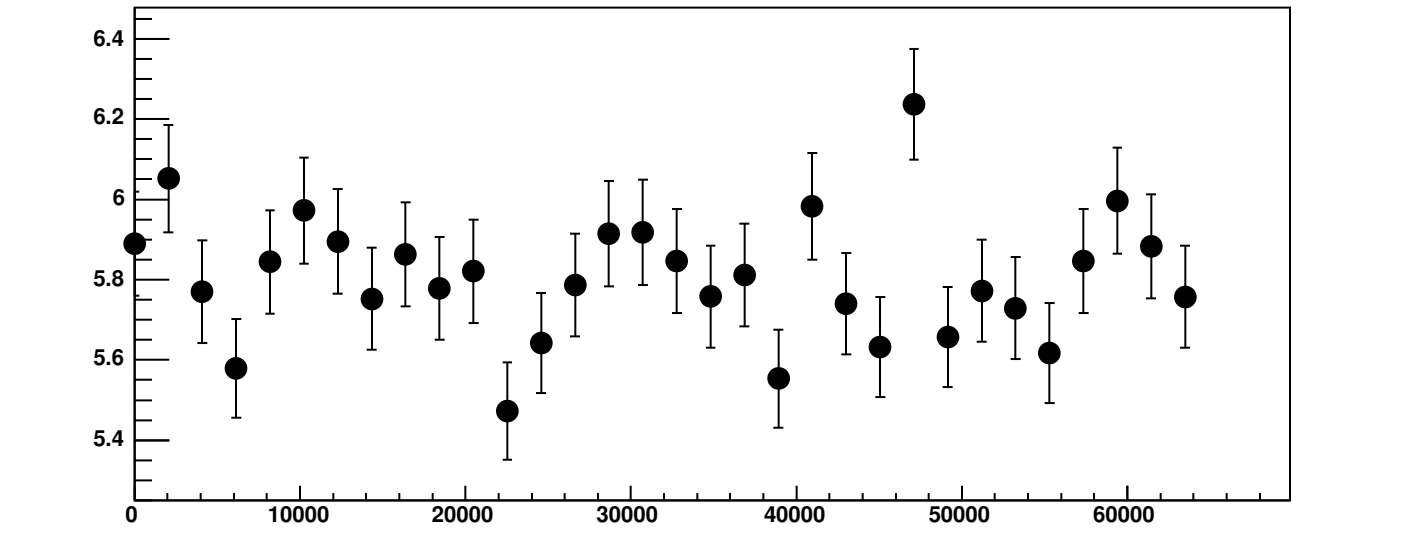
Chip 0, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC



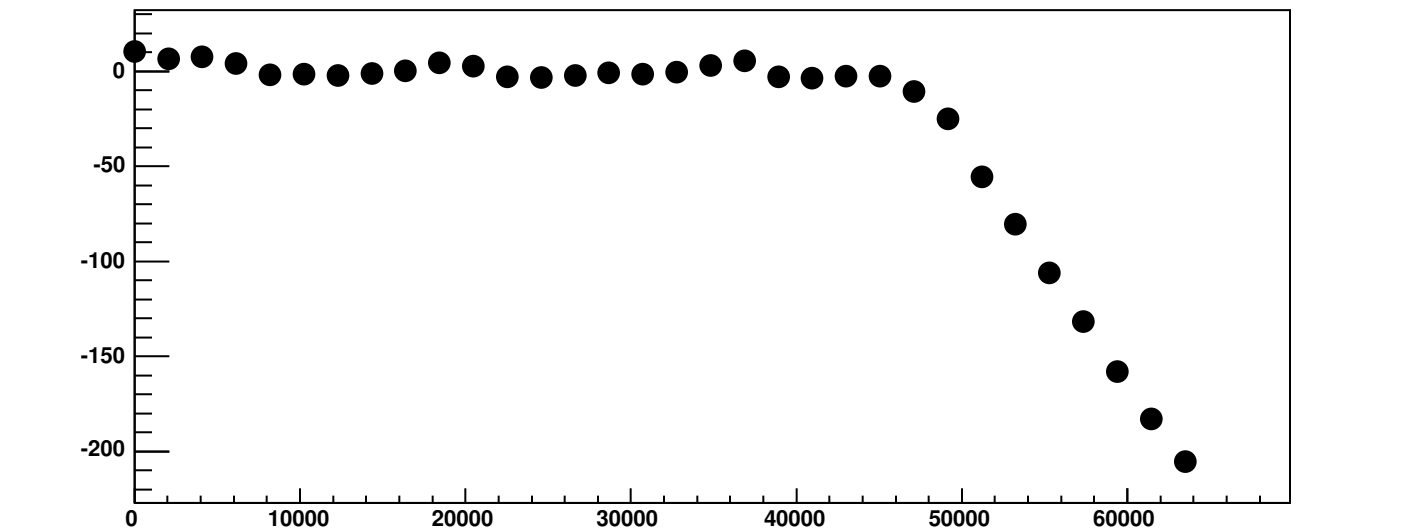
Chip 0, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC



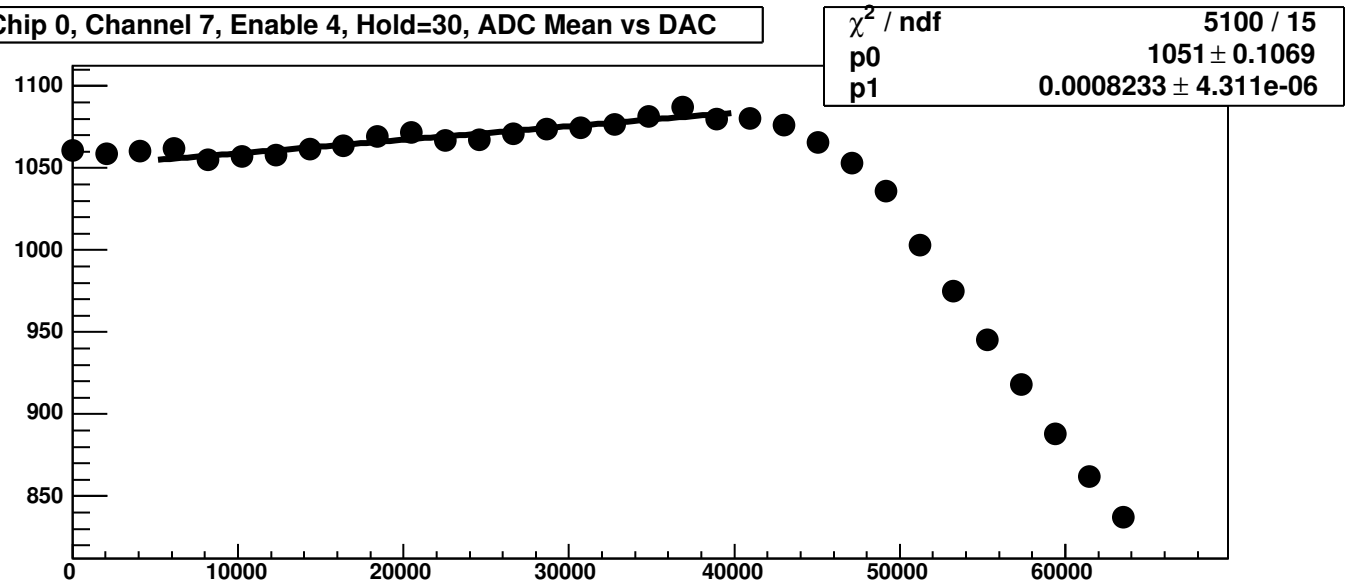
Chip 0, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC



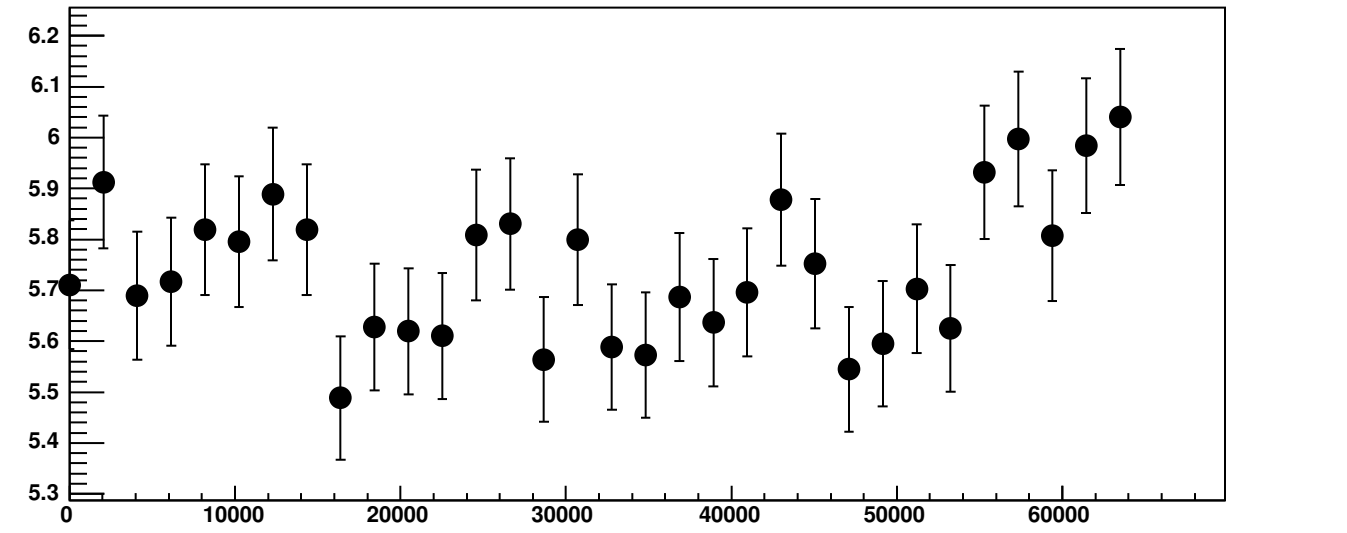
Chip 0, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC



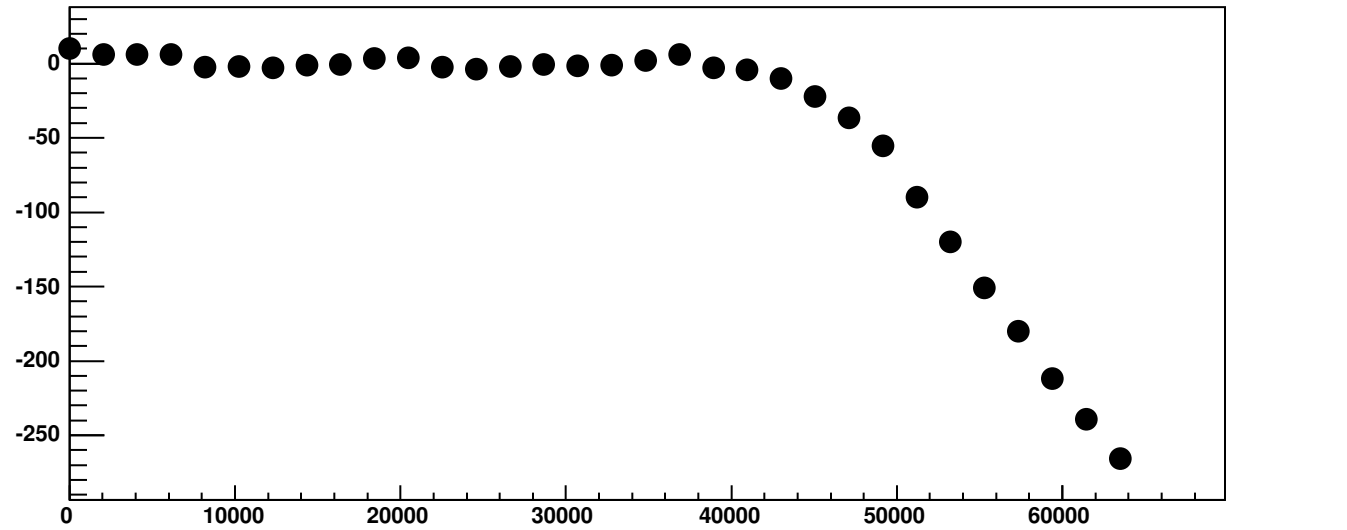
Chip 0, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC

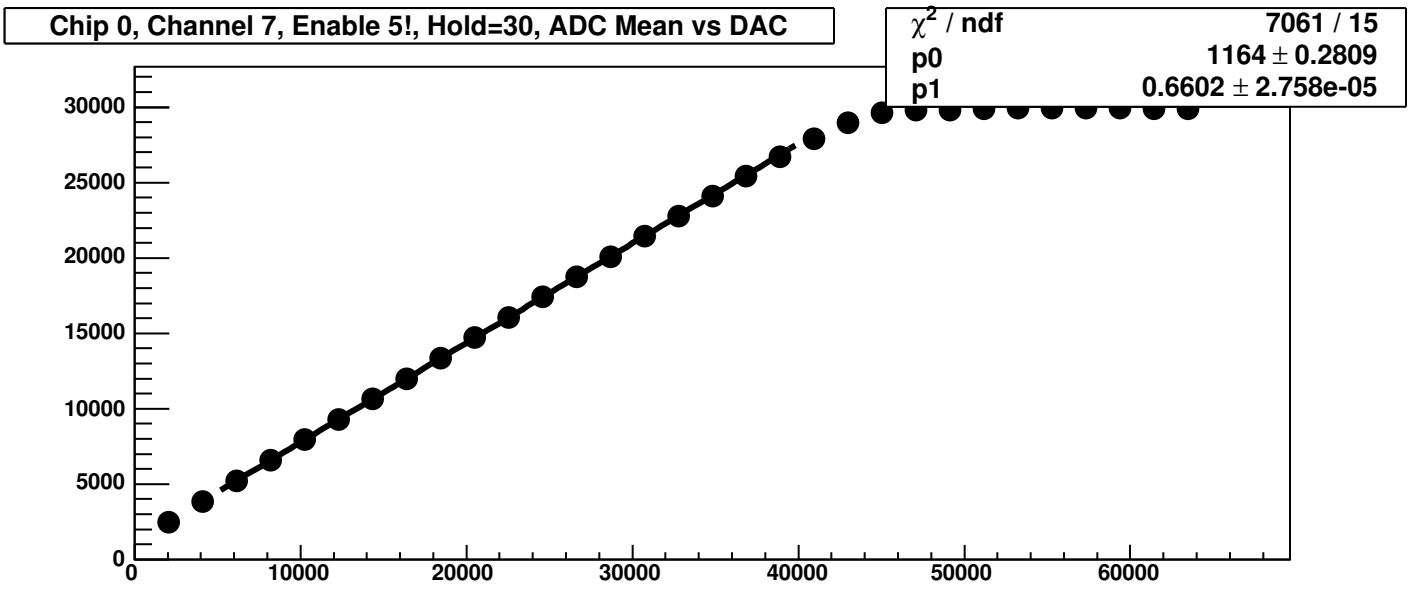


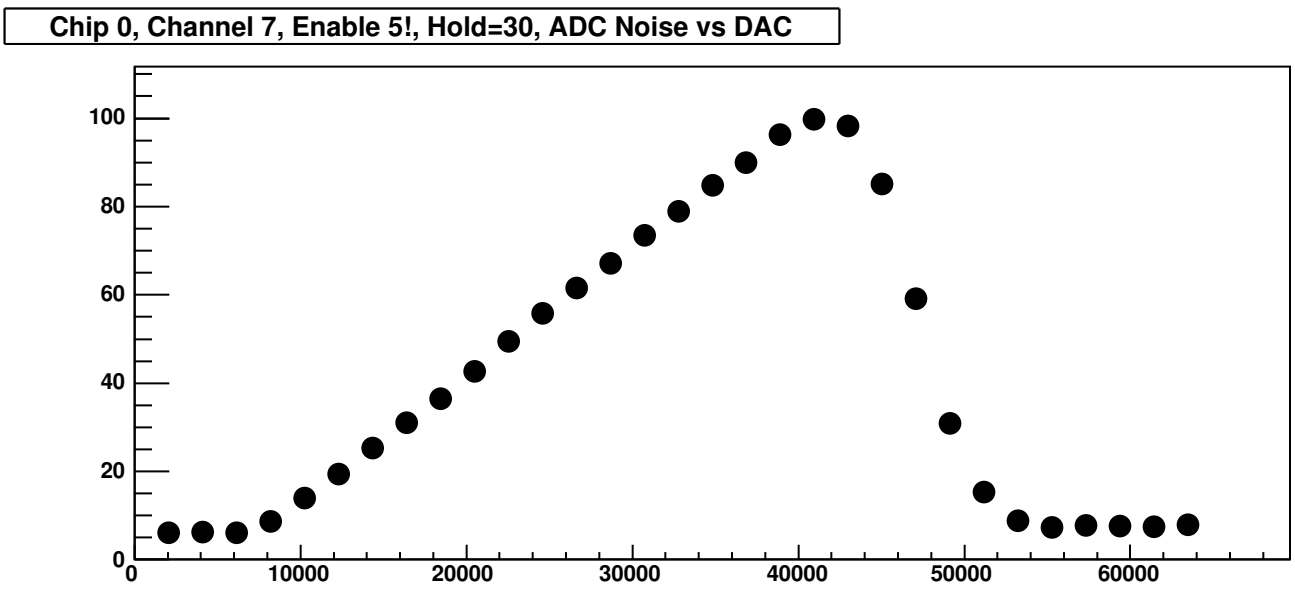
Chip 0, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC

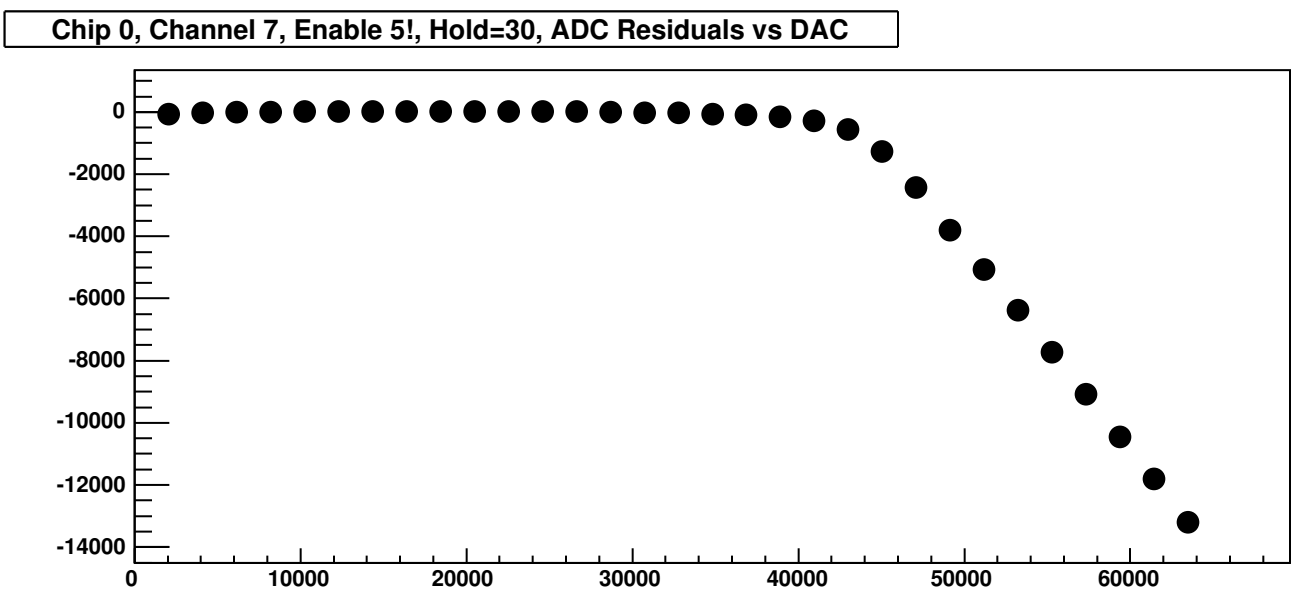


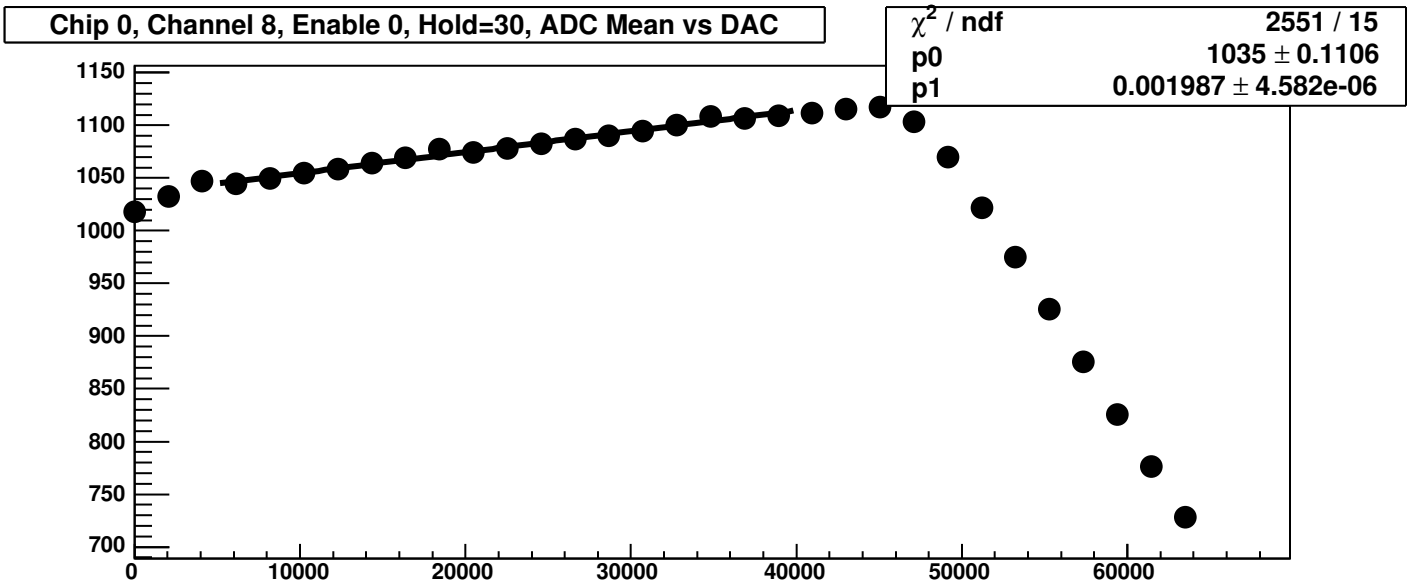
Chip 0, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC

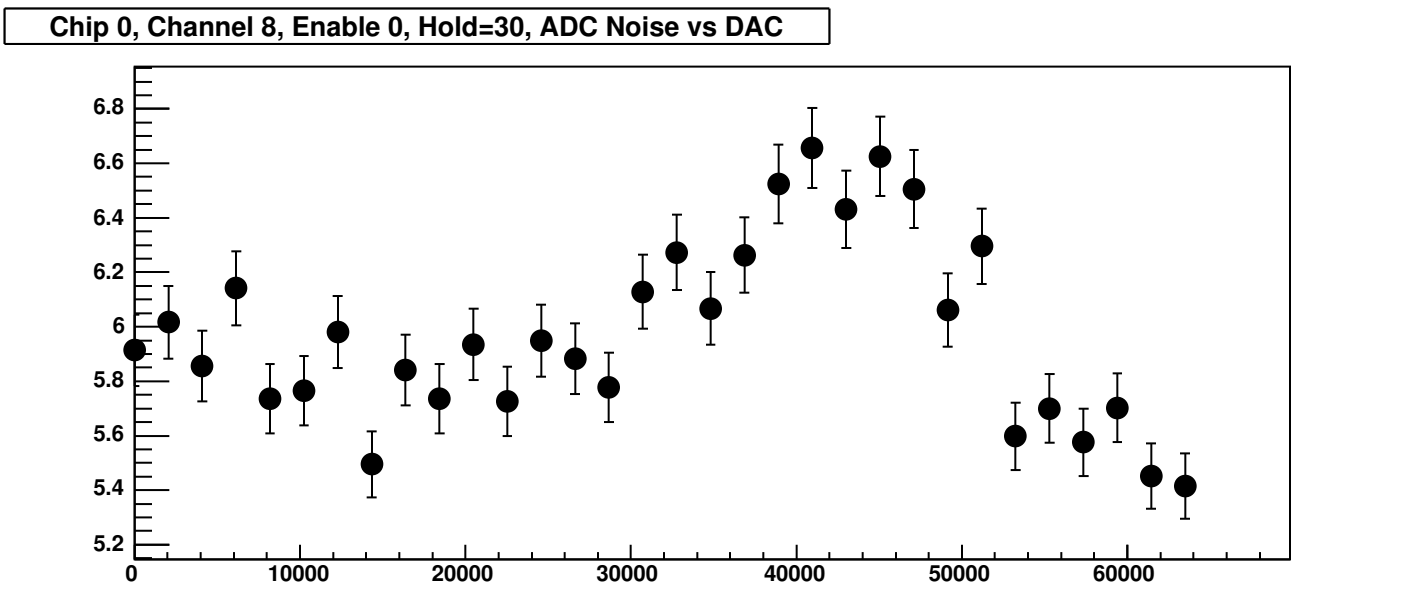


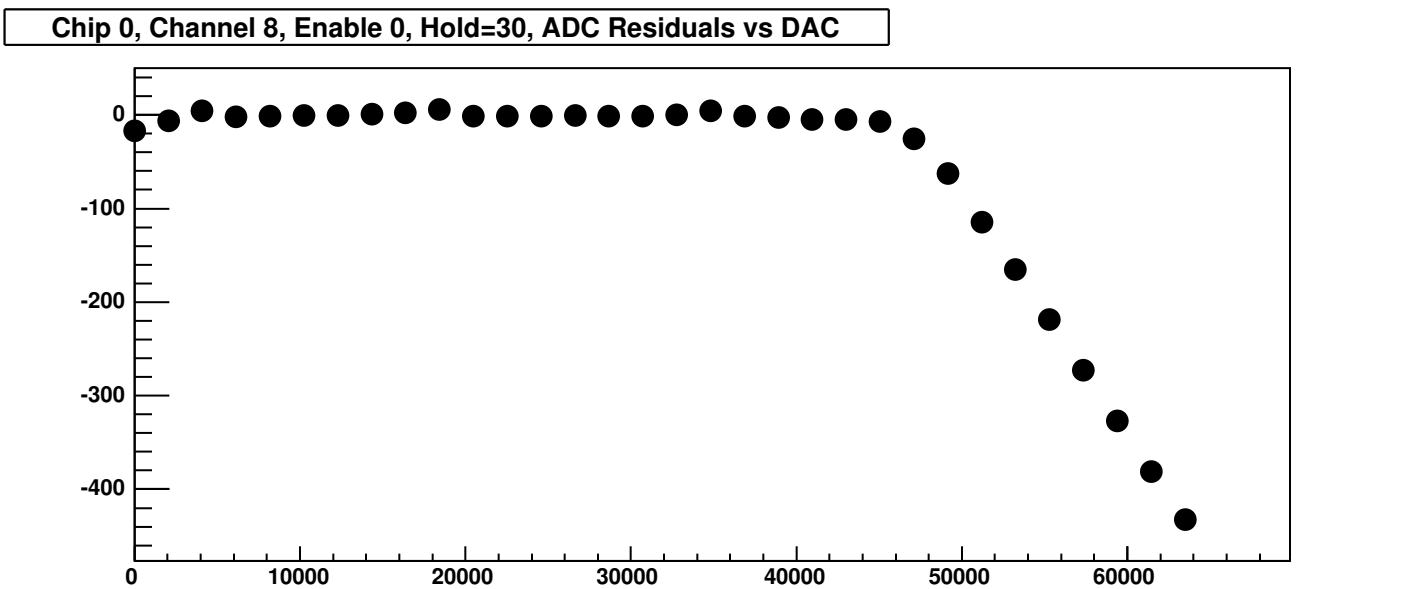

Chip 0, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7061 / 15
p0 1164 ± 0.2809
p1 0.6602 ± 2.758e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

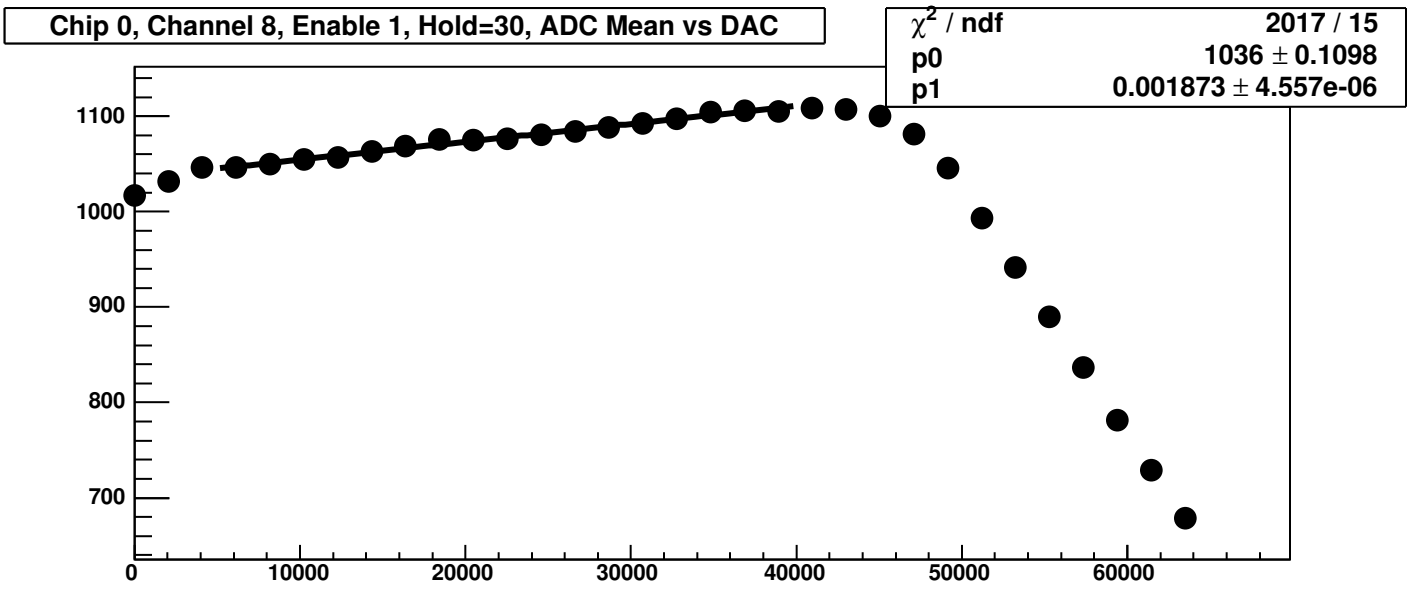

Chip 0, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

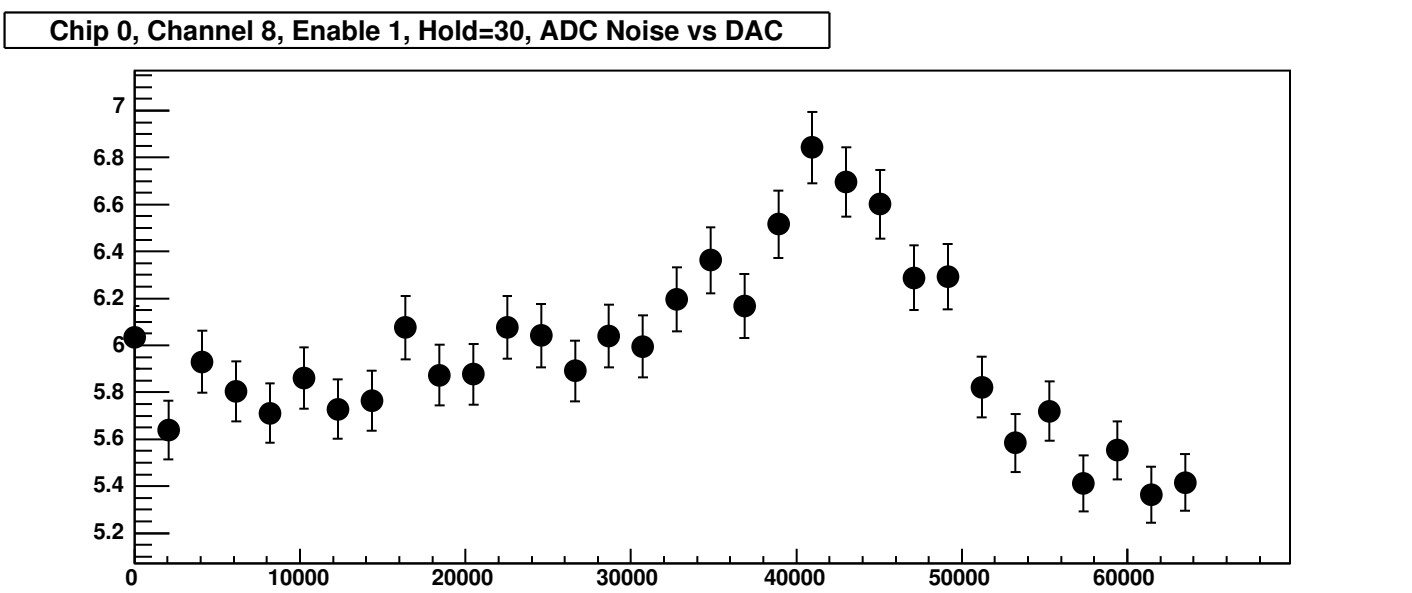

Chip 0, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

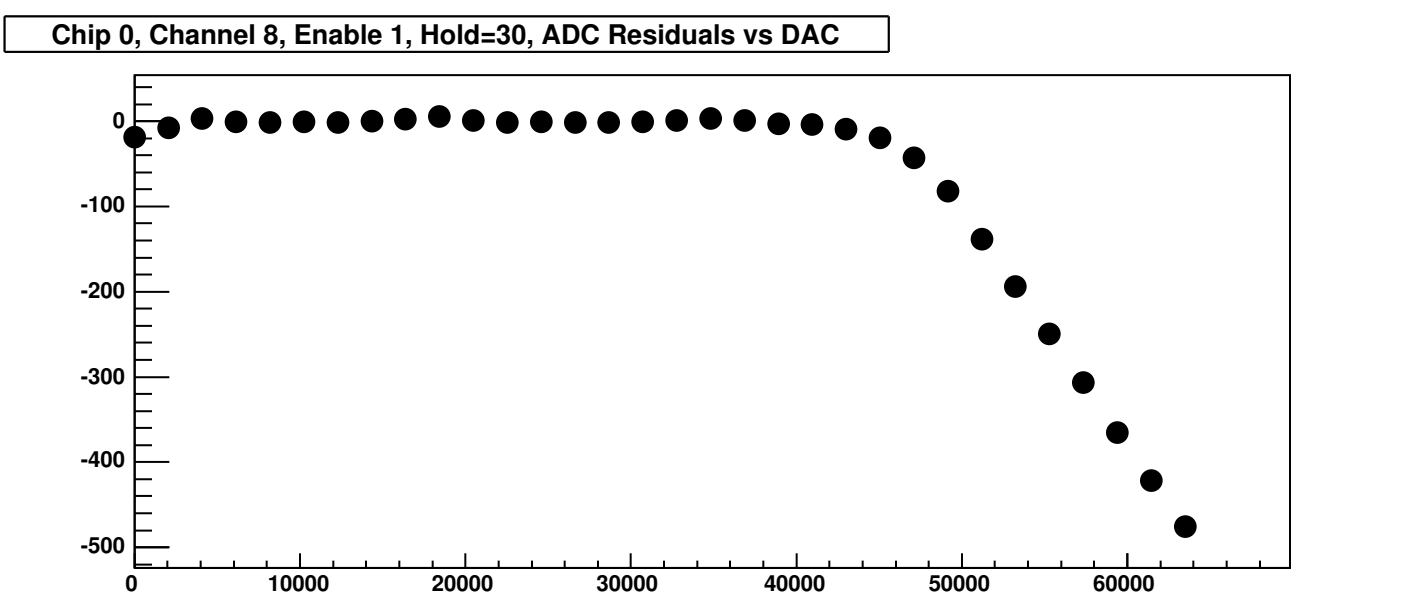

Chip 0, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2551 / 15
p0 1035 ± 0.1106
p1 0.001987 ± 4.582e-06
1150
1100
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

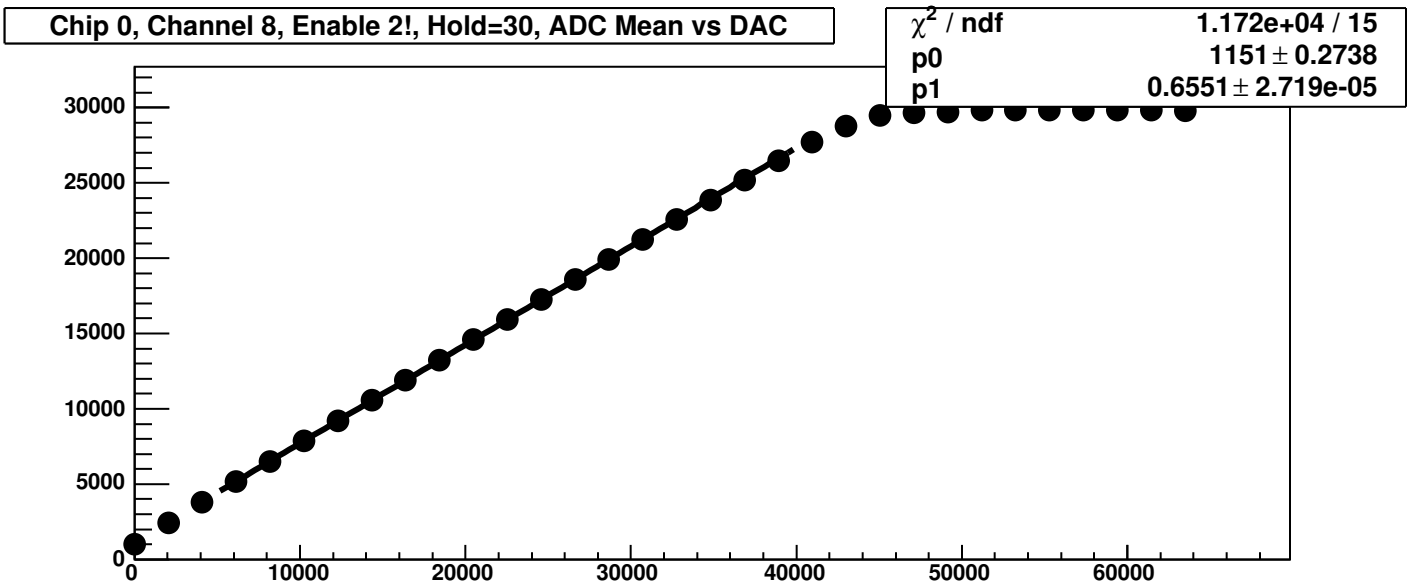

Chip 0, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

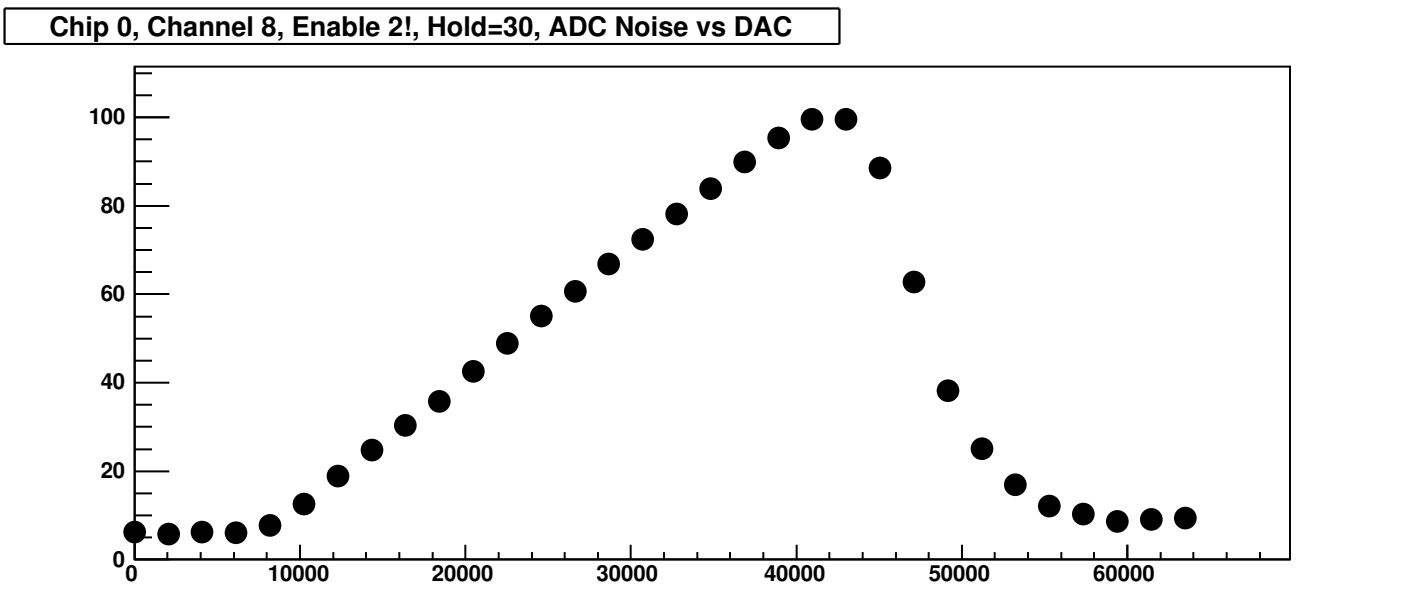

Chip 0, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

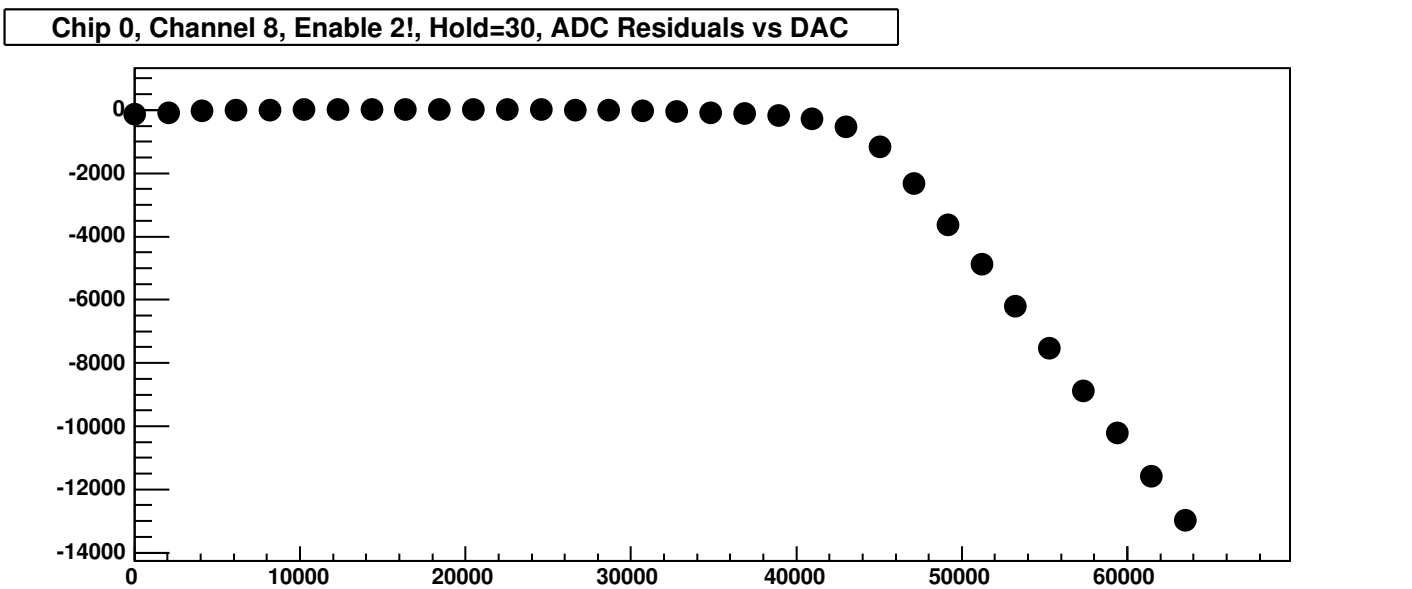

Chip 0, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2017 / 15
p0 1036 ± 0.1098
p1 0.001873 ± 4.557e-06
1100
1000
900
800
700
0
10000
20000
30000
40000
50000
60000

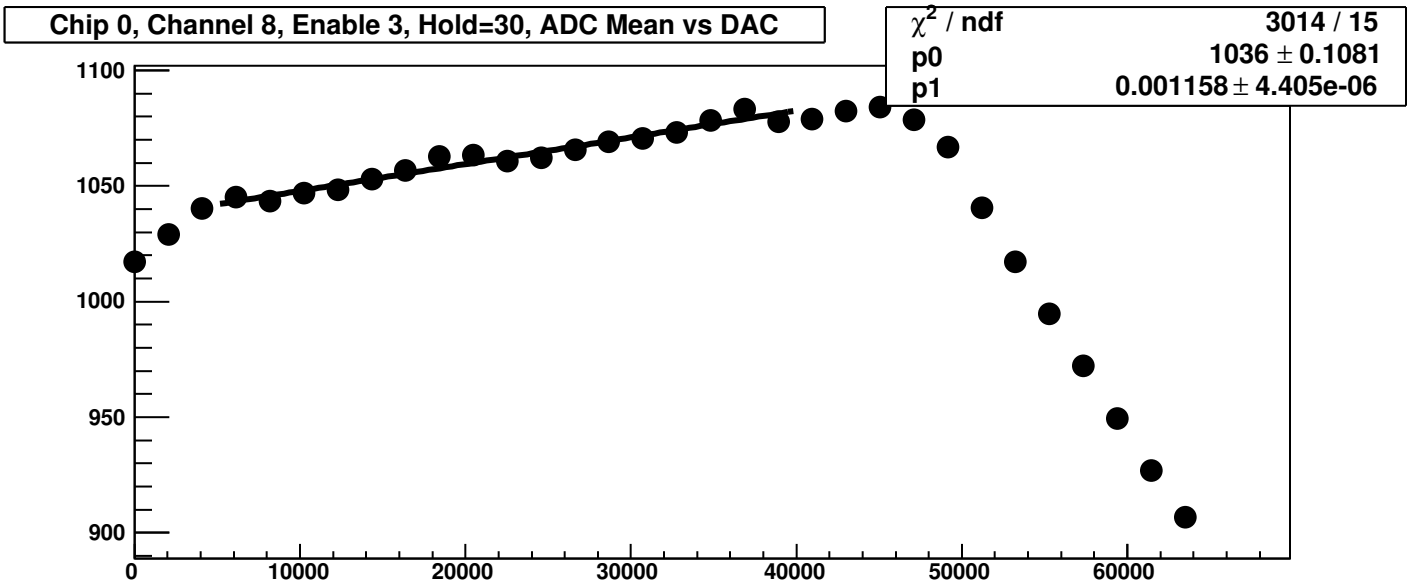

Chip 0, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

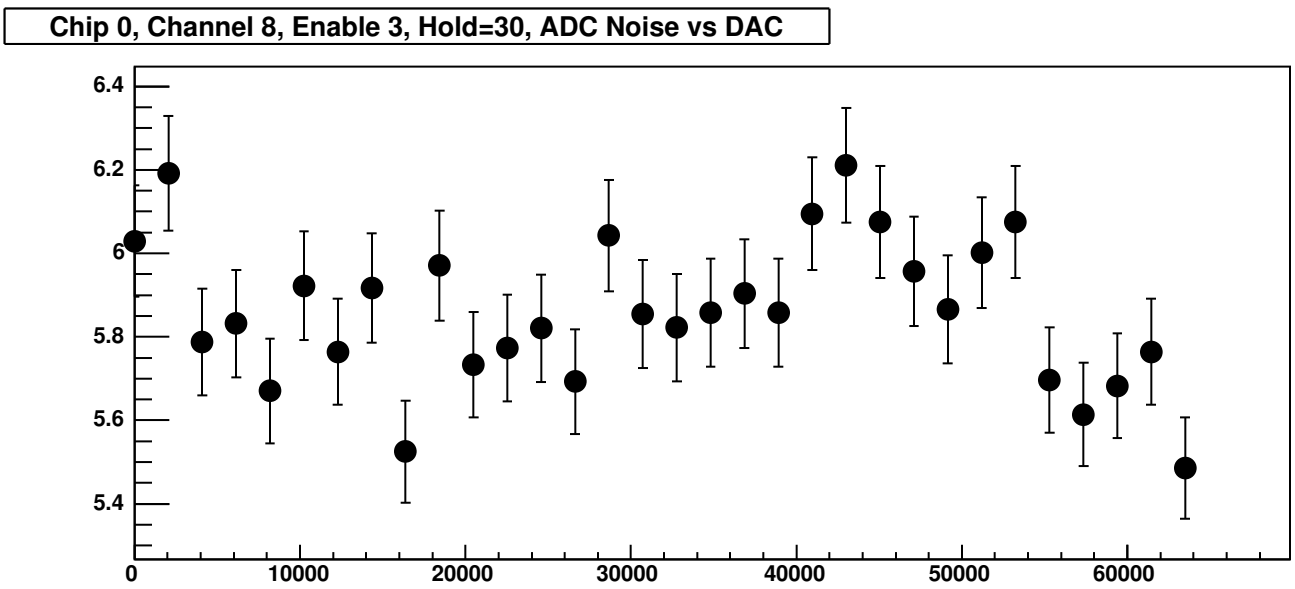

Chip 0, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

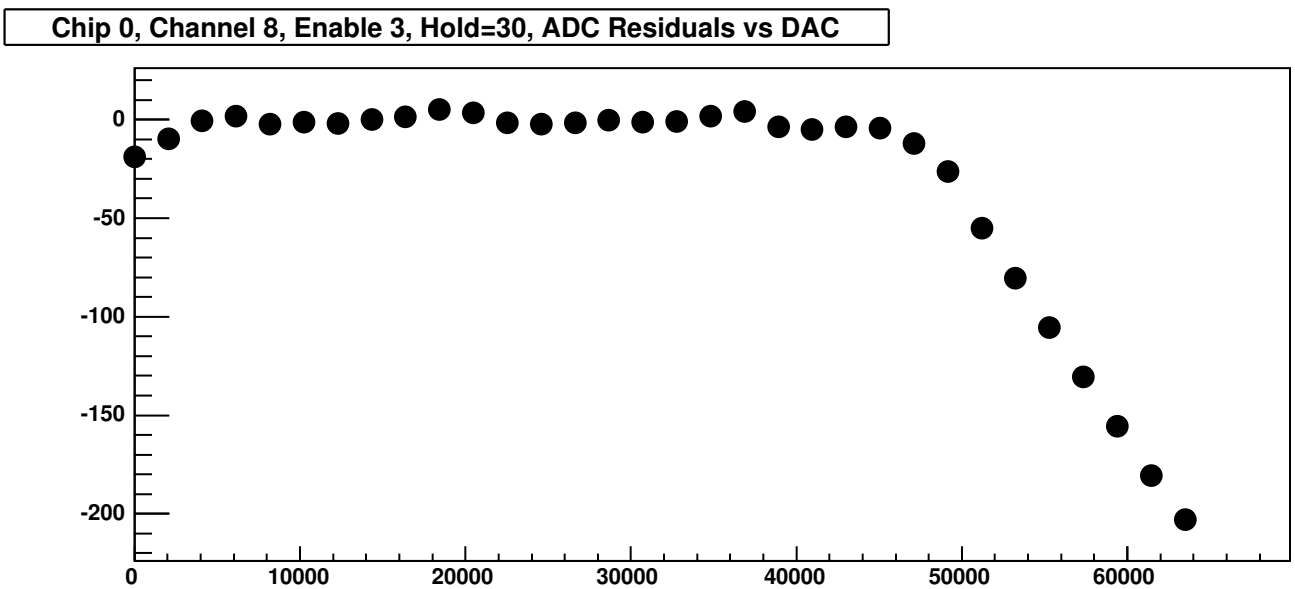

Chip 0, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.172e+04 / 15
p0 1151 ± 0.2738
p1 0.6551 ± 2.719e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

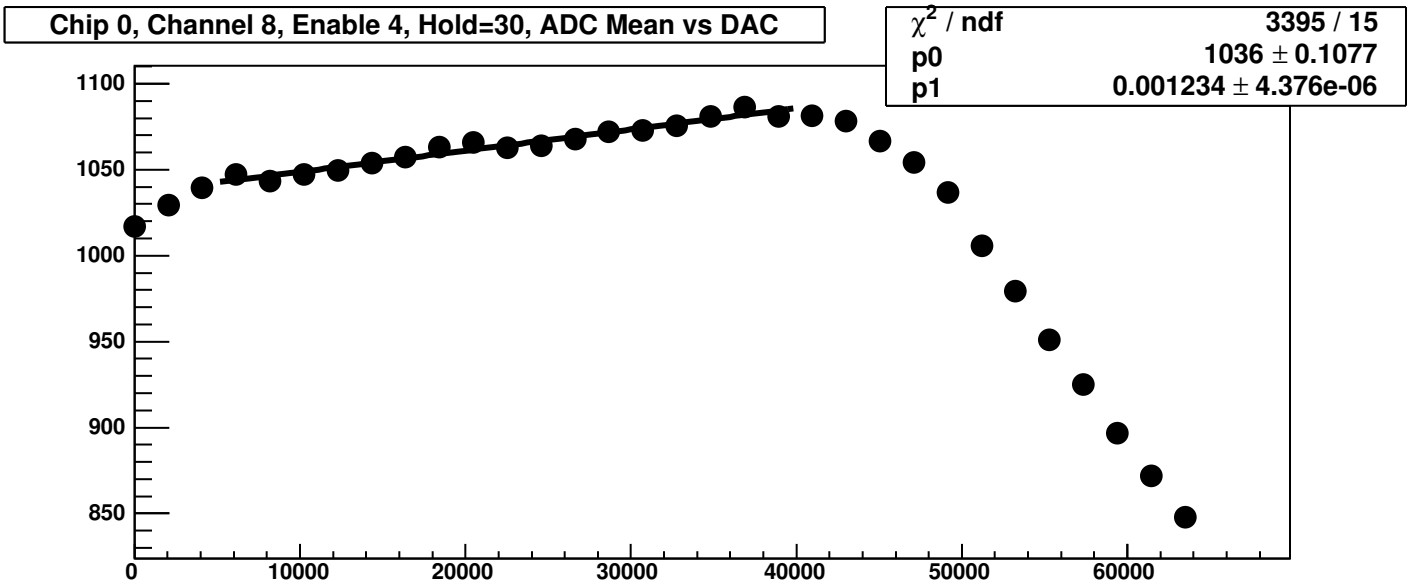

Chip 0, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

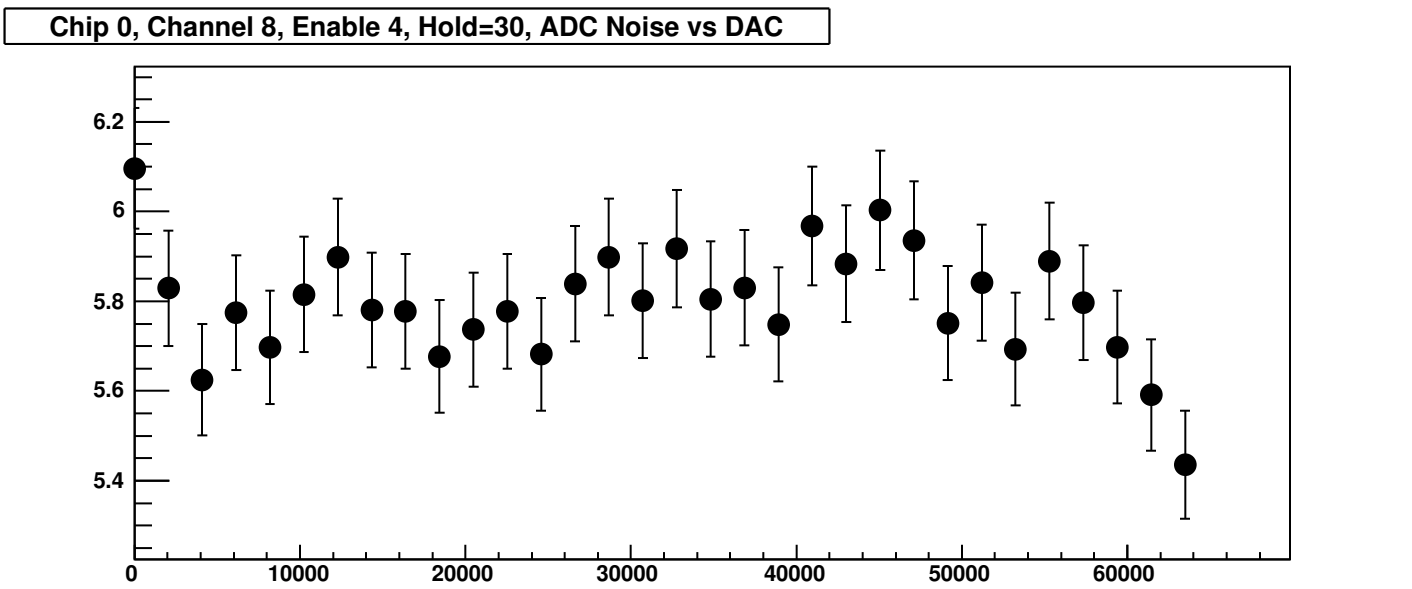

Chip 0, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

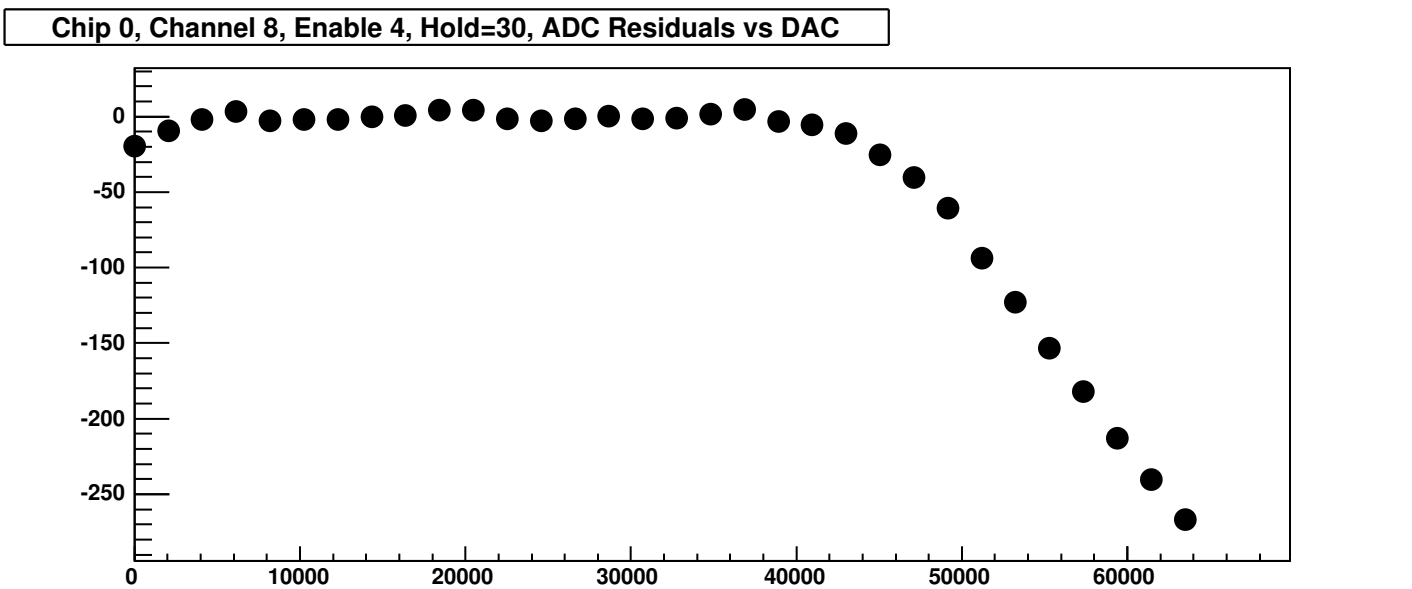

Chip 0, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3014 / 15
p0 1036 ± 0.1081
p1 0.001158 ± 4.405e-06
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

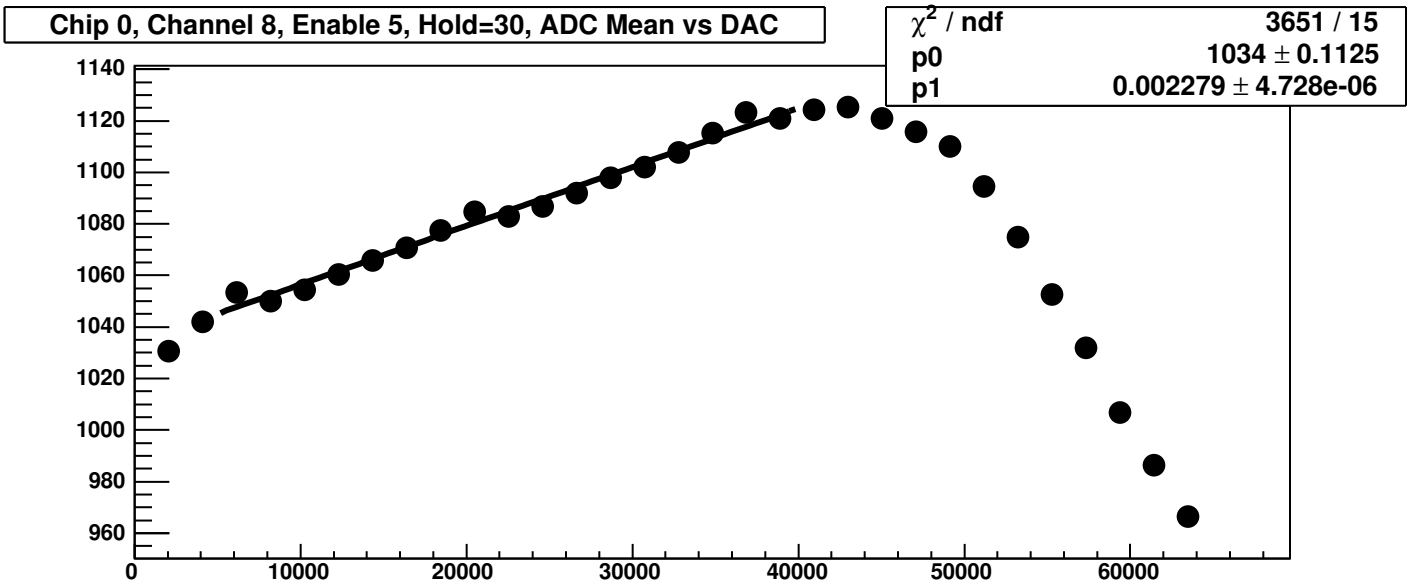

Chip 0, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

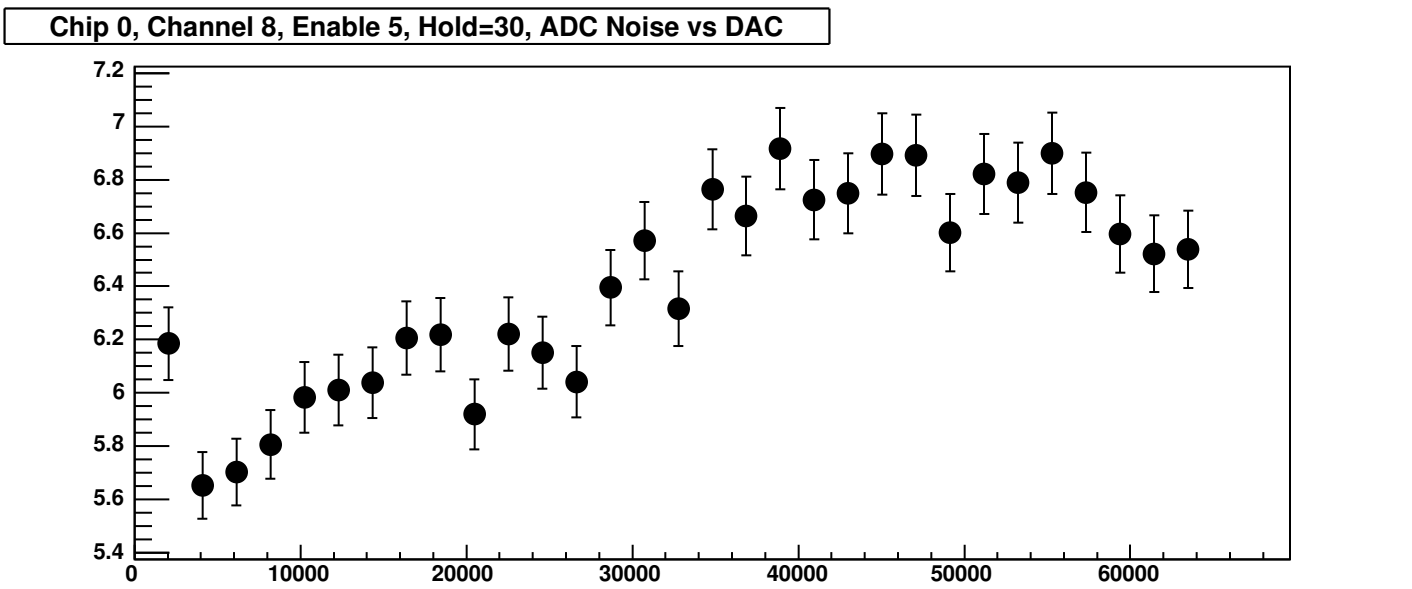

Chip 0, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

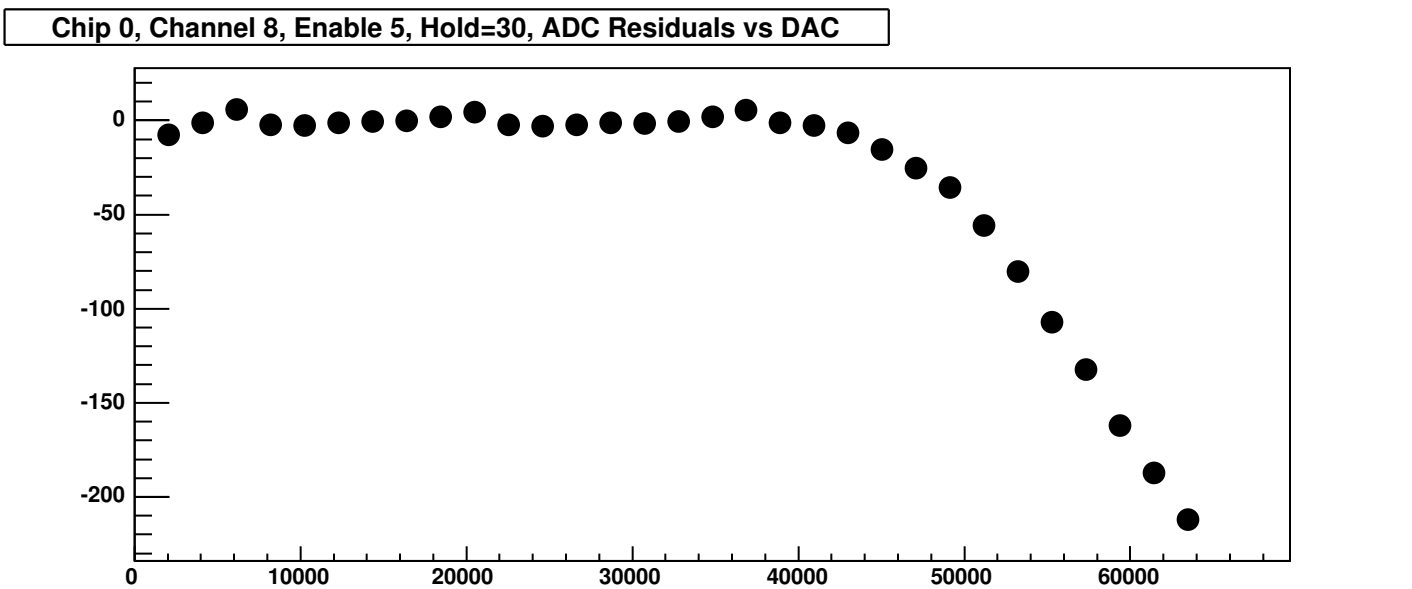

Chip 0, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 3395 / 15
p0 1036 ± 0.1077
p1 0.001234 ± 4.376e-06
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

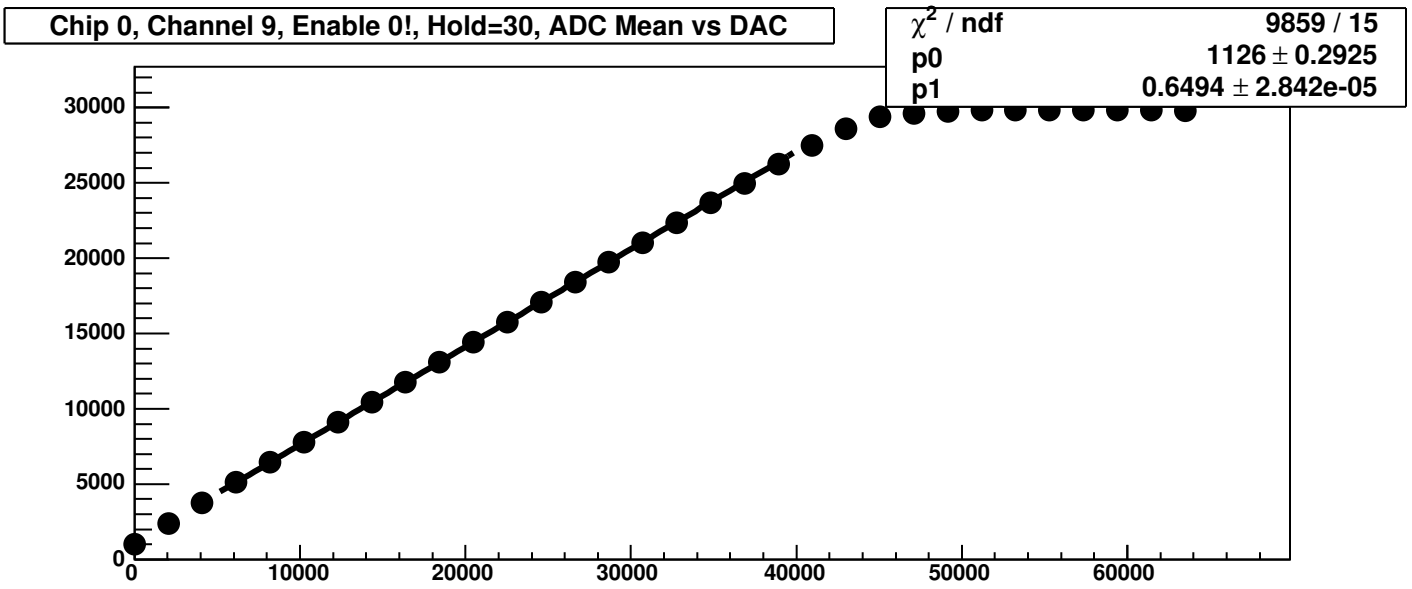

Chip 0, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

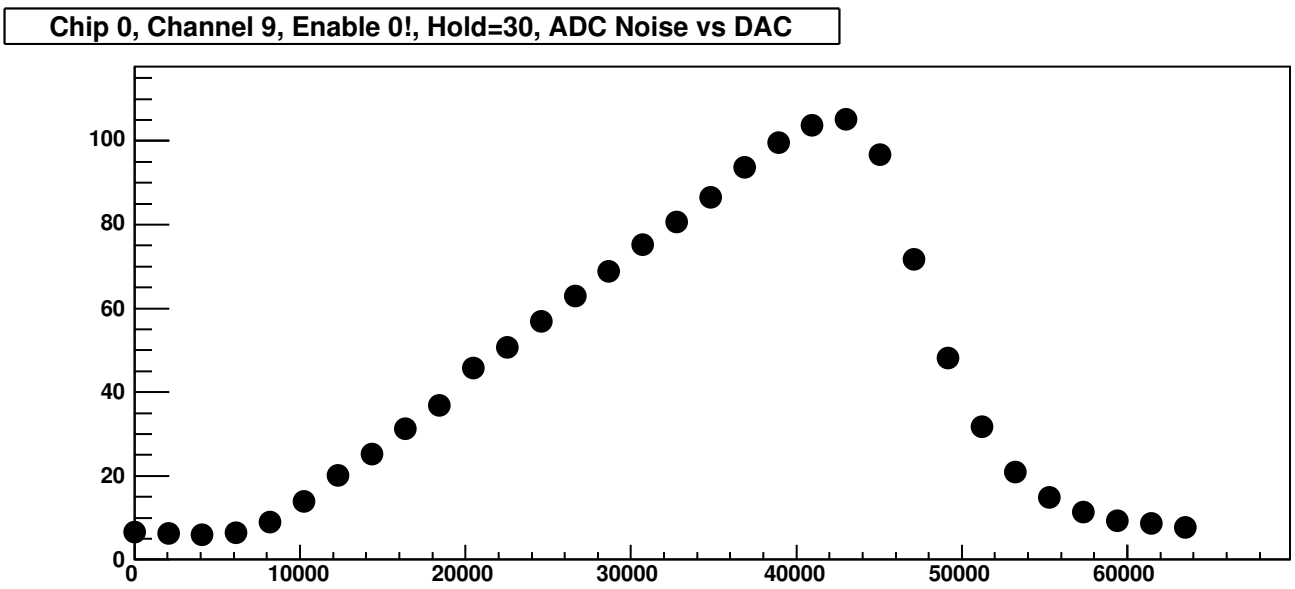

Chip 0, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

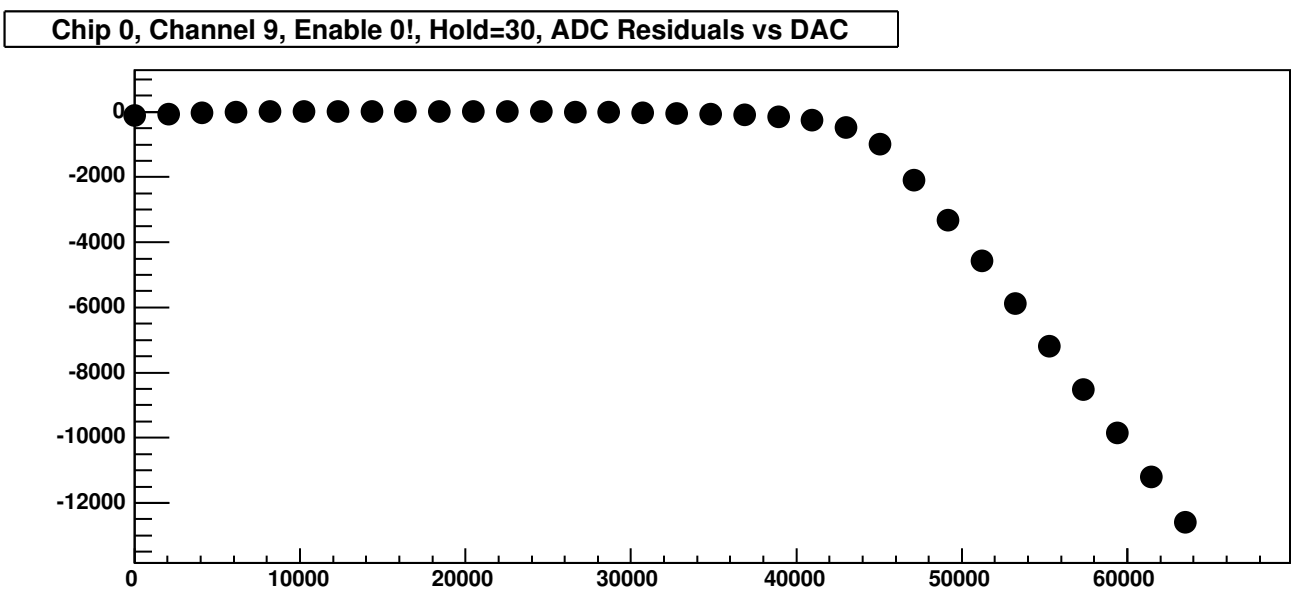

Chip 0, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3651 / 15
p0 1034 ± 0.1125
p1 0.002279 ± 4.728e-06
1140
1120
1100
1080
1060
1040
1020
1000
980
960
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

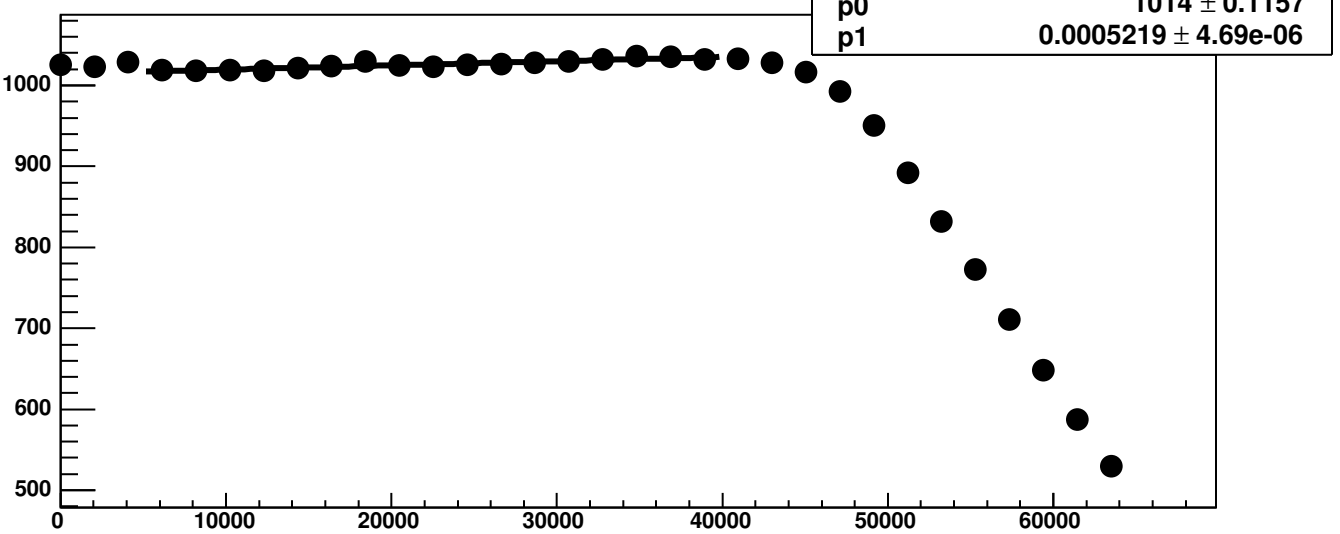

Chip 0, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 9859 / 15
p0 1126 ± 0.2925
p1 0.6494 ± 2.842e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

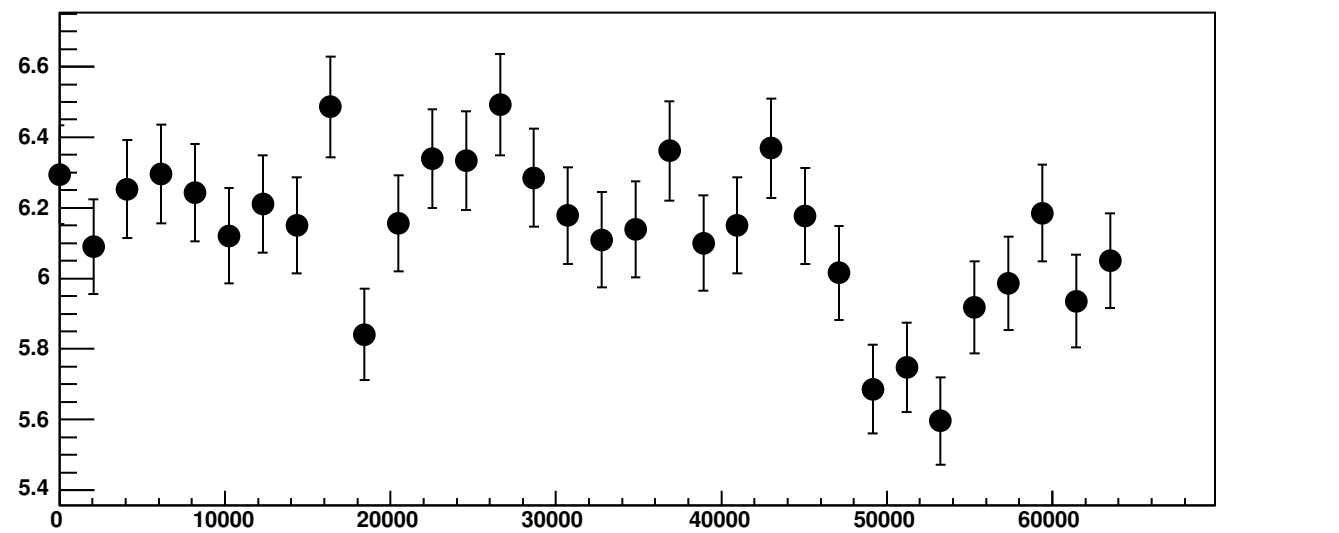

Chip 0, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

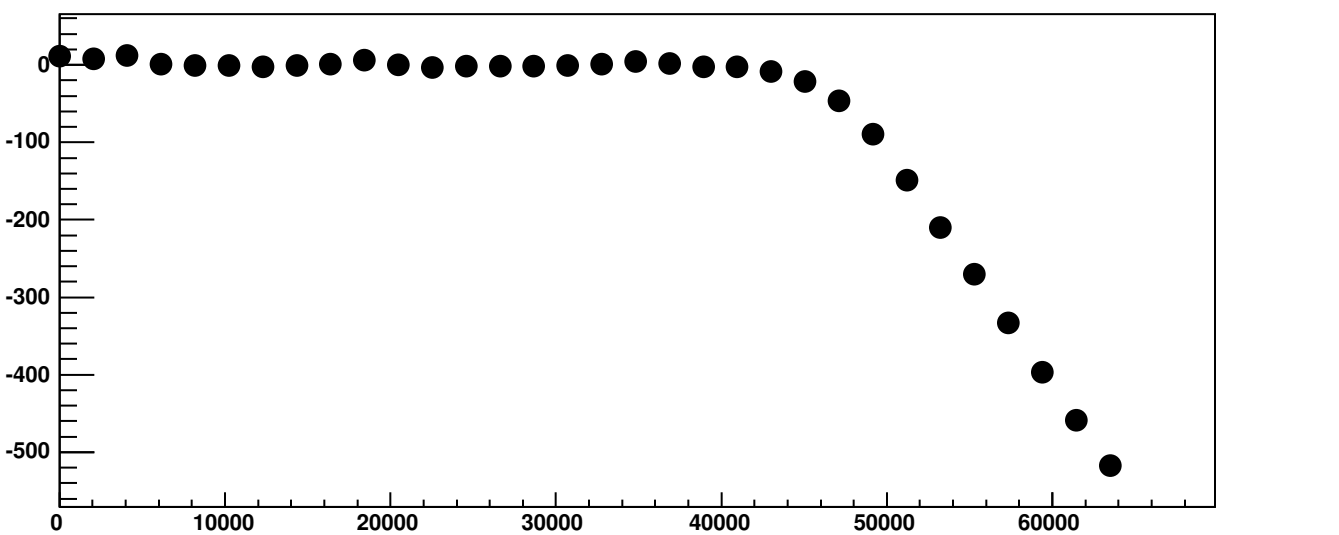
Chip 0, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC



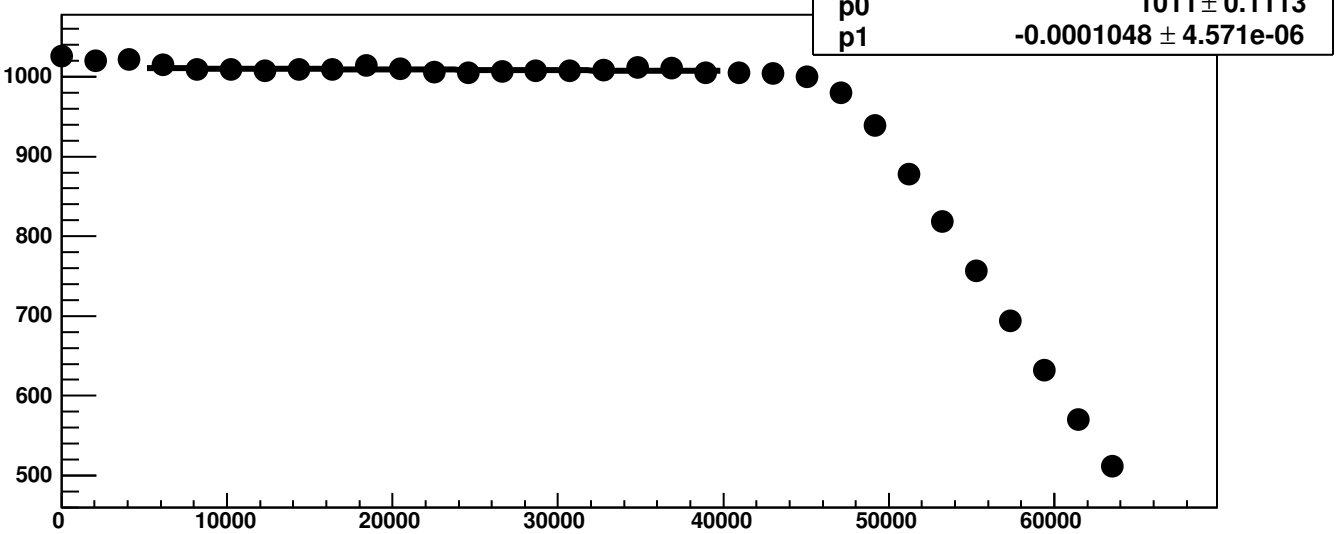
Chip 0, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



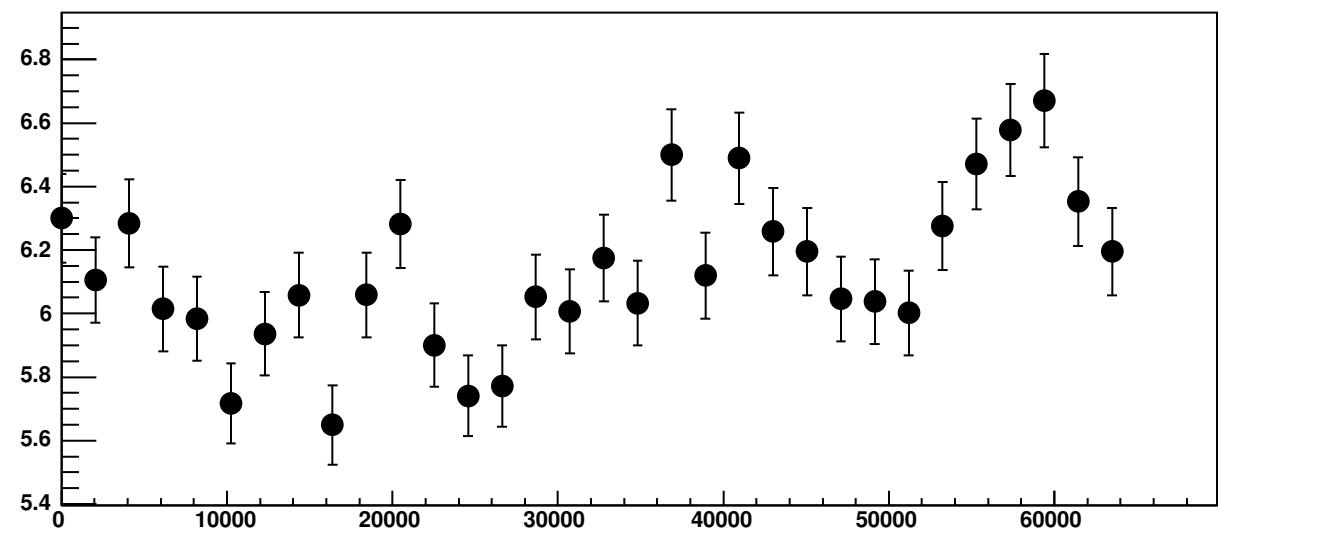
Chip 0, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



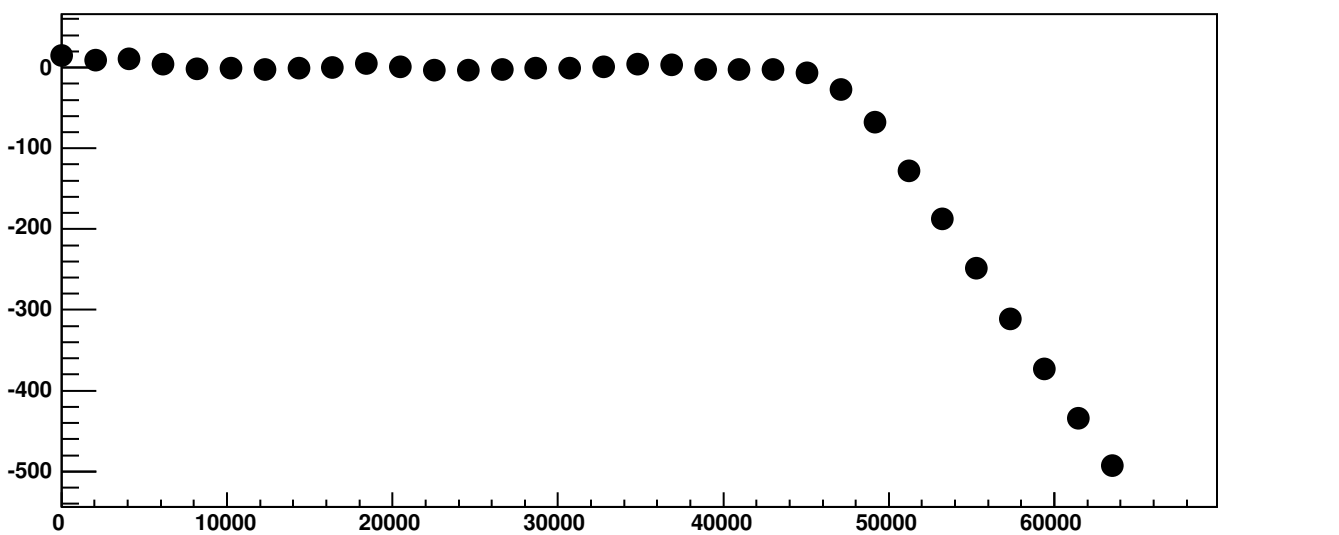
Chip 0, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC



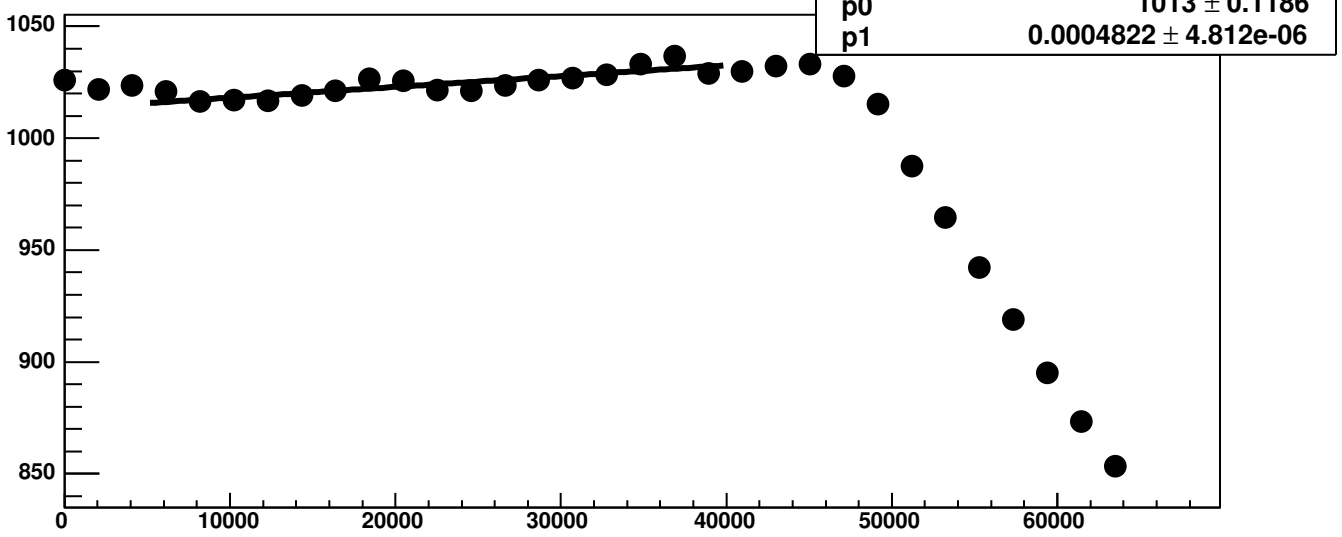
Chip 0, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



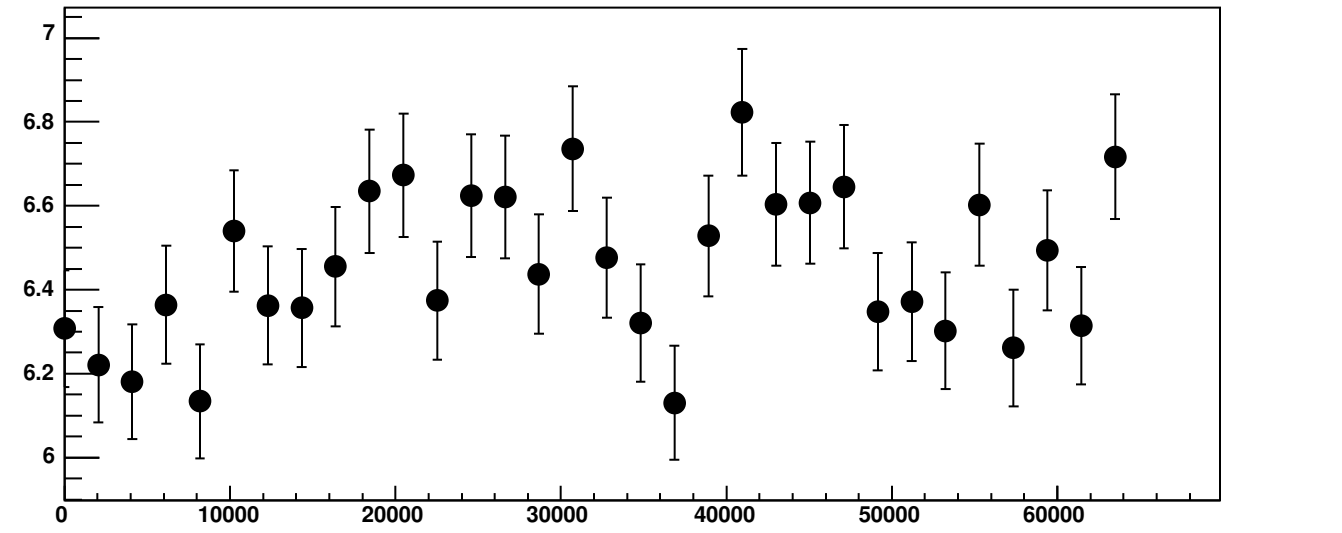
Chip 0, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC



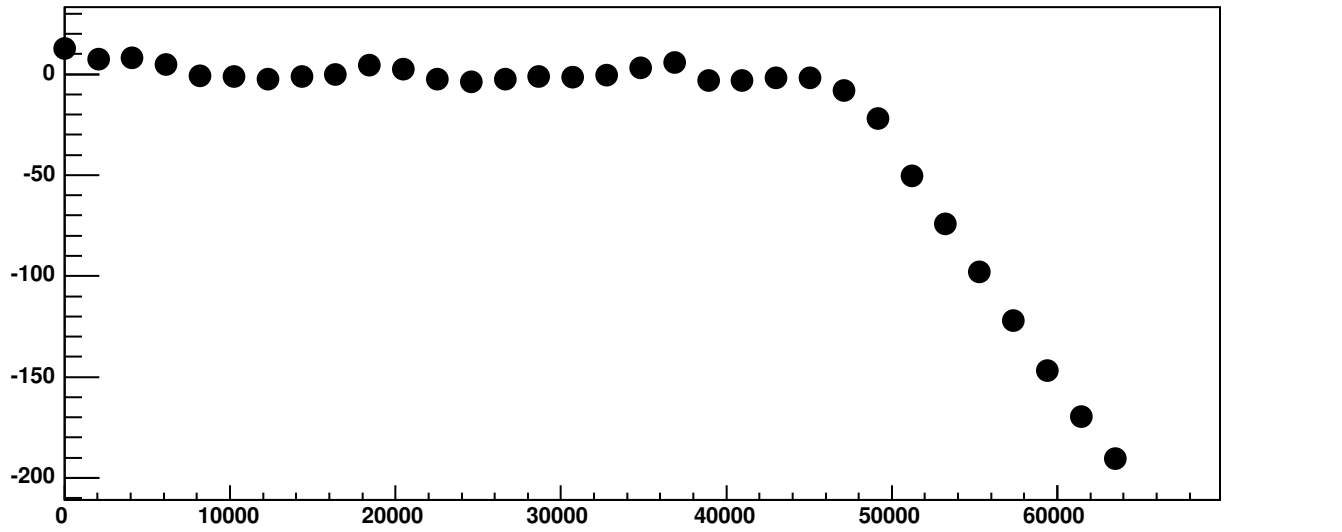
Chip 0, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC

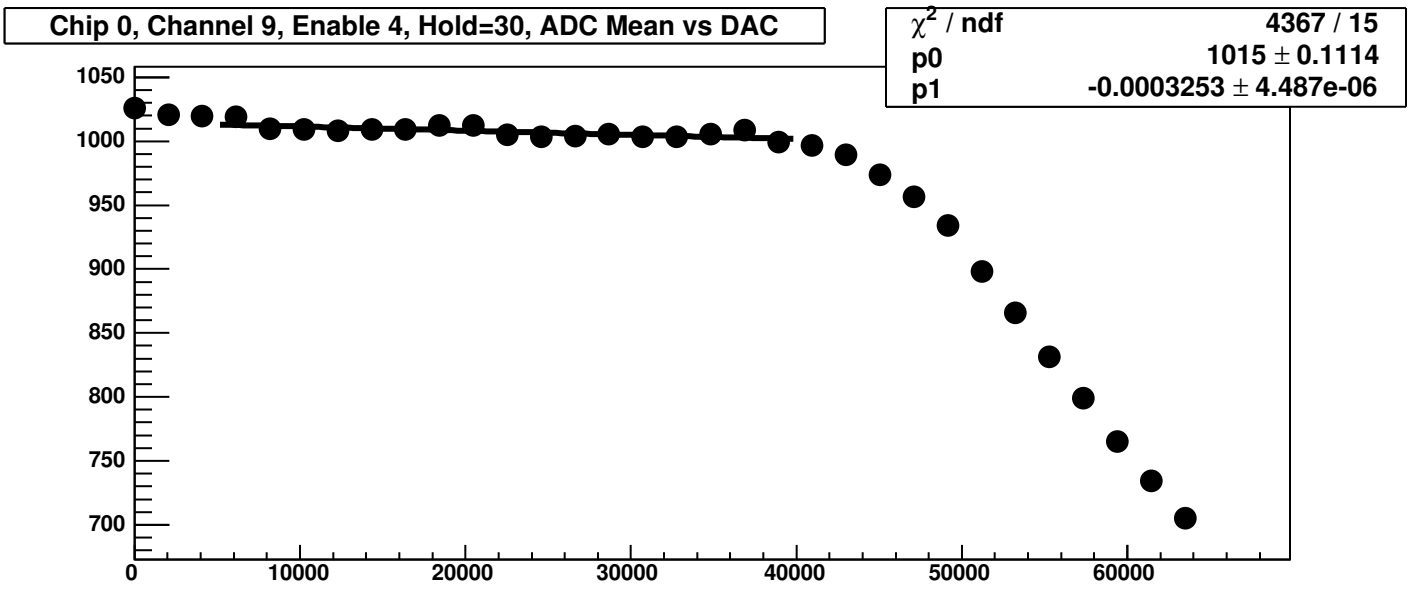


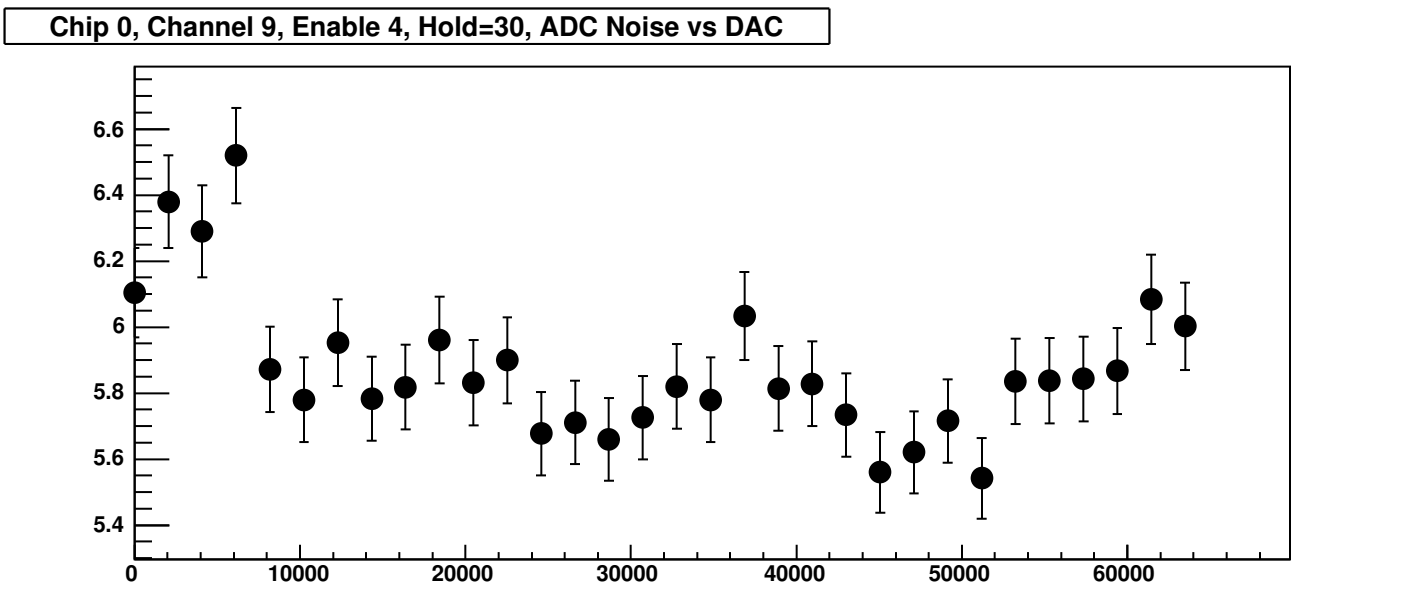
Chip 0, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC

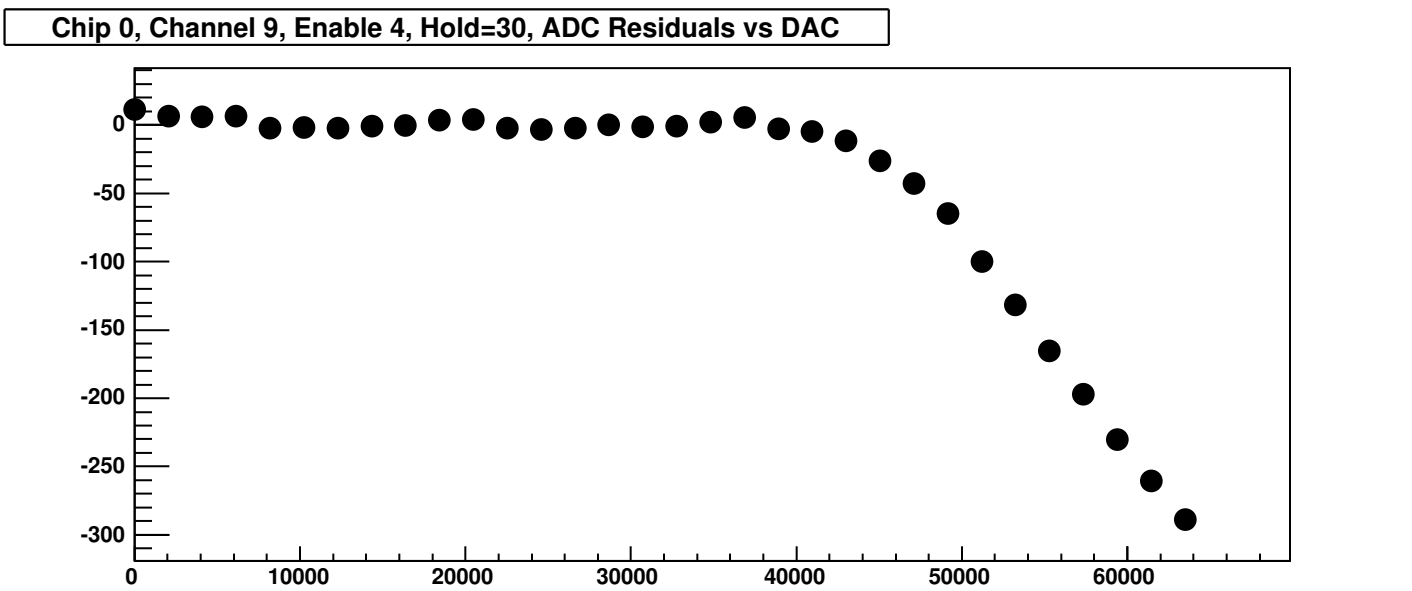


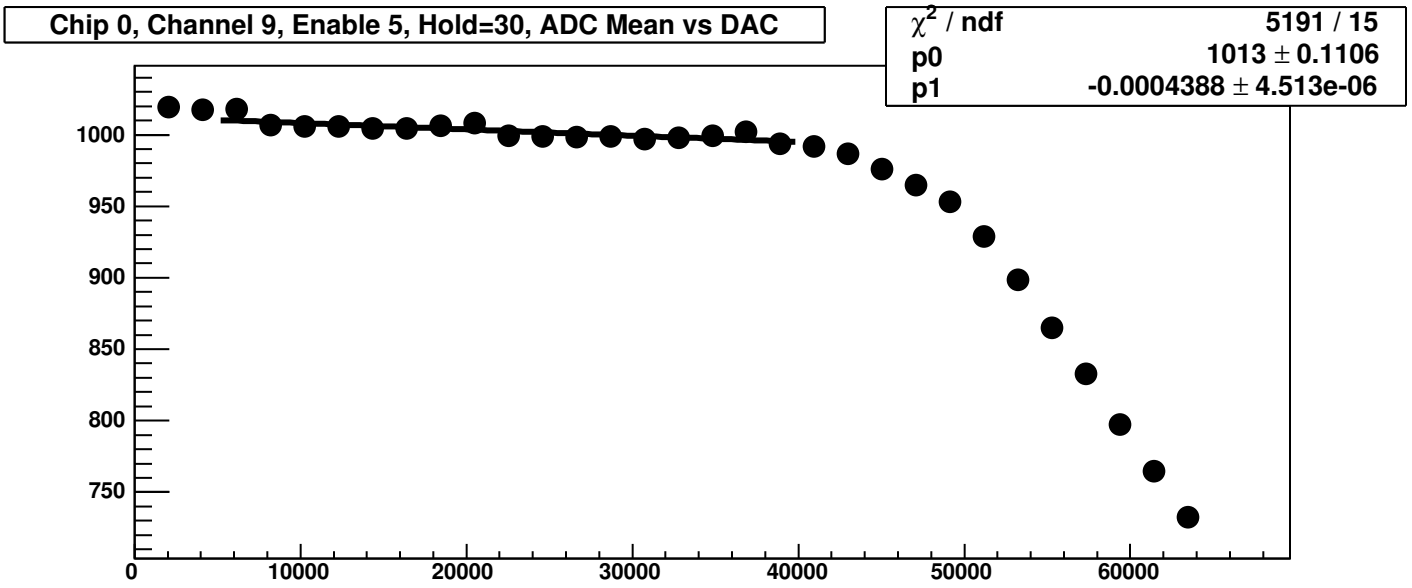
Chip 0, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC

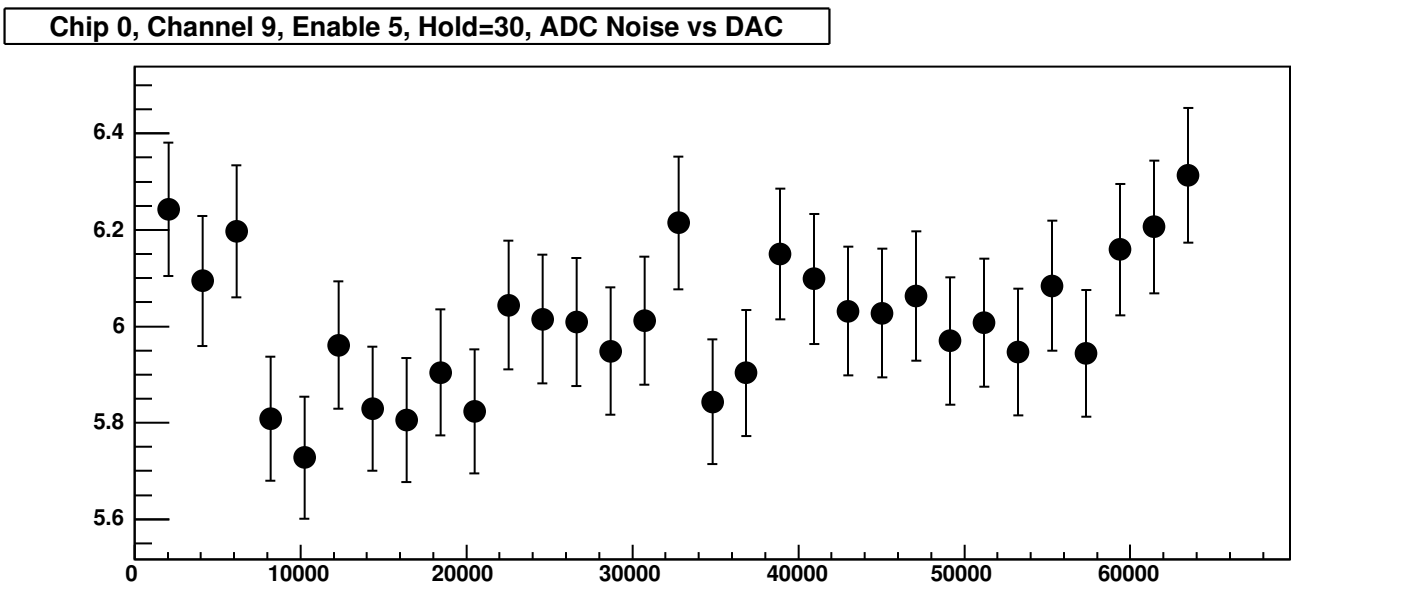


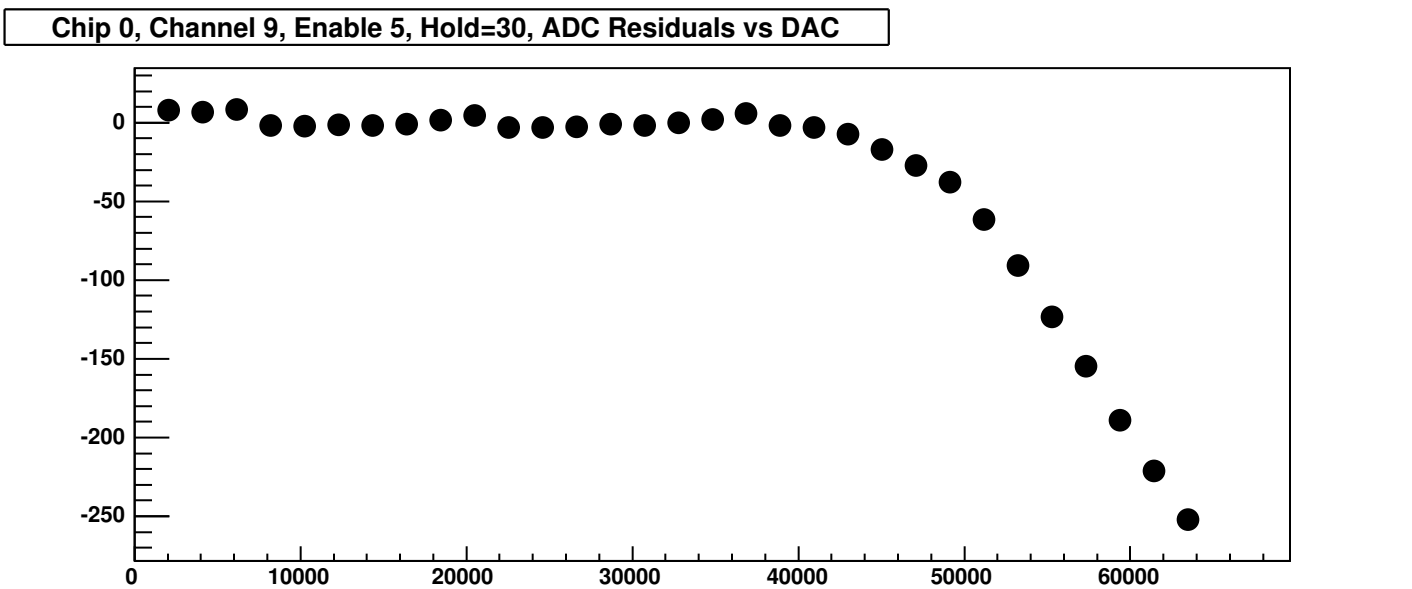

Chip 0, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4367 / 15
p0 1015 ± 0.1114
p1 -0.0003253 ± 4.487e-06
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

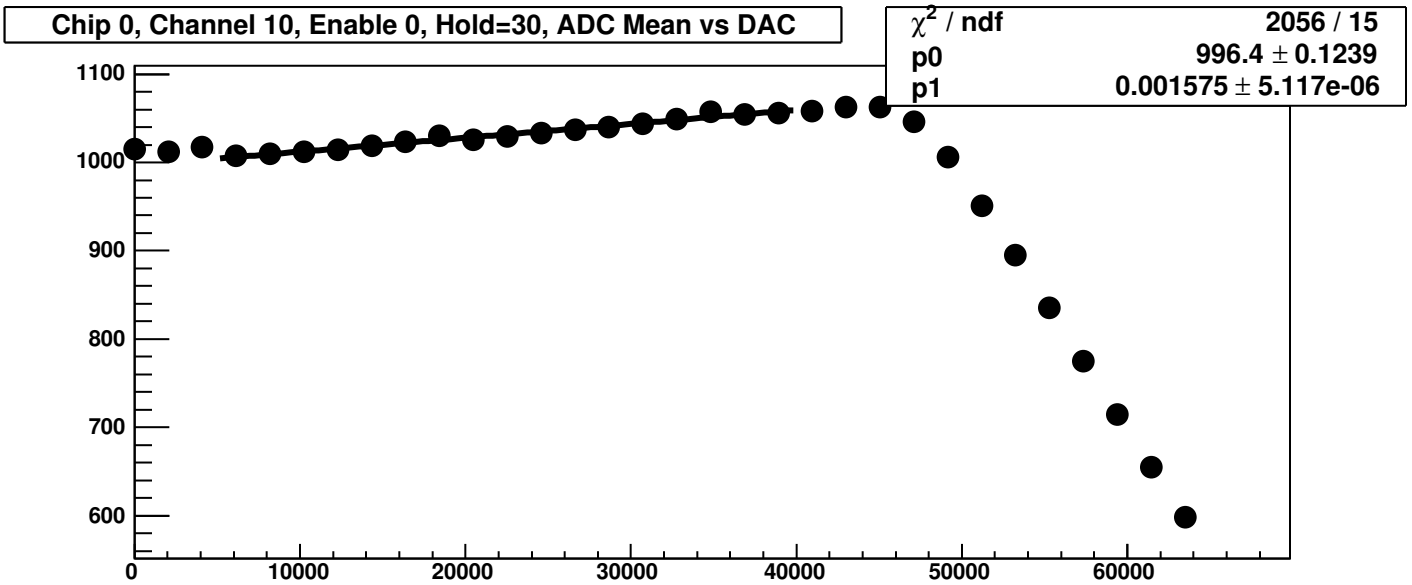

Chip 0, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

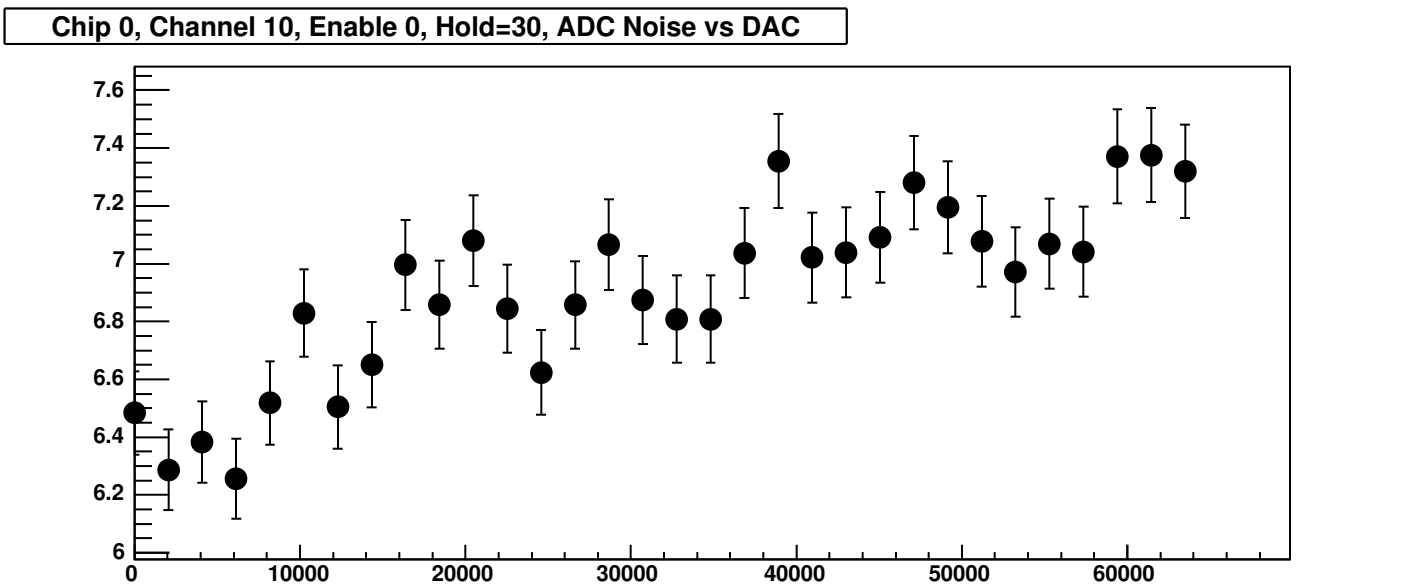

Chip 0, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

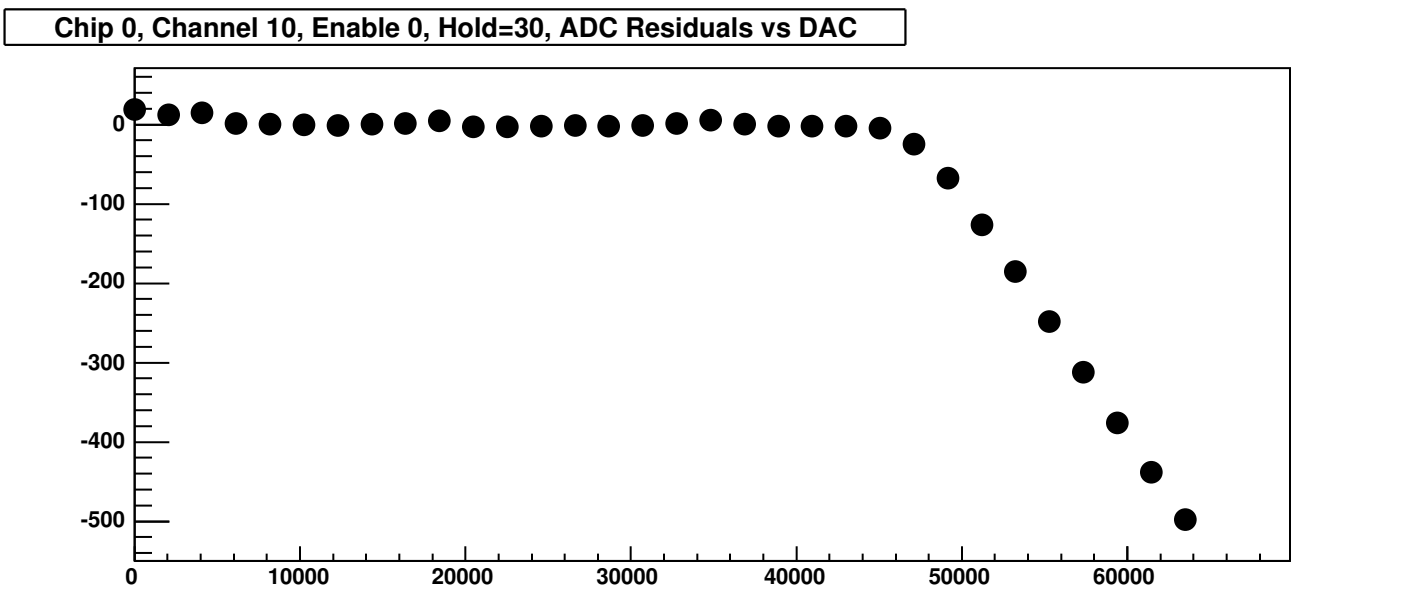

Chip 0, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5191 / 15
p0 1013 ± 0.1106
p1 -0.0004388 ± 4.513e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

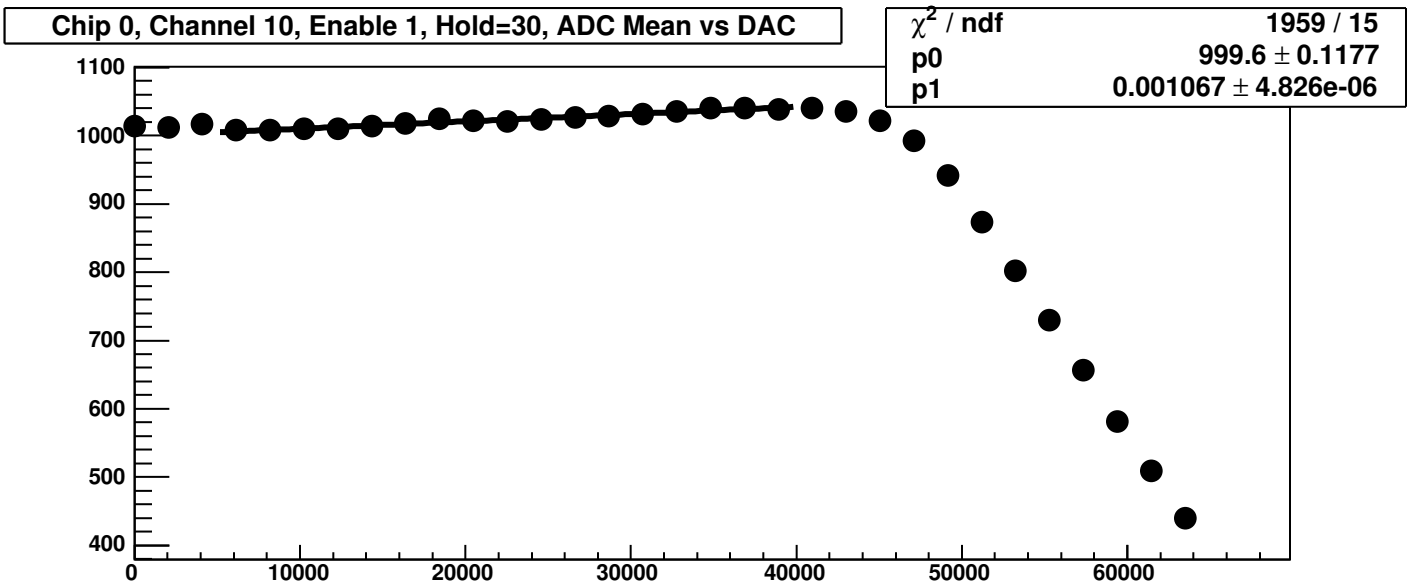

Chip 0, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

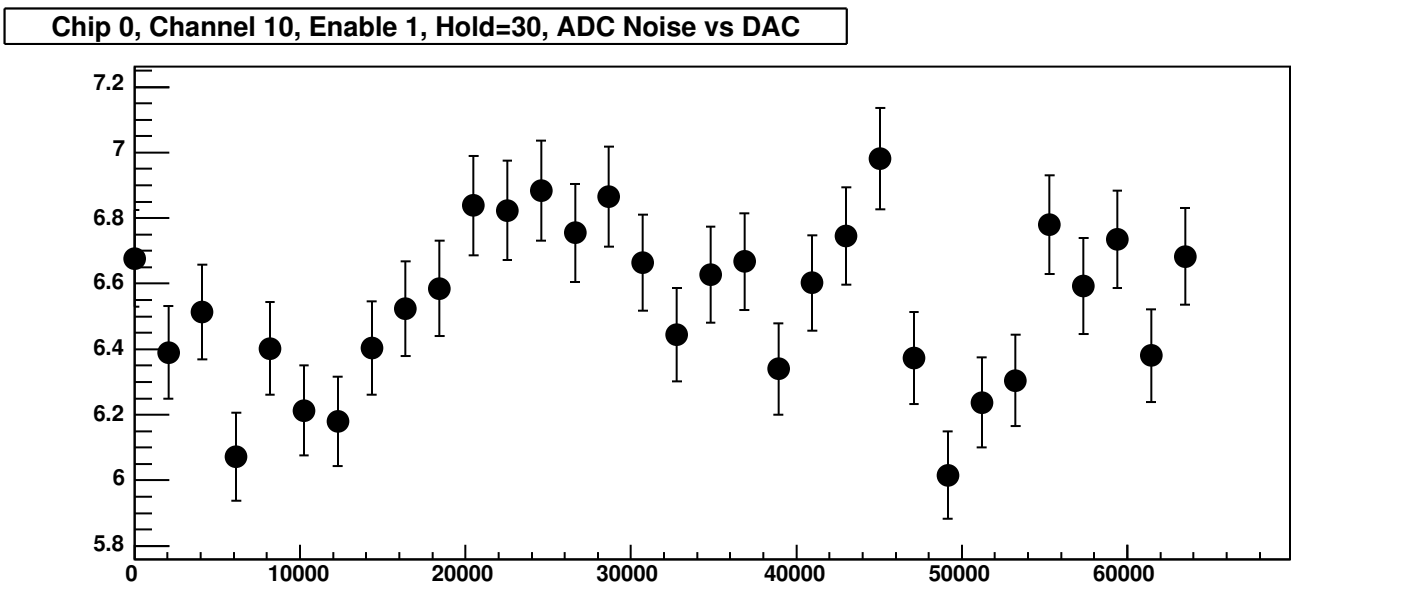

Chip 0, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

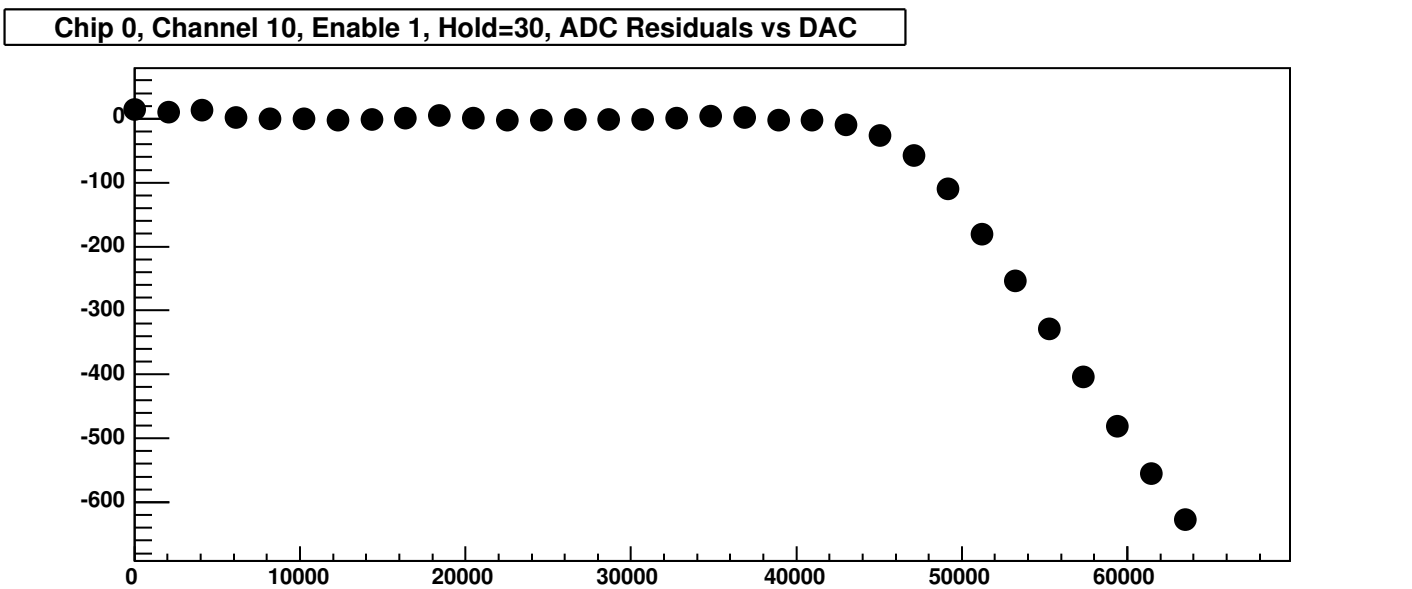

Chip 0, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2056 / 15
p0 996.4 ± 0.1239
p1 0.001575 ± 5.117e-06
1100
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000

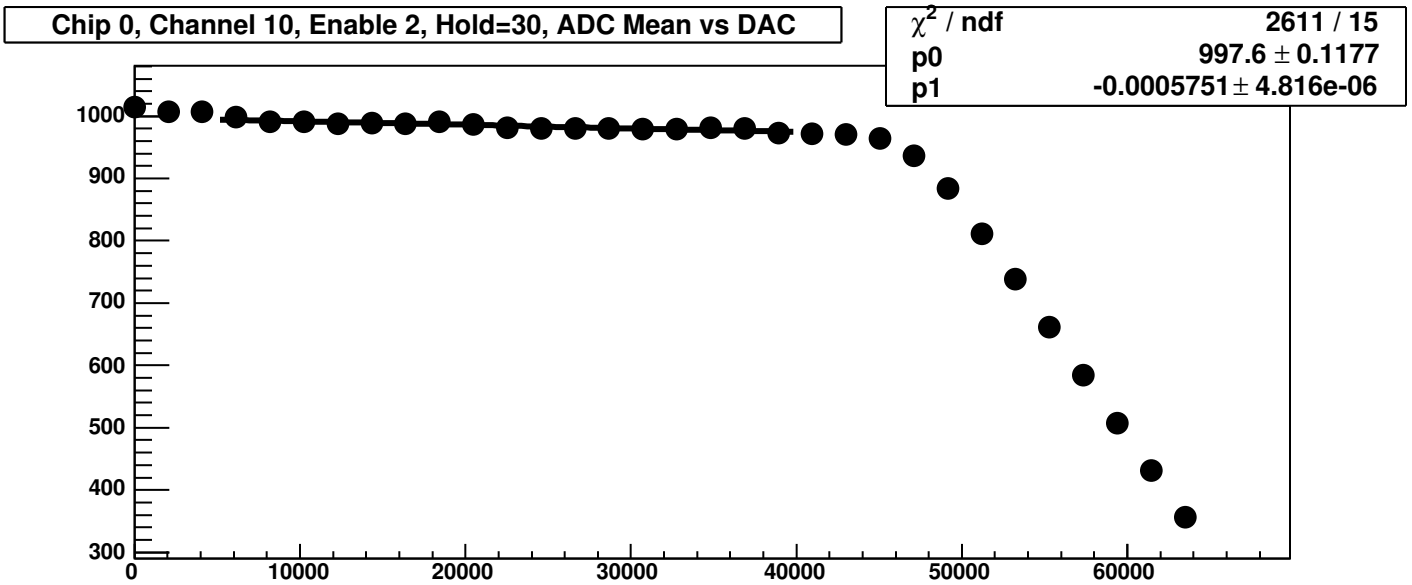

Chip 0, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

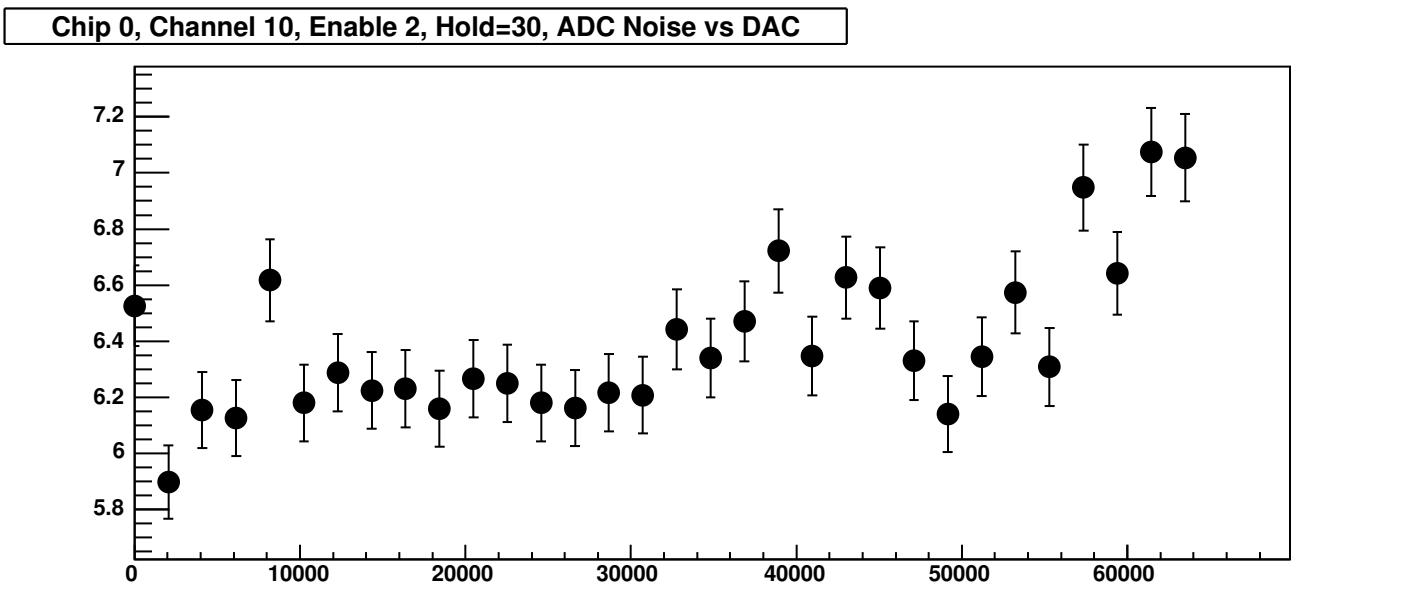

Chip 0, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

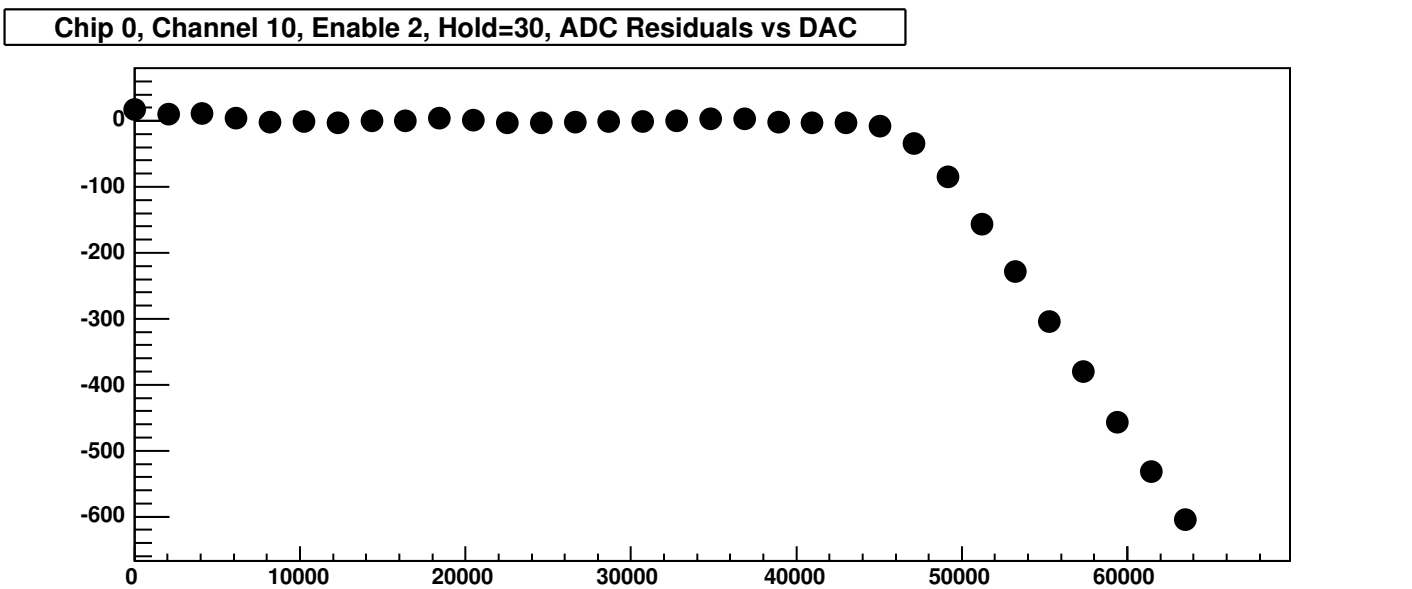

Chip 0, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1959 / 15
p0 999.6 ± 0.1177
p1 0.001067 ± 4.826e-06
1100
1000
900
800
700
600
500
400
0
10000
20000
30000
40000
50000
60000

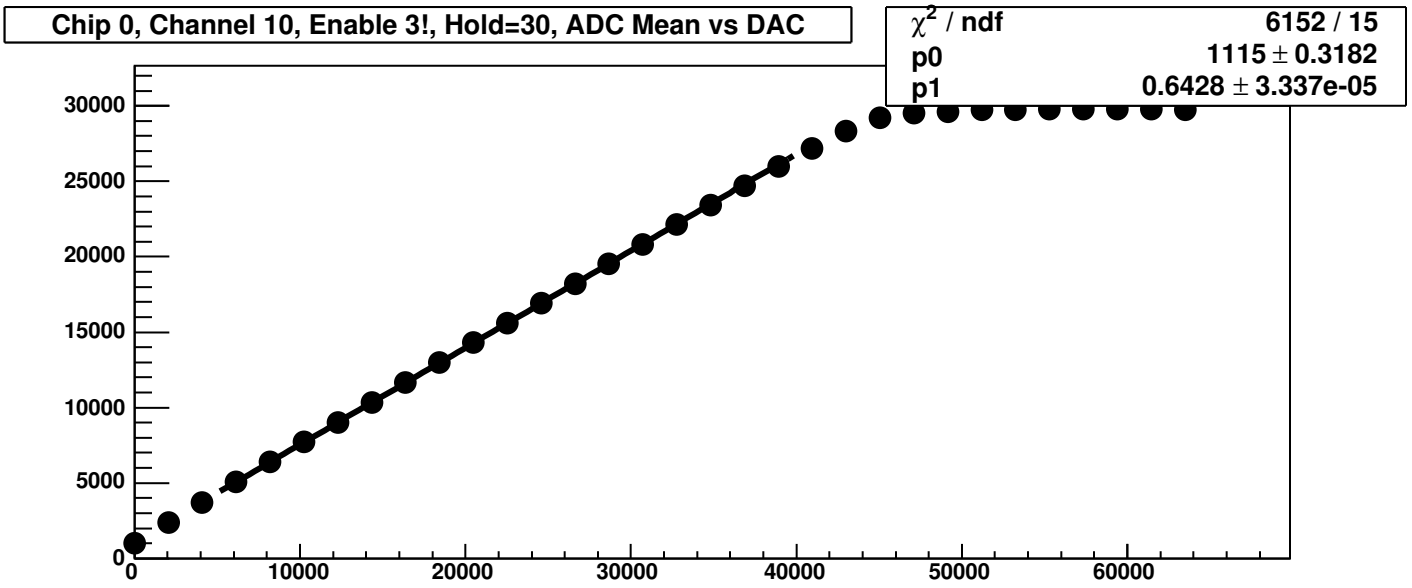

Chip 0, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

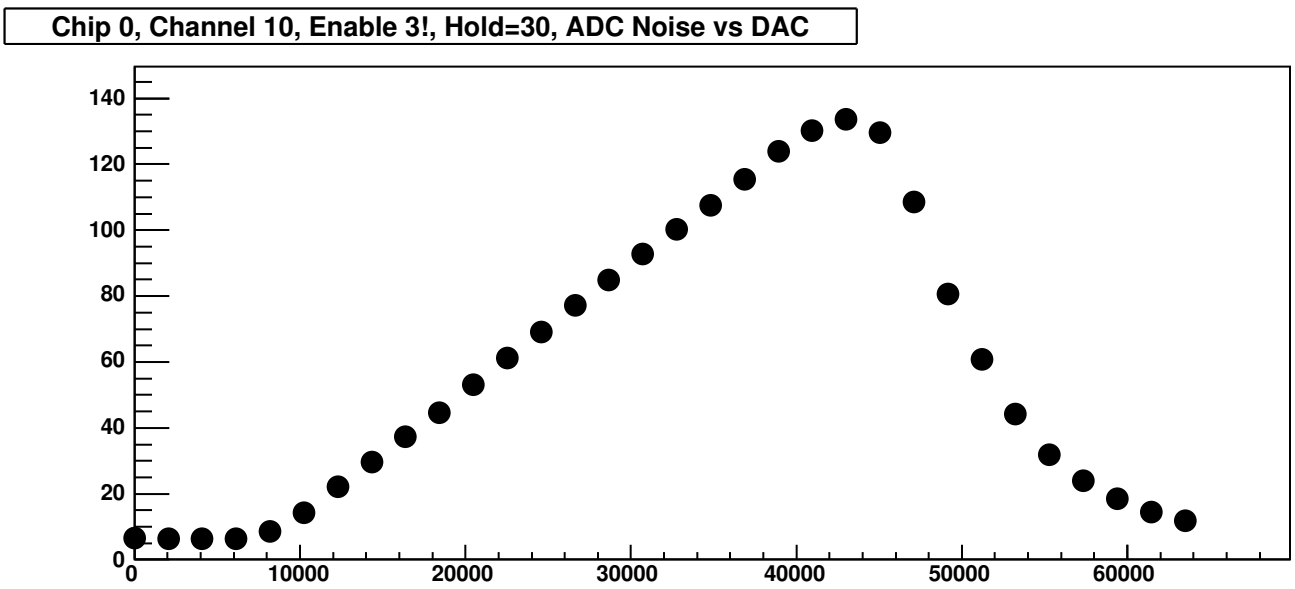

Chip 0, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
-600
0
10000
20000
30000
40000
50000
60000

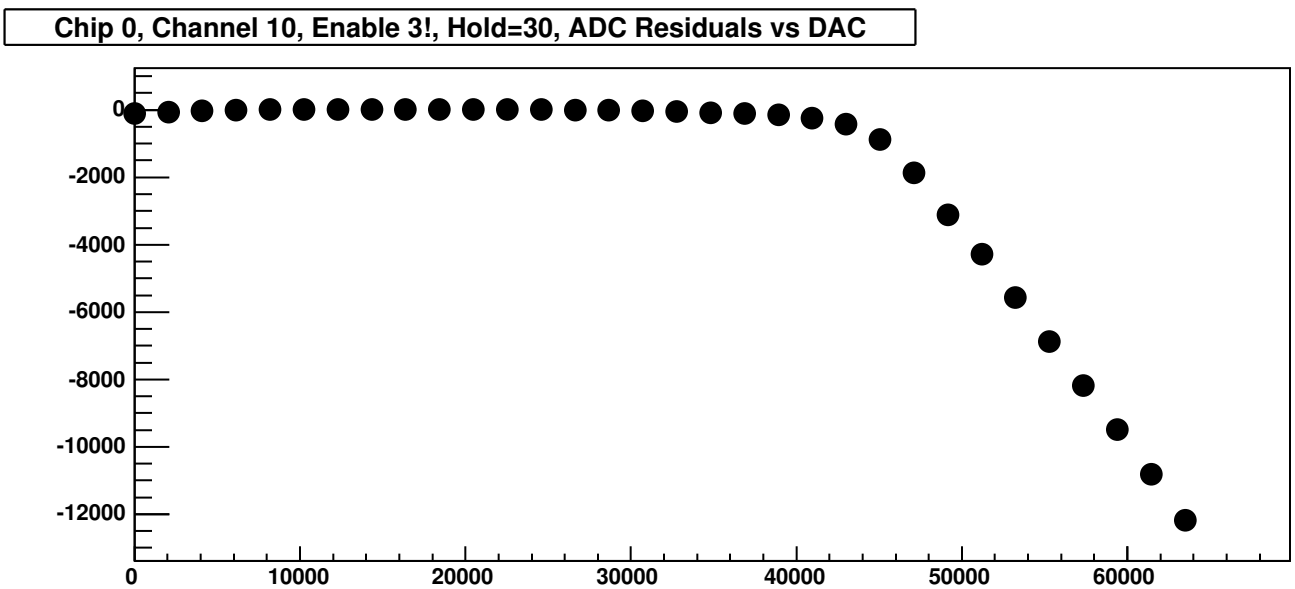

Chip 0, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2611 / 15
p0 997.6 ± 0.1177
p1 -0.0005751 ± 4.816e-06
1000
900
800
700
600
500
400
300
0
10000
20000
30000
40000
50000
60000

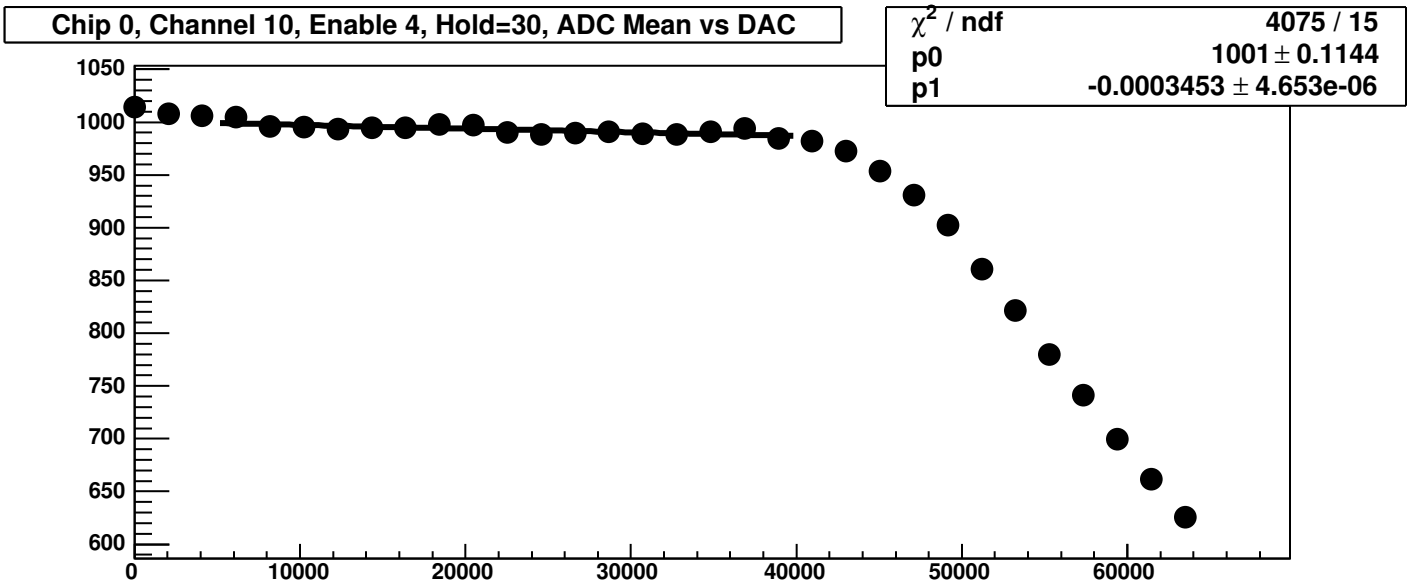

Chip 0, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

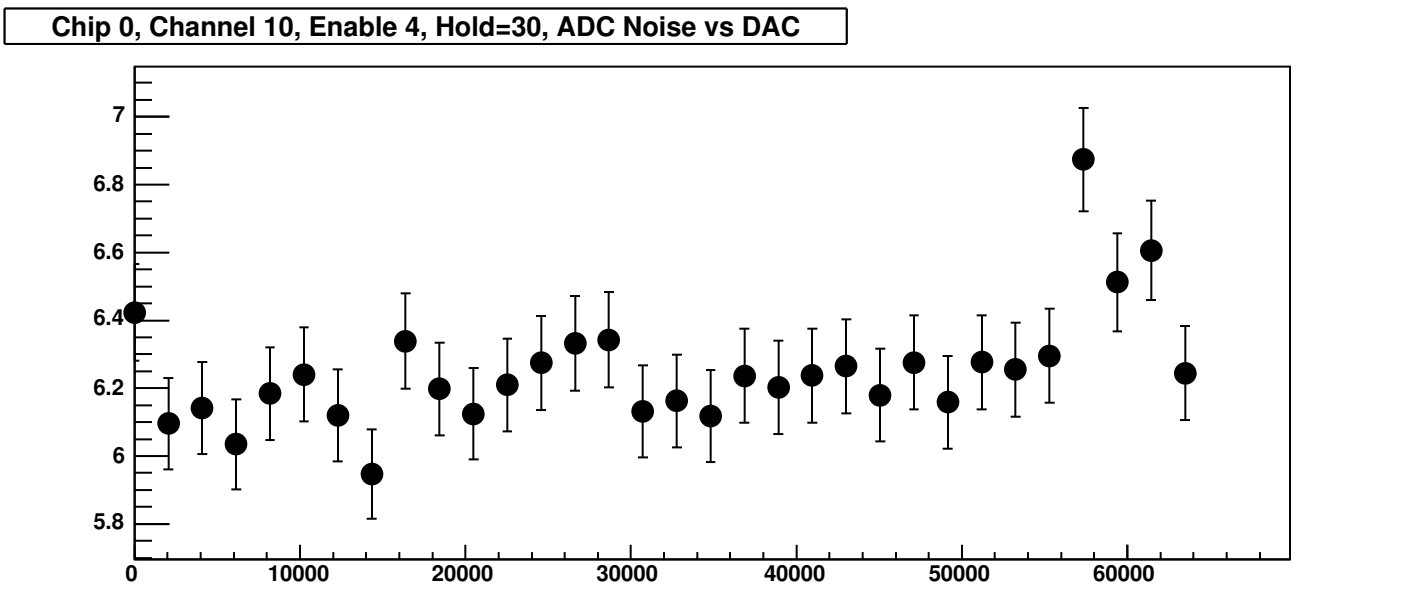

Chip 0, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
-600
0
10000
20000
30000
40000
50000
60000

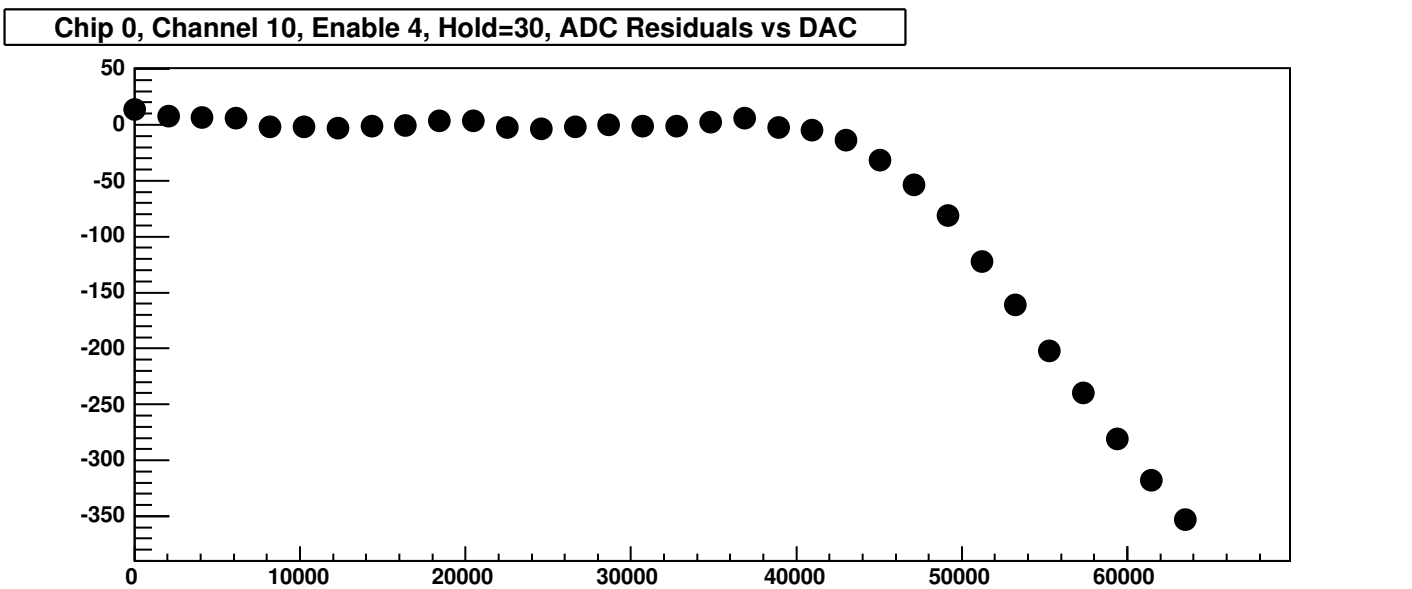

Chip 0, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 6152 / 15
p0 1115 ± 0.3182
p1 0.6428 ± 3.337e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

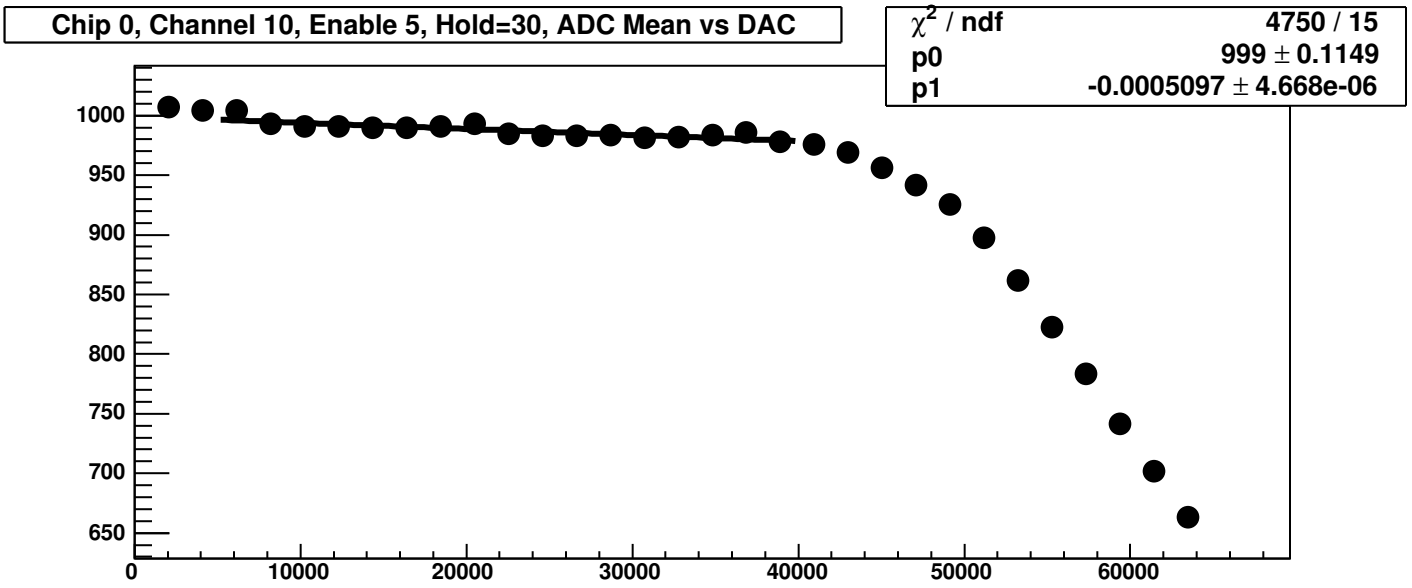

Chip 0, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

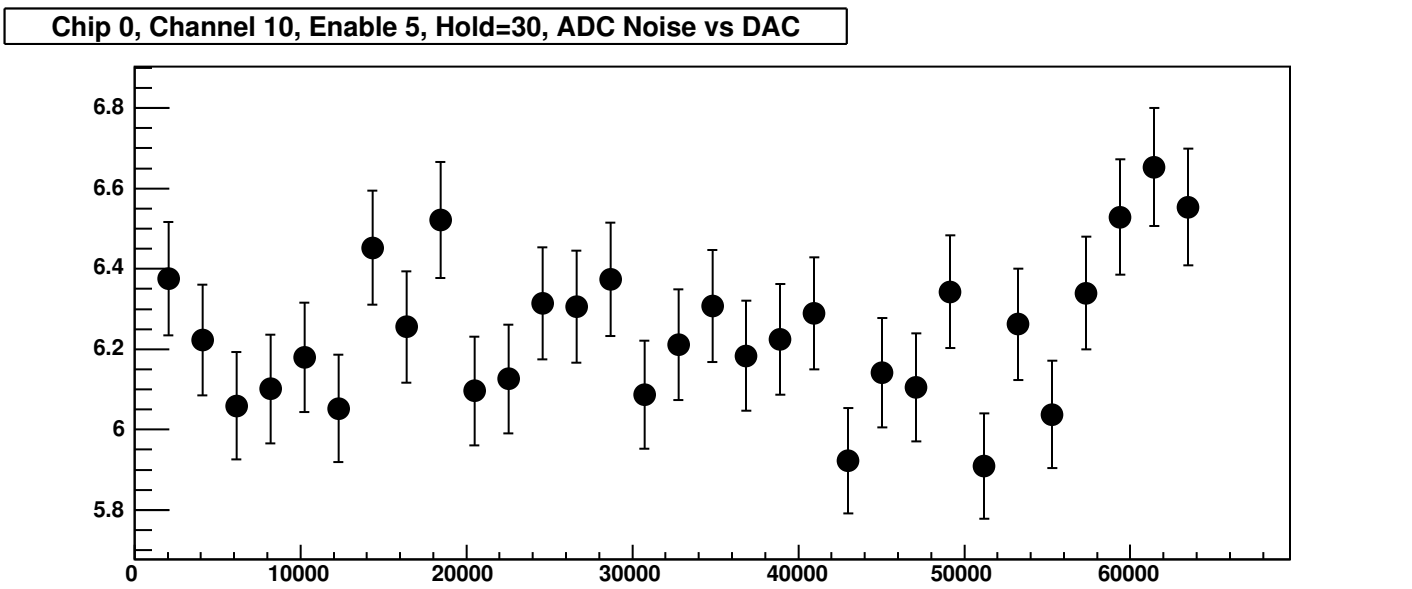

Chip 0, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

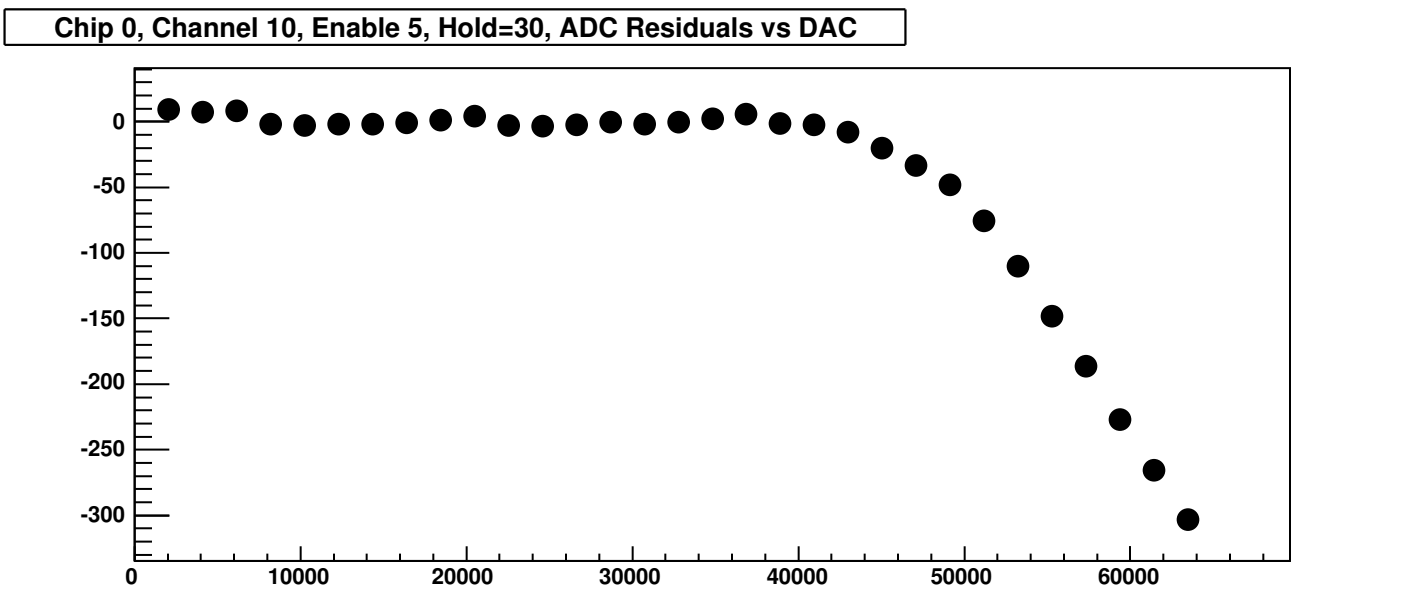

Chip 0, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4075 / 15
p0 1001 ± 0.1144
p1 -0.0003453 ± 4.653e-06
1050
1000
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

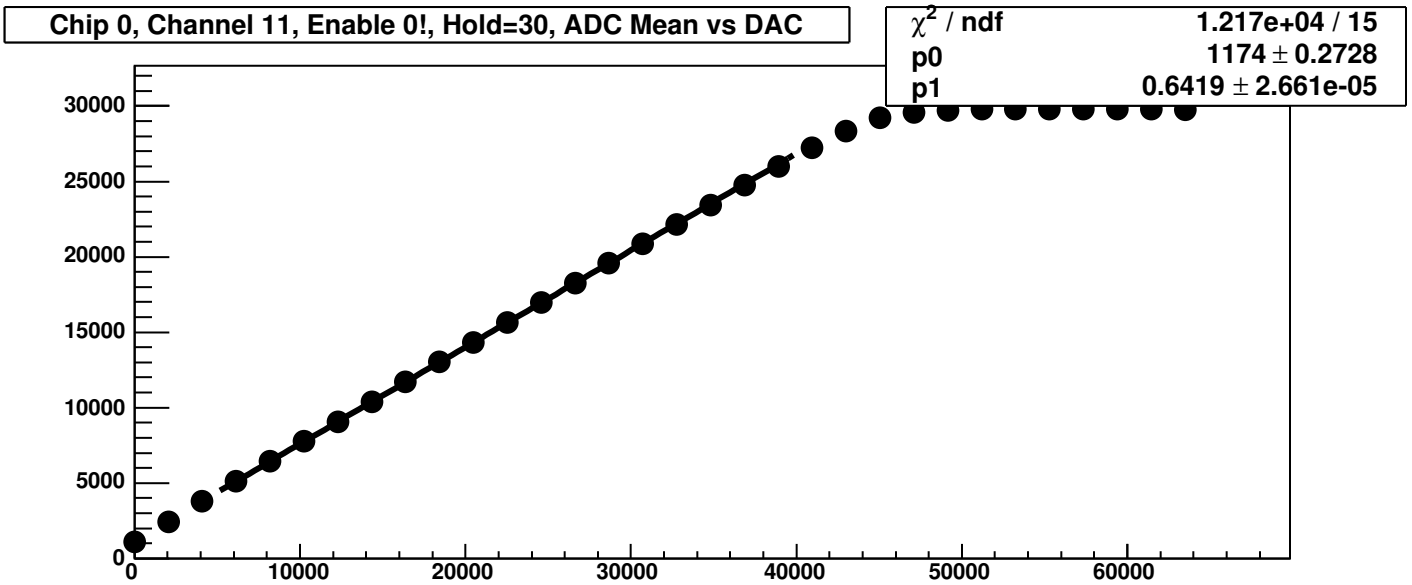

Chip 0, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

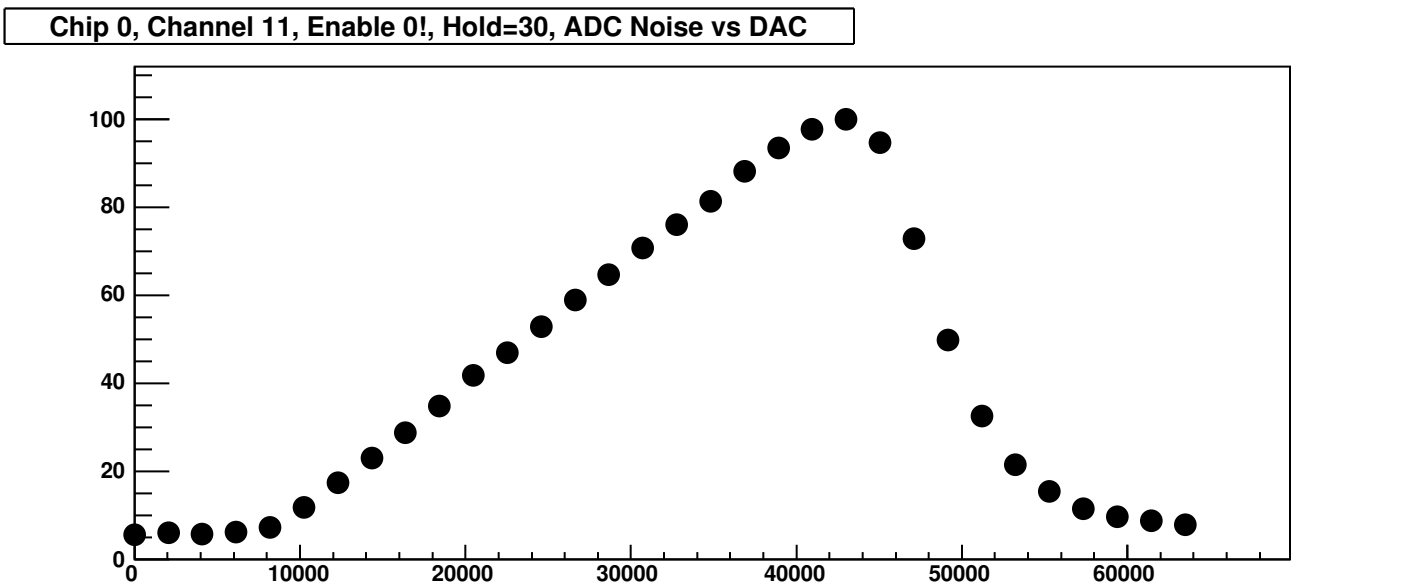

Chip 0, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

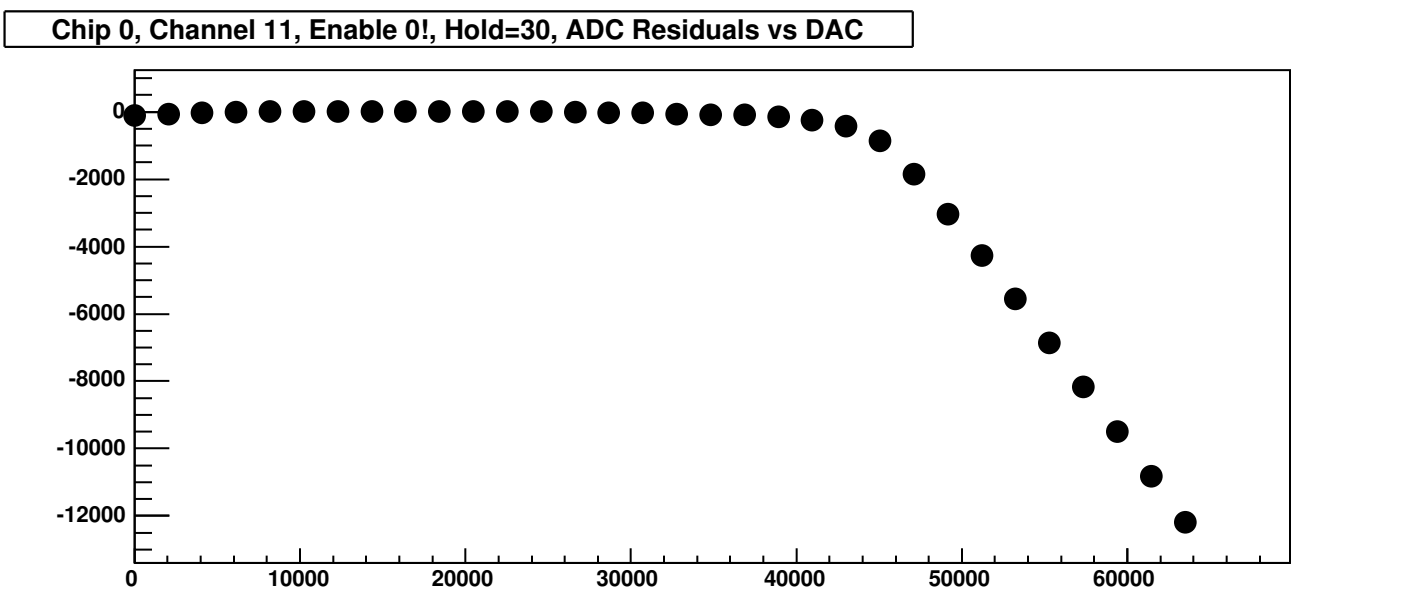

Chip 0, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4750 / 15
p0 999 ± 0.1149
p1 -0.0005097 ± 4.668e-06
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

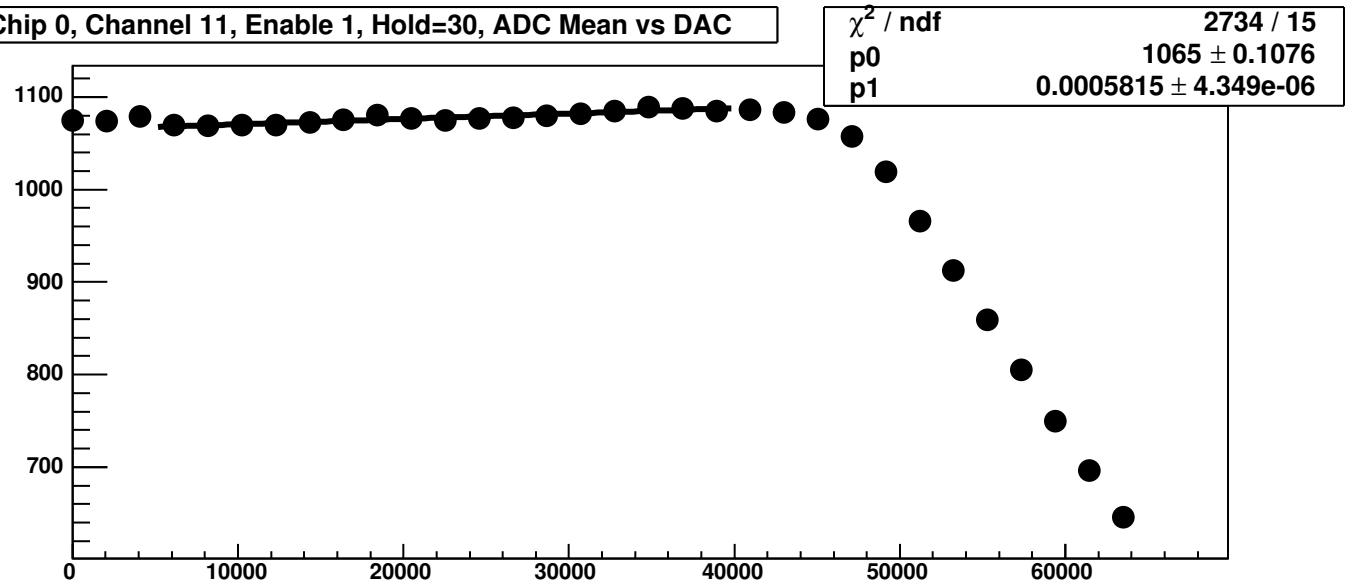

Chip 0, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.217e+04 / 15
p0 1174 ± 0.2728
p1 0.6419 ± 2.661e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

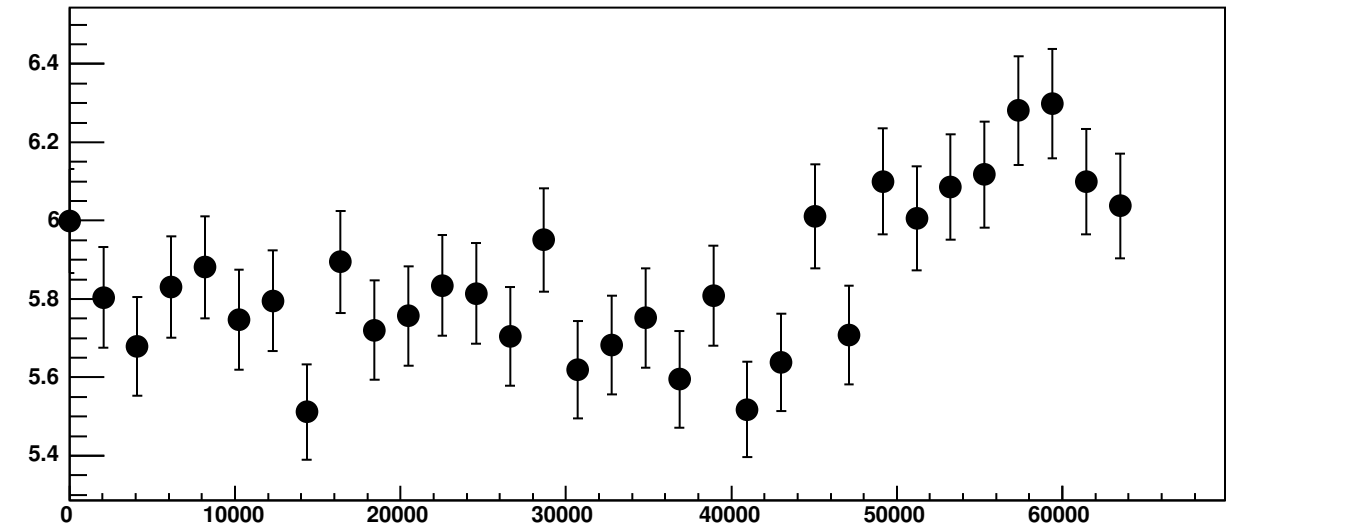

Chip 0, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

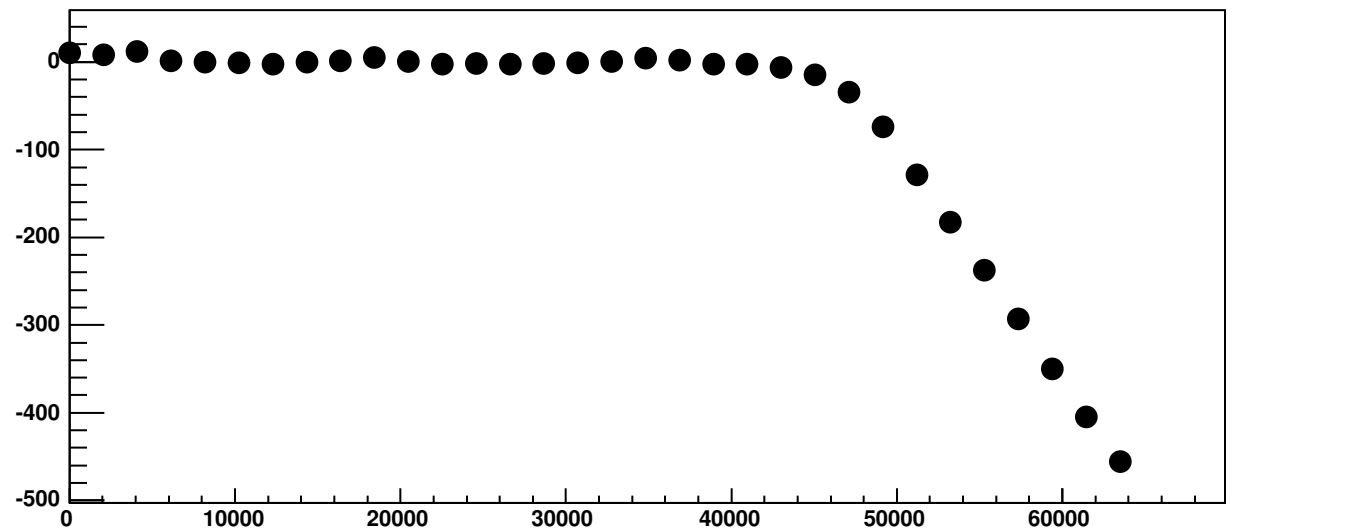
Chip 0, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

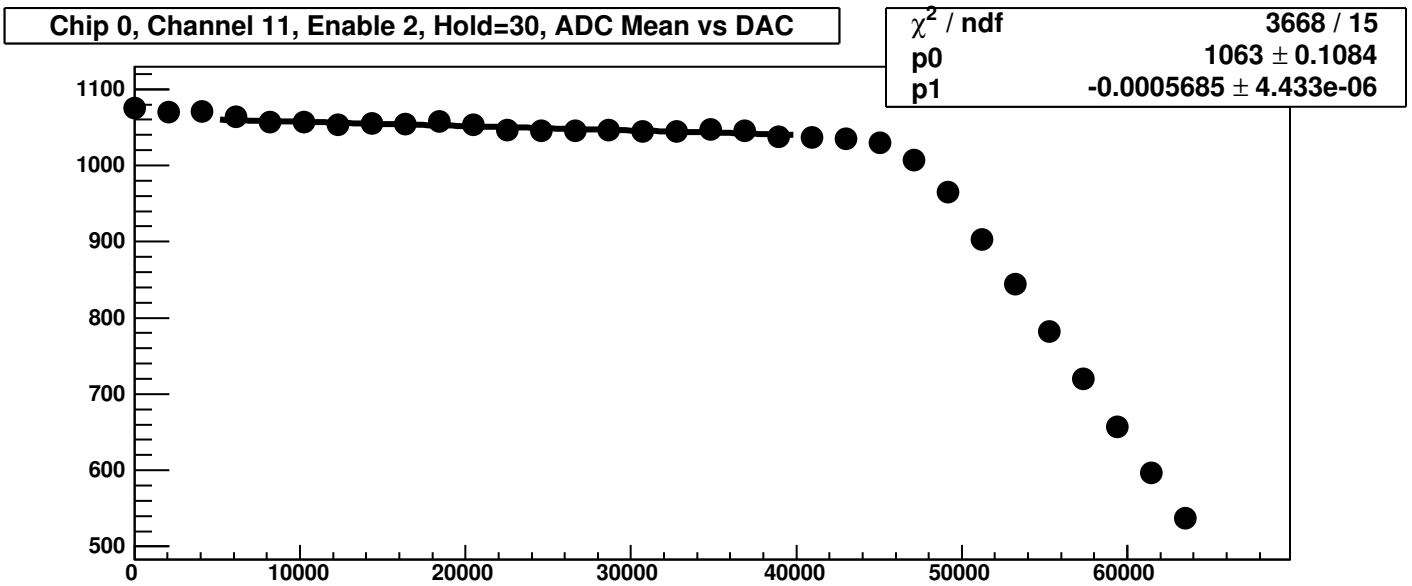


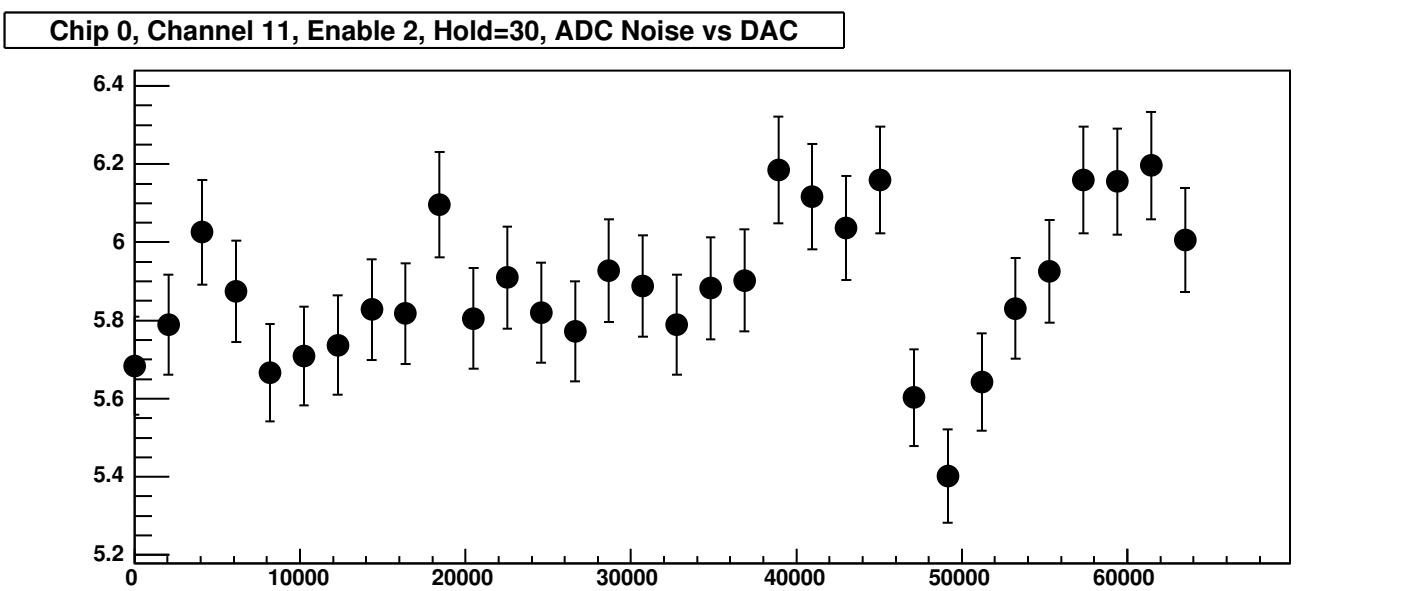
Chip 0, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC

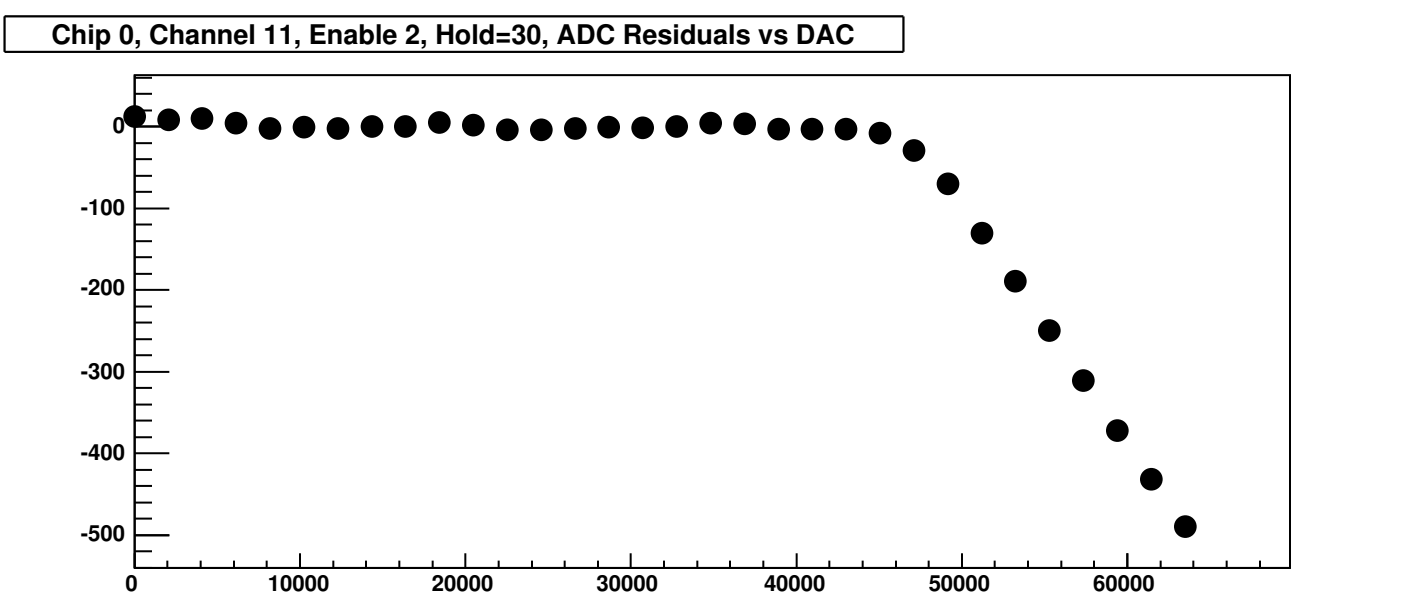


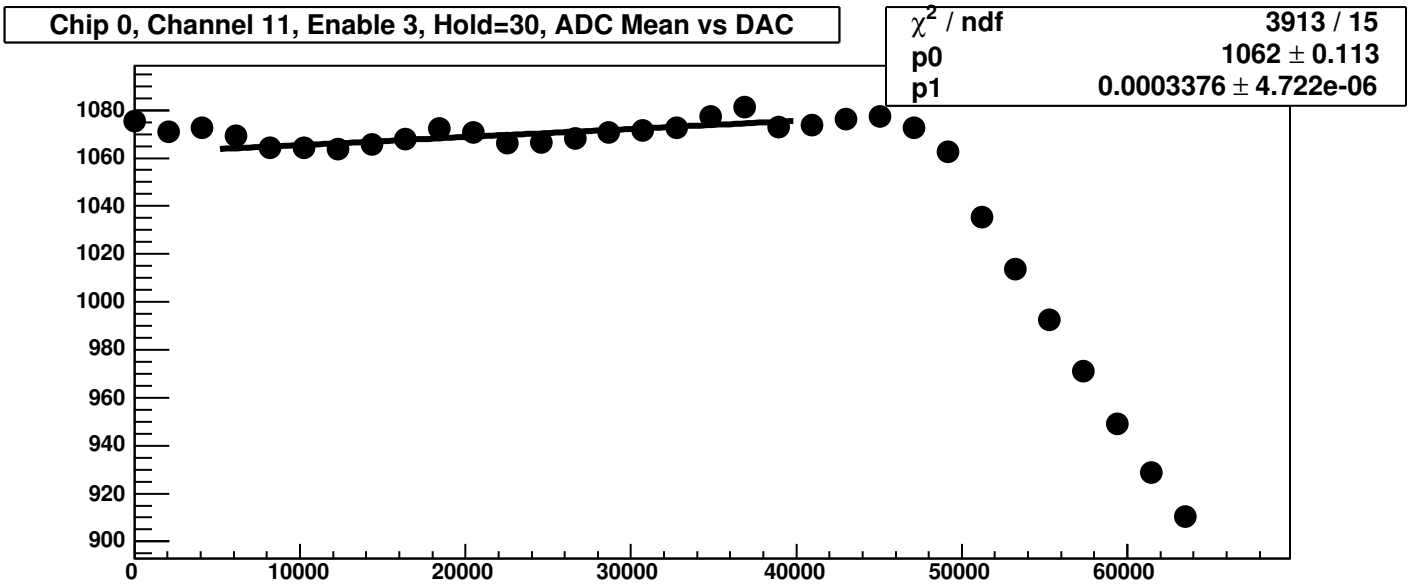
Chip 0, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

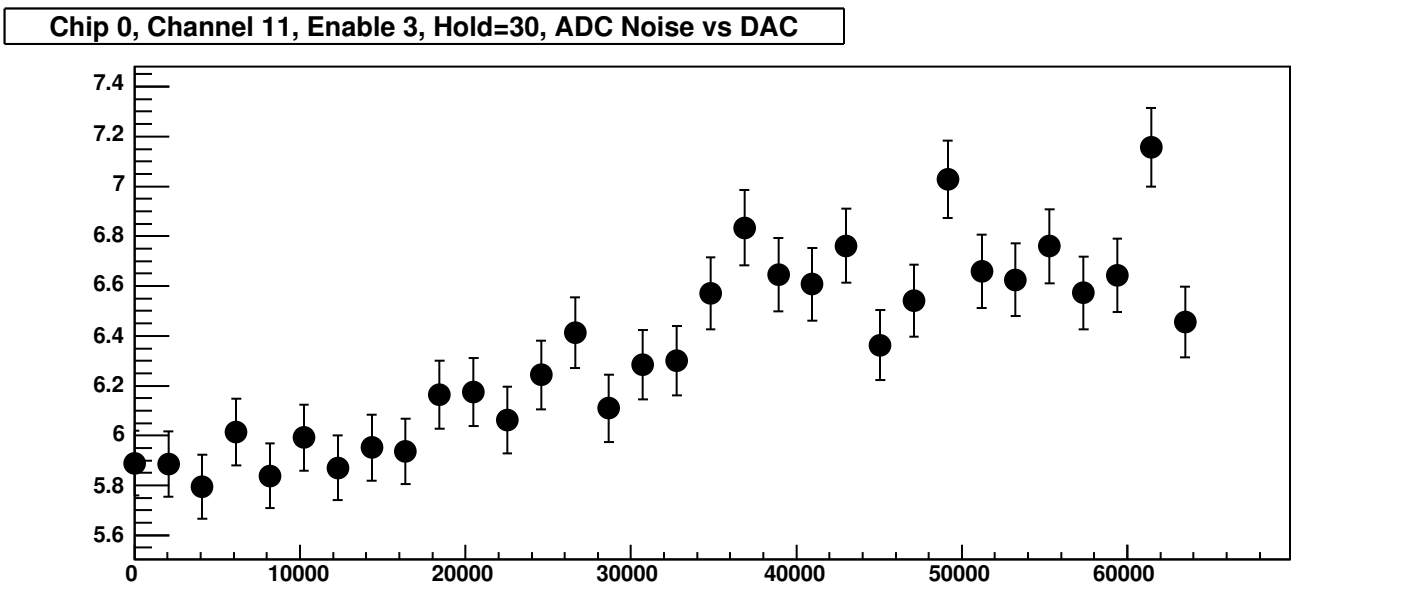


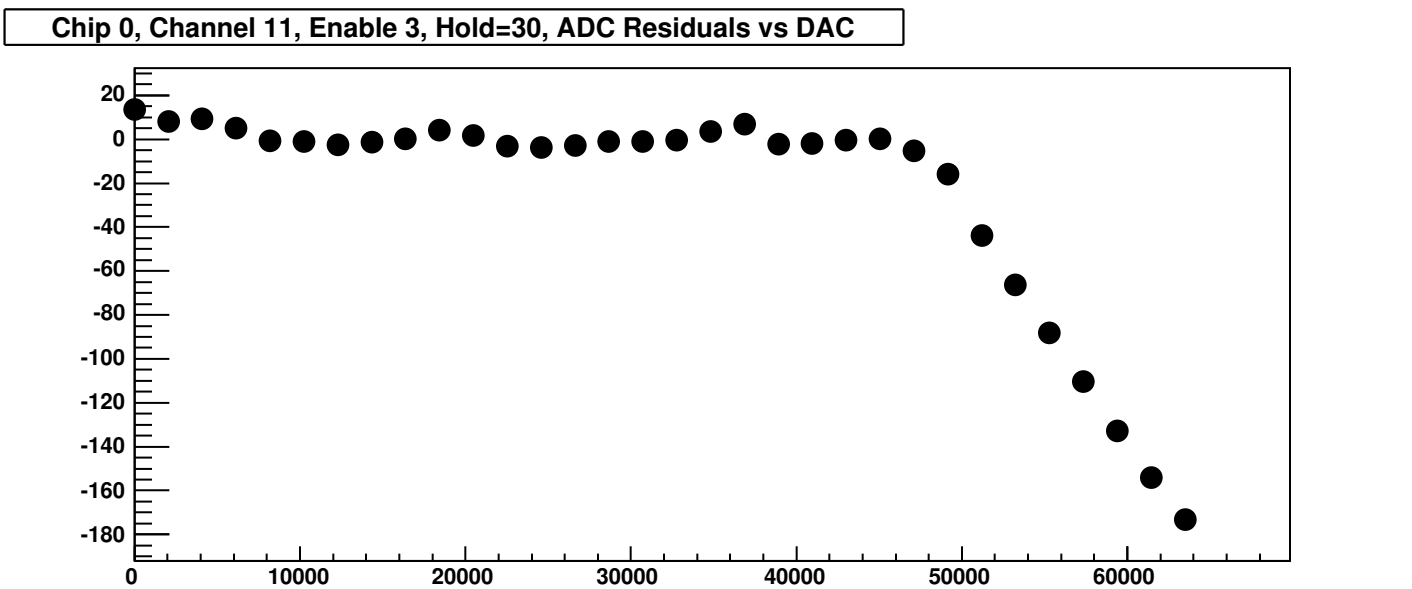

Chip 0, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3668 / 15
p0 1063 ± 0.1084
p1 -0.0005685 ± 4.433e-06
1100
1000
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000

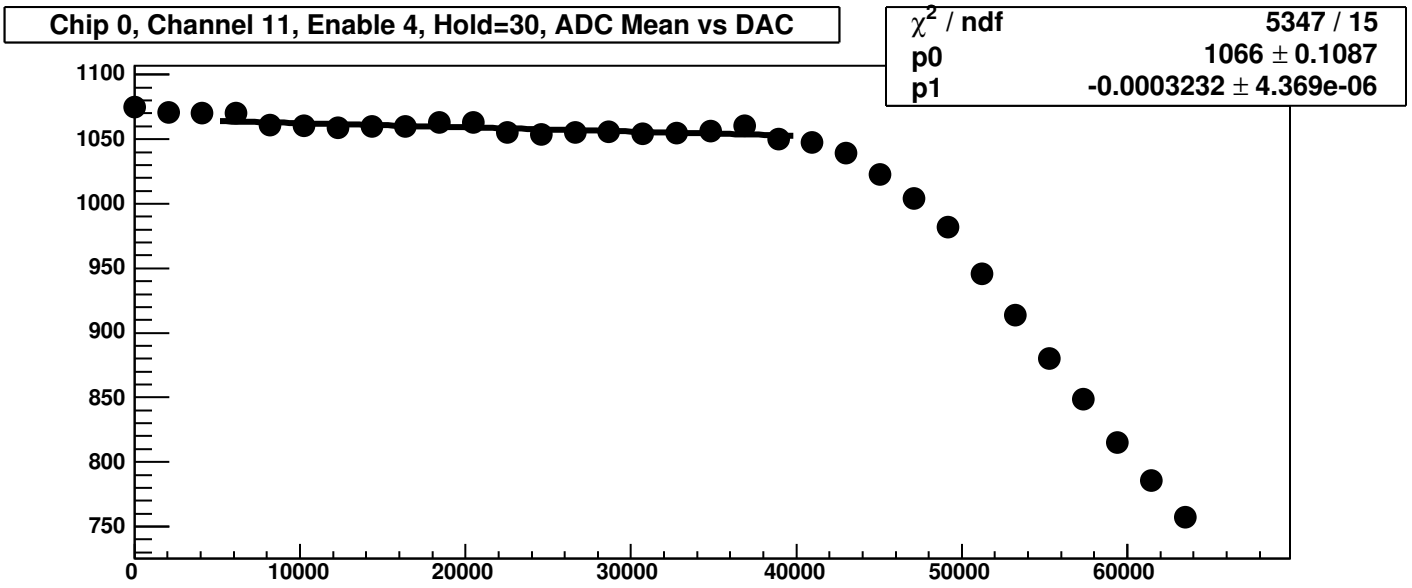

Chip 0, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

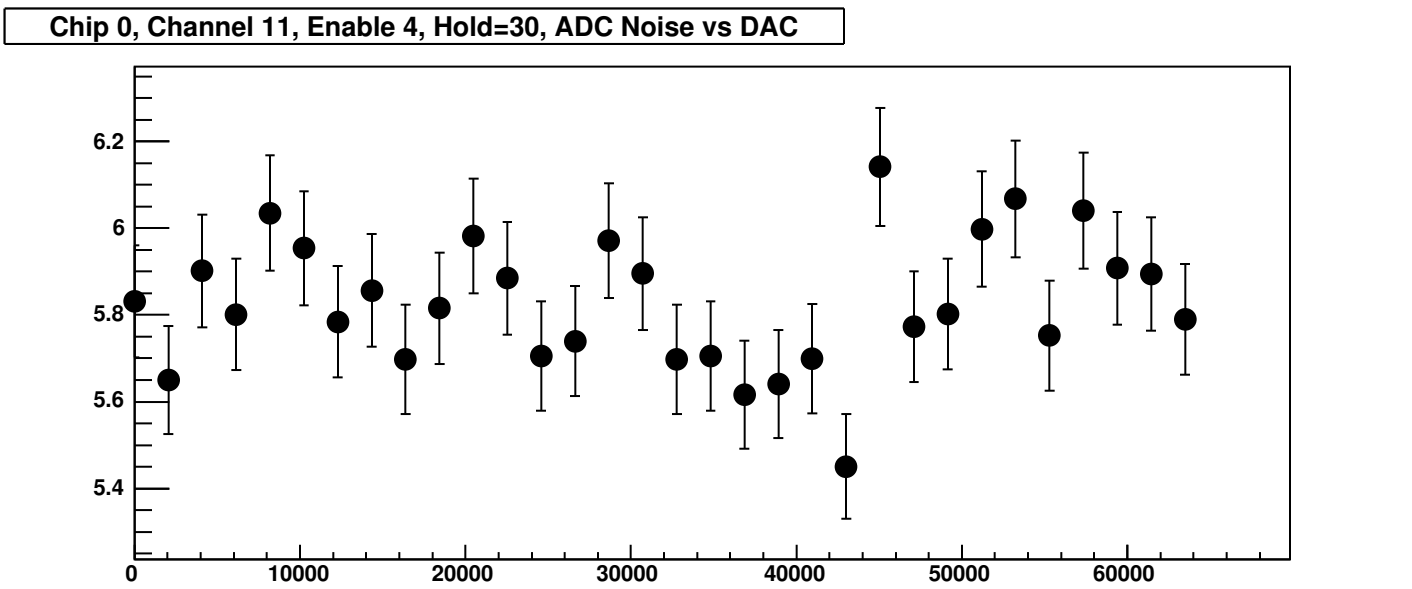

Chip 0, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

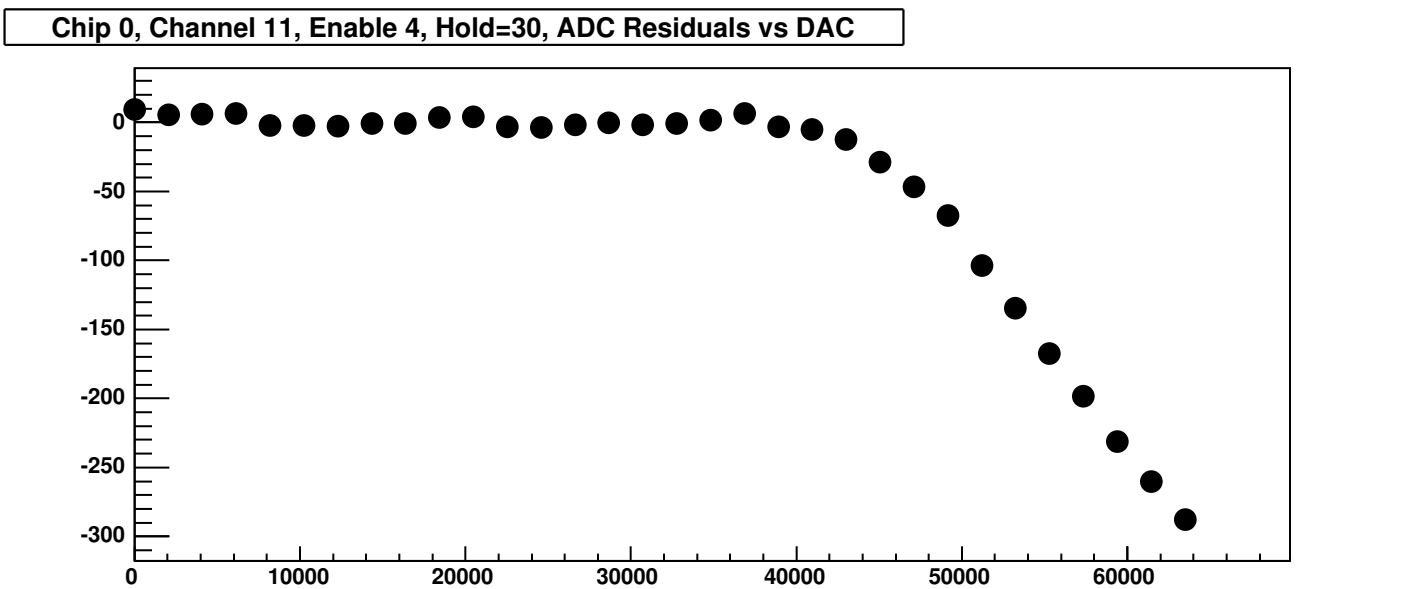

Chip 0, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3913 / 15
p0 1062 ± 0.113
p1 0.0003376 ± 4.722e-06
1080
1060
1040
1020
1000
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000

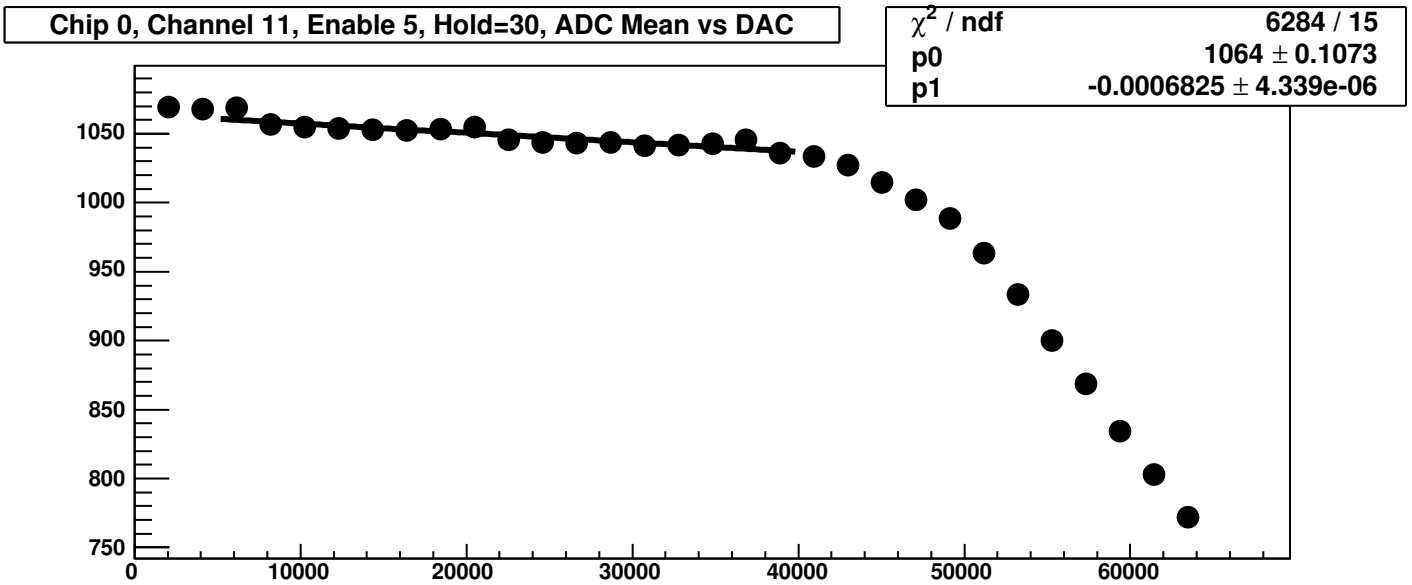

Chip 0, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

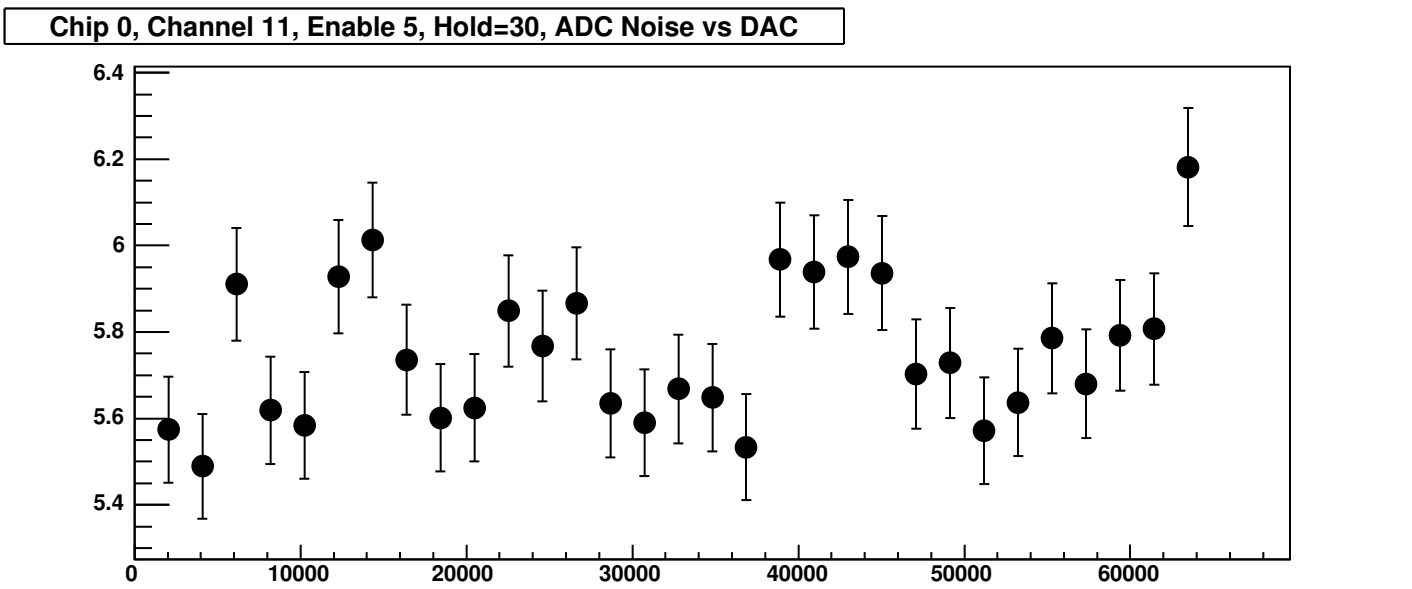

Chip 0, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

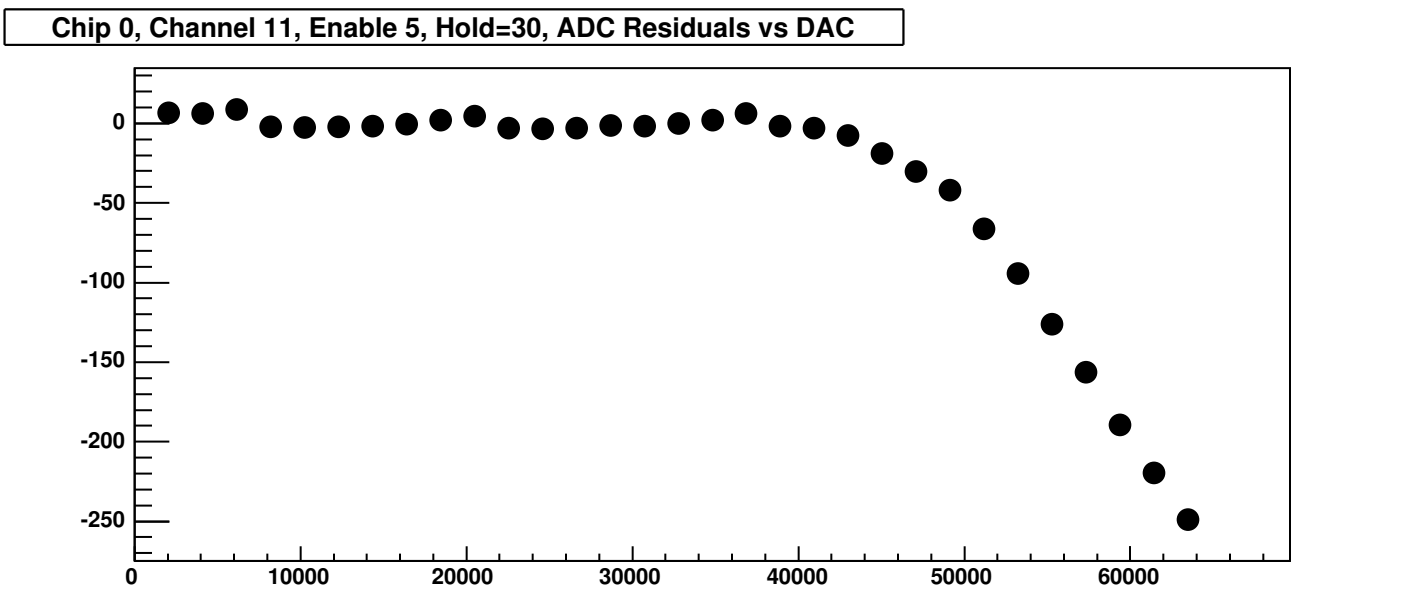

Chip 0, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5347 / 15
p0 1066 ± 0.1087
p1 -0.0003232 ± 4.369e-06
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

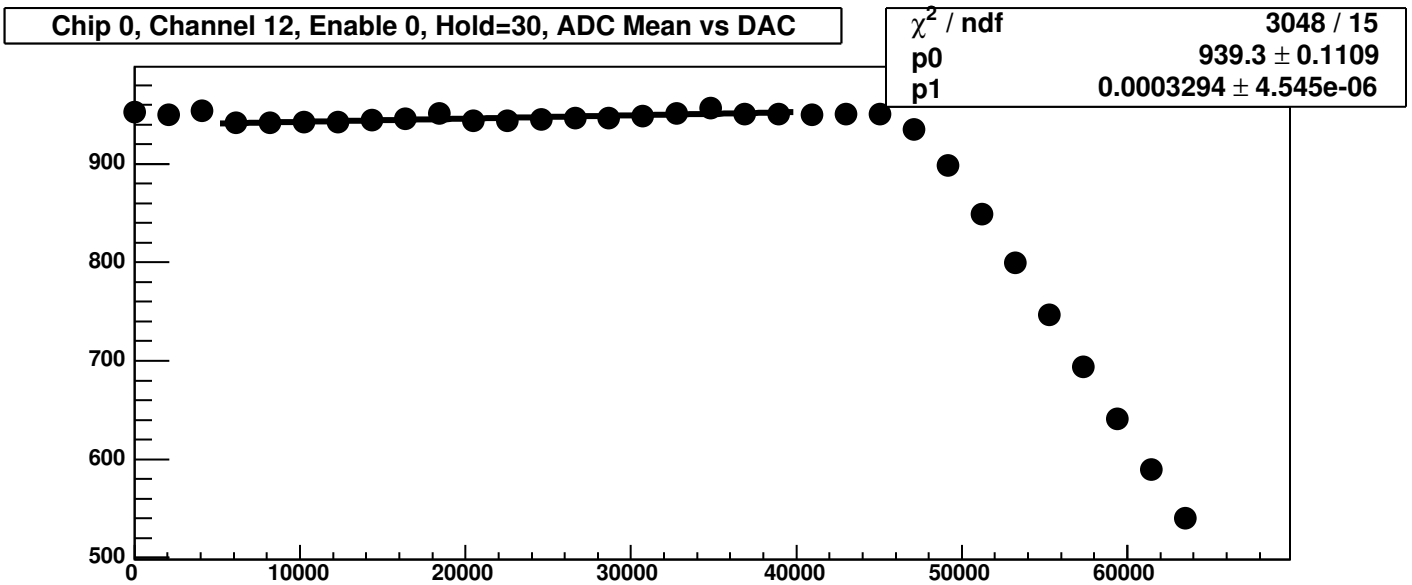

Chip 0, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

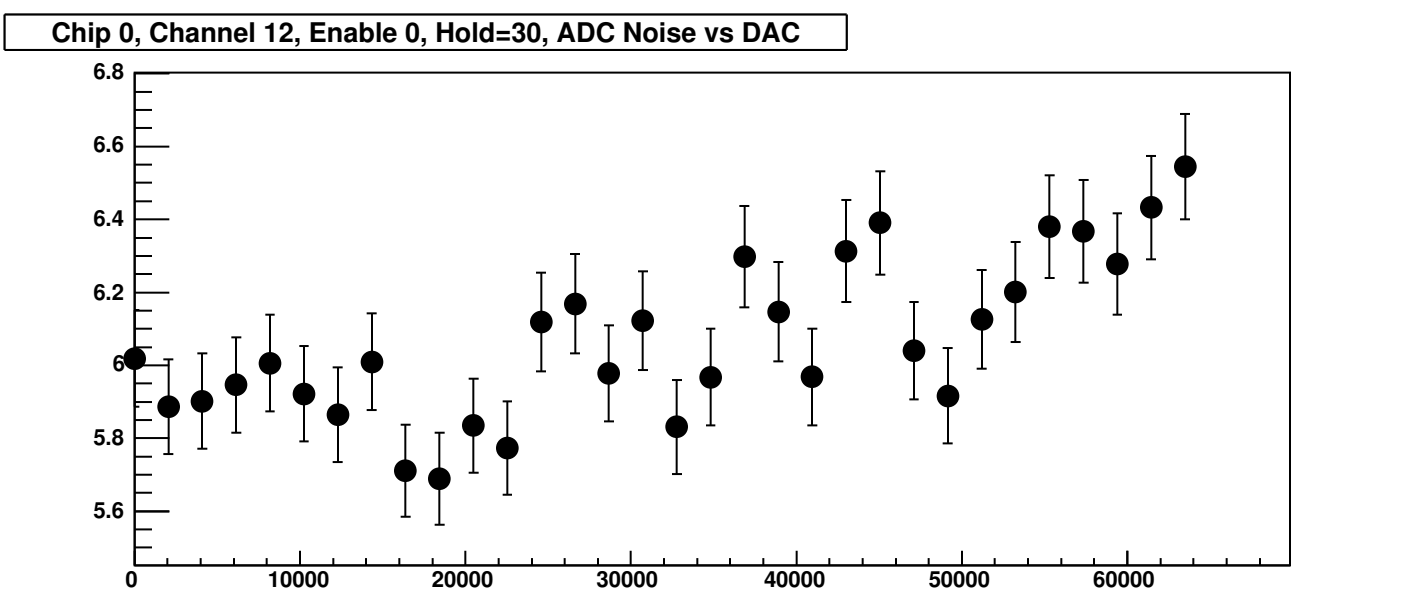

Chip 0, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

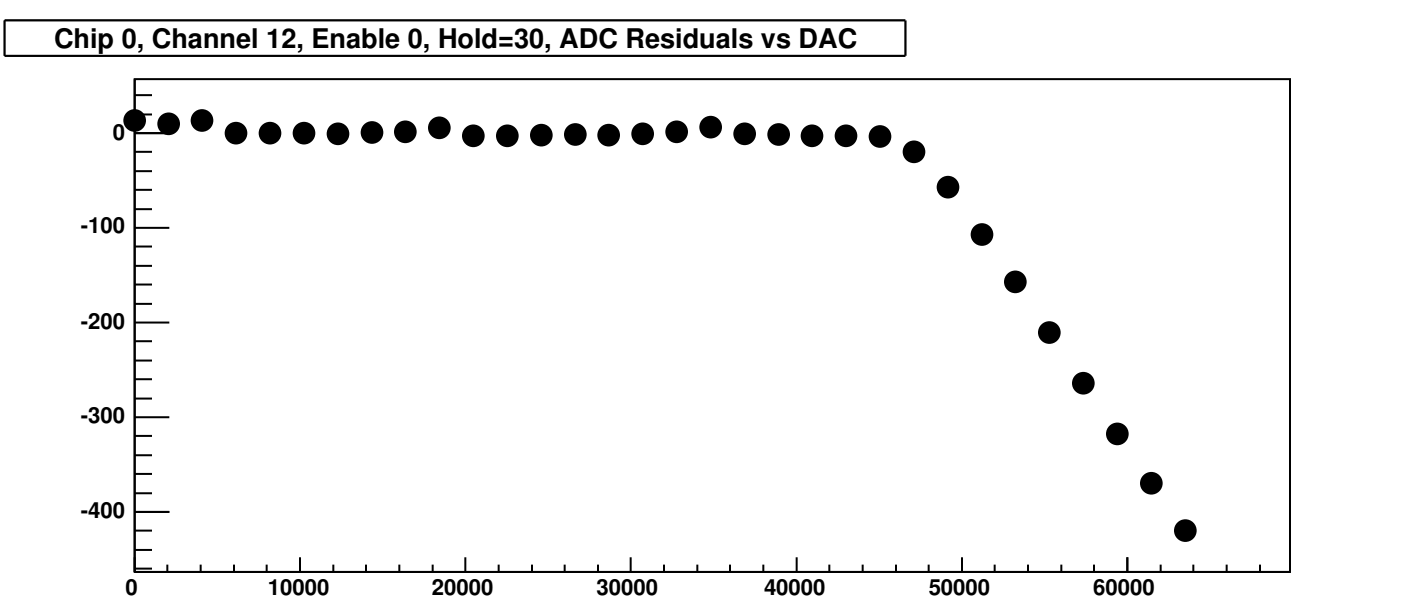

Chip 0, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6284 / 15
p0 1064 ± 0.1073
p1 -0.0006825 ± 4.339e-06
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

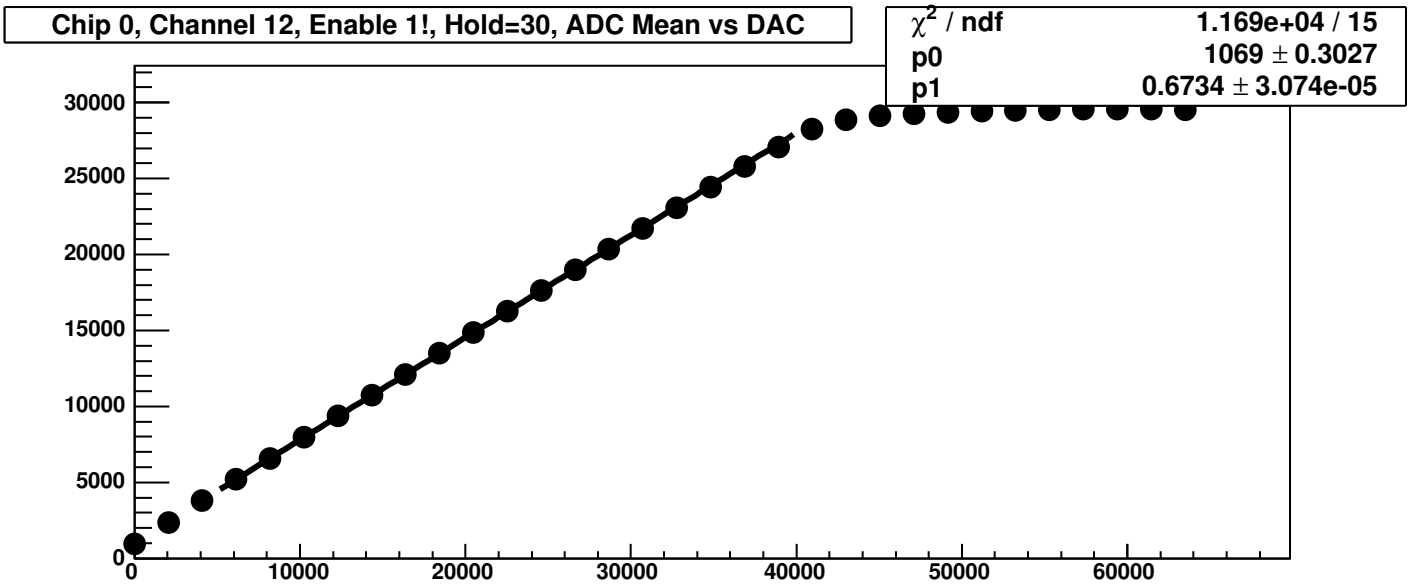

Chip 0, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

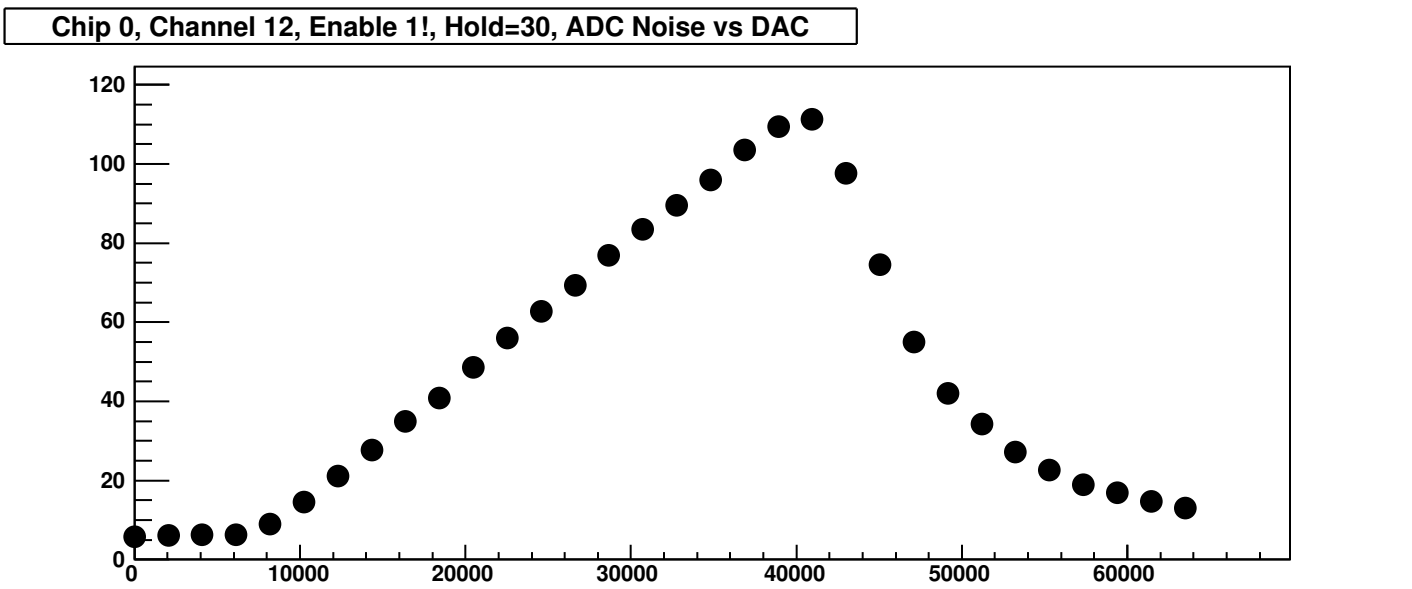

Chip 0, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

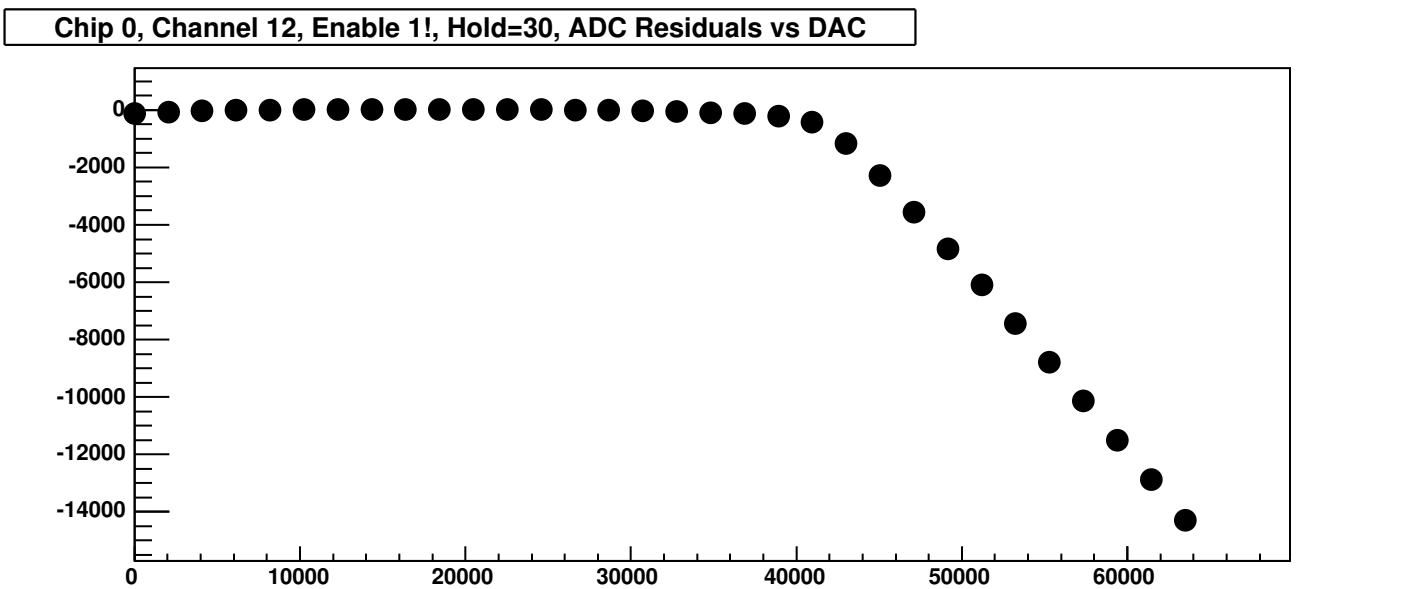

Chip 0, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3048 / 15
p0 939.3 ± 0.1109
p1 0.0003294 ± 4.545e-06
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000

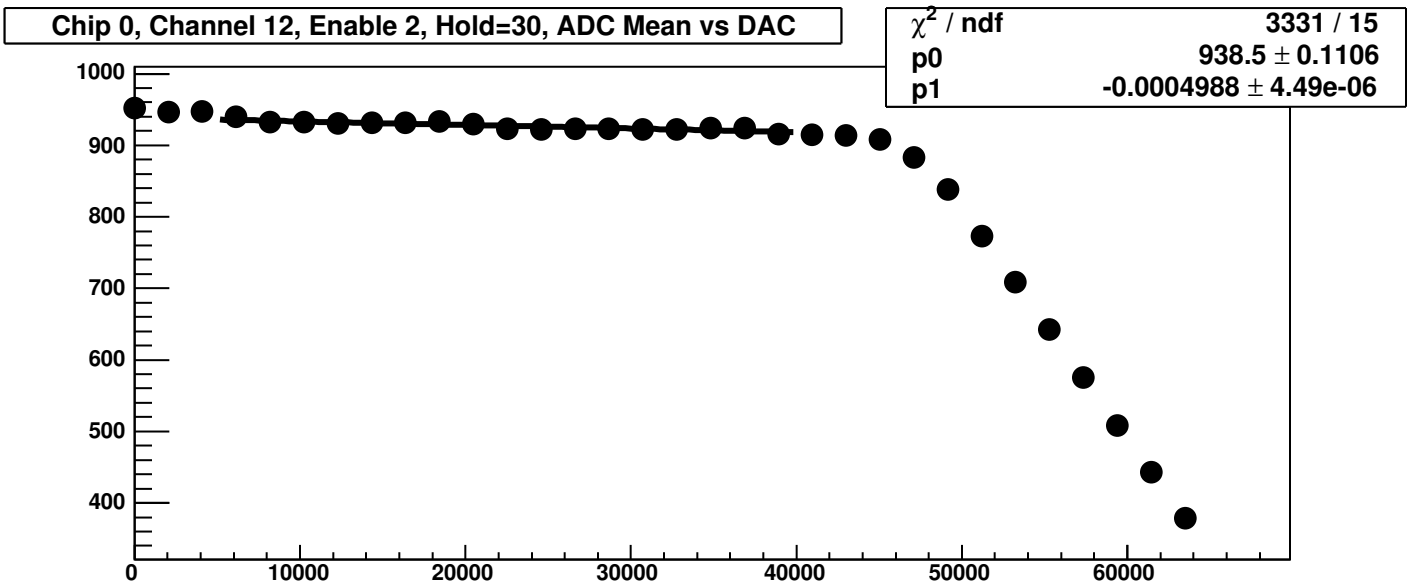

Chip 0, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

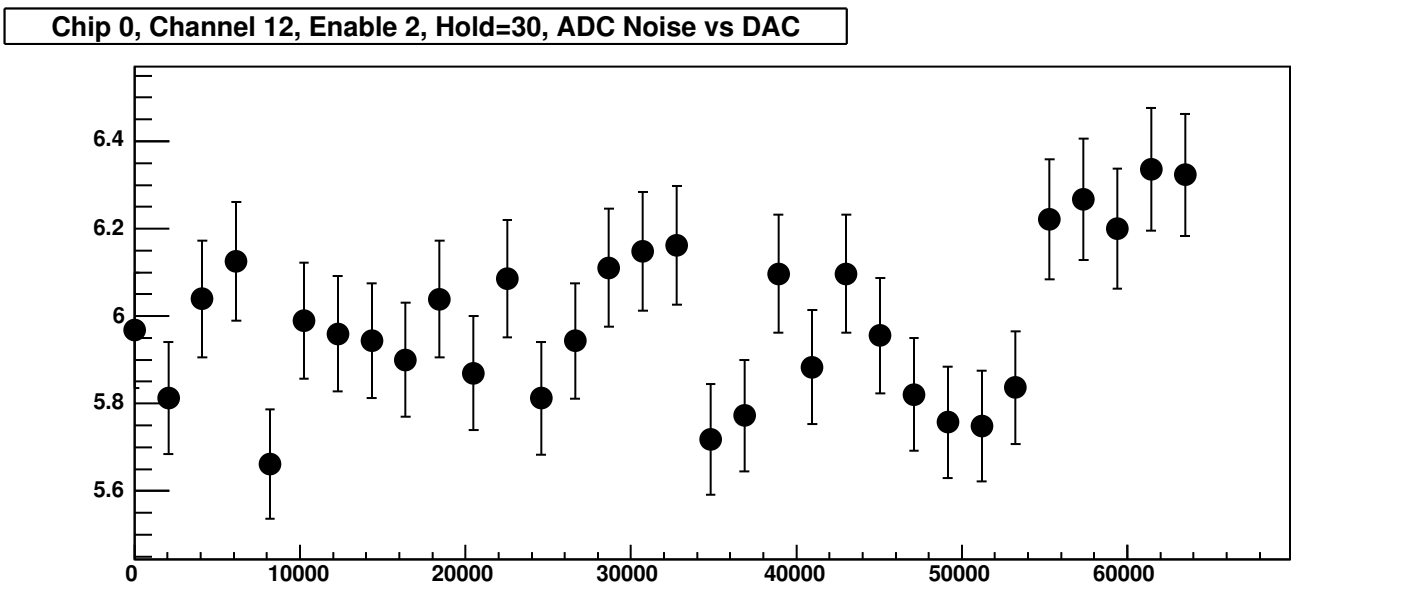

Chip 0, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

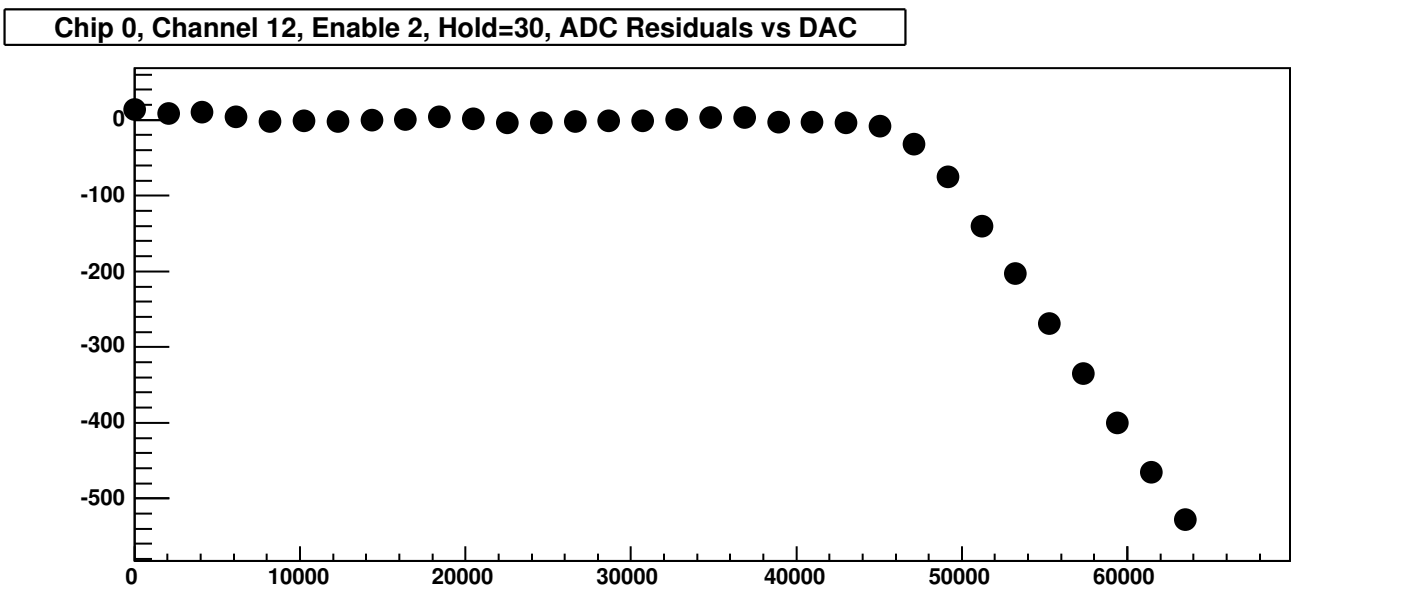

Chip 0, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.169e+04 / 15
p0 1069 ± 0.3027
p1 0.6734 ± 3.074e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

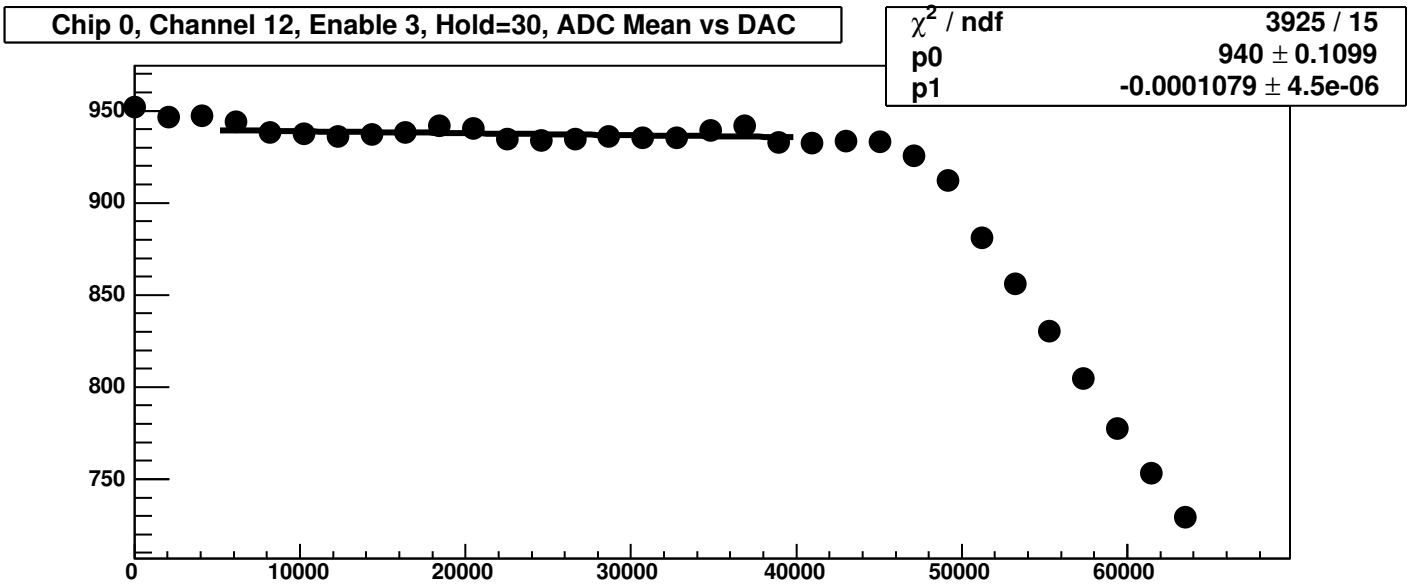

Chip 0, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

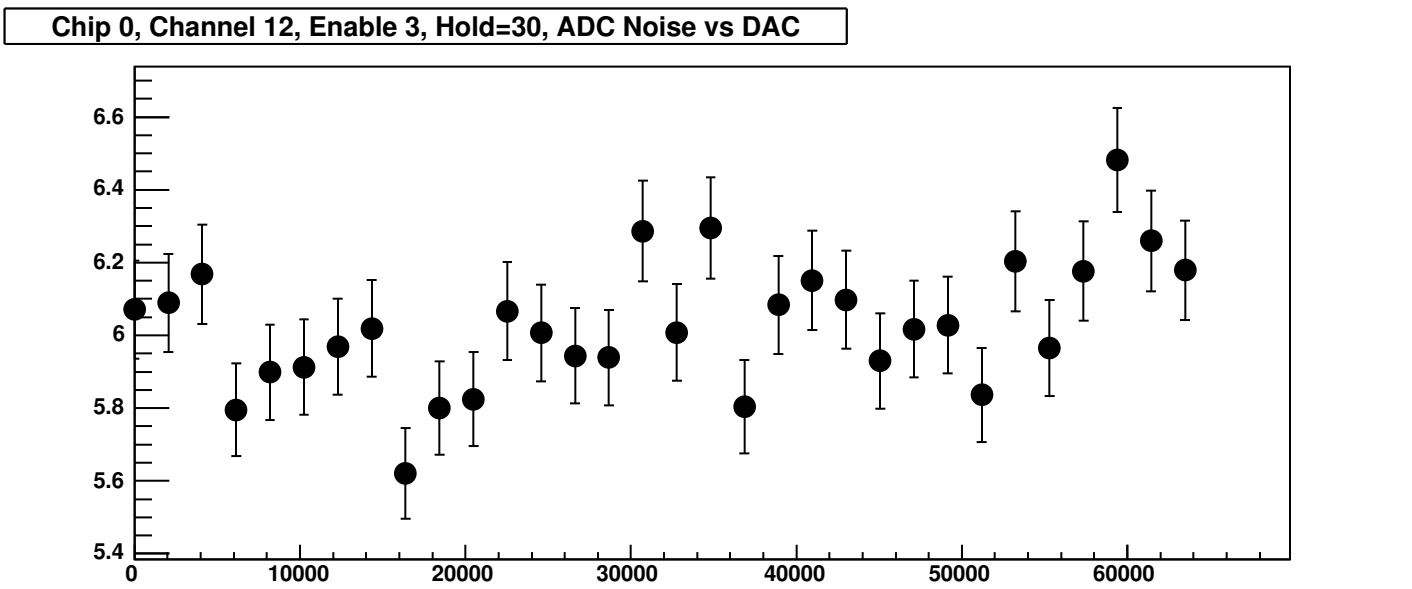

Chip 0, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

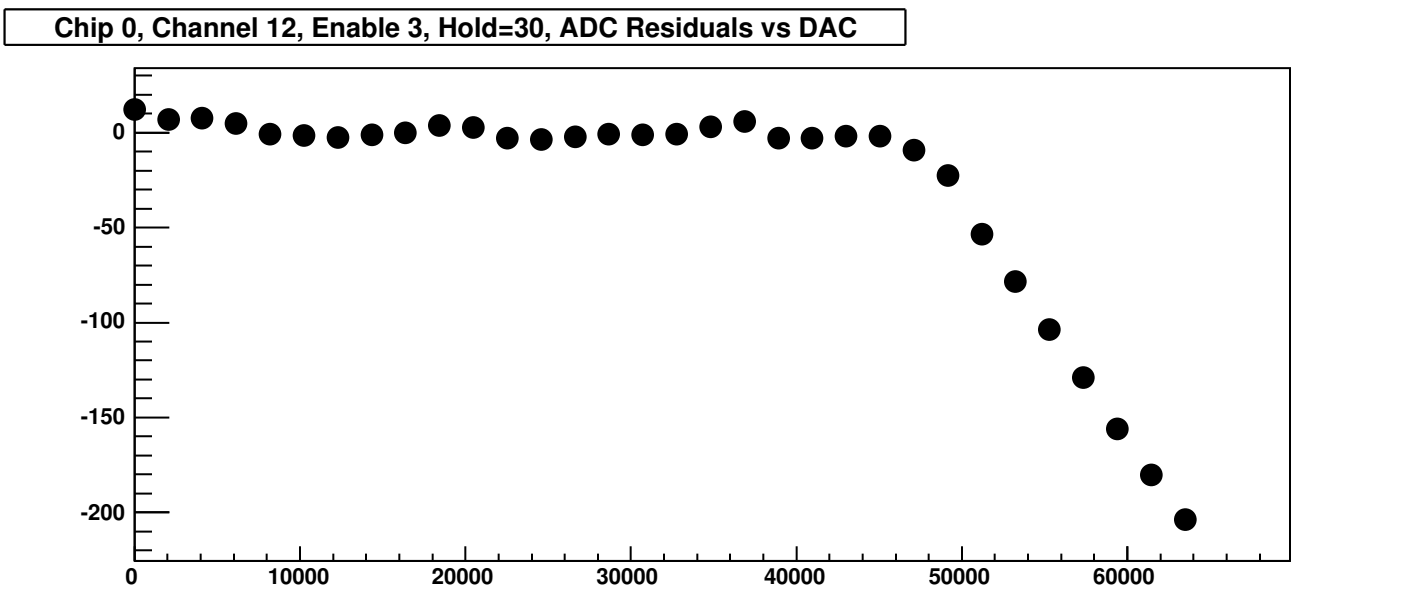

Chip 0, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3331 / 15
p0 938.5 ± 0.1106
p1 -0.0004988 ± 4.49e-06
1000
900
800
700
600
500
400
0
10000
20000
30000
40000
50000
60000

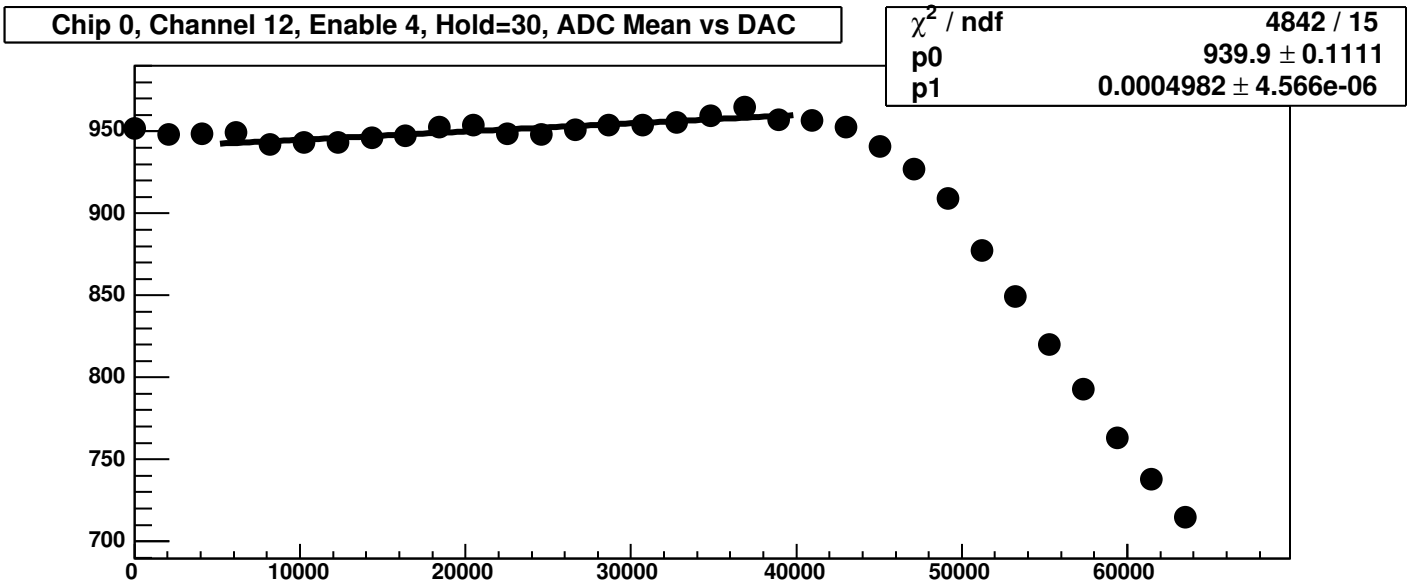

Chip 0, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

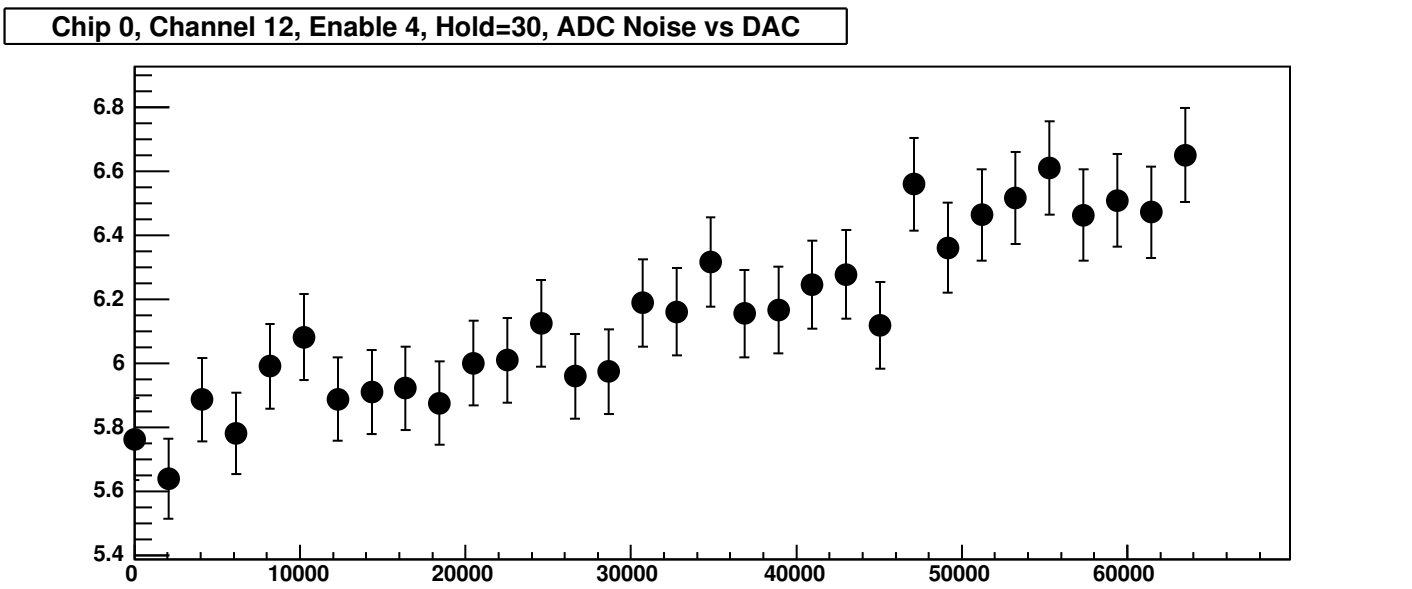

Chip 0, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

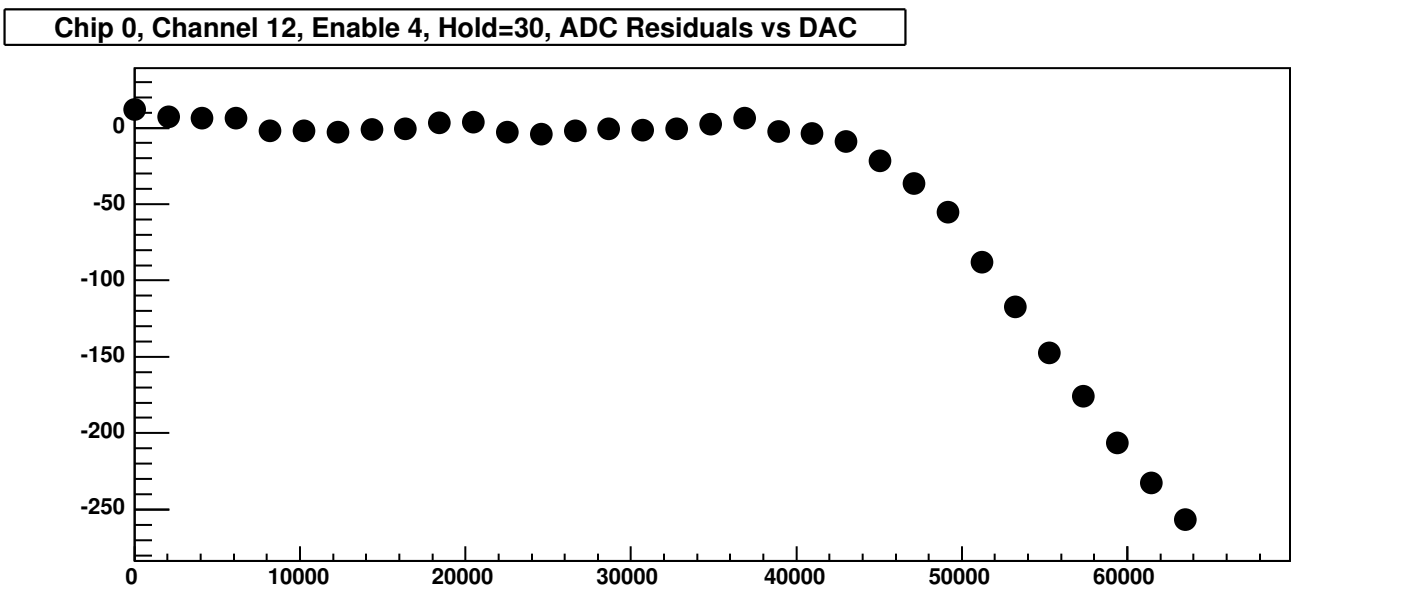

Chip 0, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3925 / 15
p0 940 ± 0.1099
p1 -0.0001079 ± 4.5e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

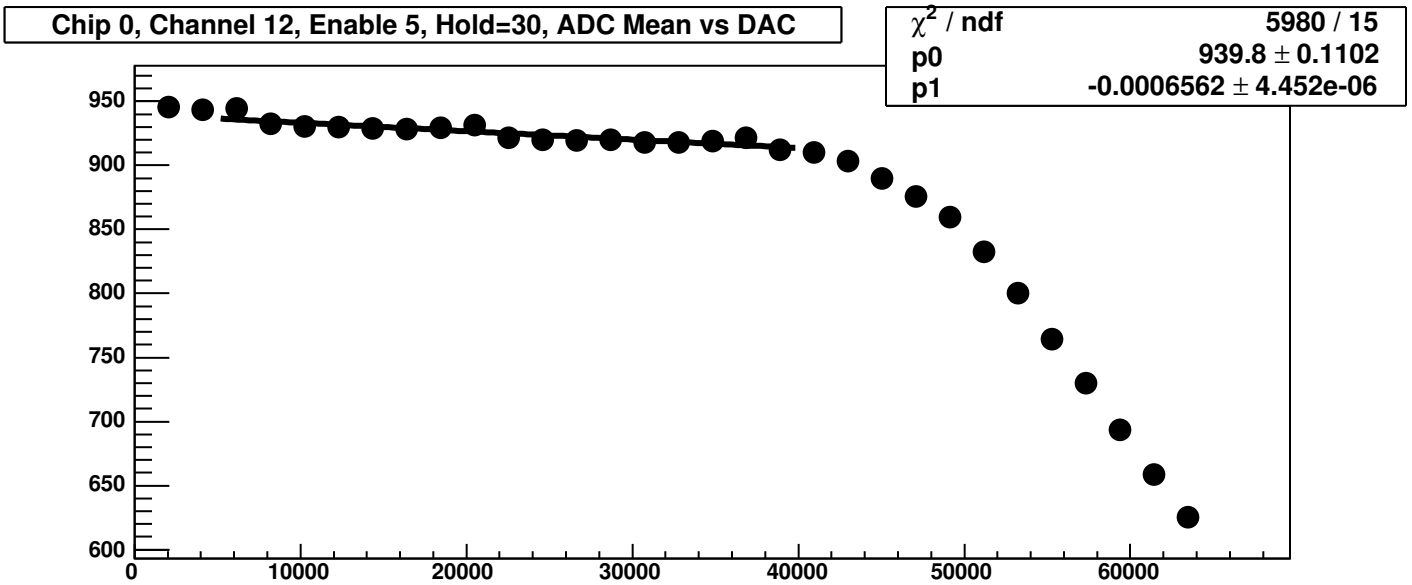

Chip 0, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

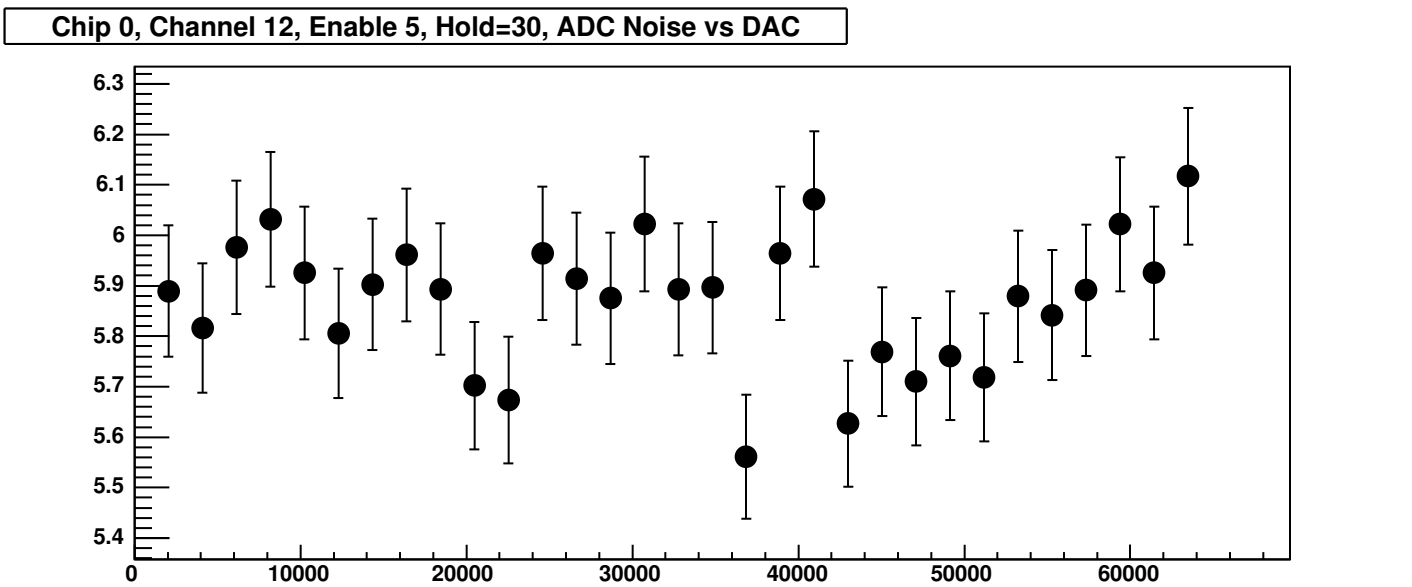

Chip 0, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

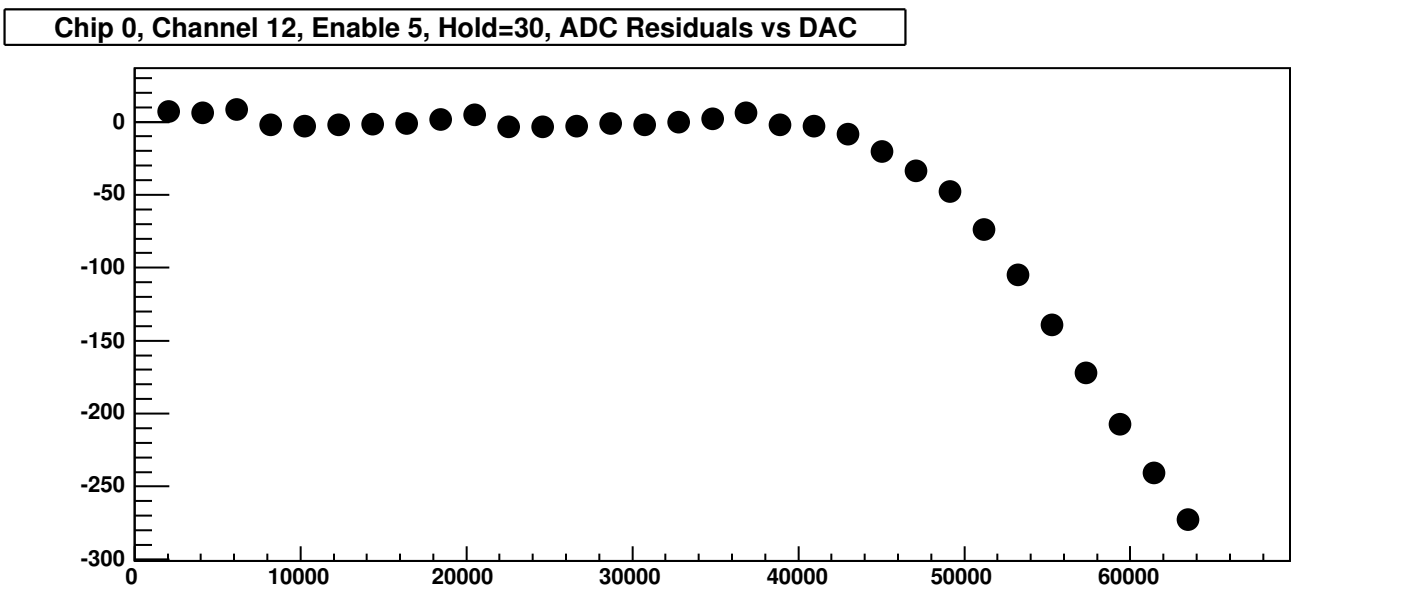

Chip 0, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4842 / 15
p0 939.9 ± 0.1111
p1 0.0004982 ± 4.566e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

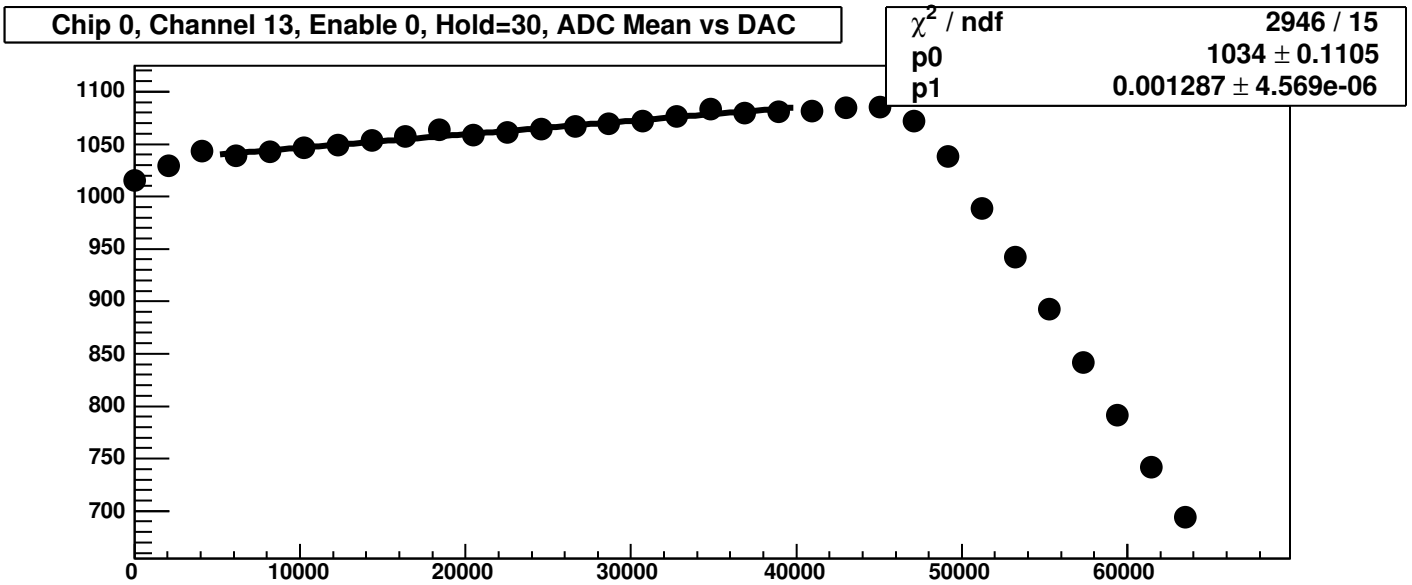

Chip 0, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

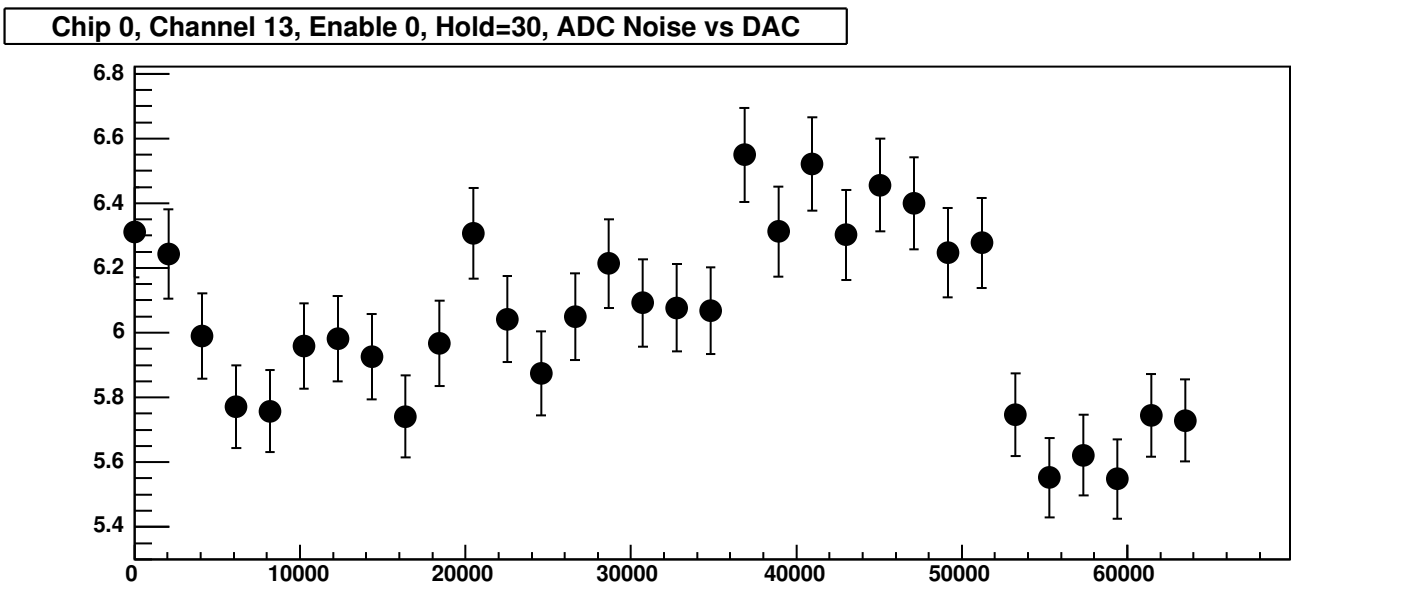

Chip 0, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

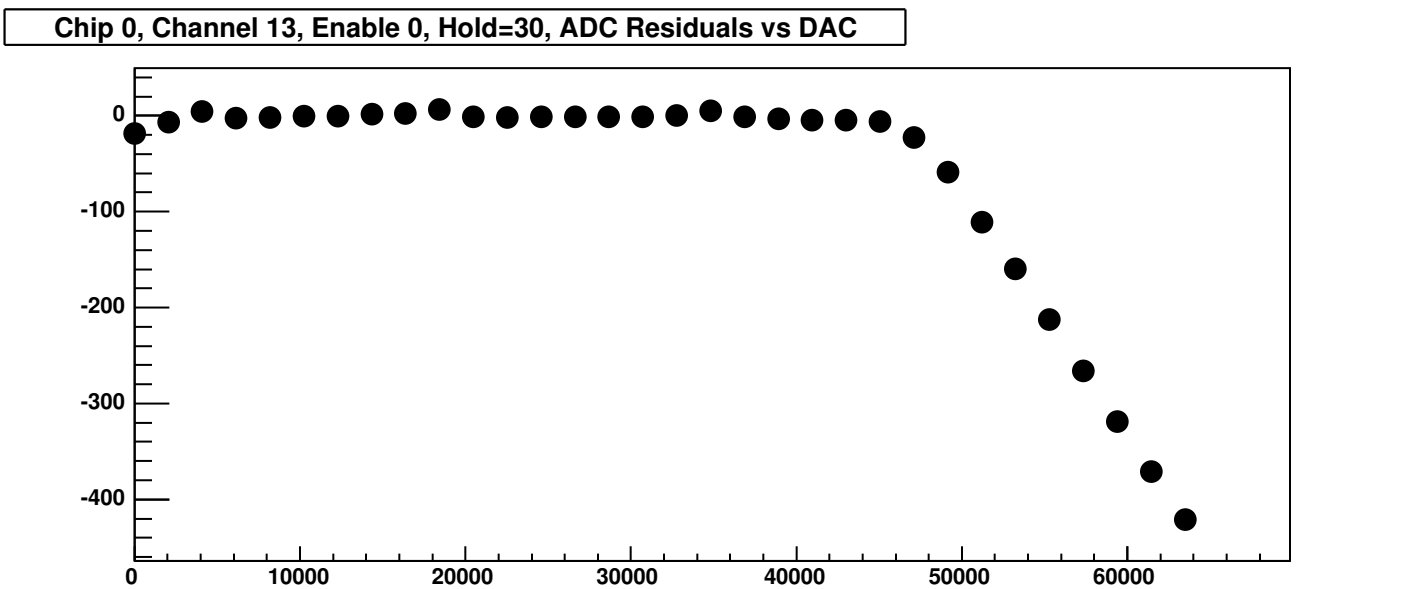

Chip 0, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5980 / 15
p0 939.8 ± 0.1102
p1 -0.0006562 ± 4.452e-06
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

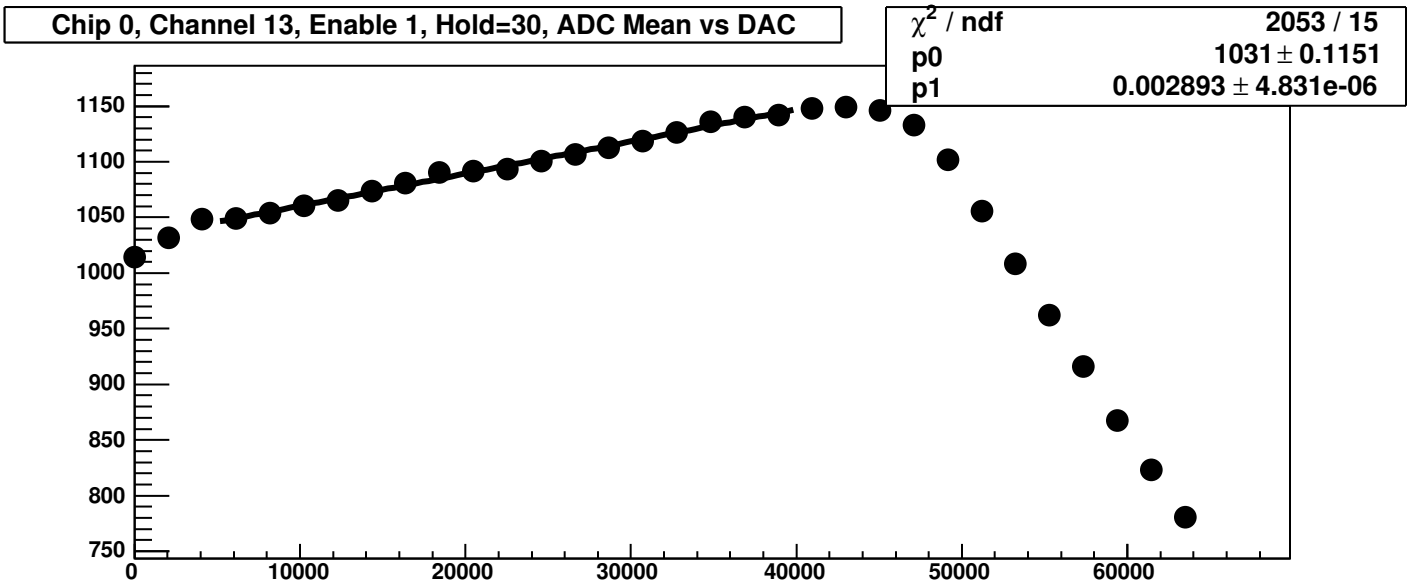

Chip 0, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

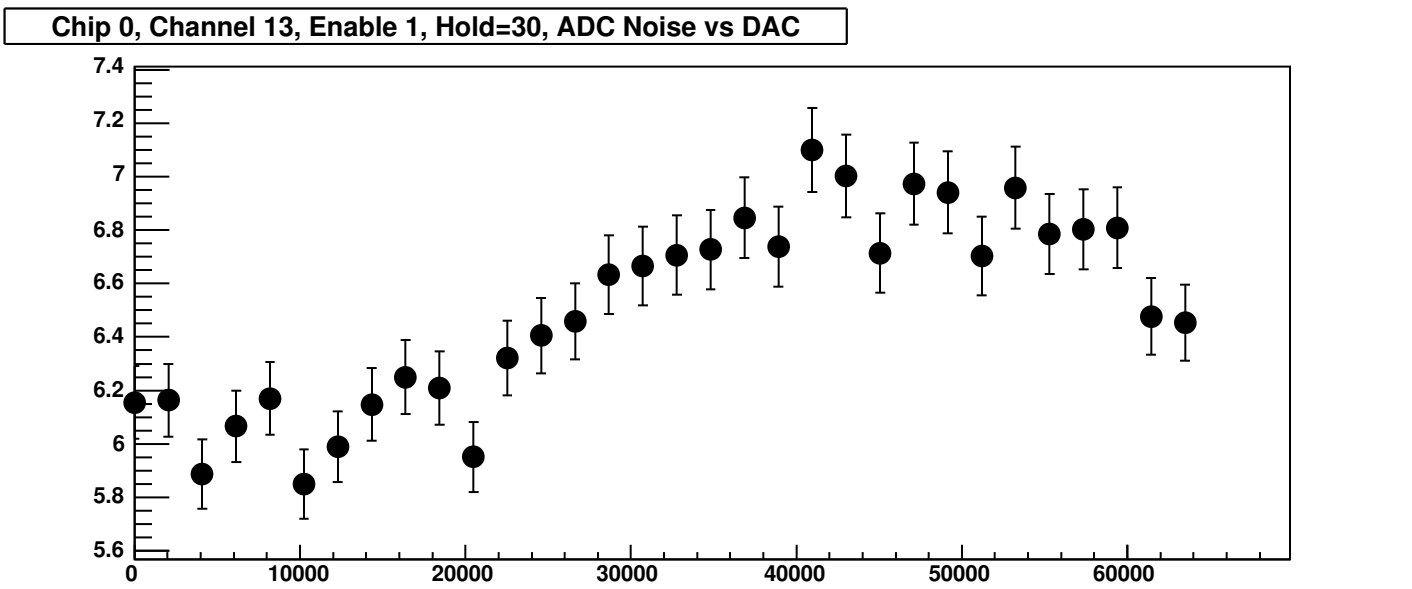

Chip 0, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

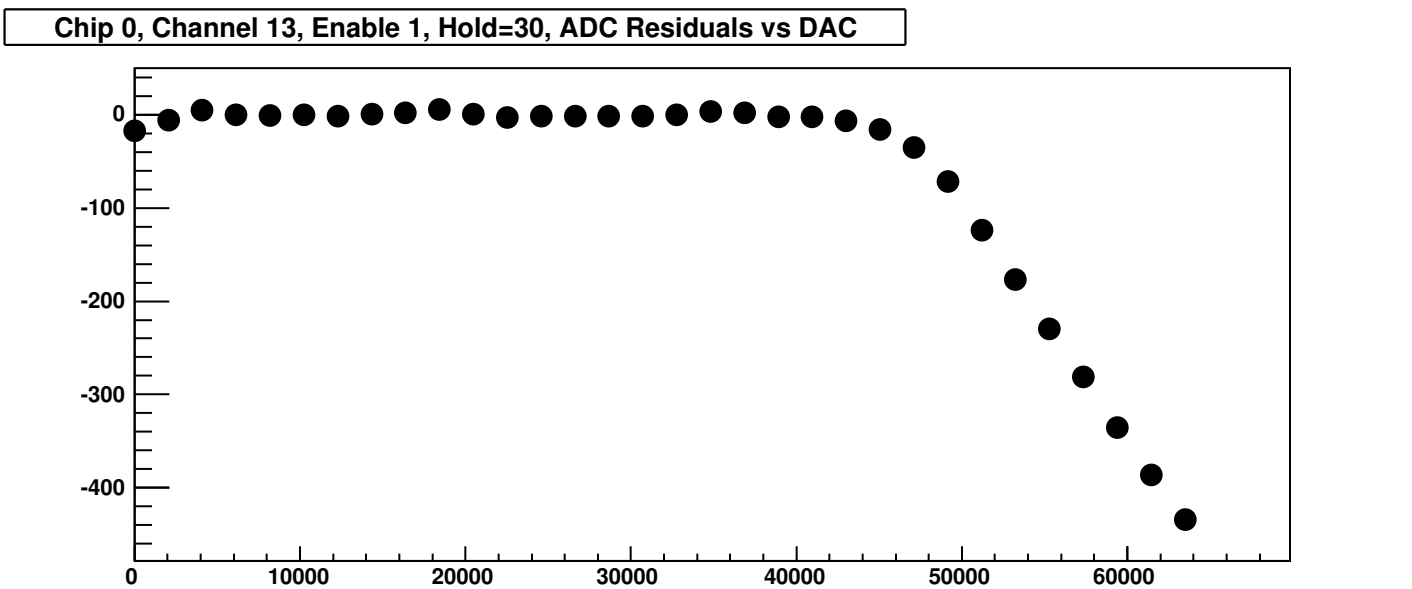

Chip 0, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2946 / 15
p0 1034 ± 0.1105
p1 0.001287 ± 4.569e-06
1100
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

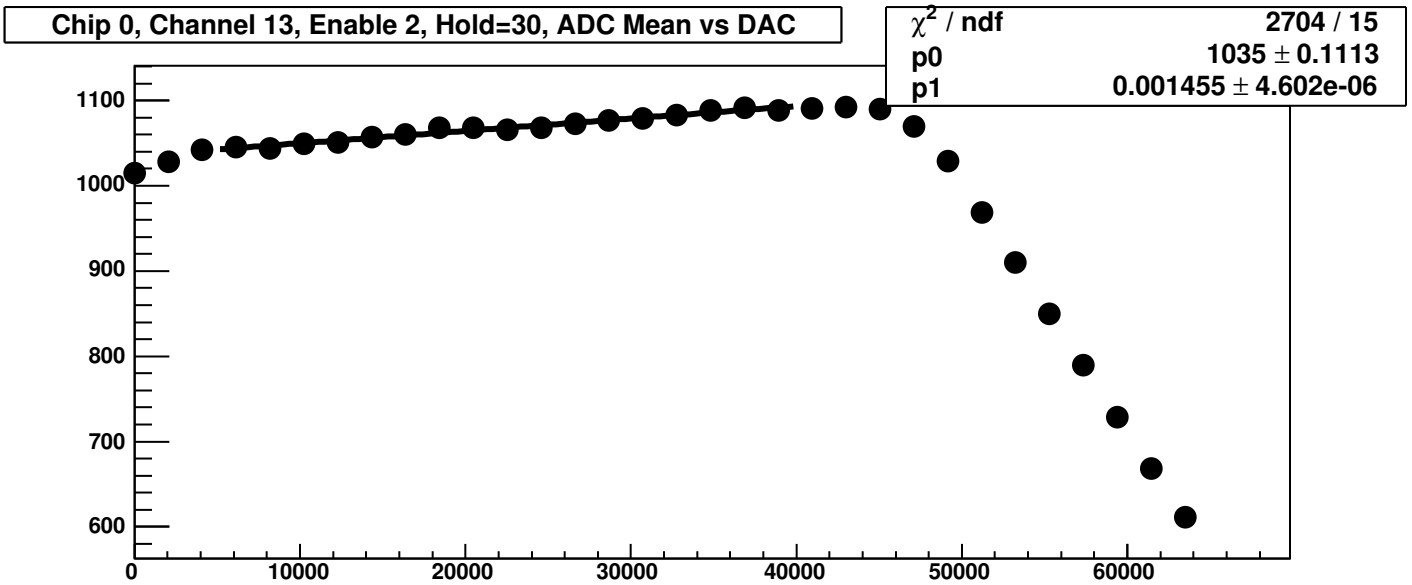

Chip 0, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

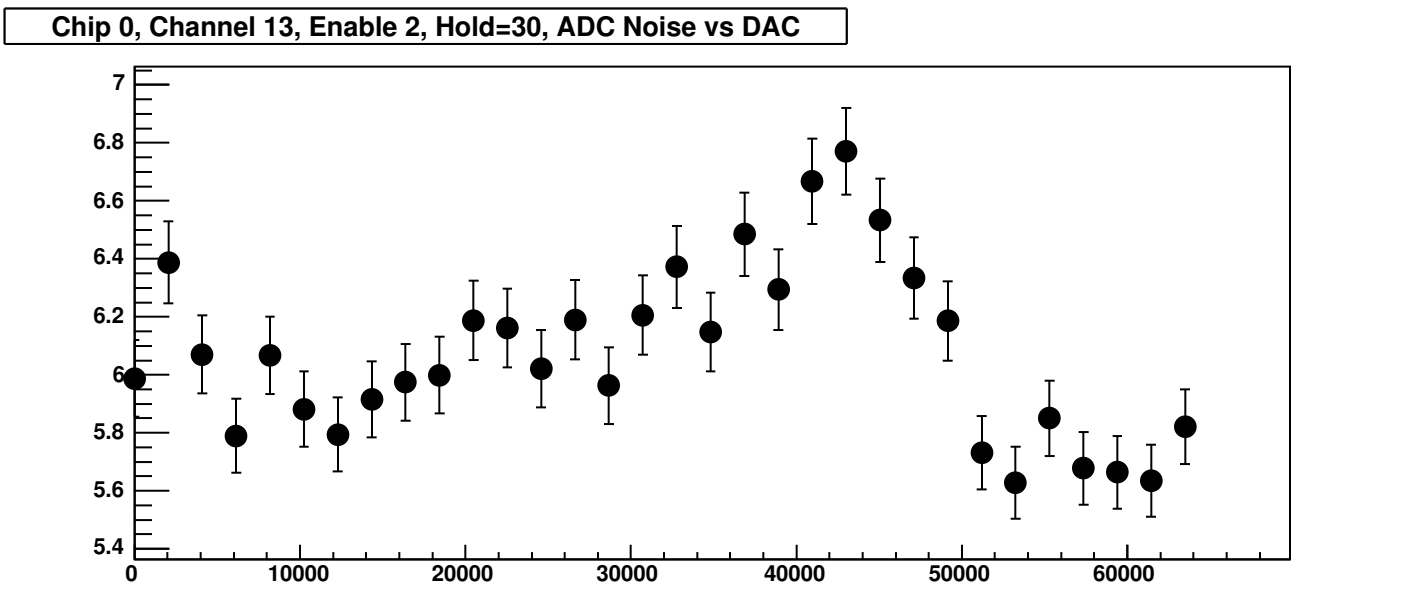

Chip 0, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

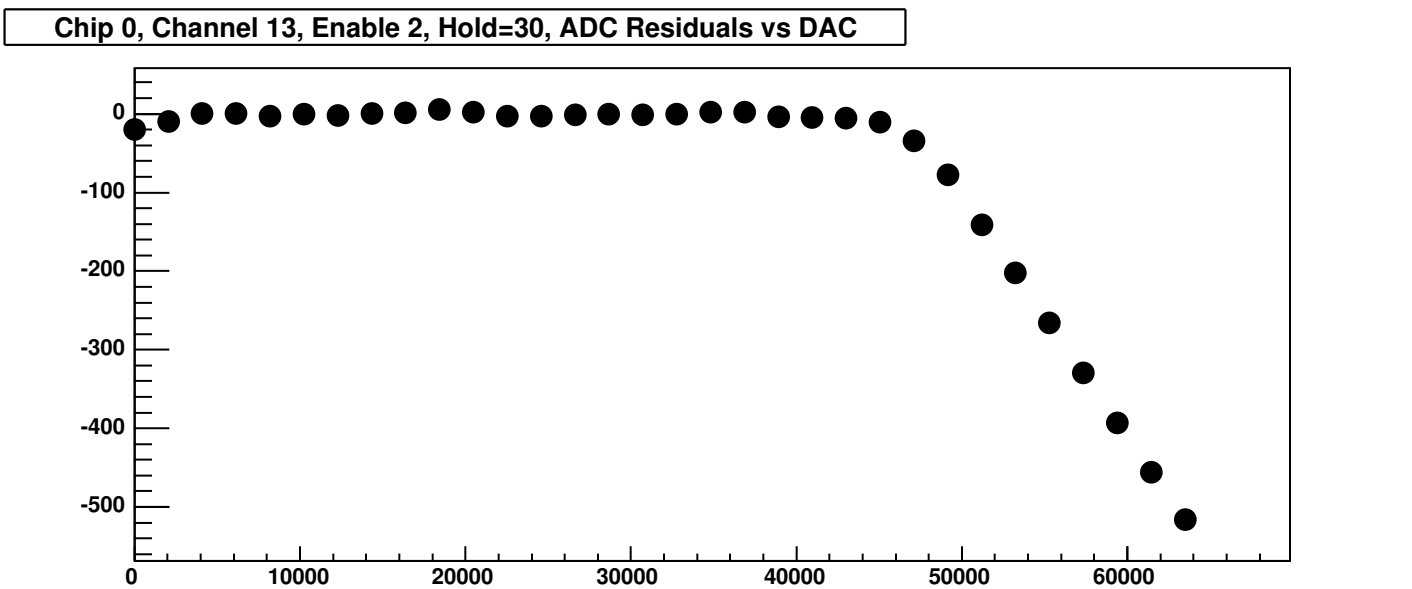

Chip 0, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2053 / 15
p0 1031 ± 0.1151
p1 0.002893 ± 4.831e-06
1150
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

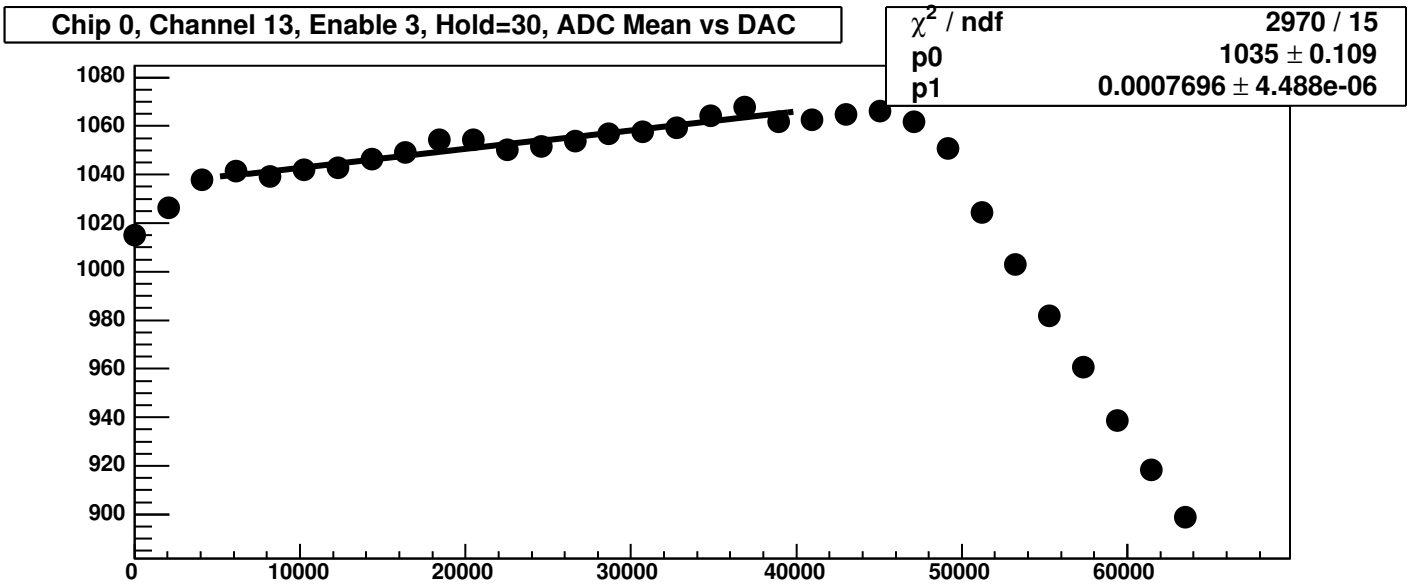

Chip 0, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

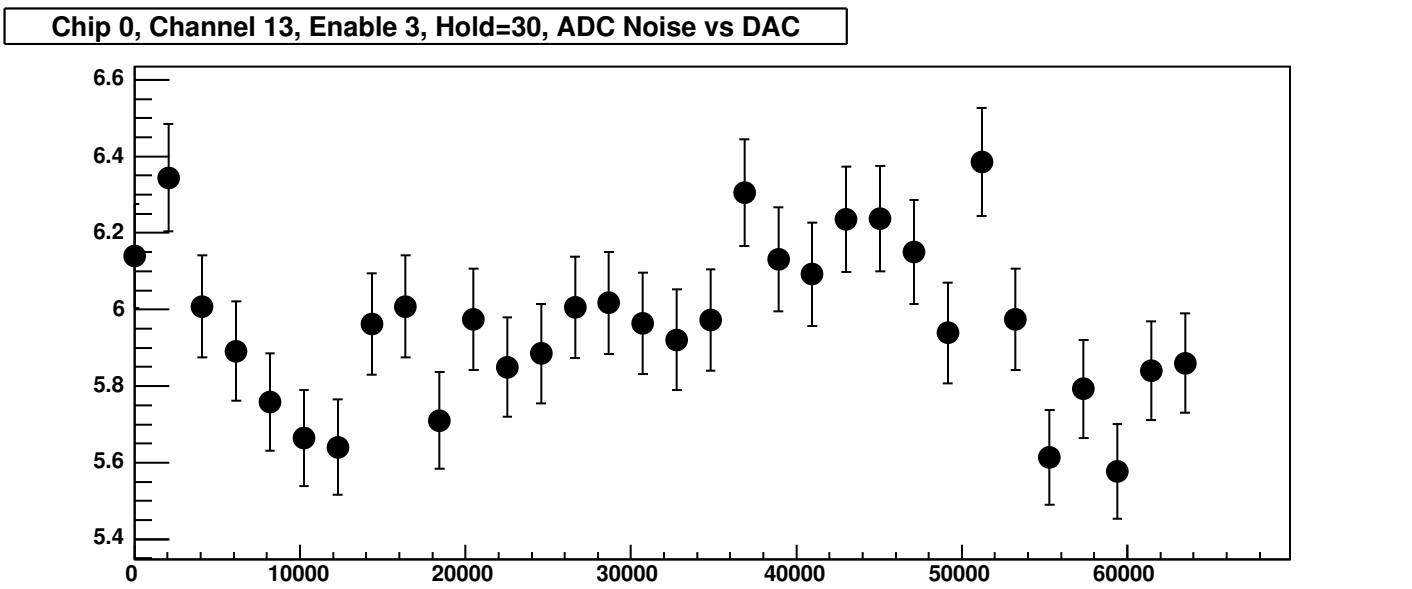

Chip 0, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

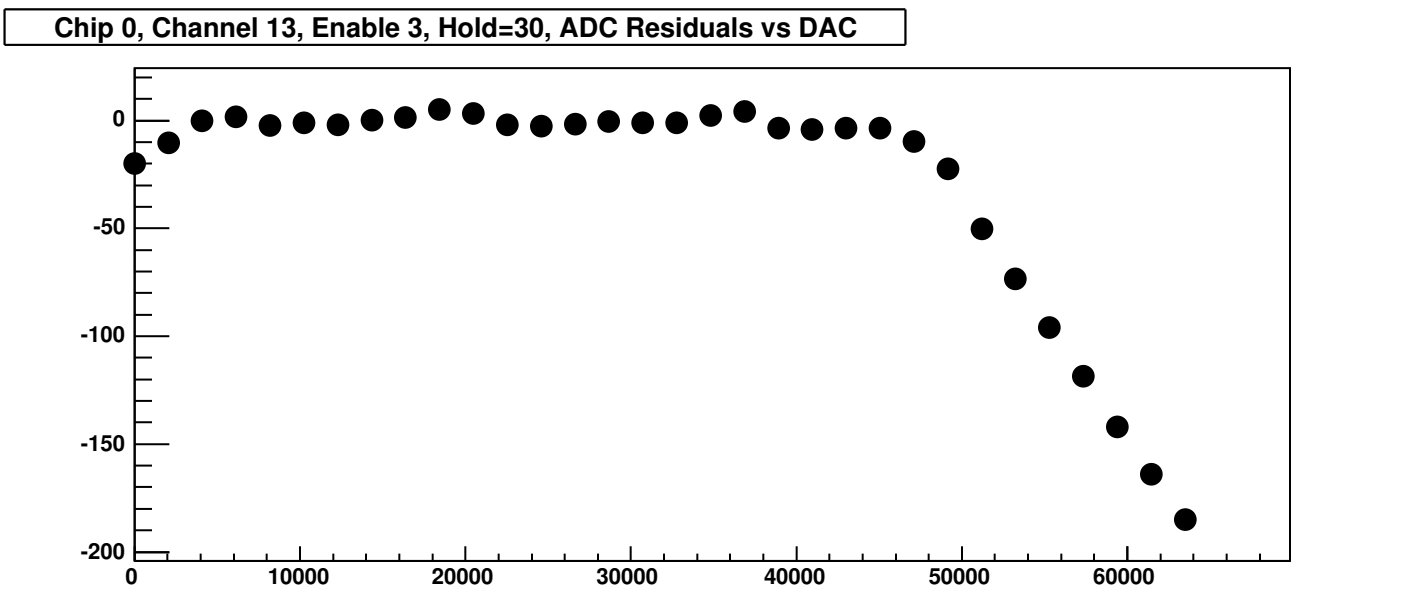

Chip 0, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2704 / 15
p0 1035 ± 0.1113
p1 0.001455 ± 4.602e-06
1100
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000

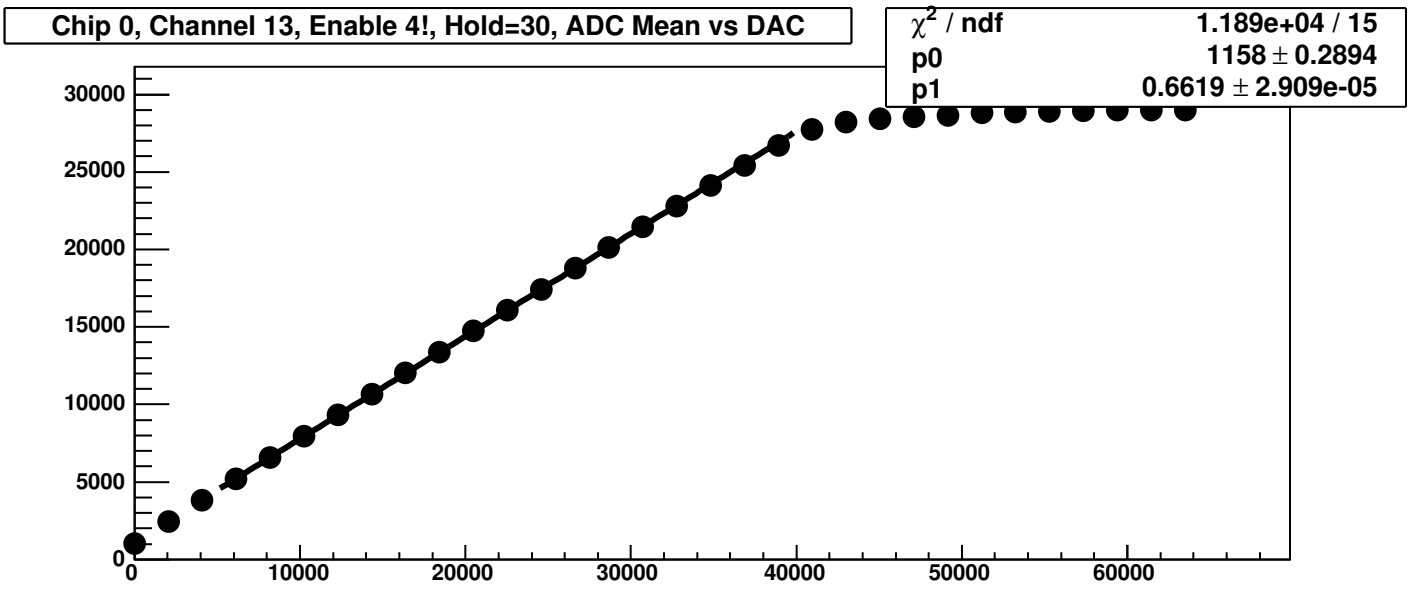

Chip 0, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

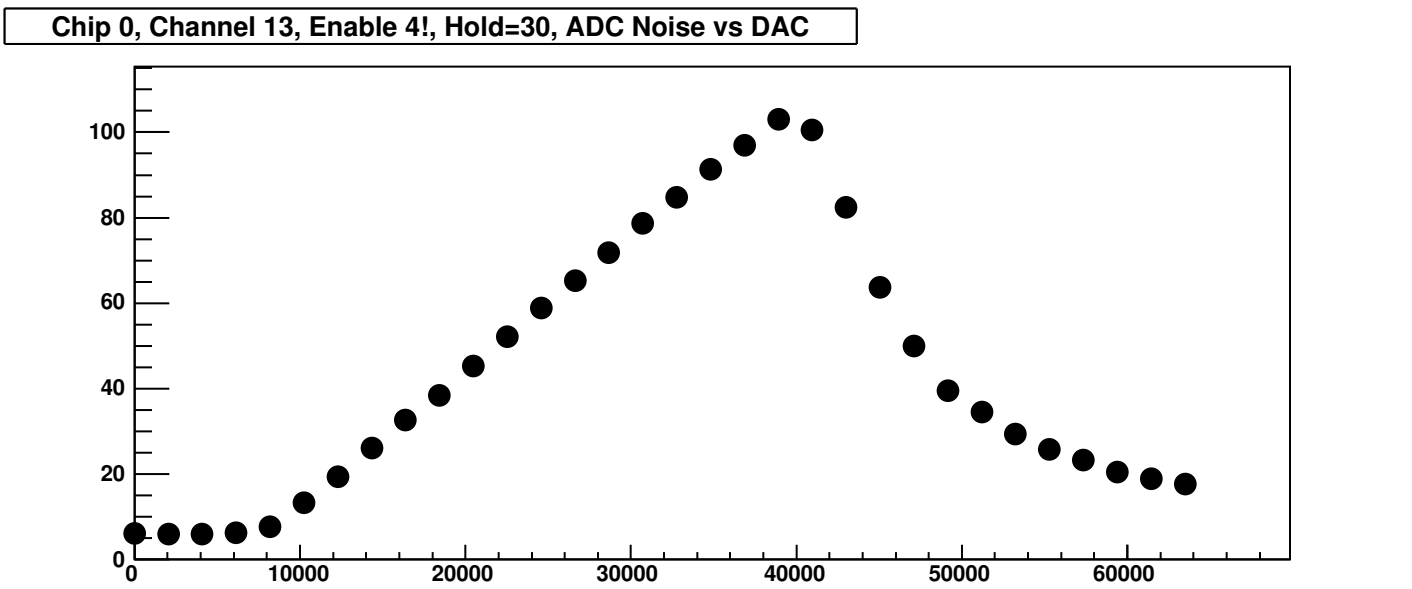

Chip 0, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

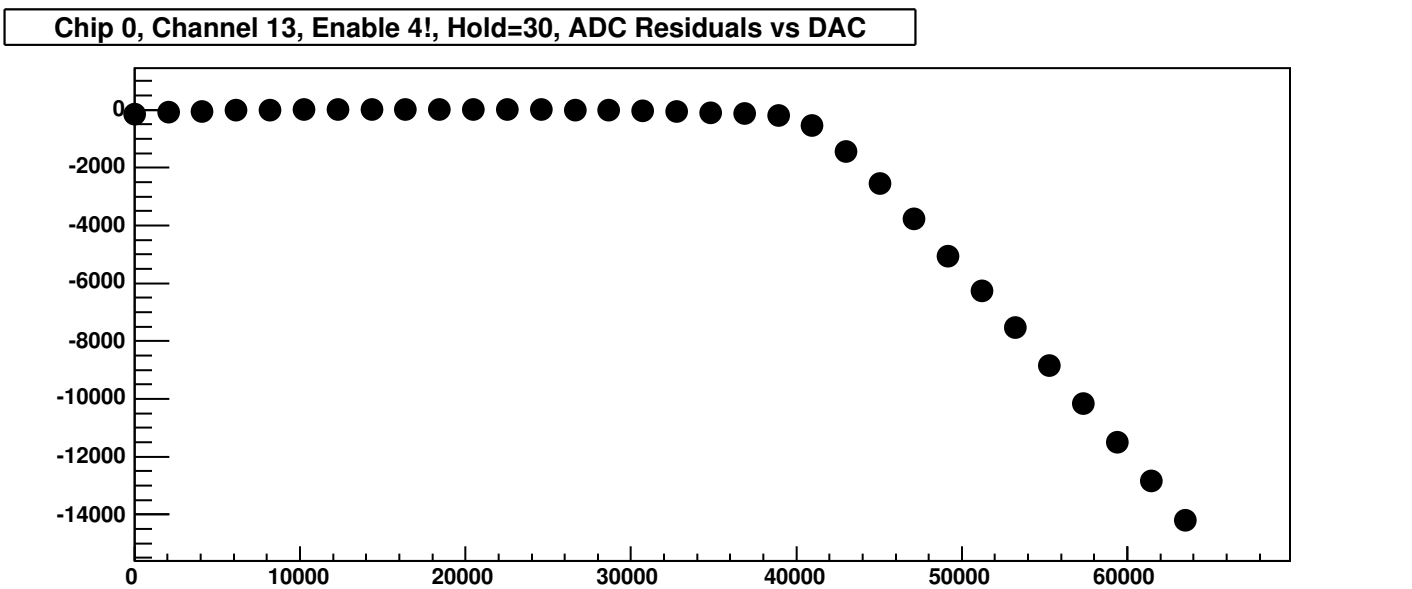

Chip 0, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2970 / 15
p0 1035 ± 0.109
p1 0.0007696 ± 4.488e-06
1080
1060
1040
1020
1000
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000

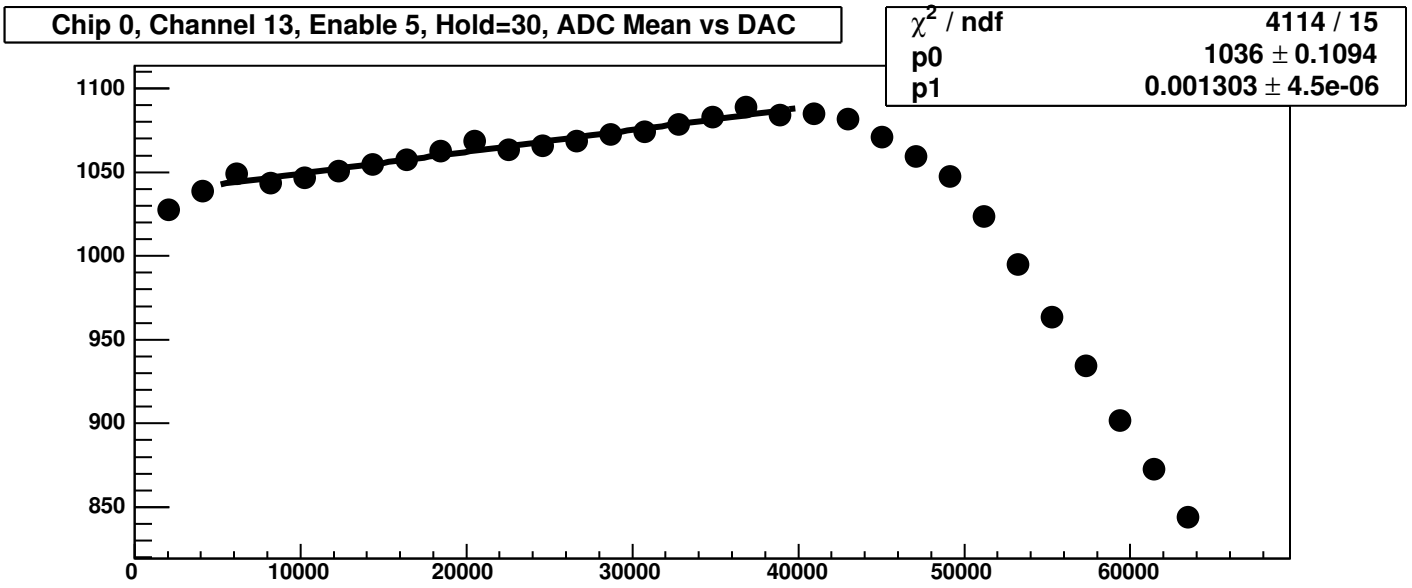

Chip 0, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

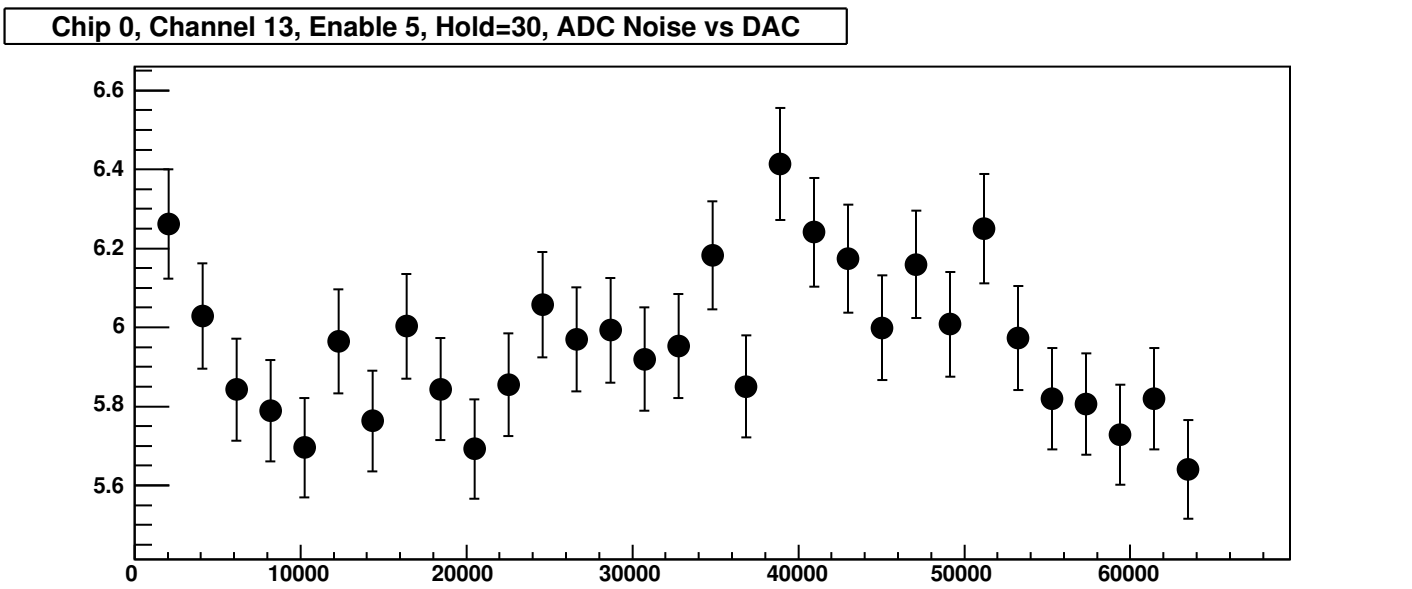

Chip 0, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

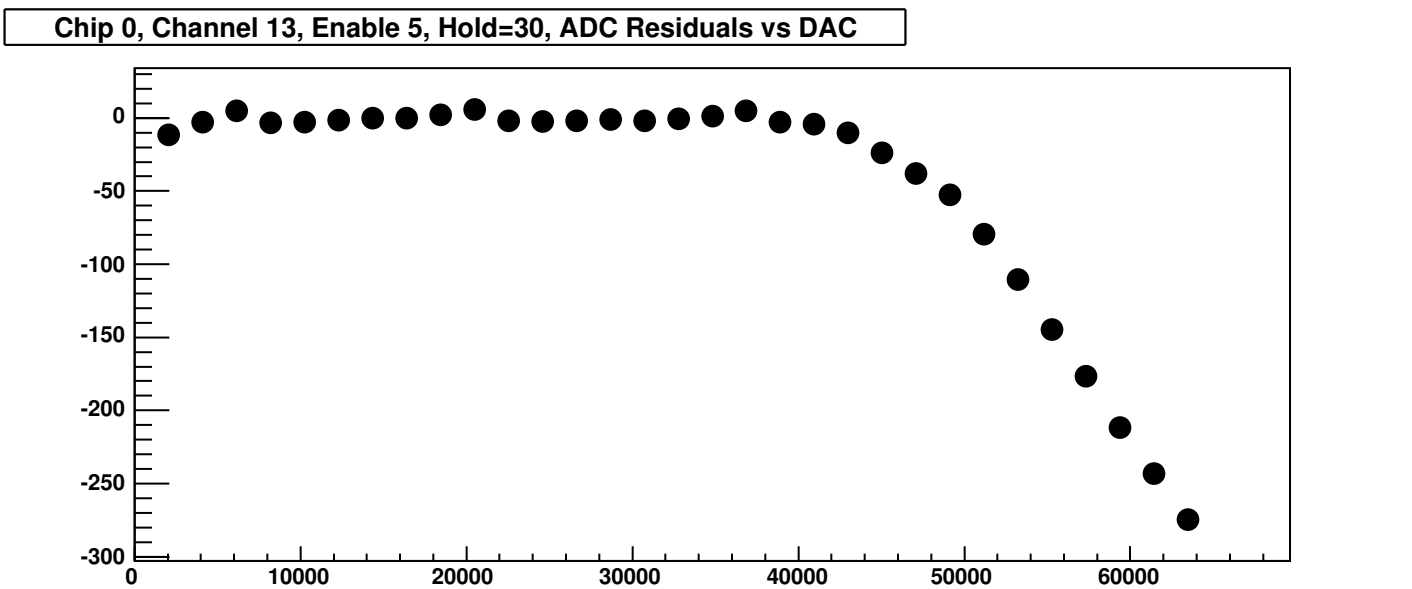

Chip 0, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.189e+04 / 15
p0 1158 ± 0.2894
p1 0.6619 ± 2.909e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

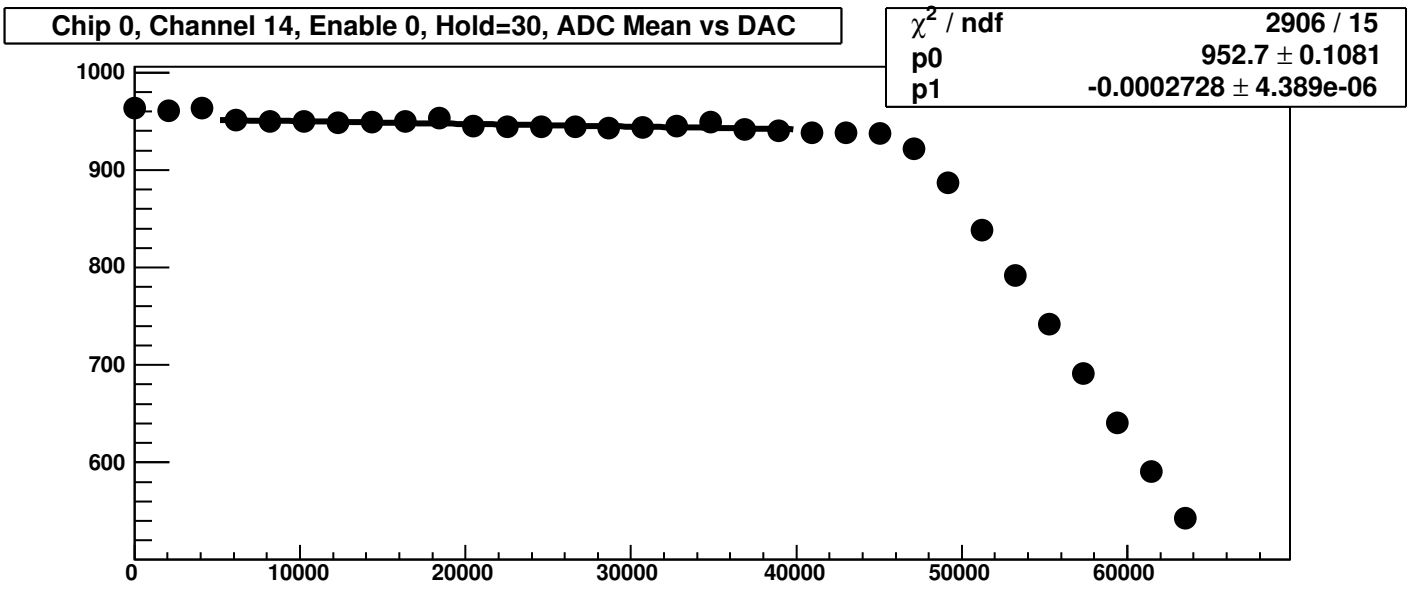

Chip 0, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

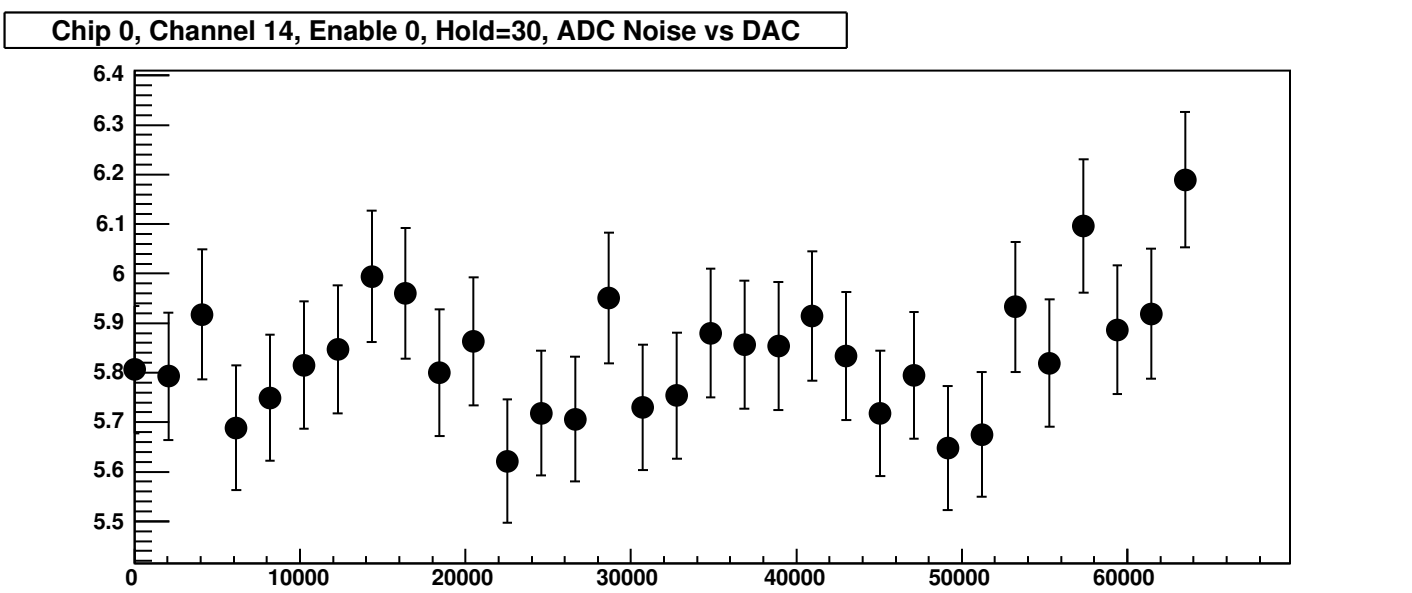

Chip 0, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

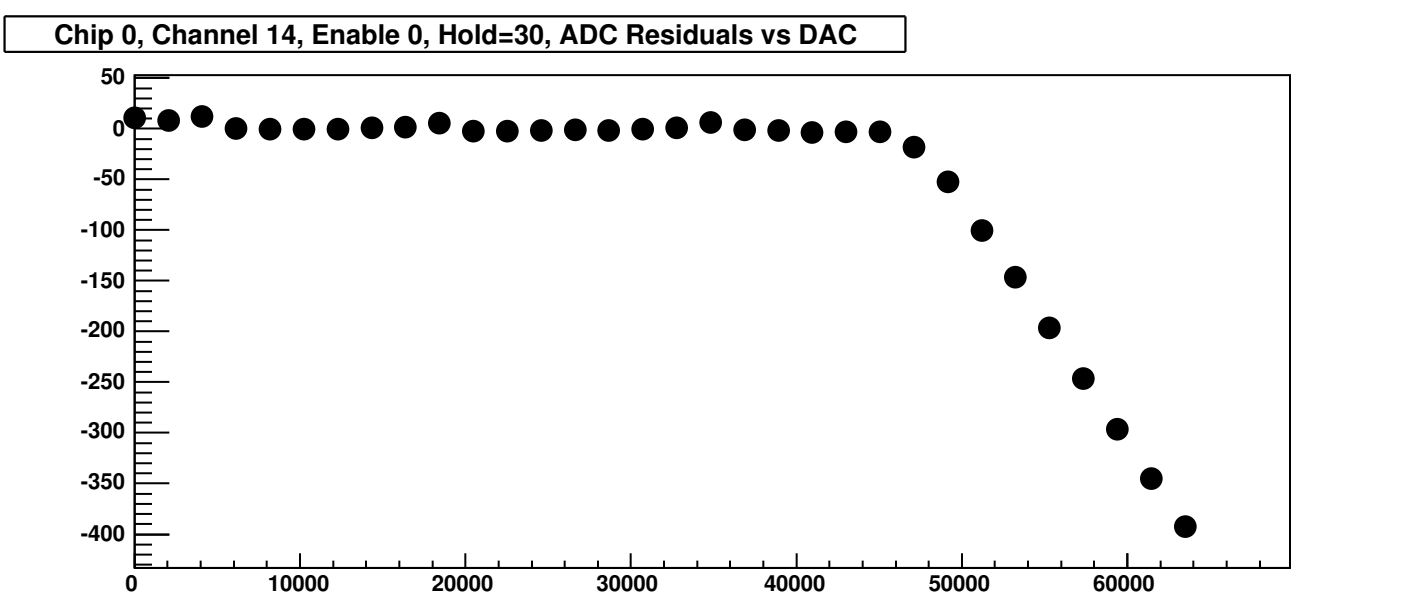

Chip 0, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4114 / 15
p0 1036 ± 0.1094
p1 0.001303 ± 4.5e-06
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

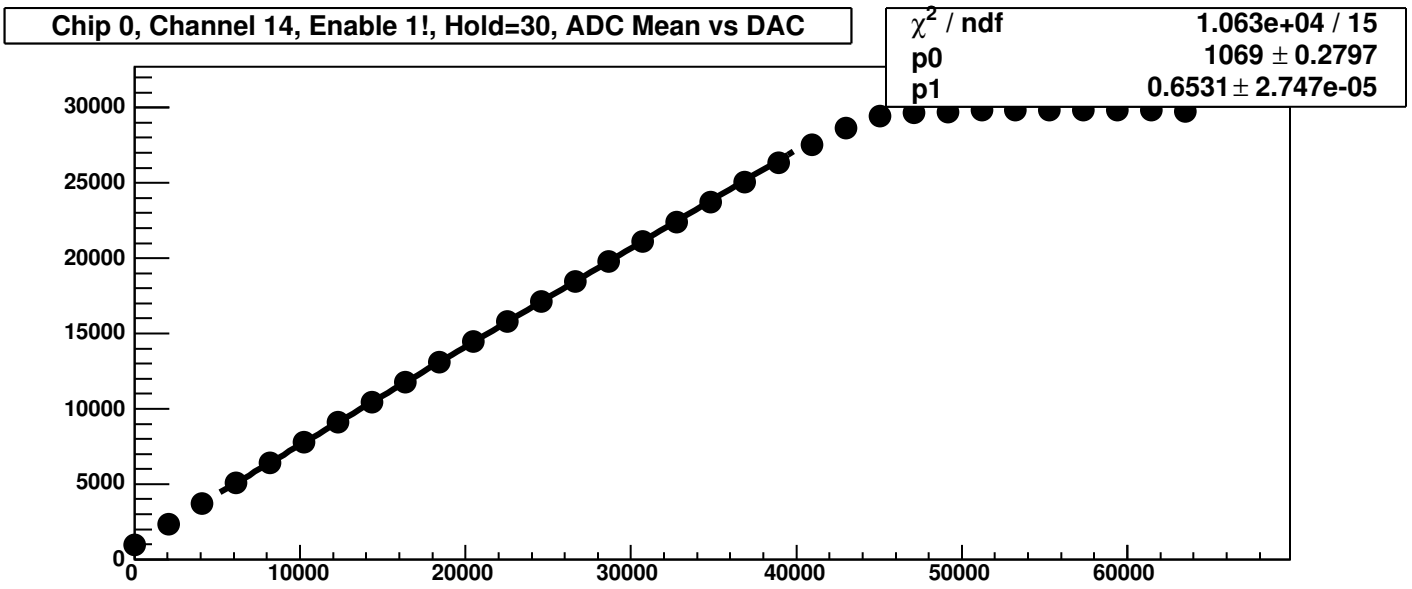

Chip 0, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

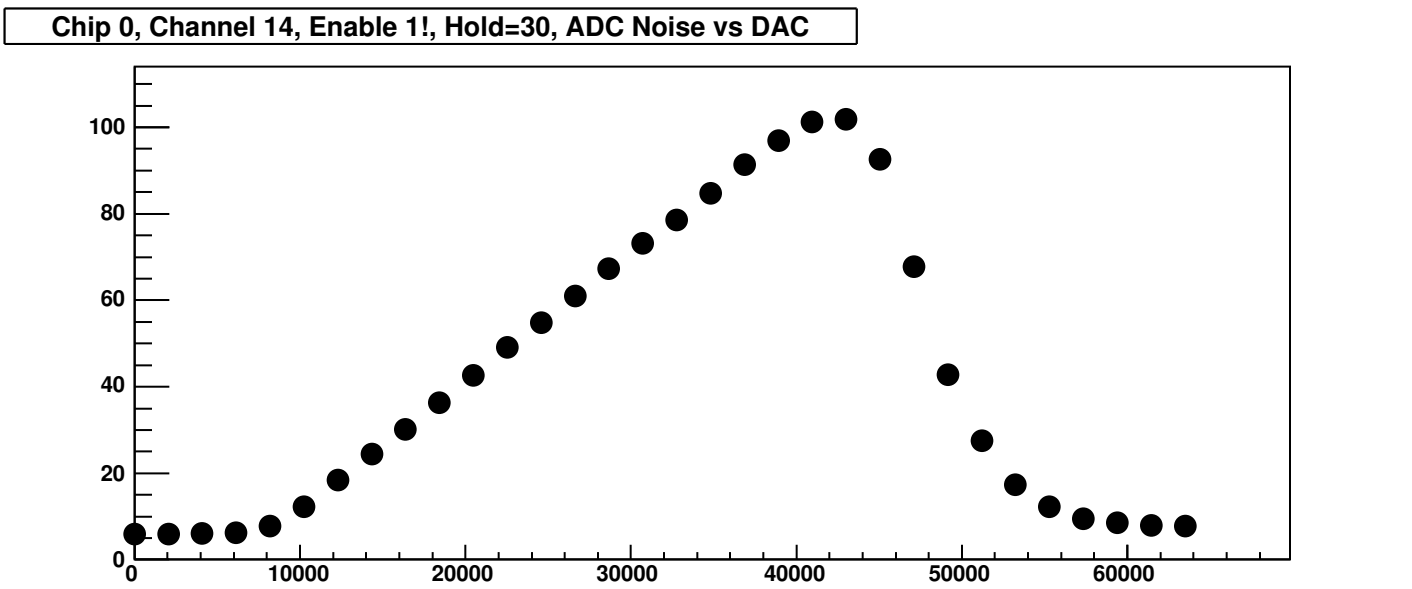

Chip 0, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

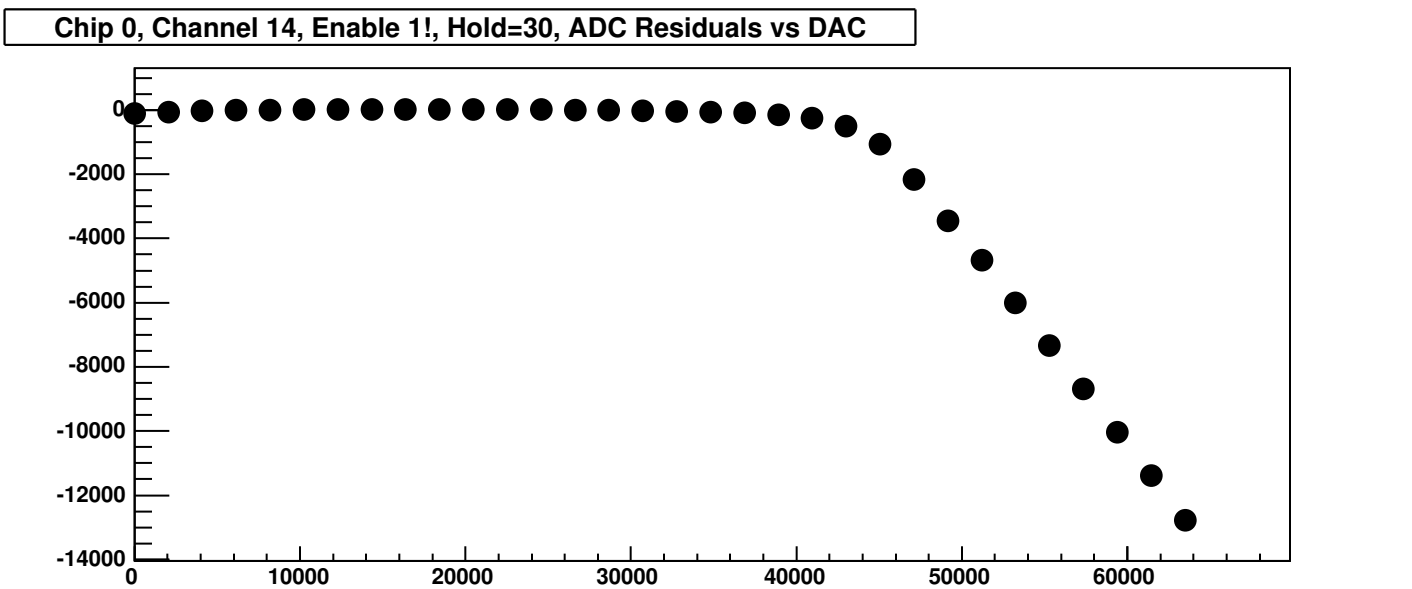

Chip 0, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2906 / 15
p0 952.7 ± 0.1081
p1 -0.0002728 ± 4.389e-06
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

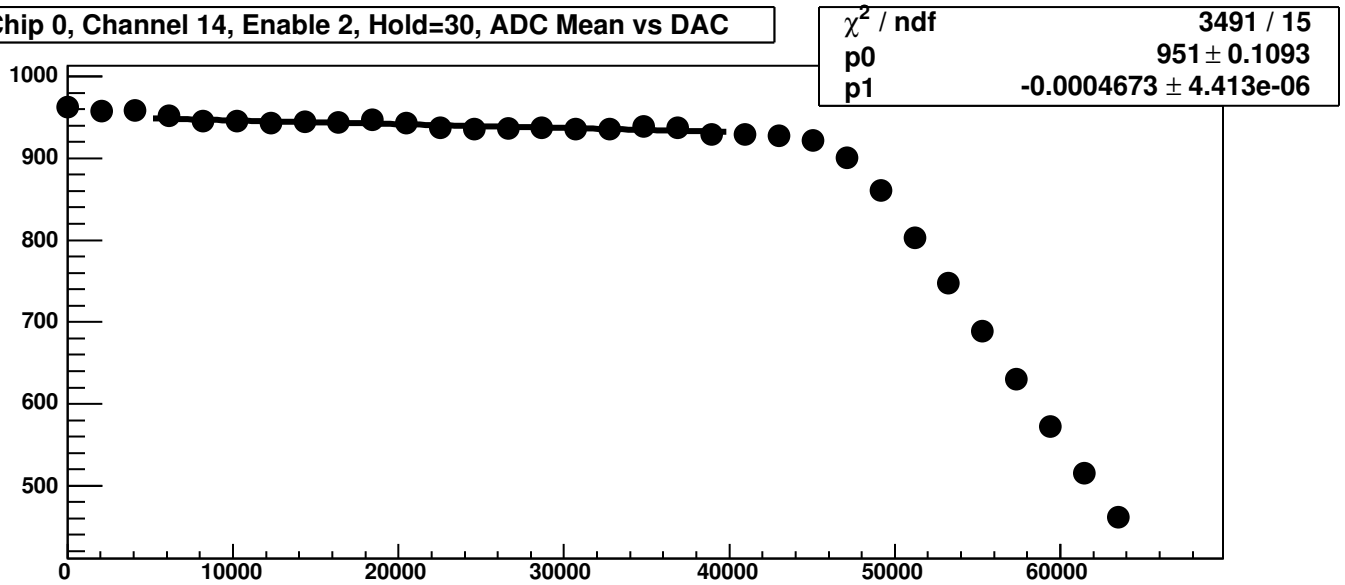

Chip 0, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.063e+04 / 15
p0 1069 ± 0.2797
p1 0.6531 ± 2.747e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

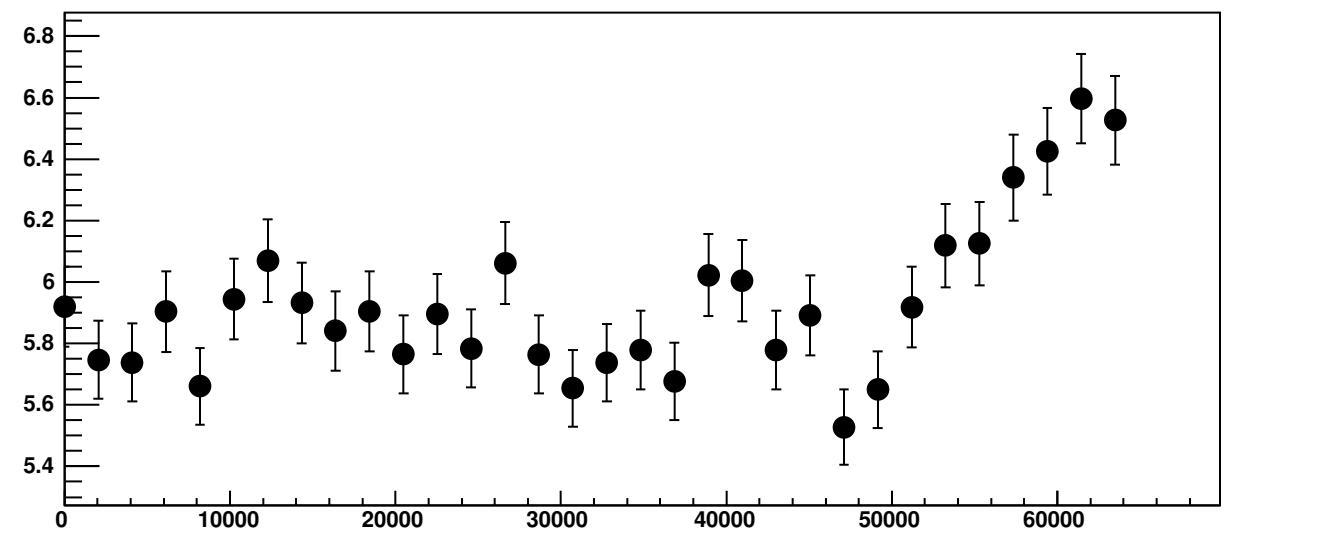

Chip 0, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

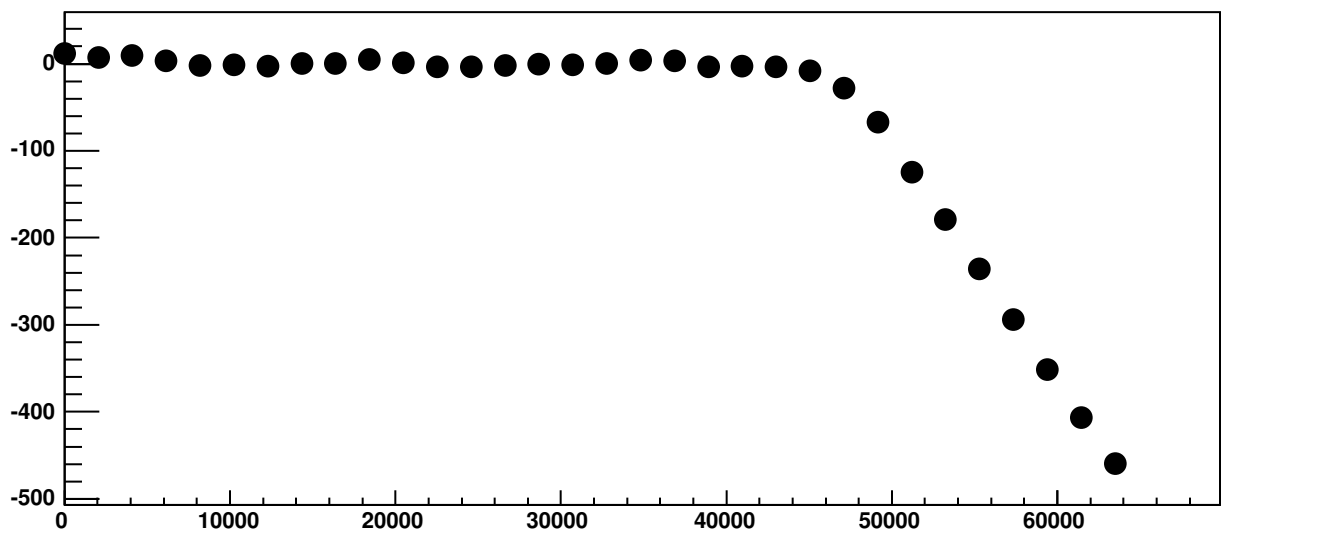
Chip 0, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC

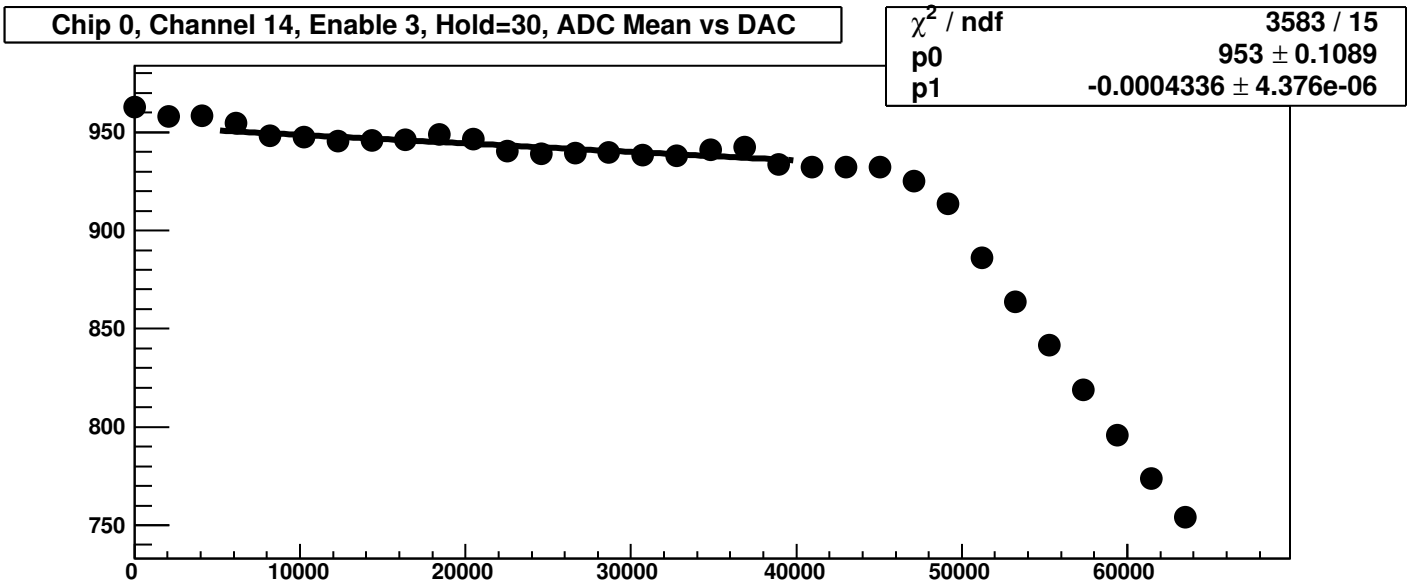


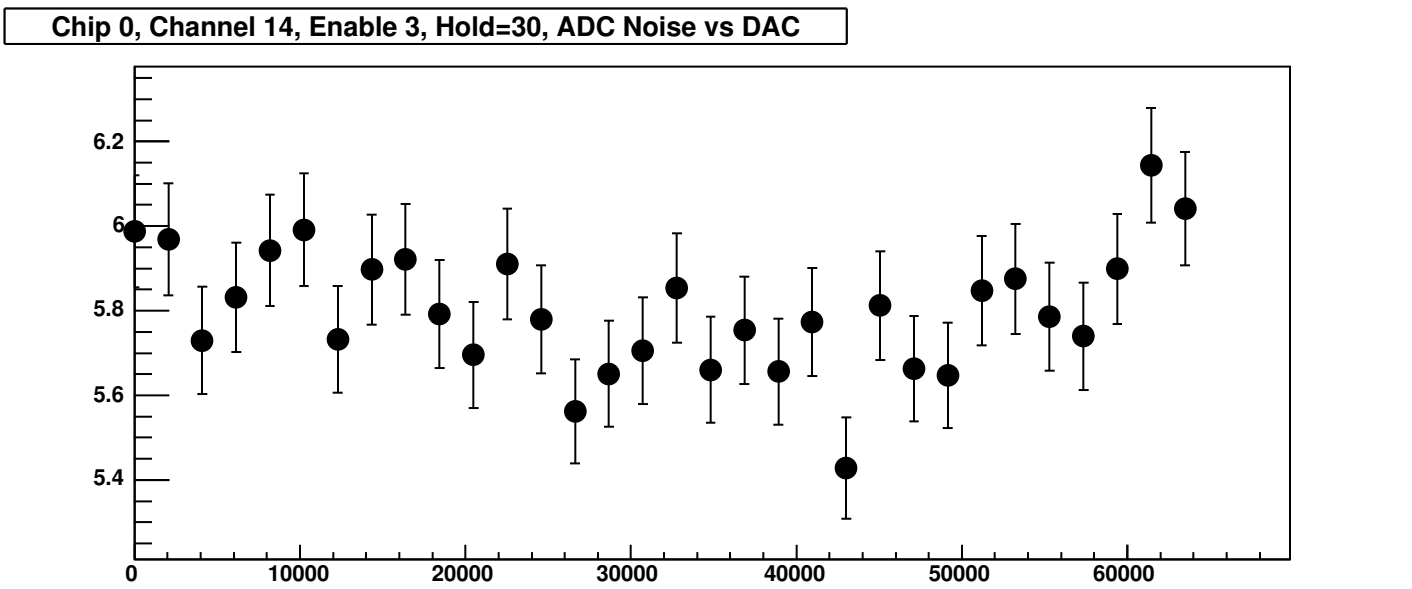
Chip 0, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC

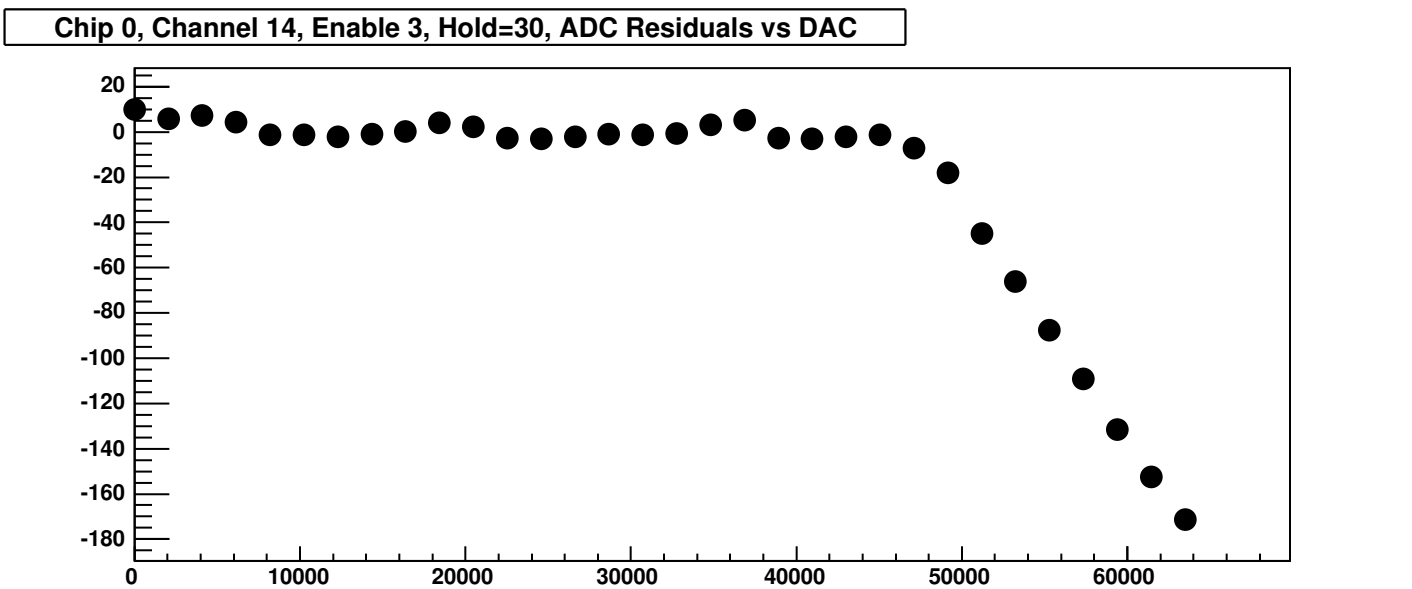


Chip 0, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

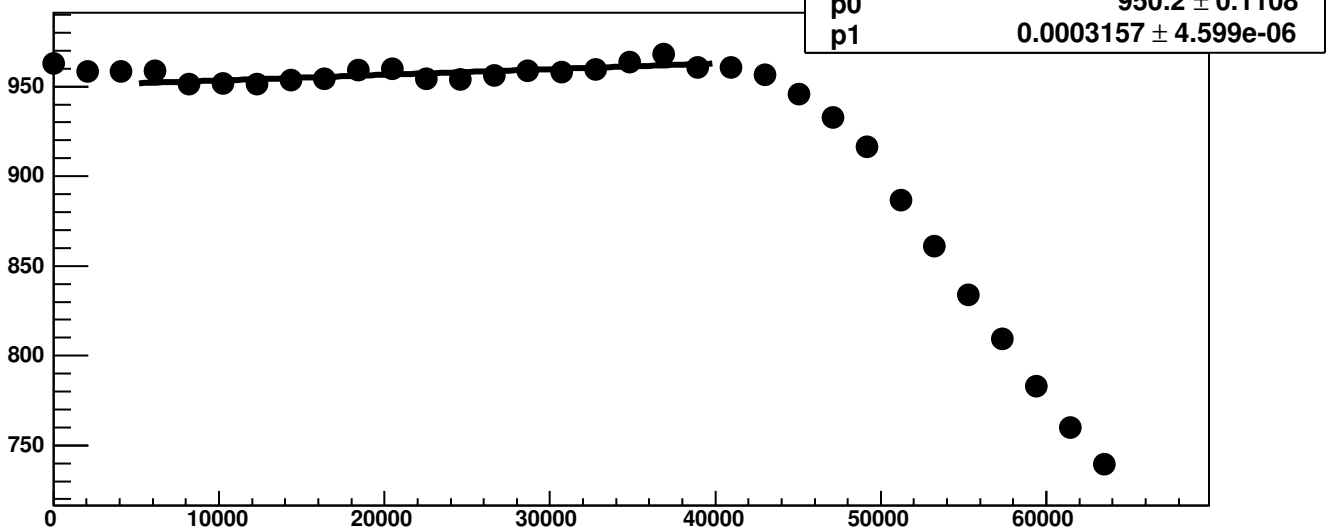



Chip 0, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3583 / 15
p0 953 ± 0.1089
p1 -0.0004336 ± 4.376e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

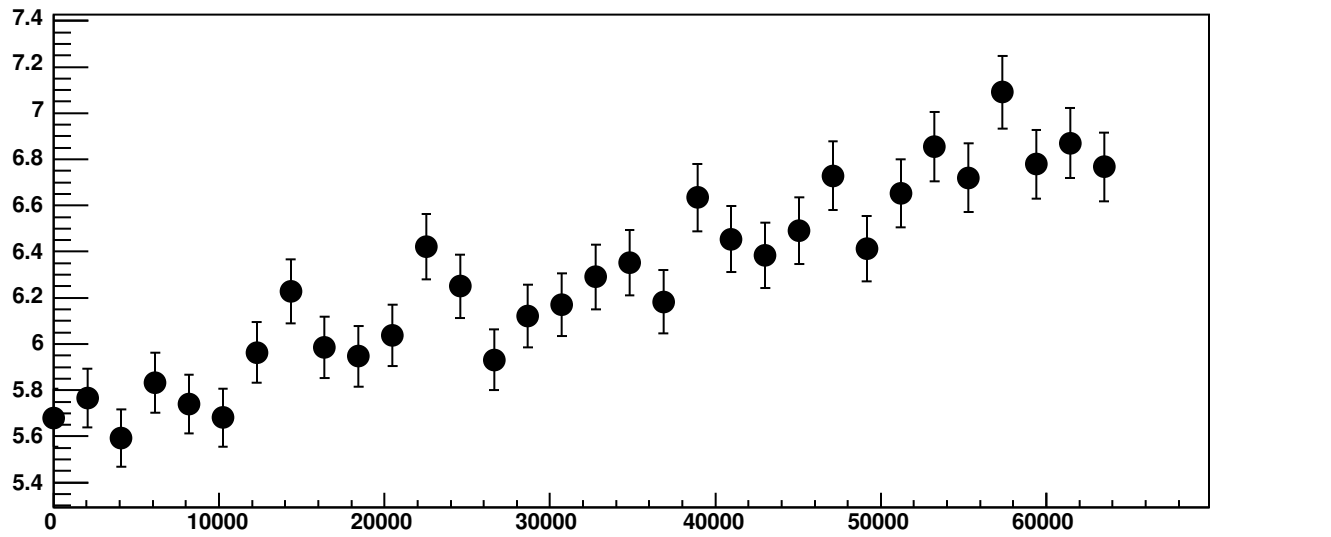

Chip 0, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

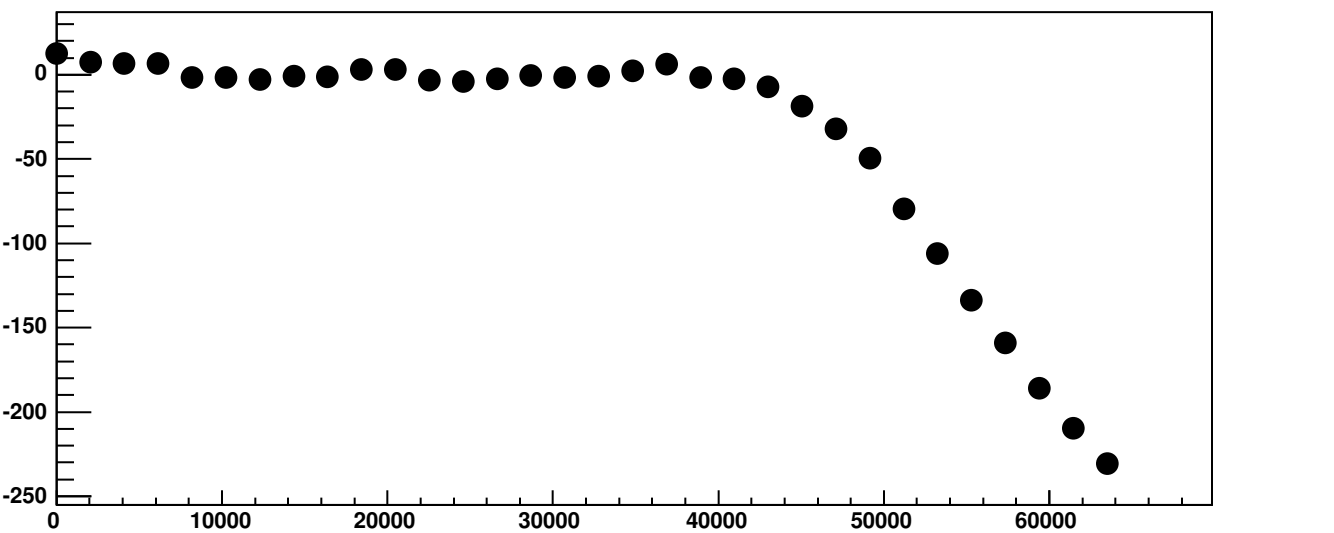
Chip 0, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC

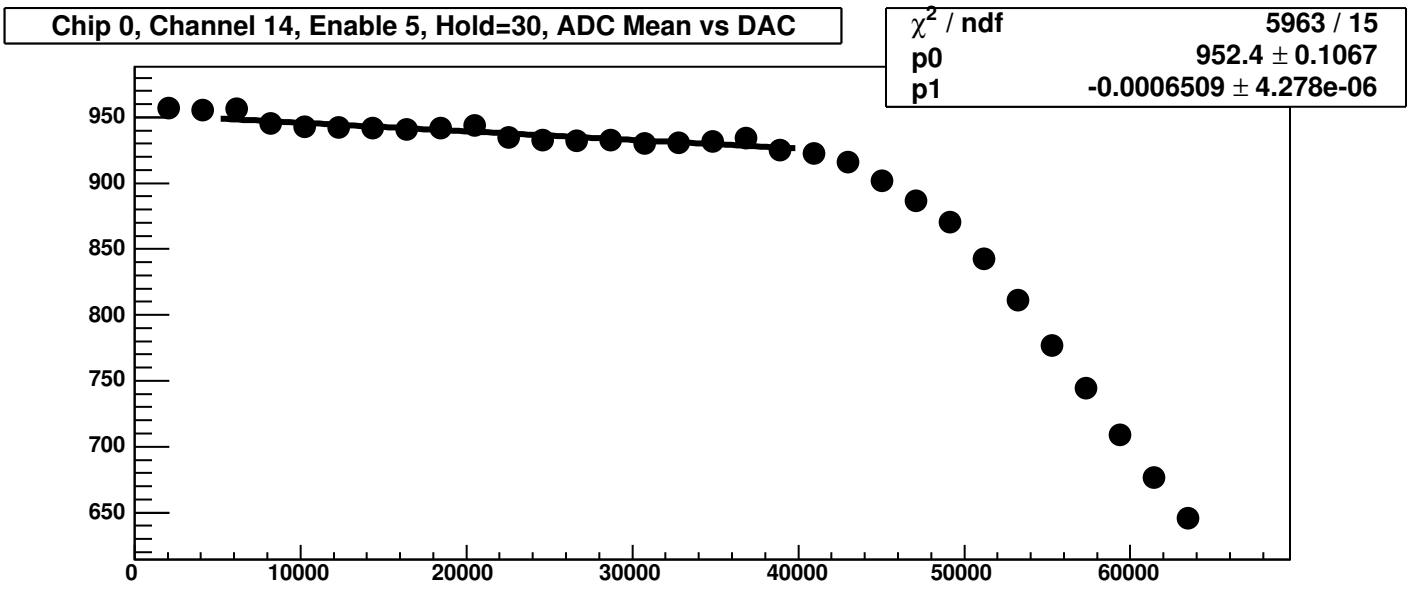


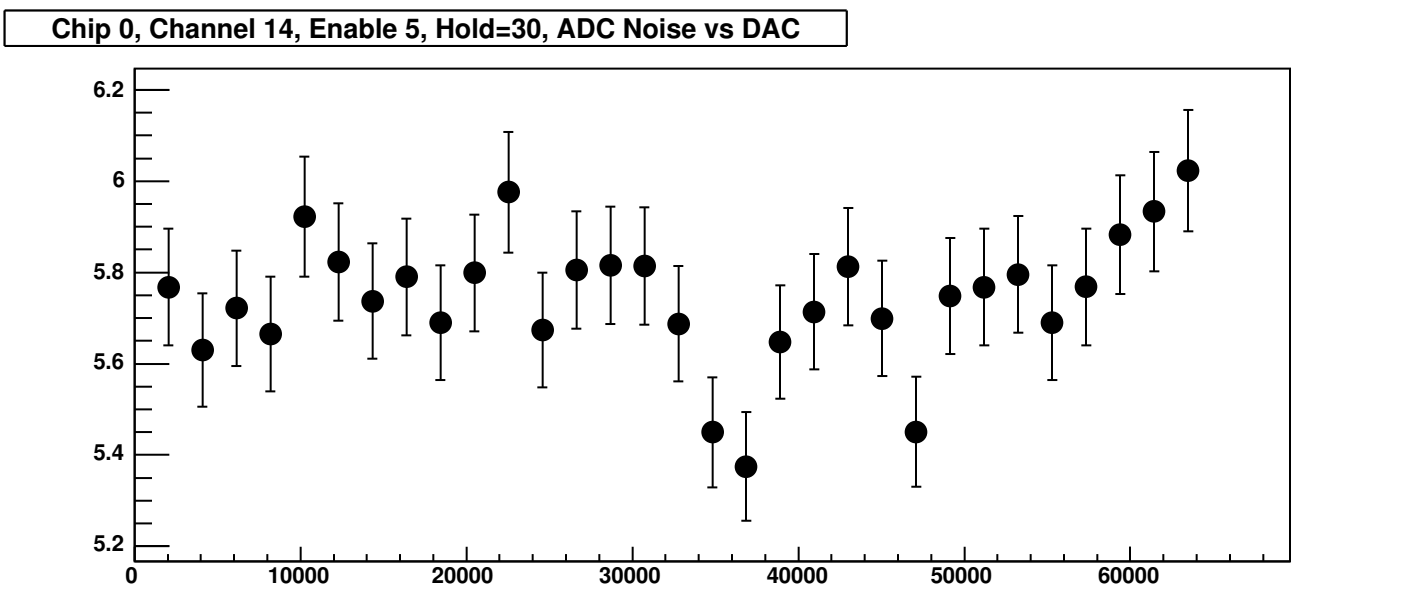
Chip 0, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC

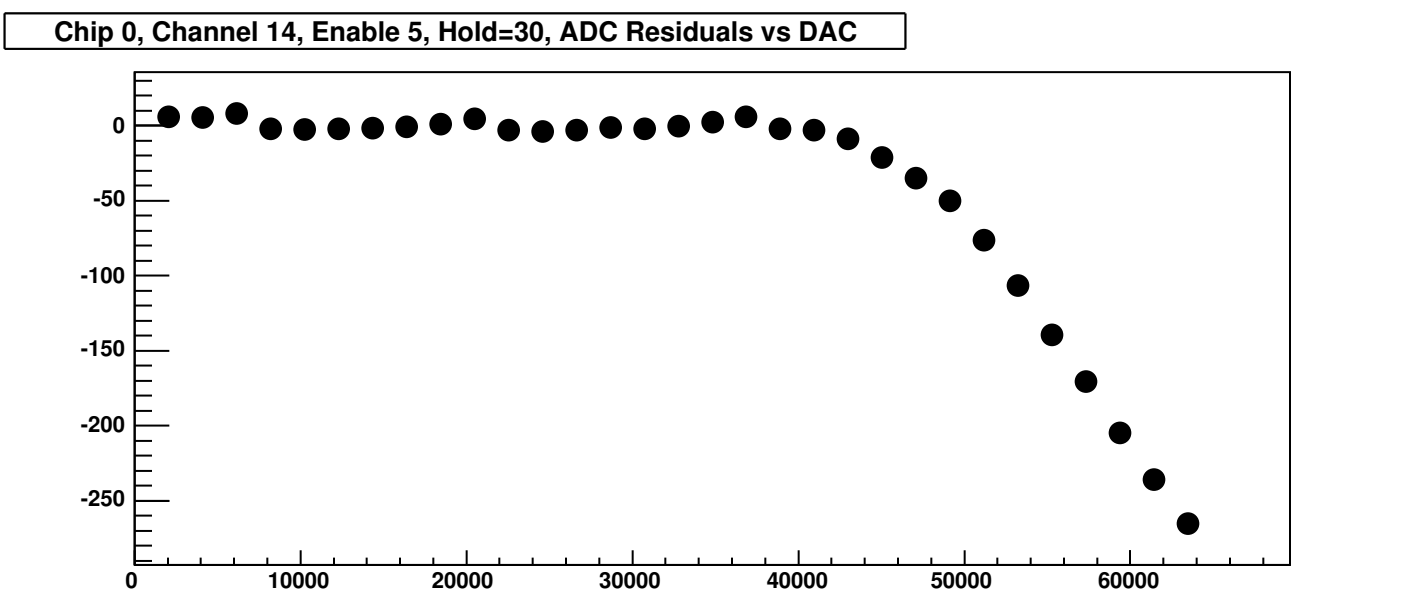


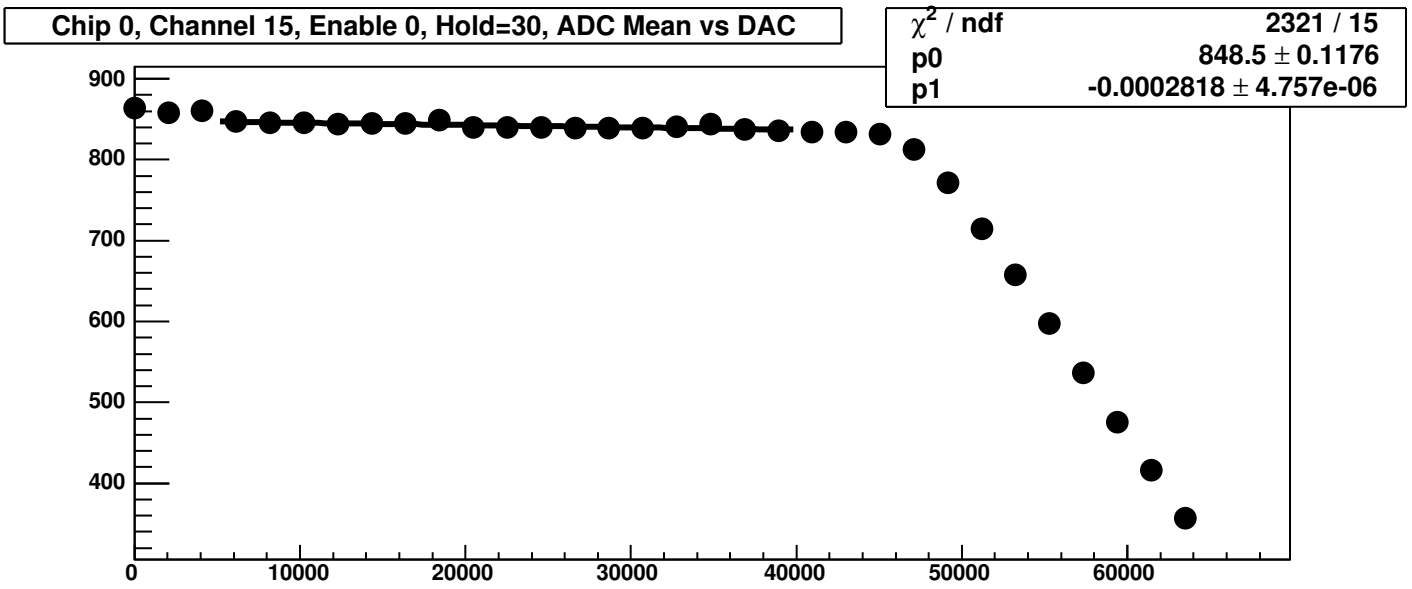
Chip 0, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC

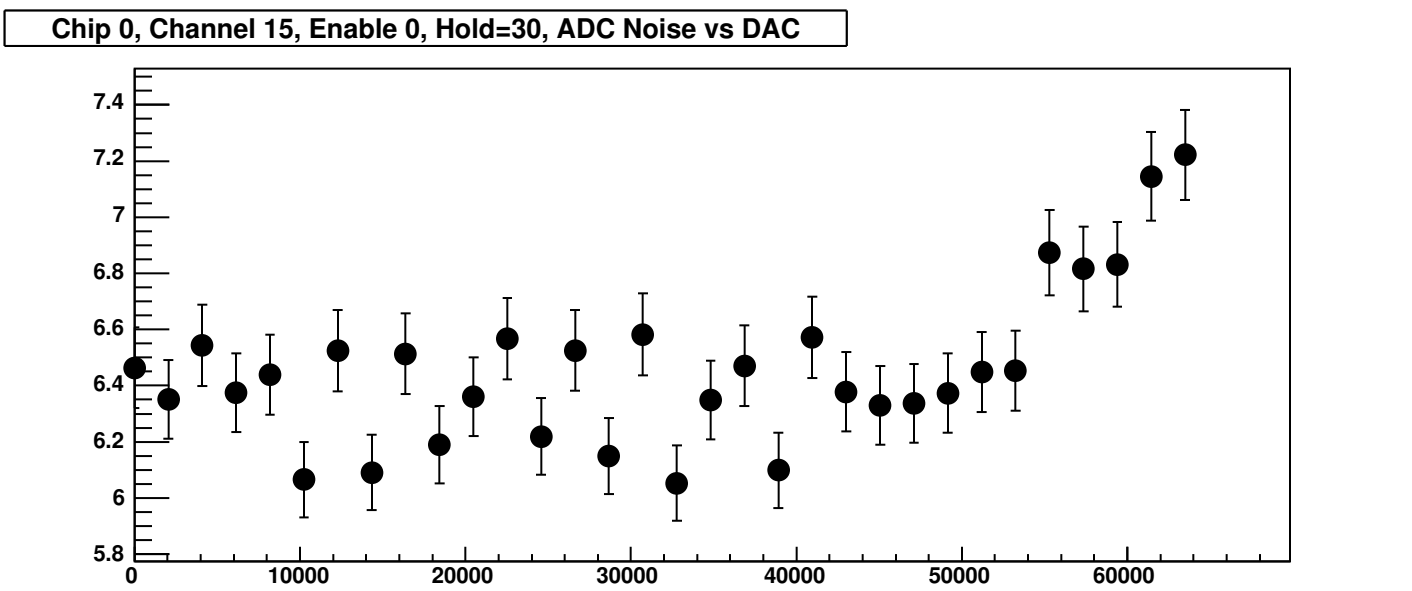


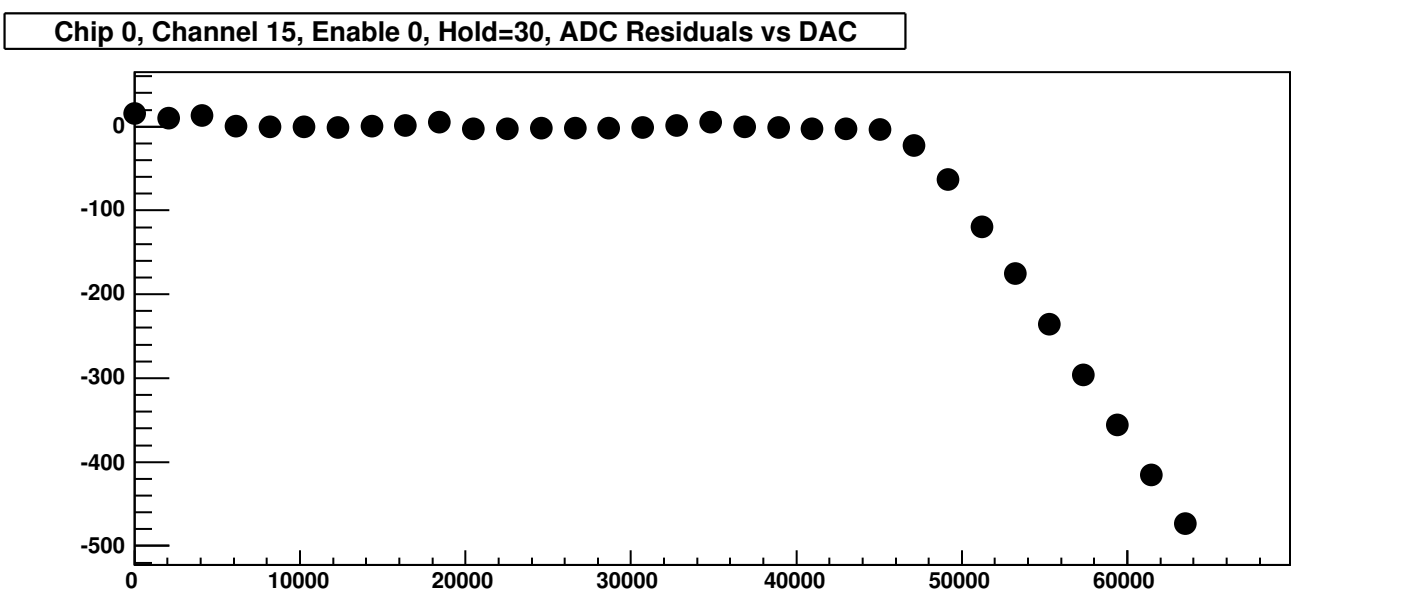

Chip 0, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5963 / 15
p0 952.4 ± 0.1067
p1 -0.0006509 ± 4.278e-06
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

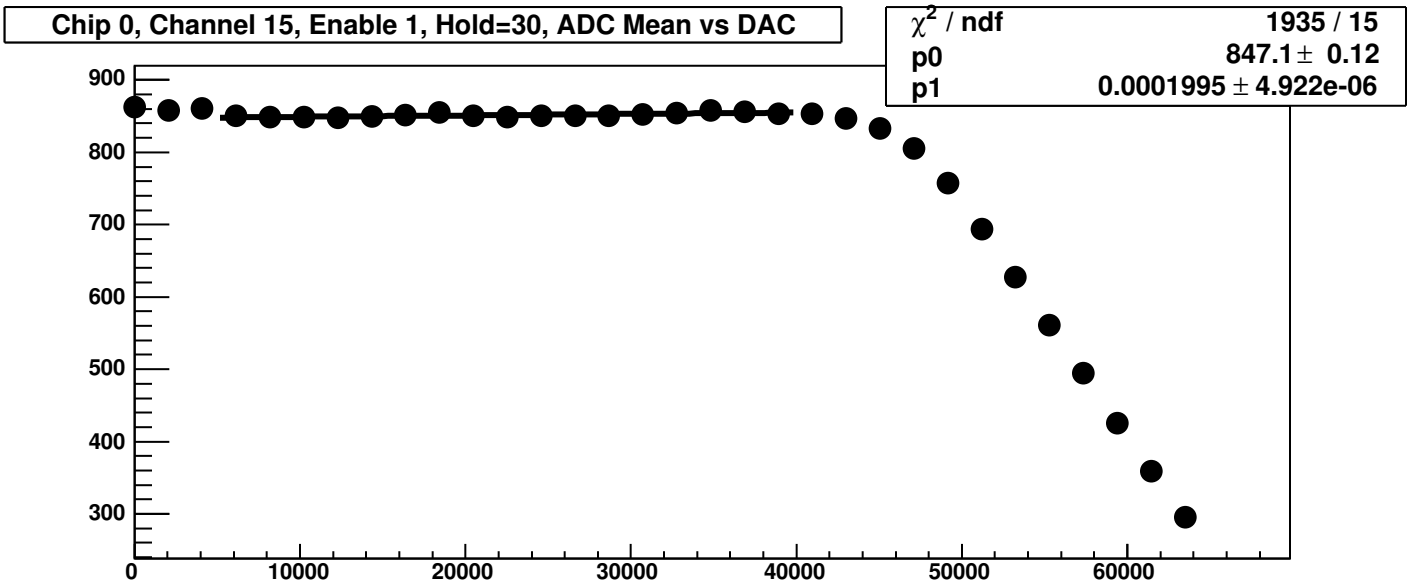

Chip 0, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

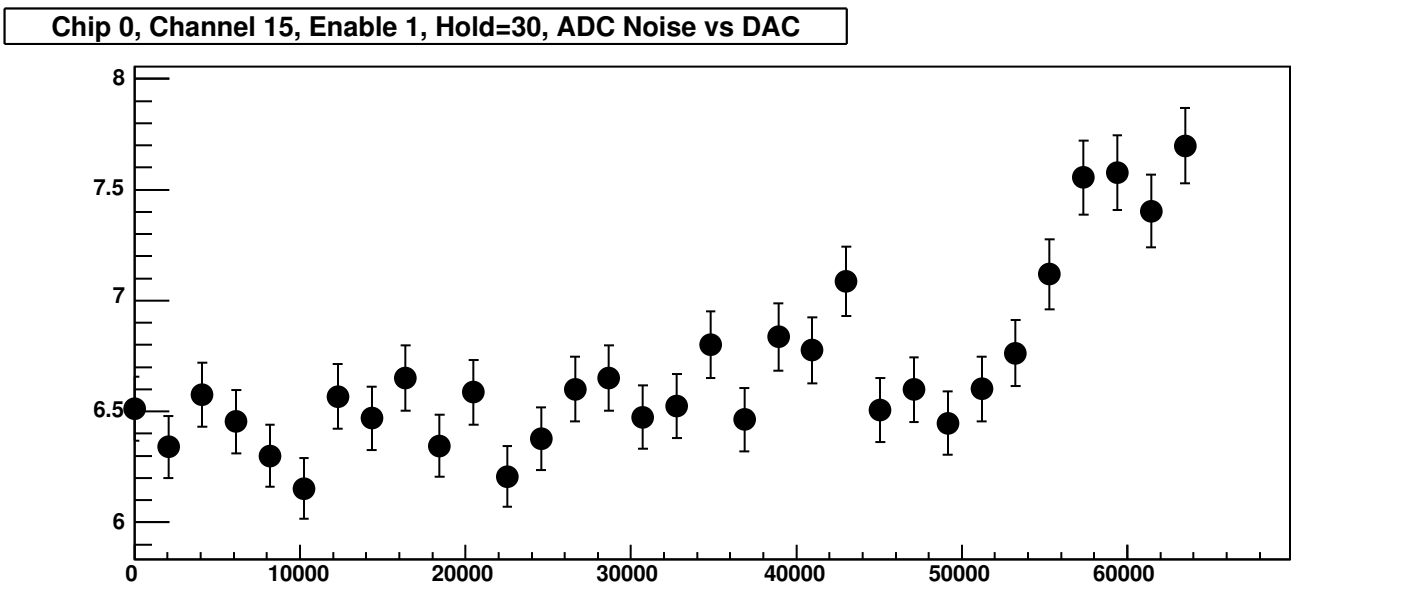

Chip 0, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

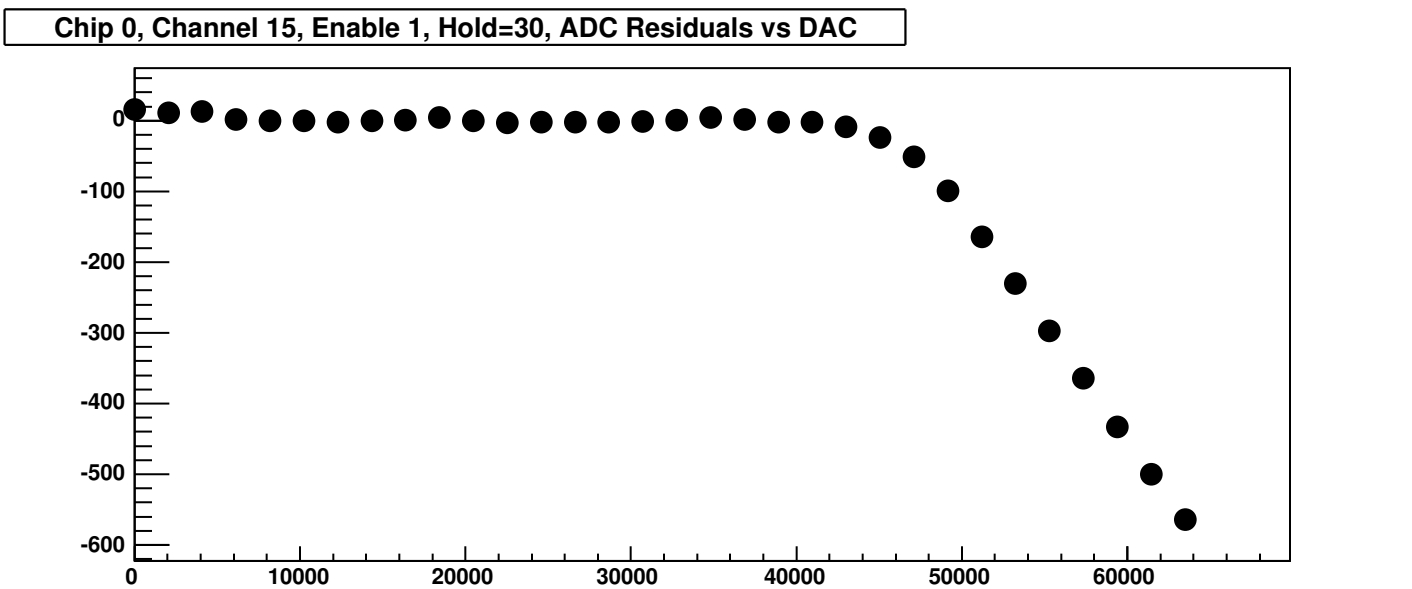

Chip 0, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2321 / 15
p0 848.5 ± 0.1176
p1 -0.0002818 ± 4.757e-06
900
800
700
600
500
400
0
10000
20000
30000
40000
50000
60000

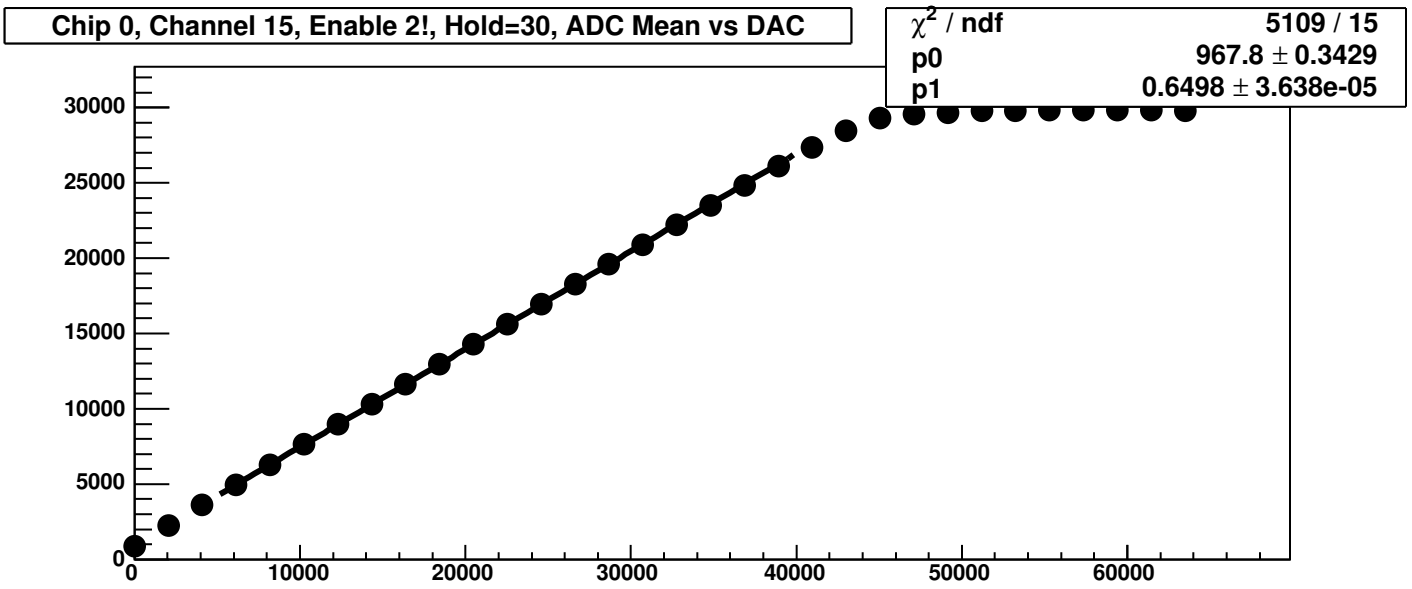

Chip 0, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

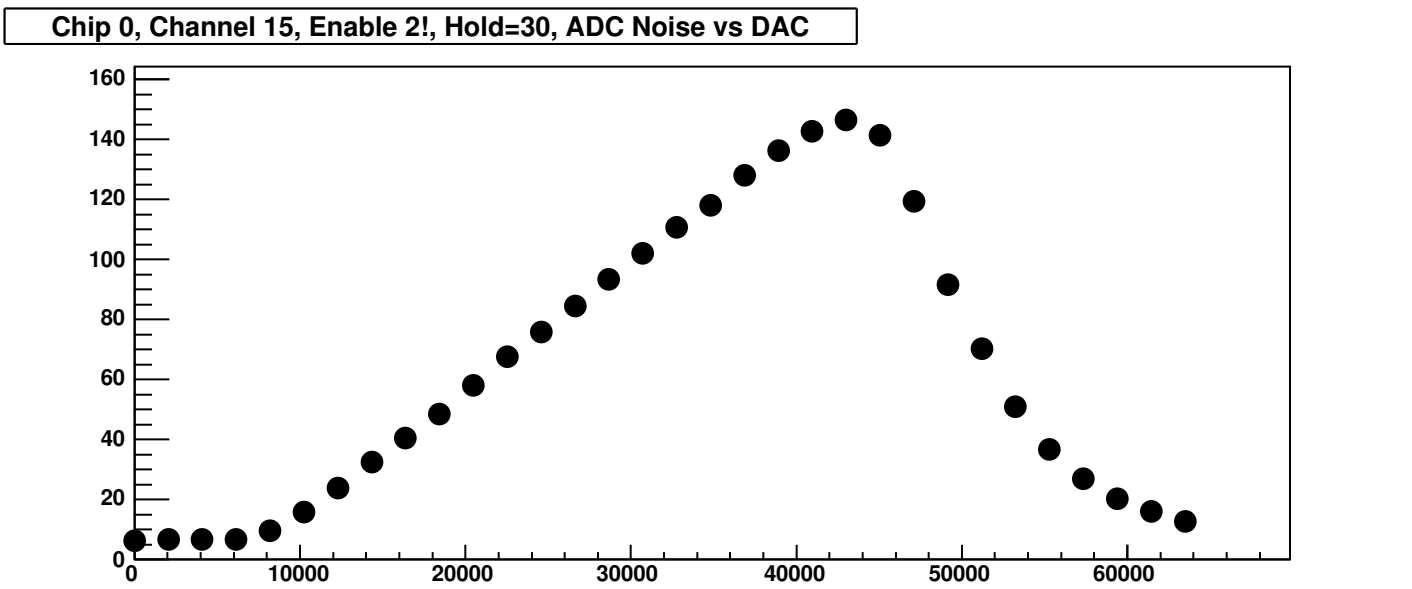

Chip 0, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

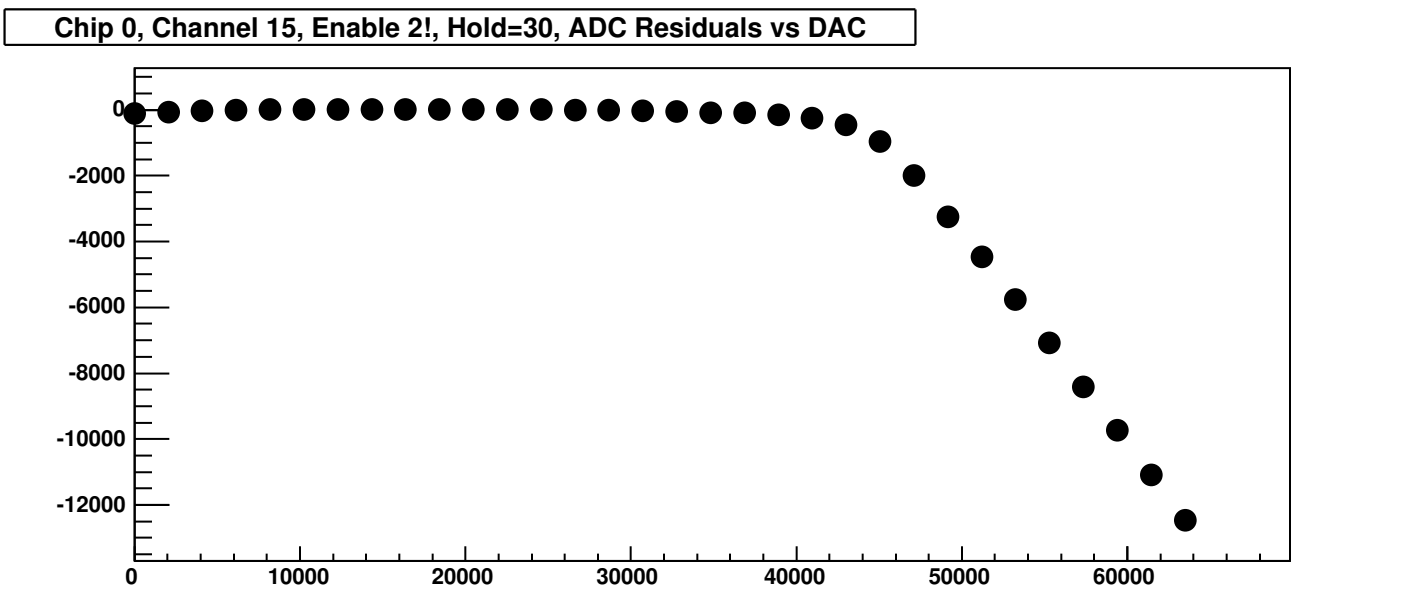

Chip 0, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1935 / 15
p0 847.1 ± 0.12
p1 0.0001995 ± 4.922e-06
900
800
700
600
500
400
300
0
10000
20000
30000
40000
50000
60000

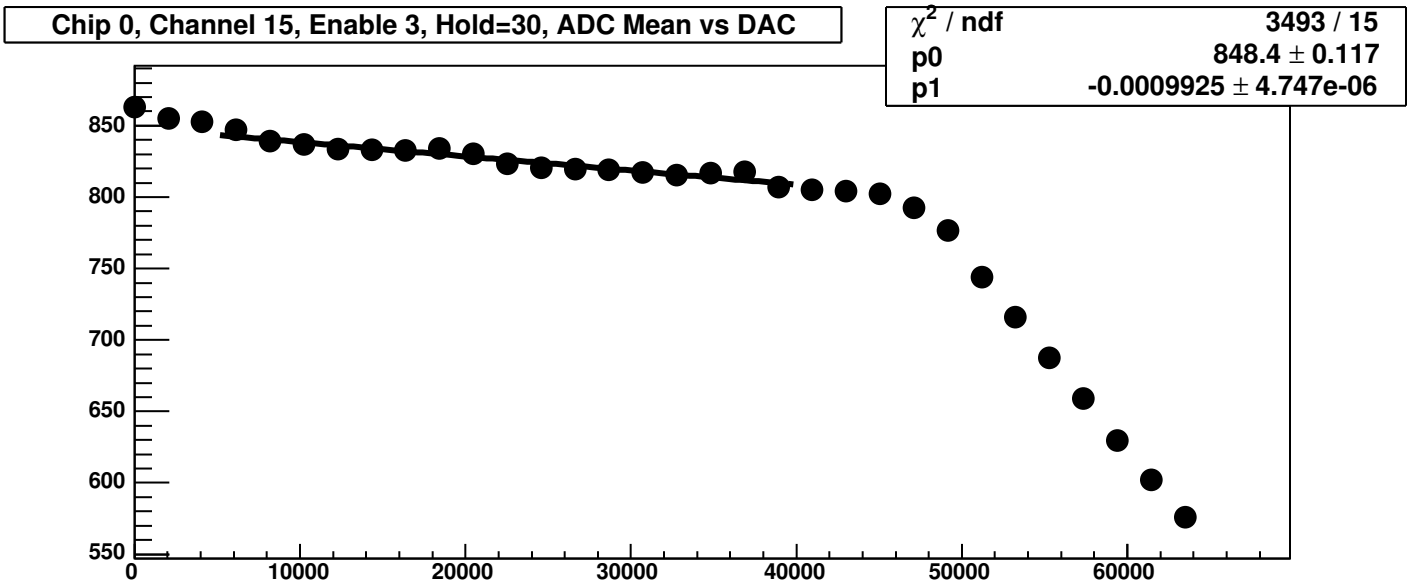

Chip 0, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
8
7.5
7
6.5
6
0
10000
20000
30000
40000
50000
60000

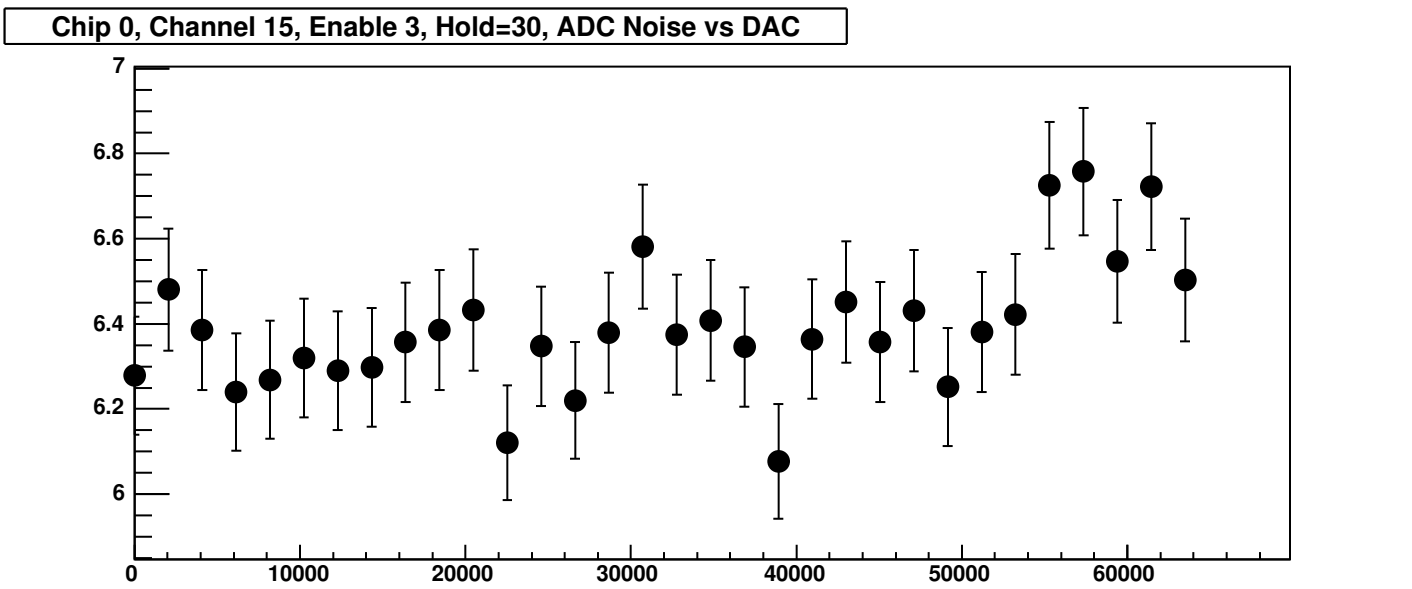

Chip 0, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
-600
0
10000
20000
30000
40000
50000
60000

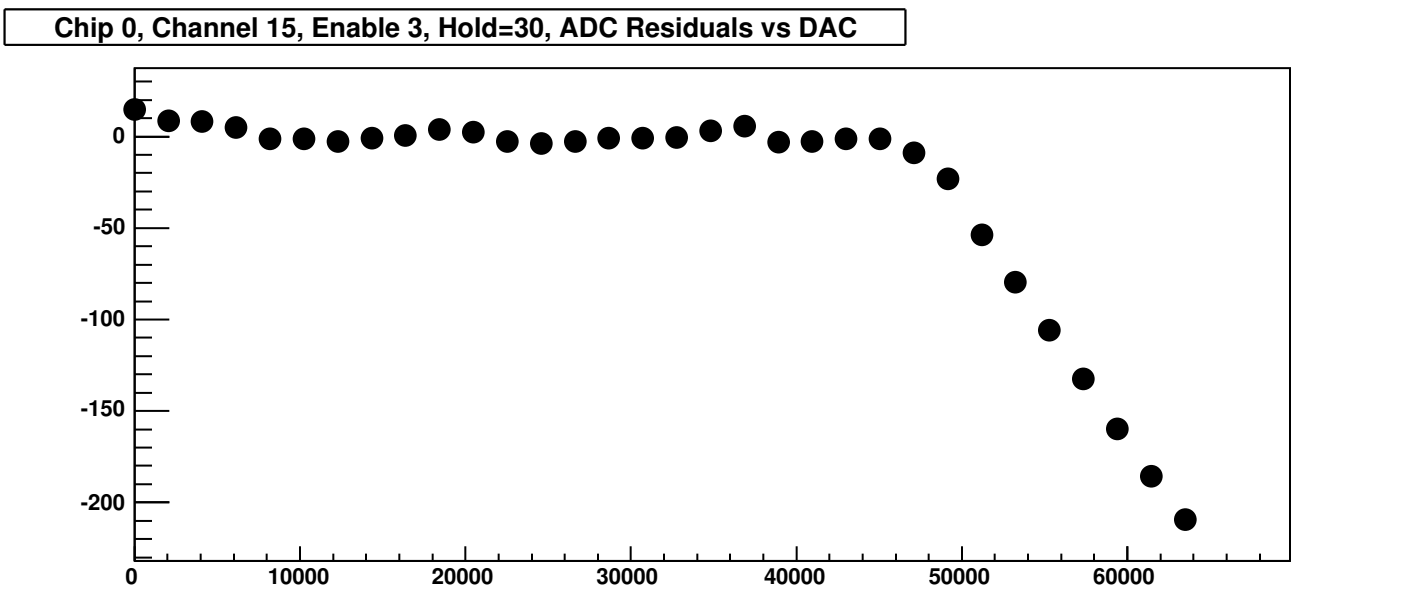

Chip 0, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 5109 / 15
p0 967.8 ± 0.3429
p1 0.6498 ± 3.638e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

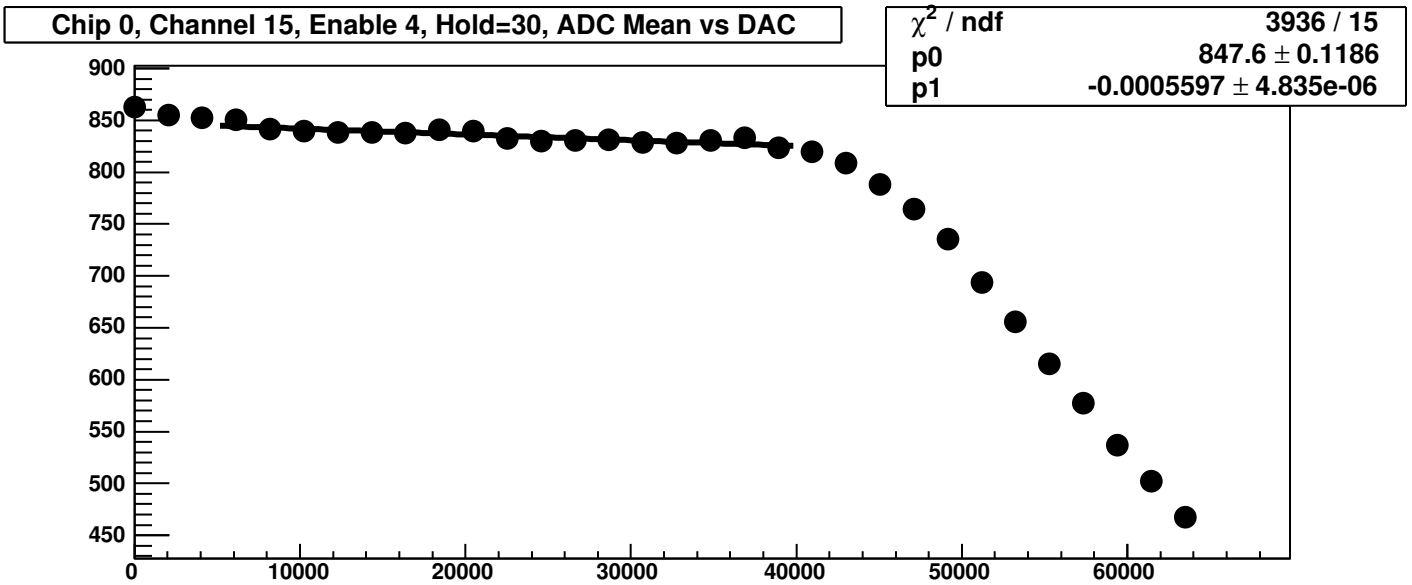

Chip 0, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

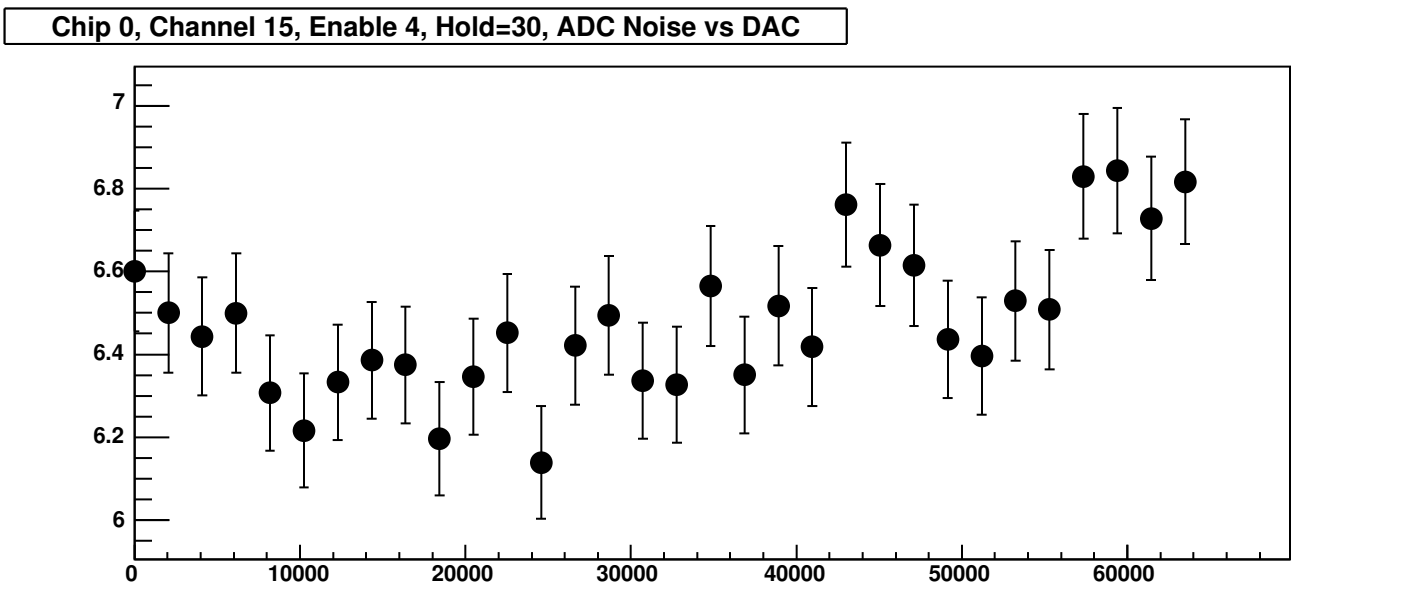

Chip 0, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

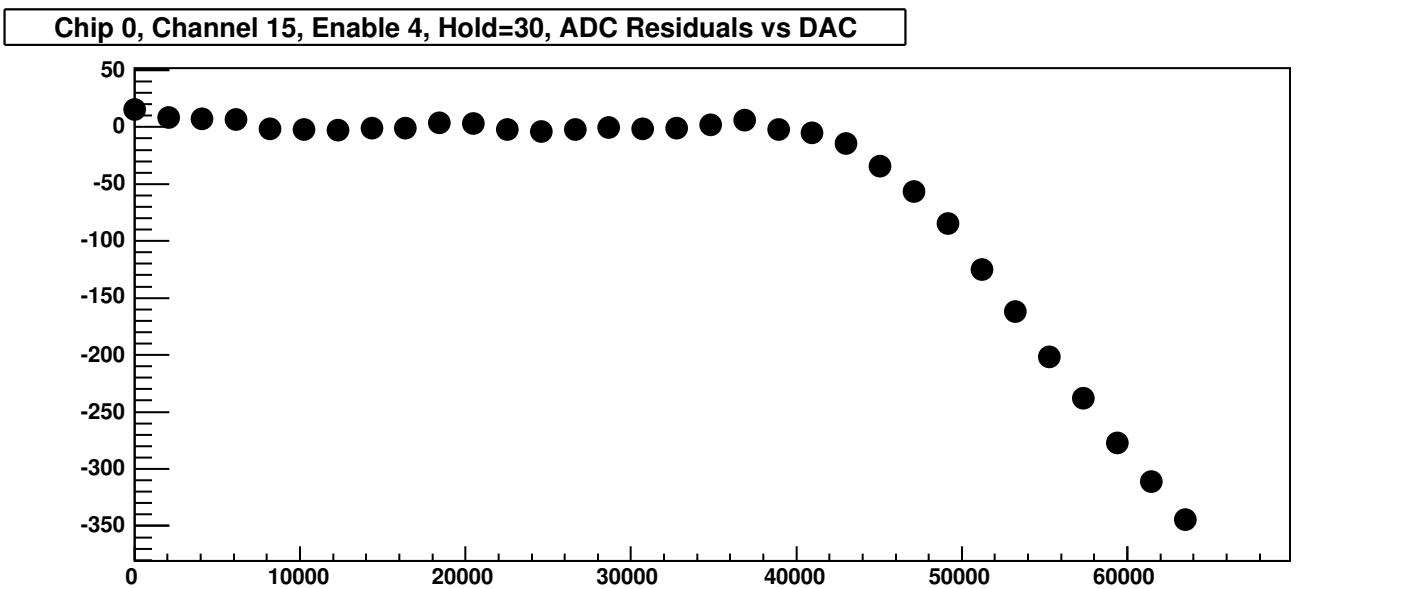

Chip 0, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3493 / 15
p0 848.4 ± 0.117
p1 -0.0009925 ± 4.747e-06
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

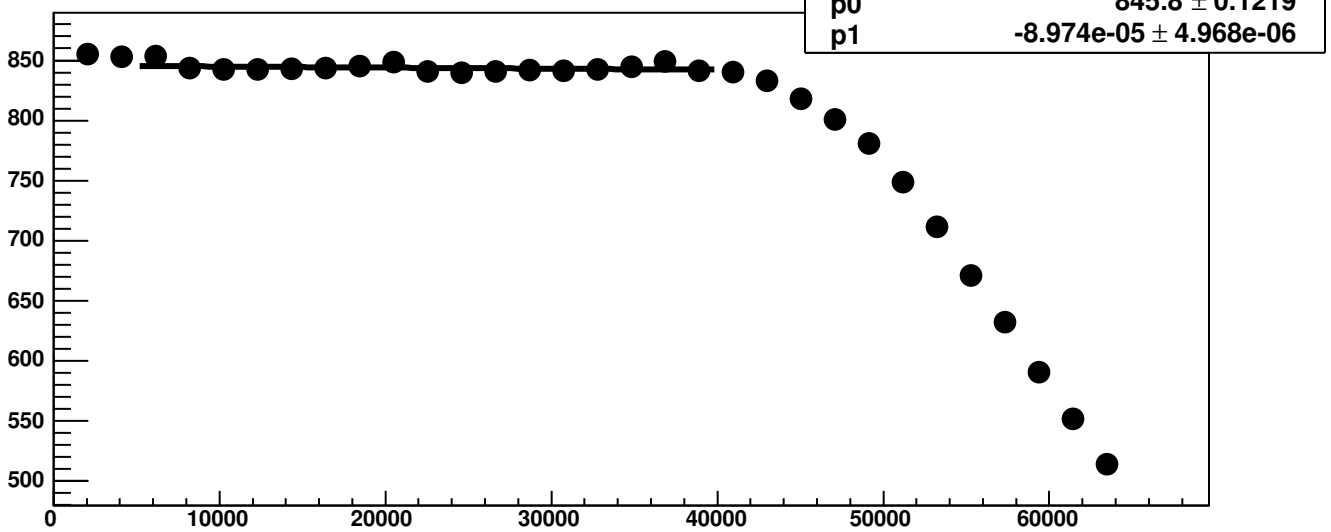

Chip 0, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 3936 / 15
p0 847.6 ± 0.1186
p1 -0.0005597 ± 4.835e-06
900
850
800
750
700
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000

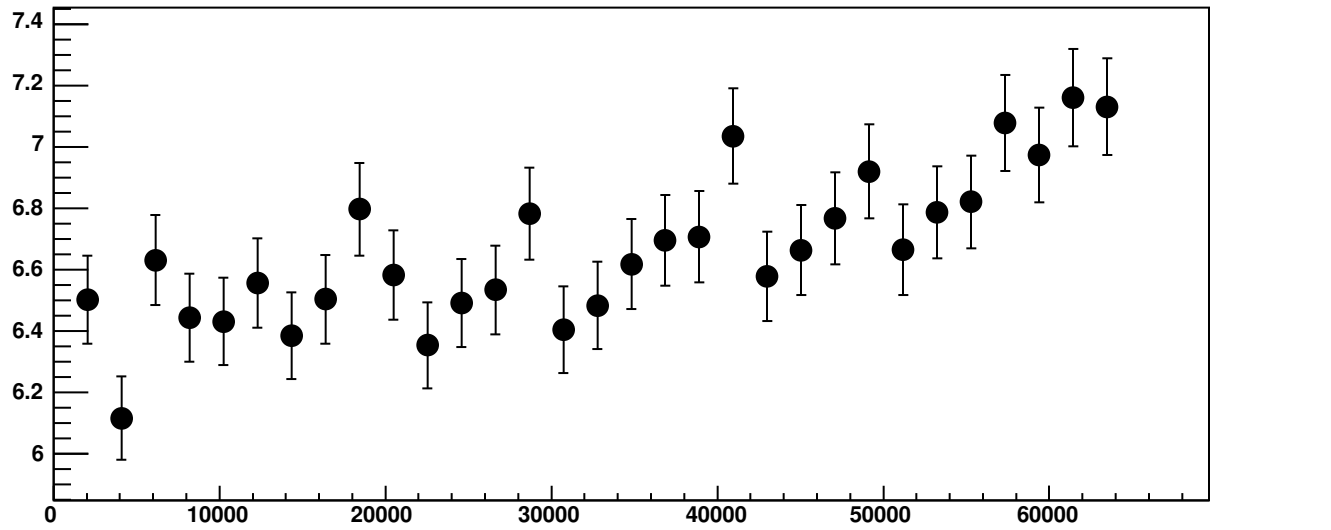

Chip 0, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

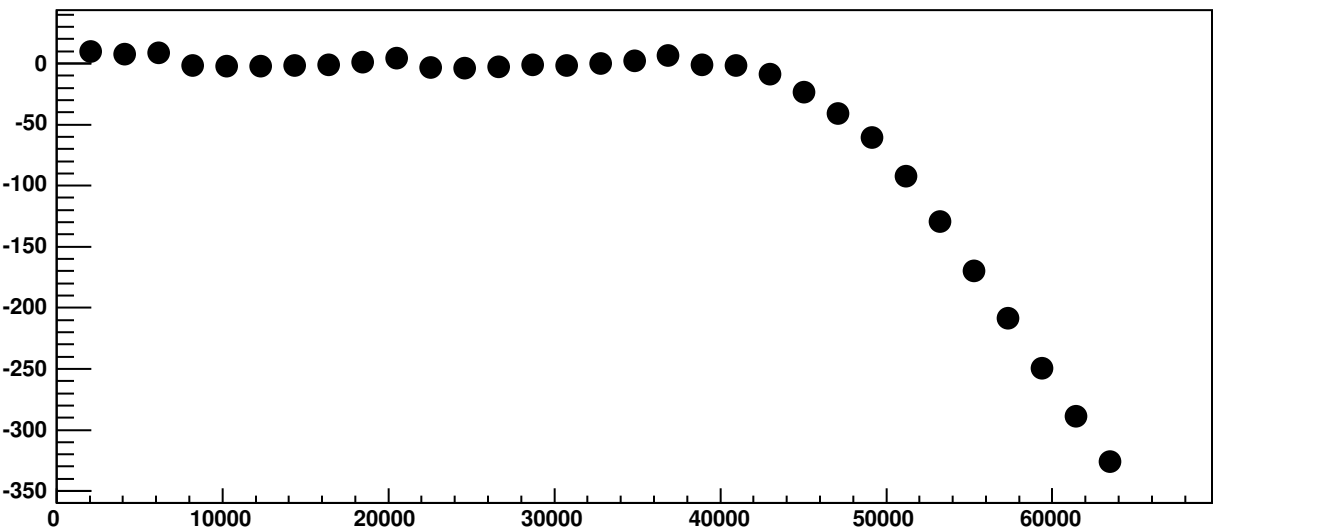
Chip 0, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC

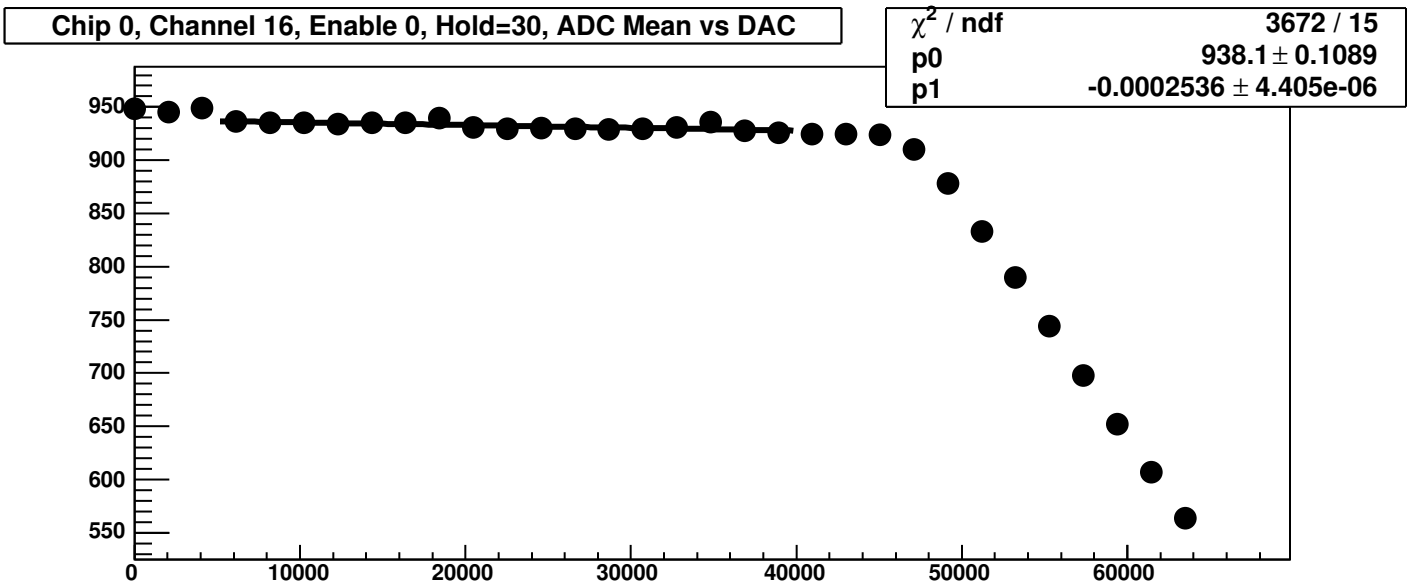


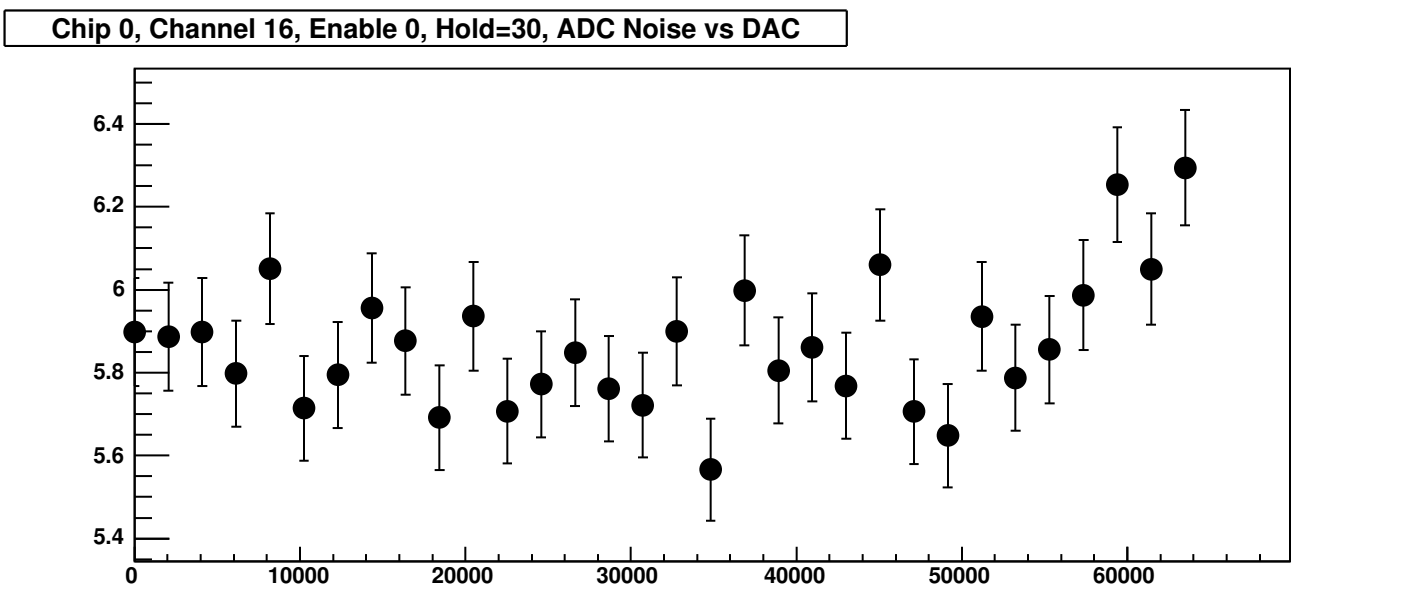
Chip 0, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC

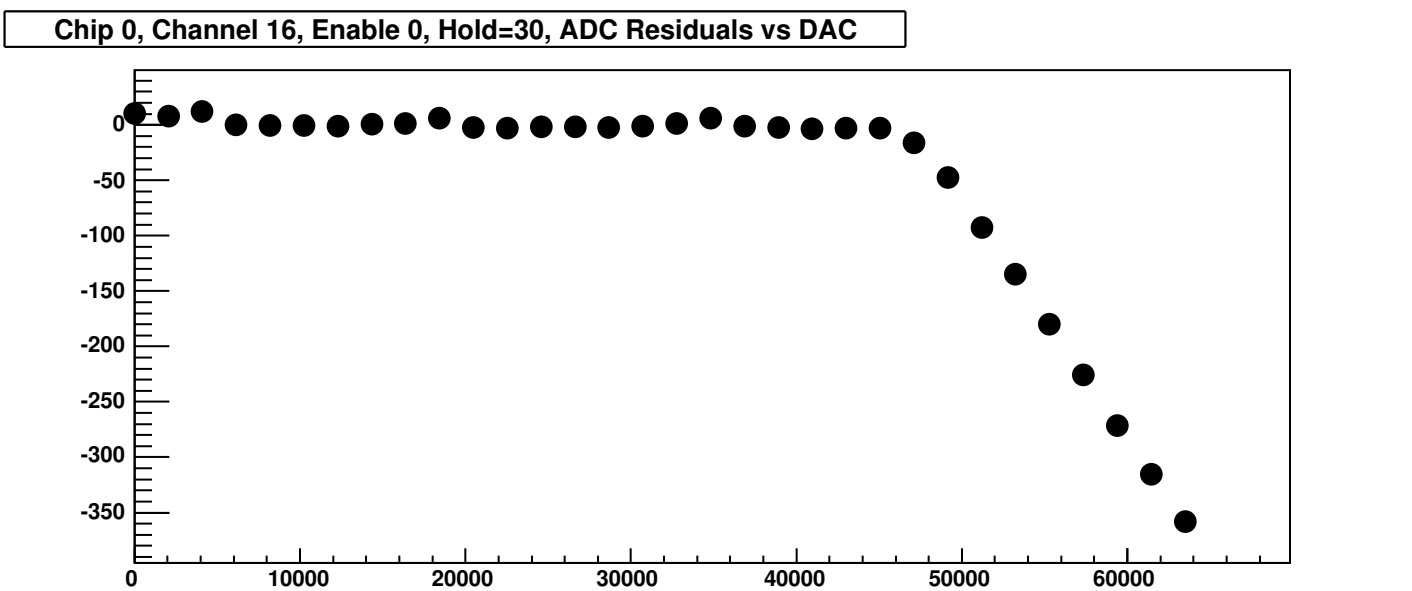


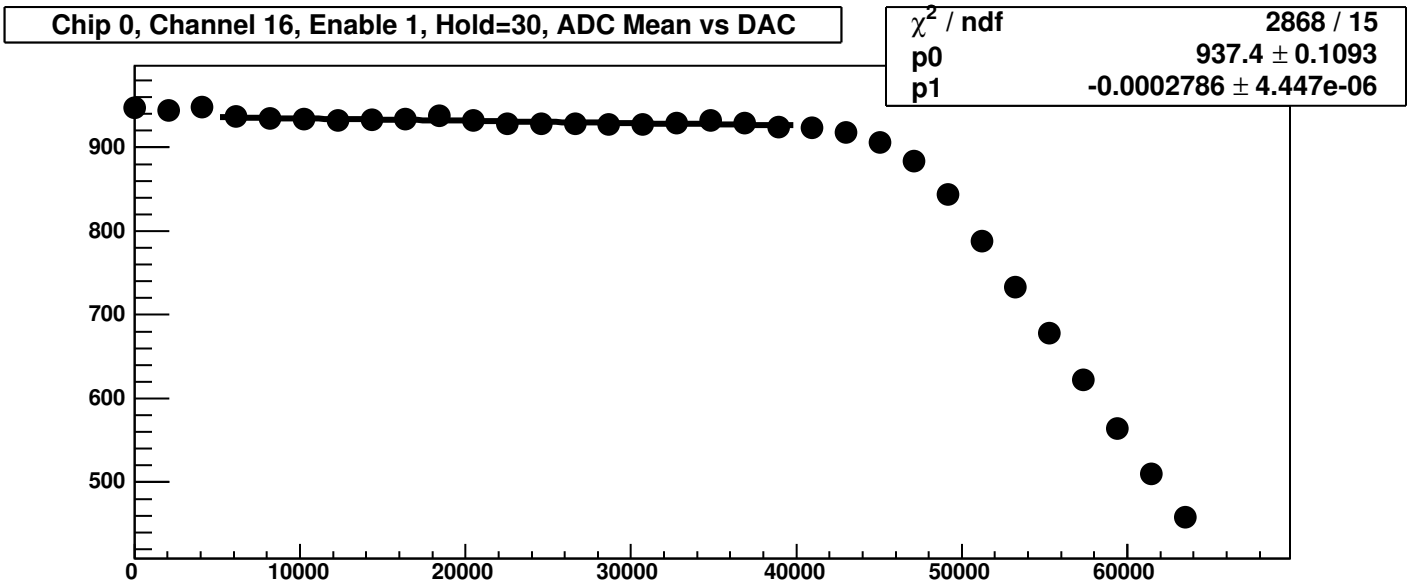
Chip 0, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC

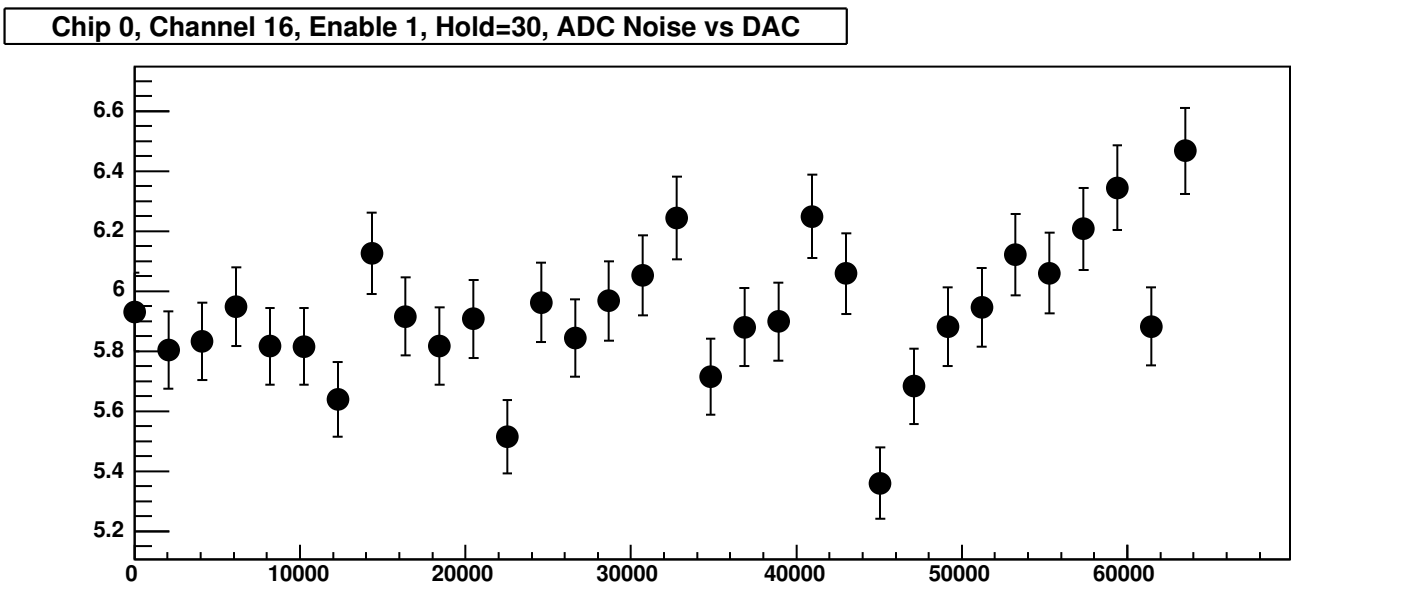


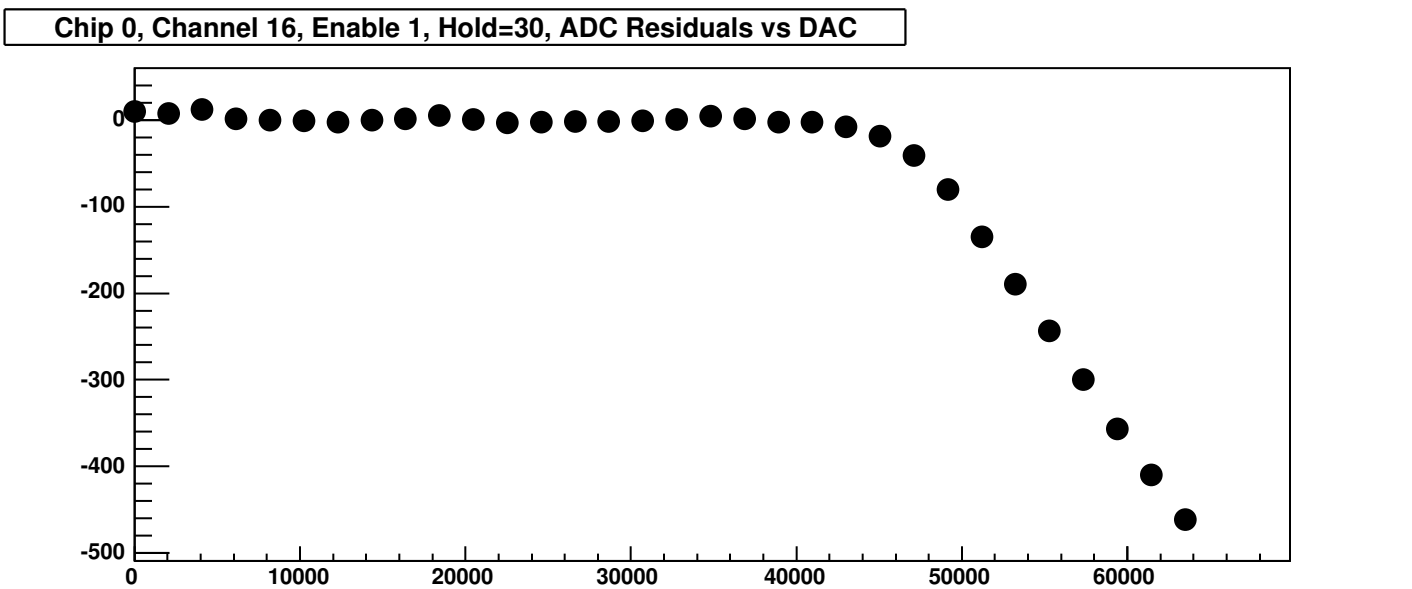

Chip 0, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3672 / 15
p0 938.1 ± 0.1089
p1 -0.0002536 ± 4.405e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

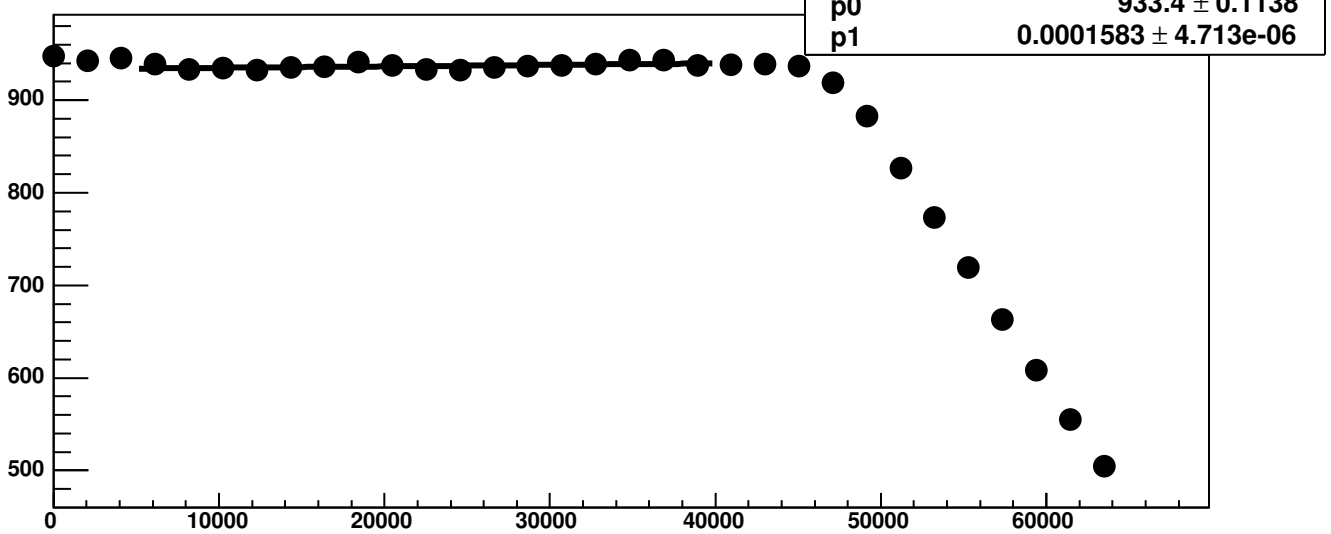

Chip 0, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2868 / 15
p0 937.4 ± 0.1093
p1 -0.0002786 ± 4.447e-06
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000

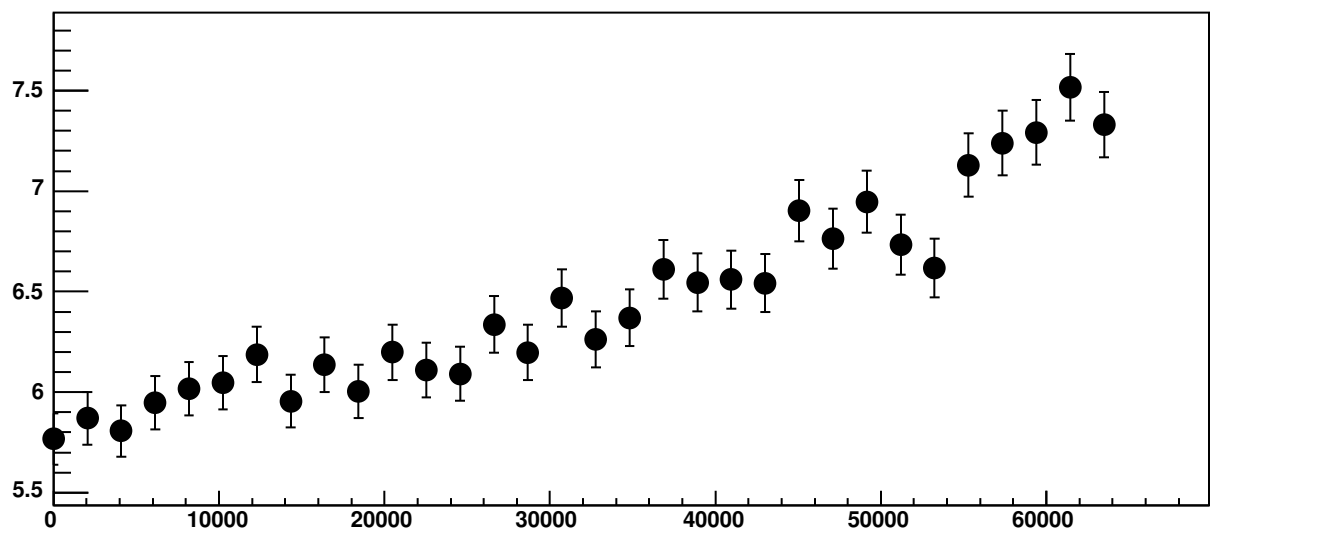

Chip 0, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

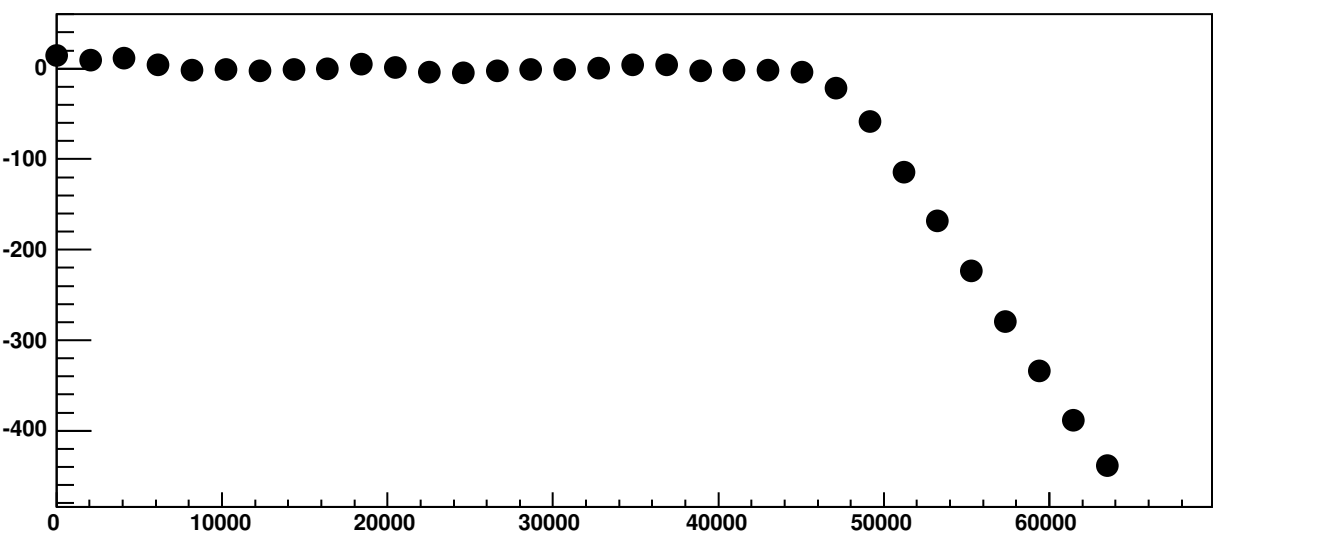
Chip 0, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC

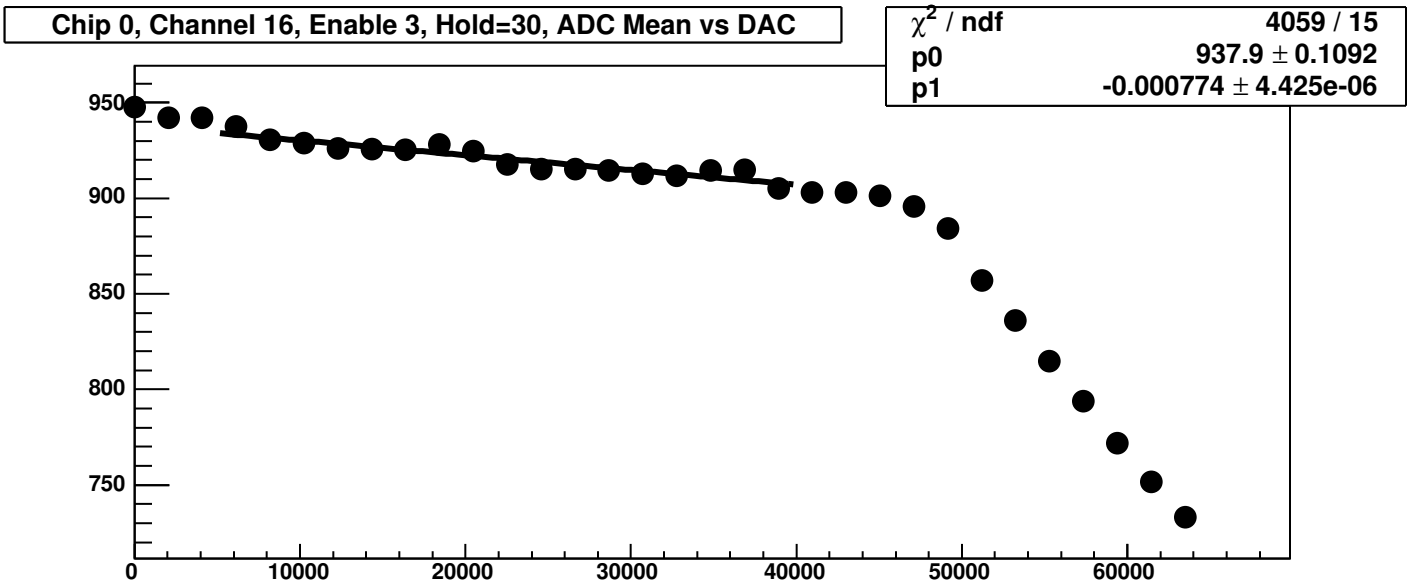


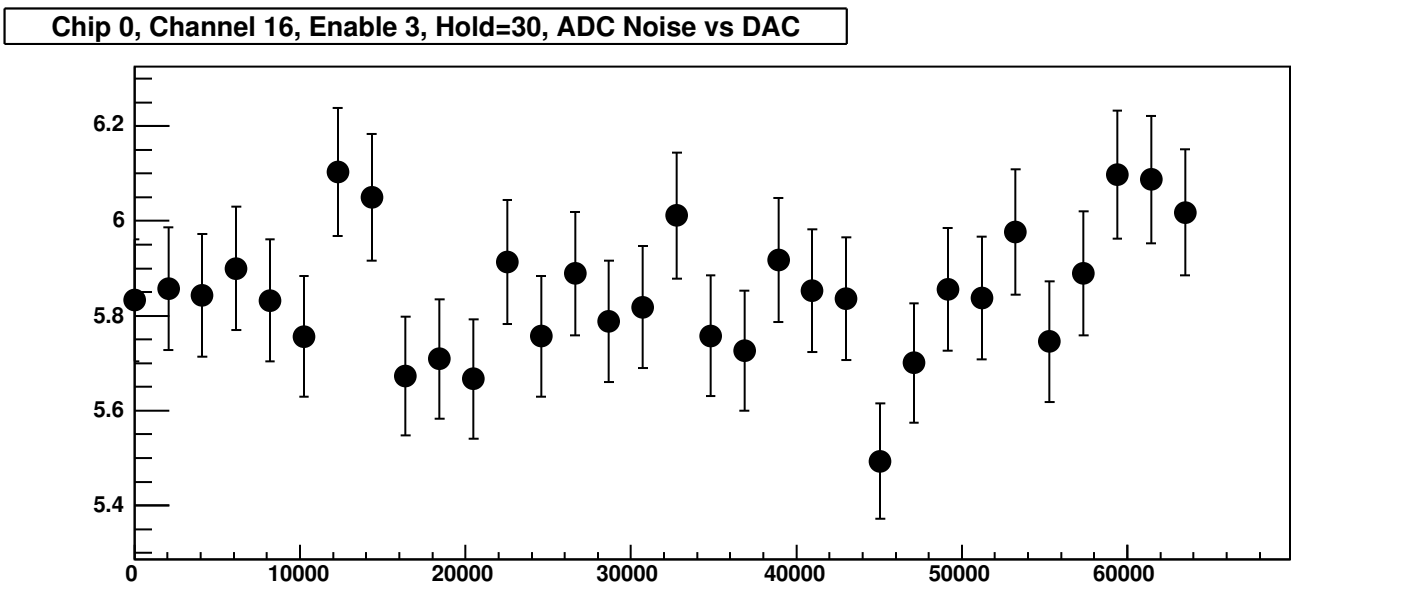
Chip 0, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC

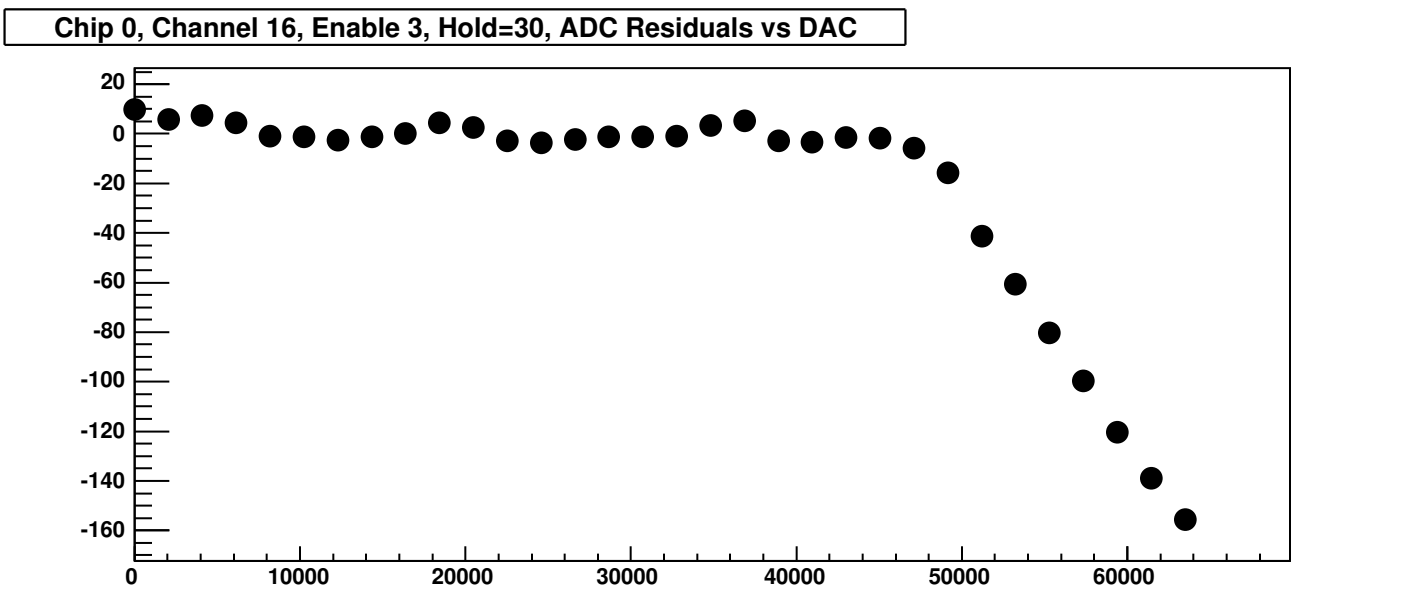


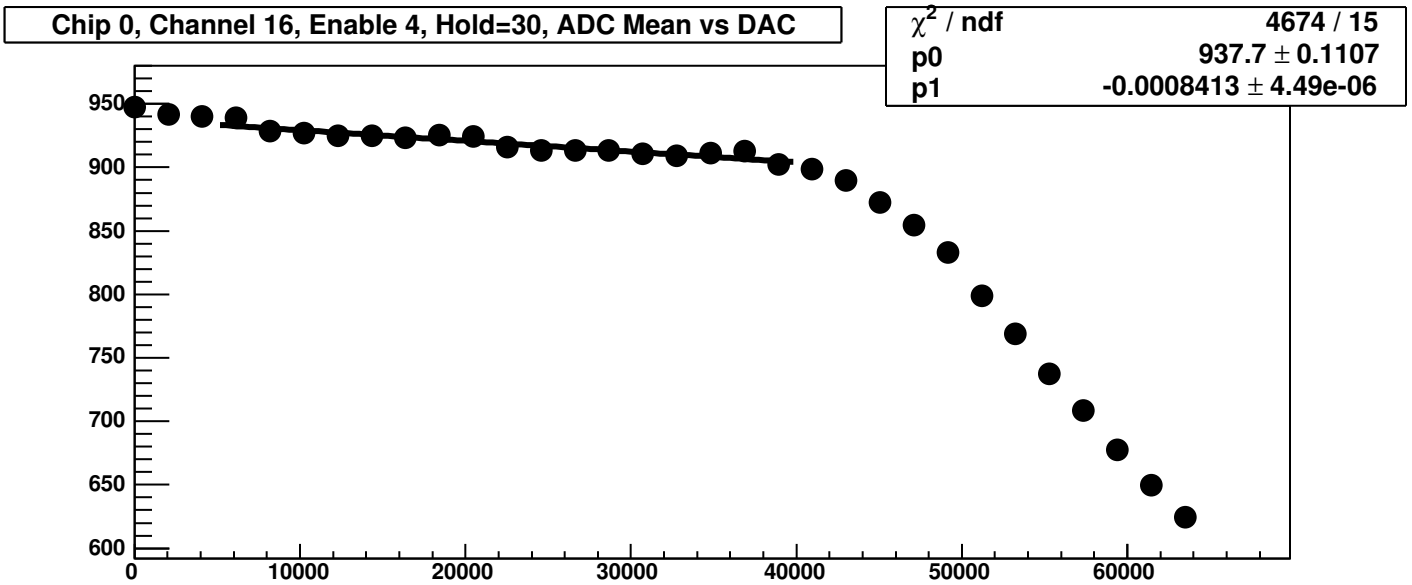
Chip 0, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC

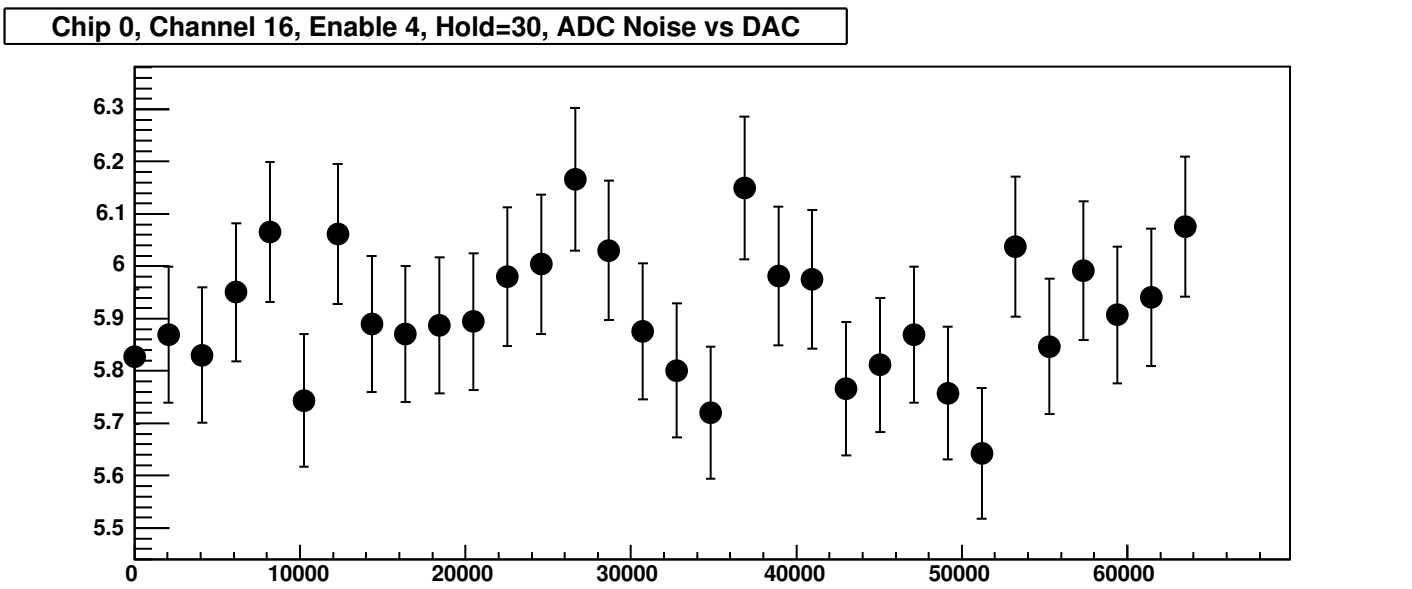


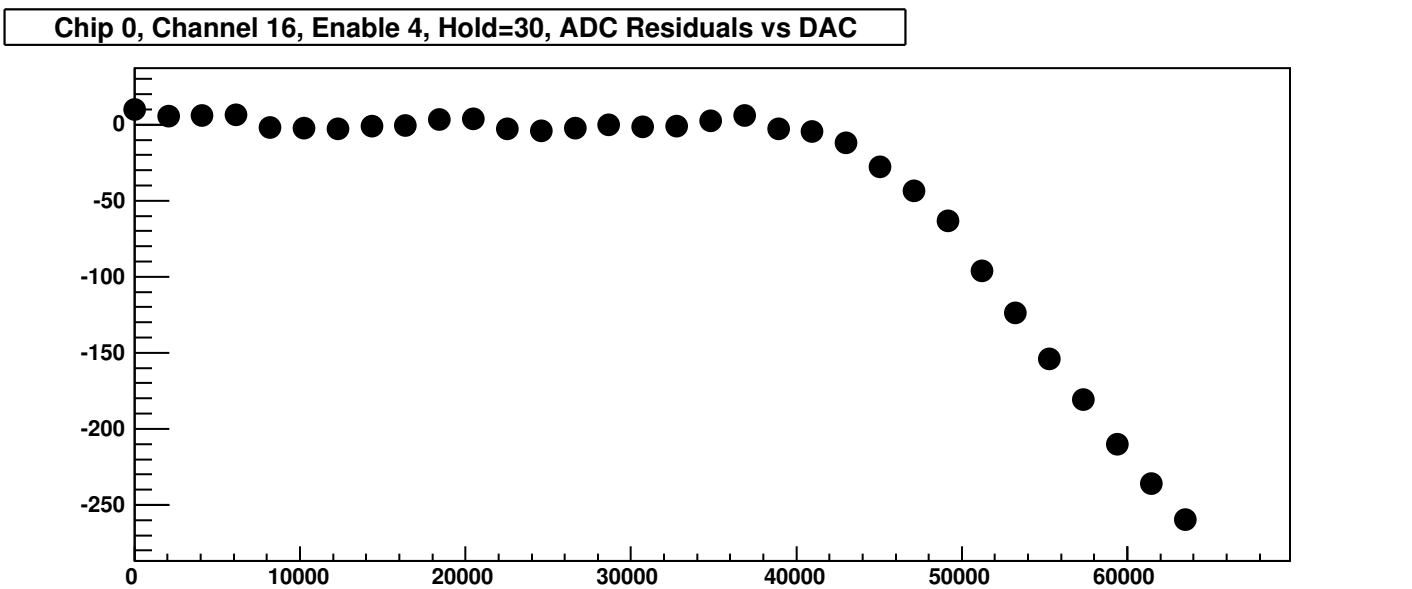

Chip 0, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4059 / 15
p0 937.9 ± 0.1092
p1 -0.000774 ± 4.425e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

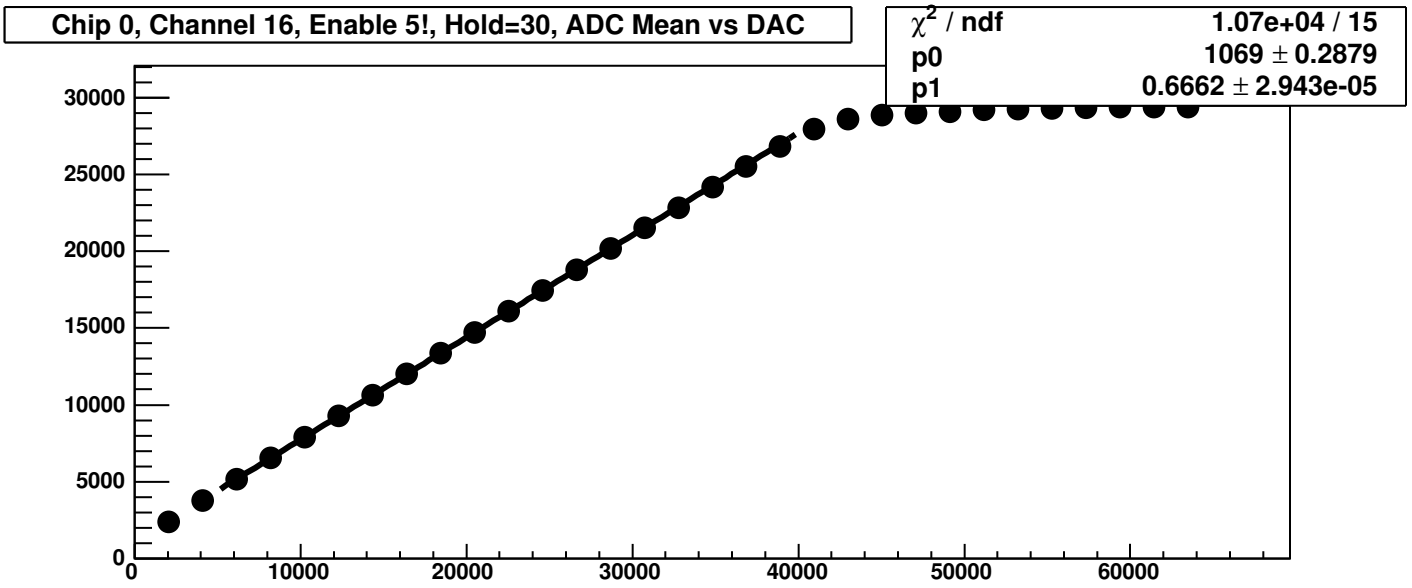

Chip 0, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

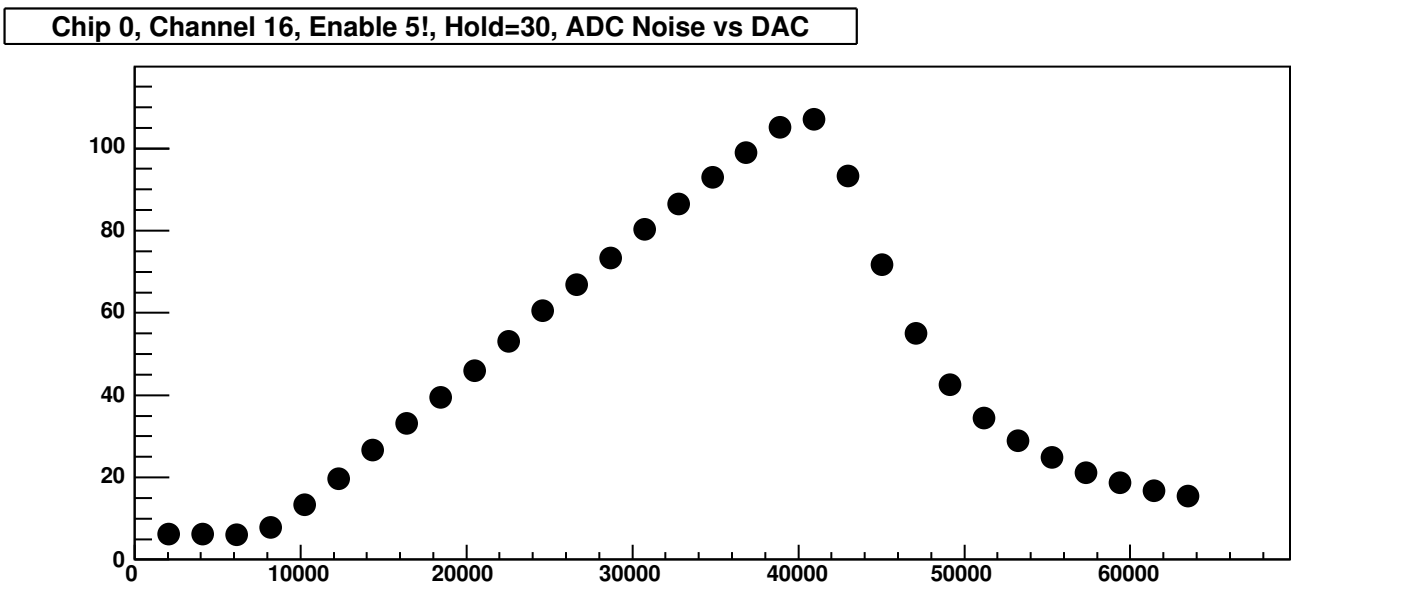

Chip 0, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

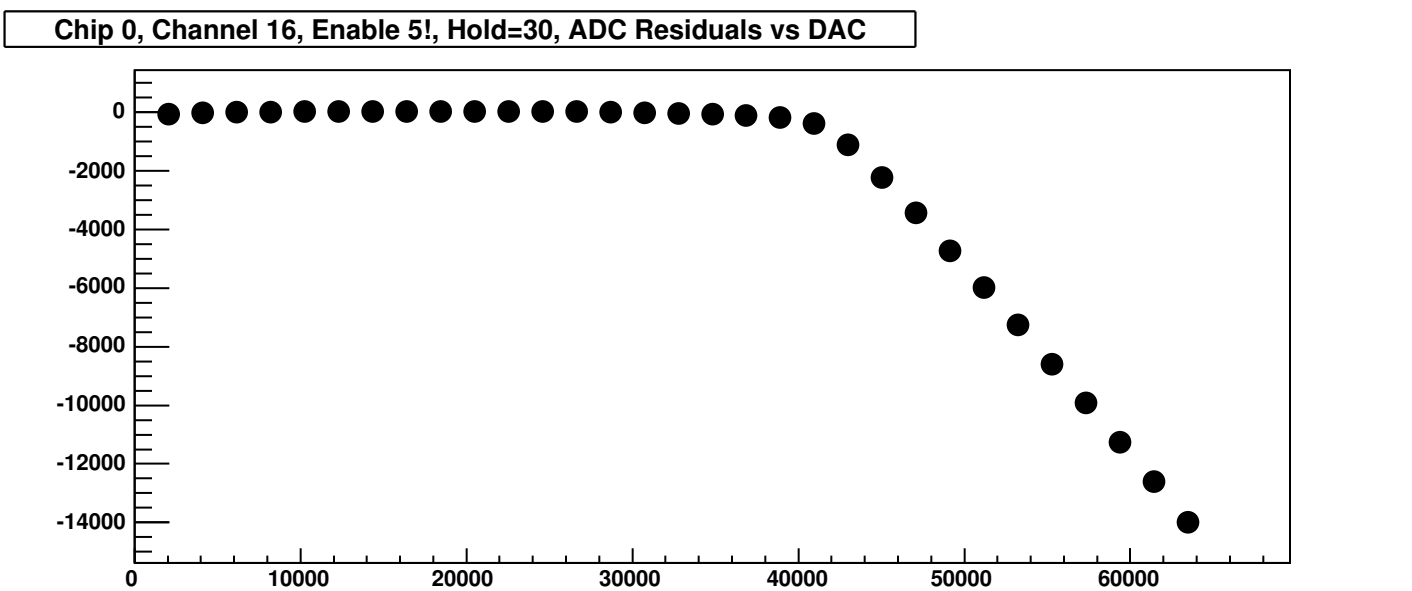

Chip 0, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4674 / 15
p0 937.7 ± 0.1107
p1 -0.0008413 ± 4.49e-06
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

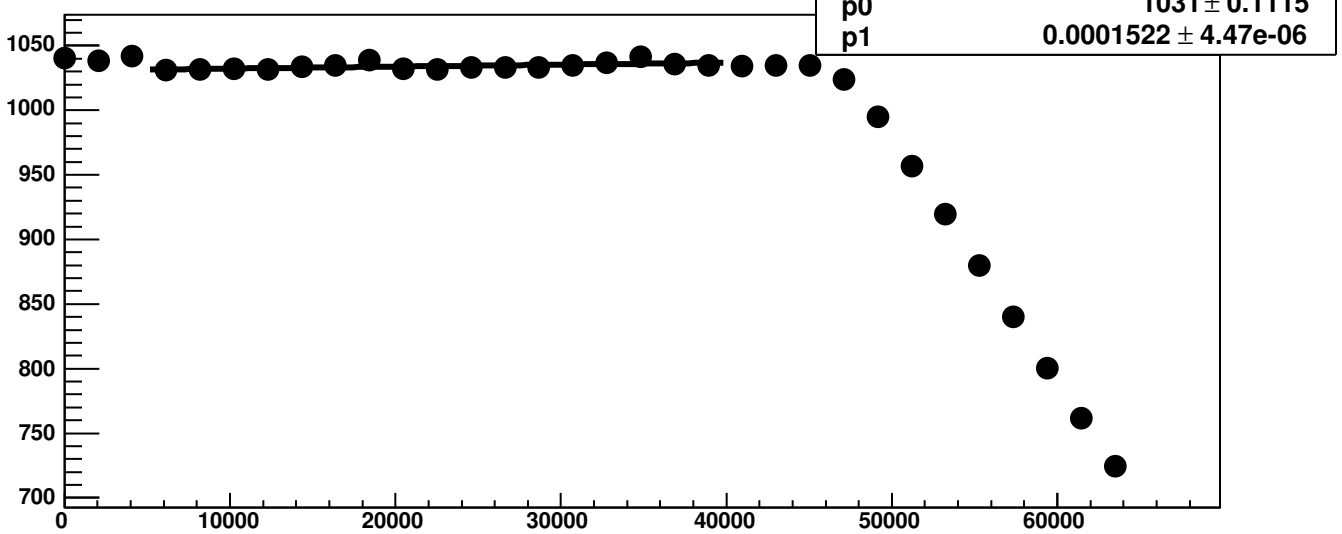

Chip 0, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.07e+04 / 15
p0 1069 ± 0.2879
p1 0.6662 ± 2.943e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

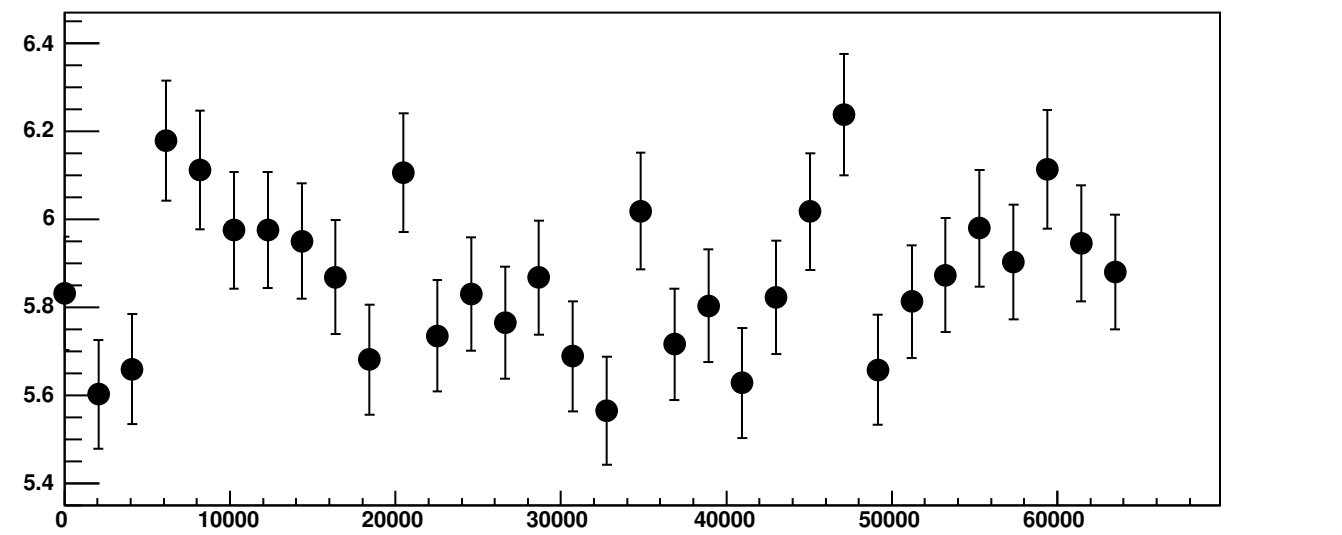

Chip 0, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

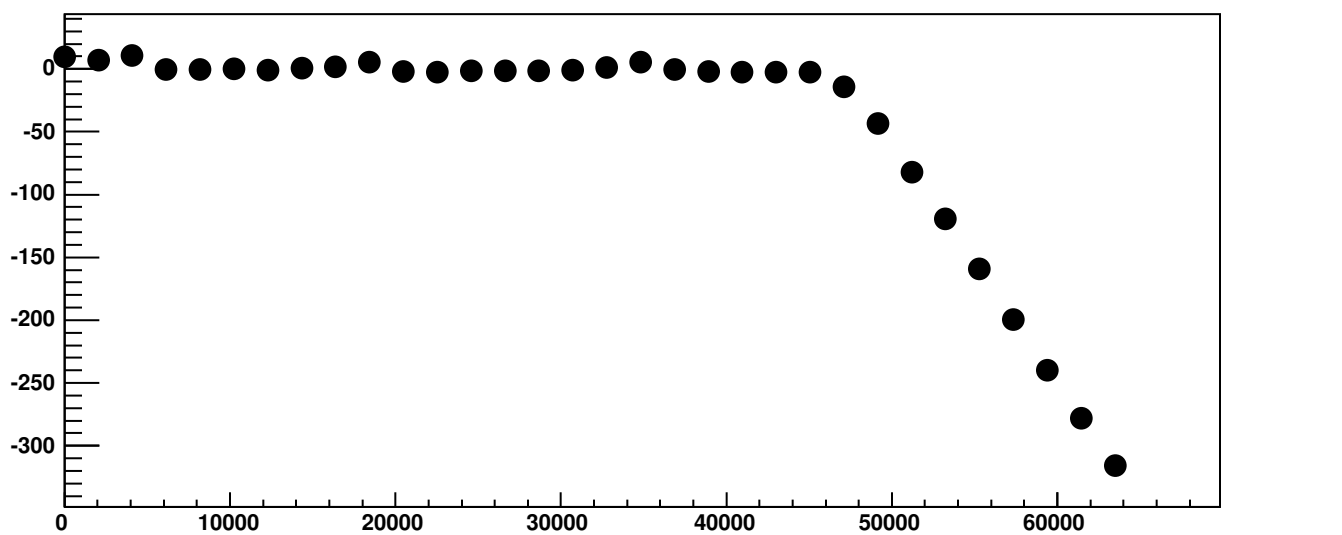
Chip 0, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC

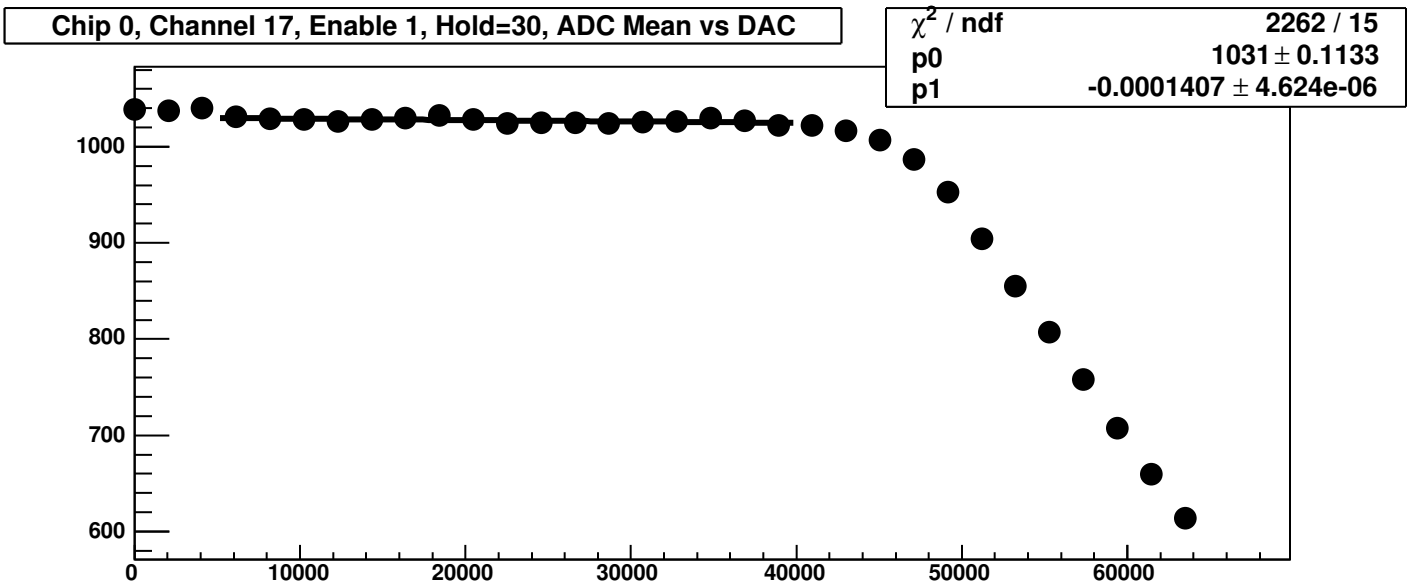


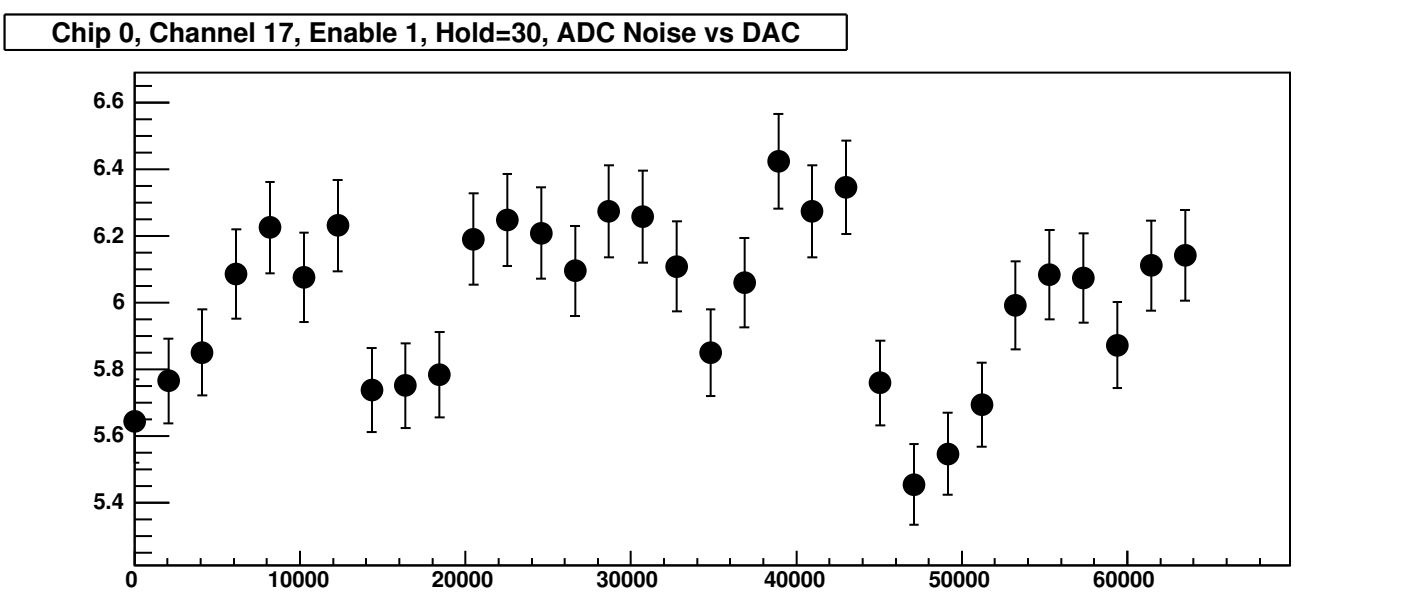
Chip 0, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC

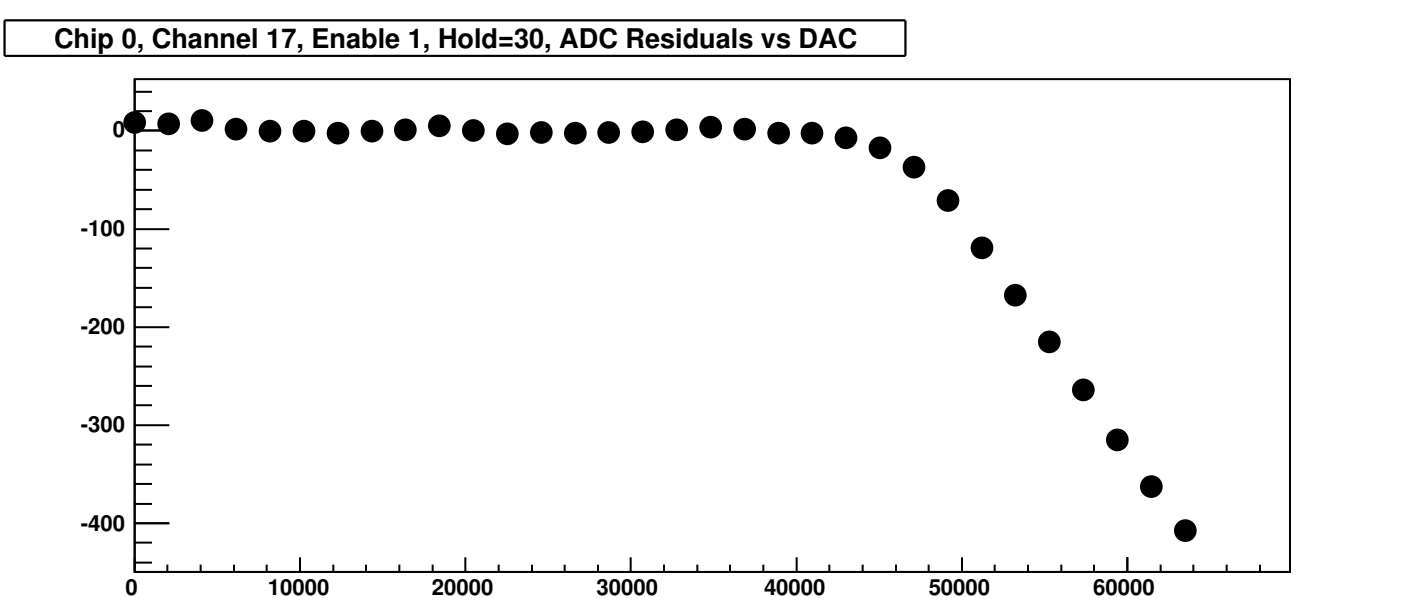


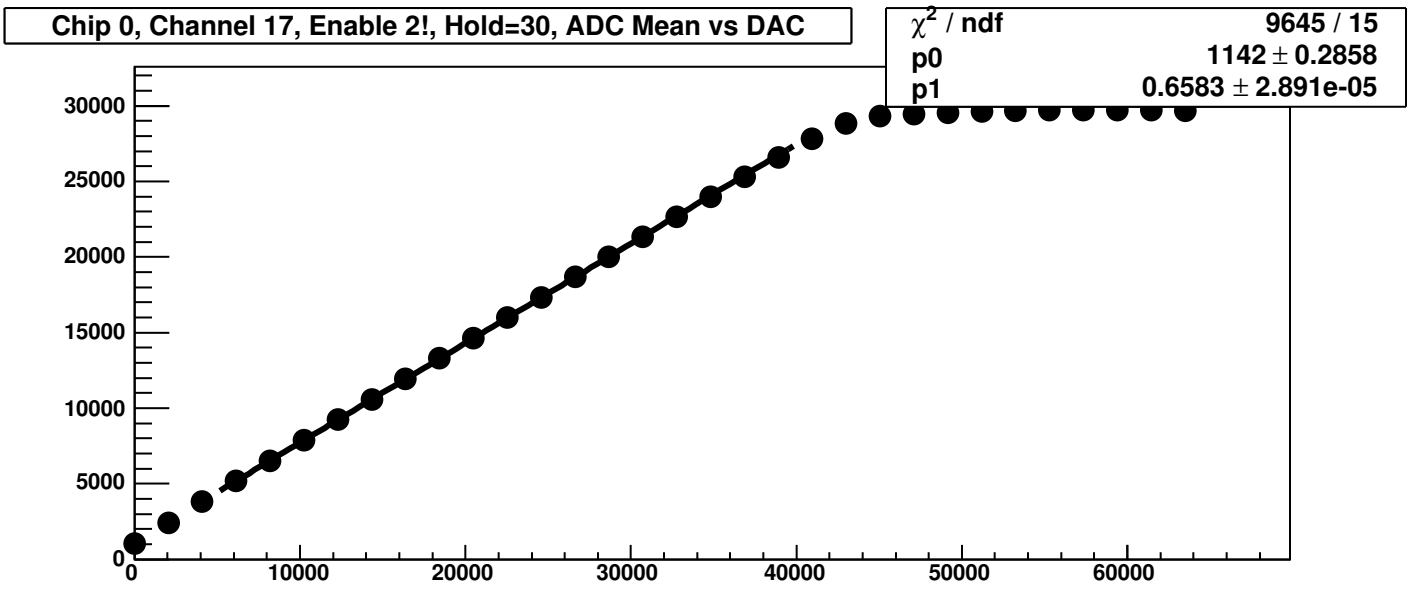
Chip 0, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

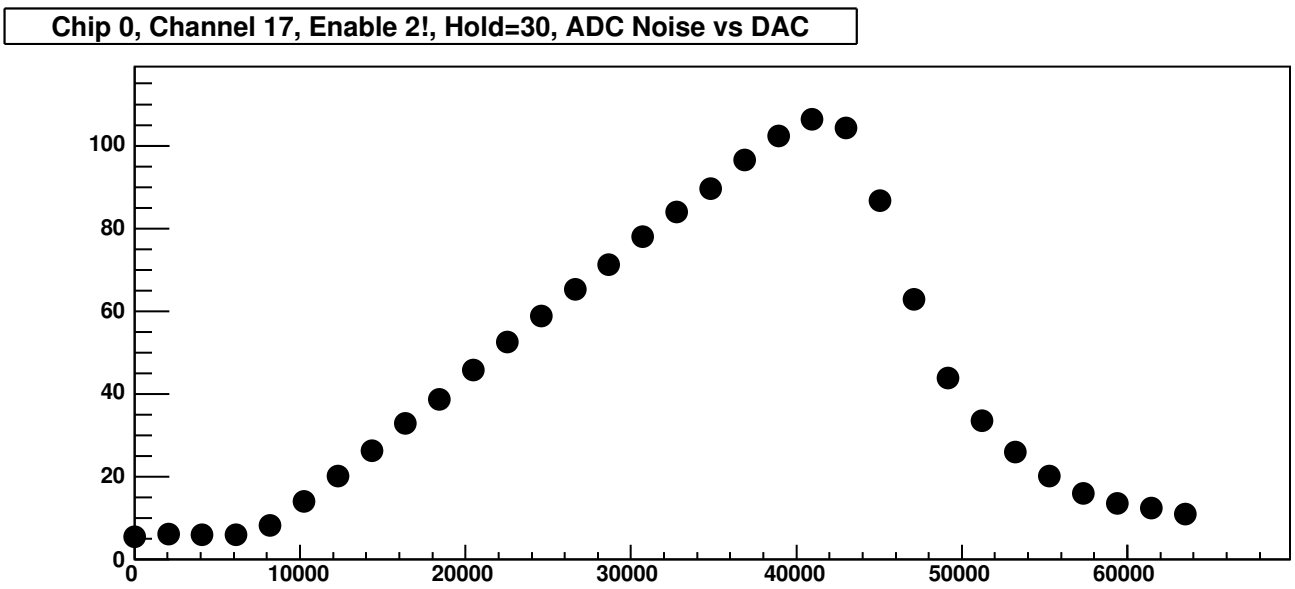


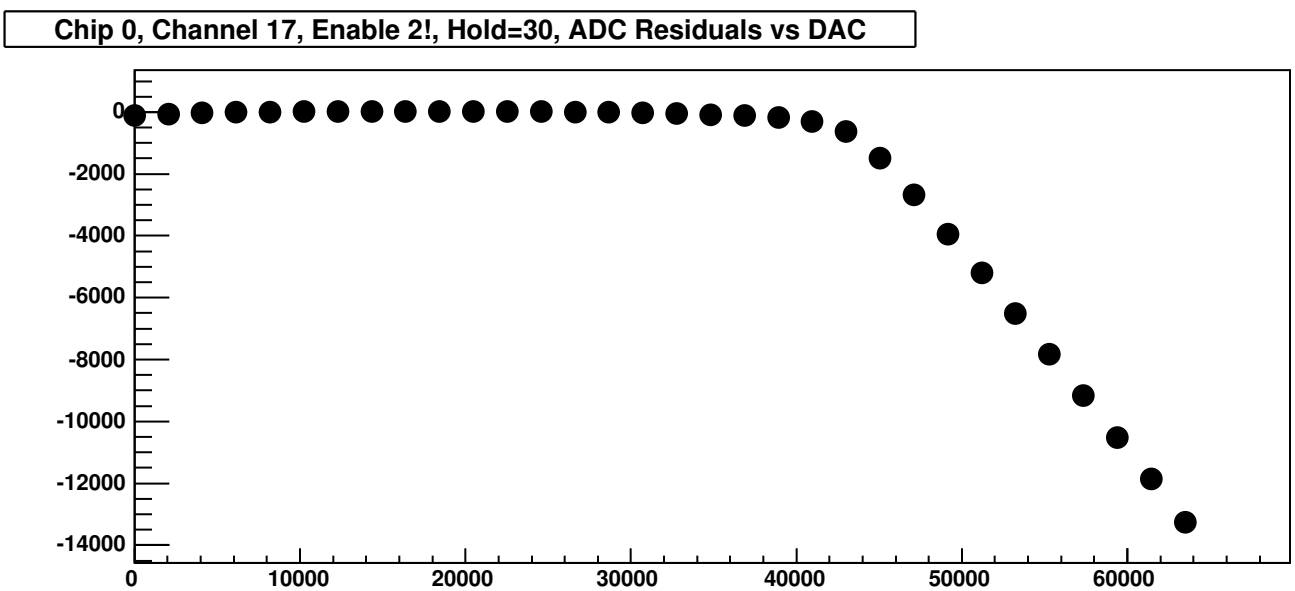

Chip 0, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2262 / 15
p0 1031 ± 0.1133
p1 -0.0001407 ± 4.624e-06
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000

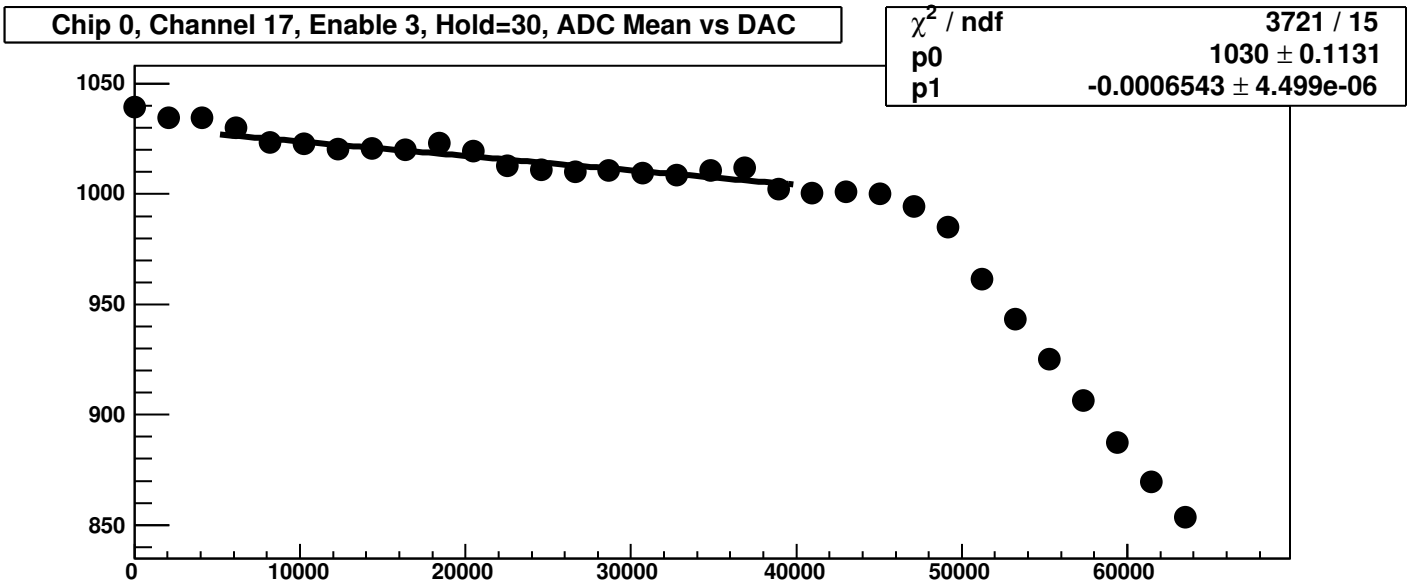

Chip 0, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

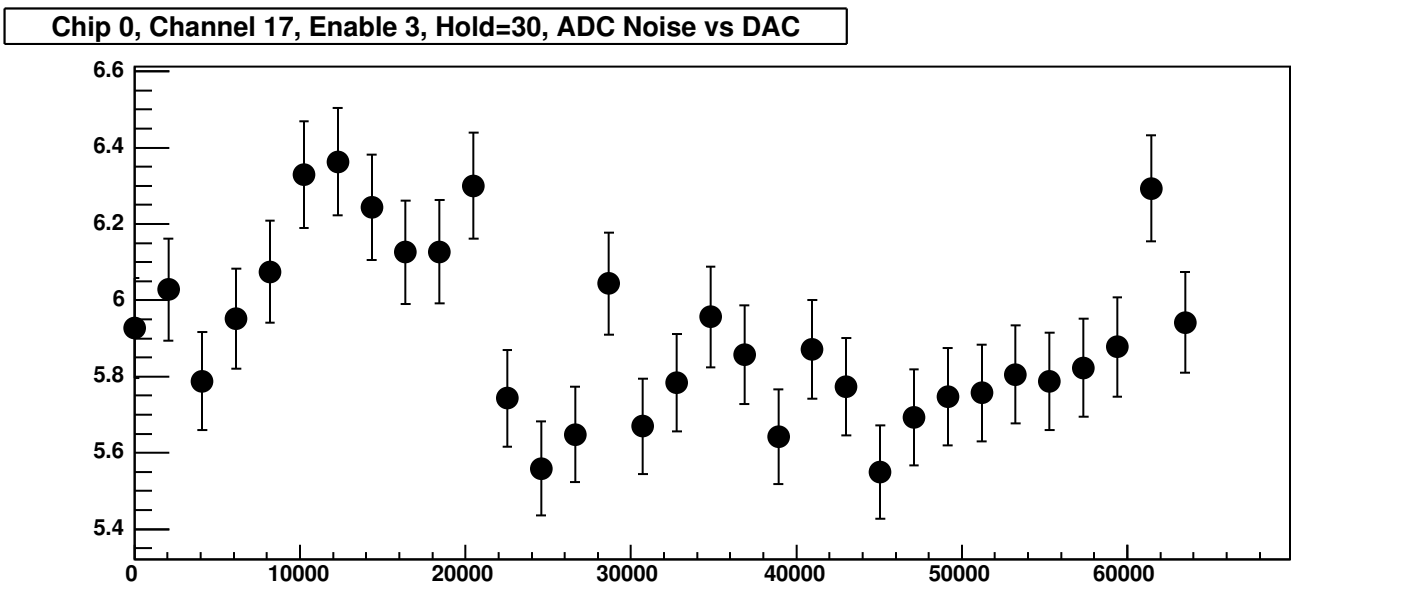

Chip 0, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

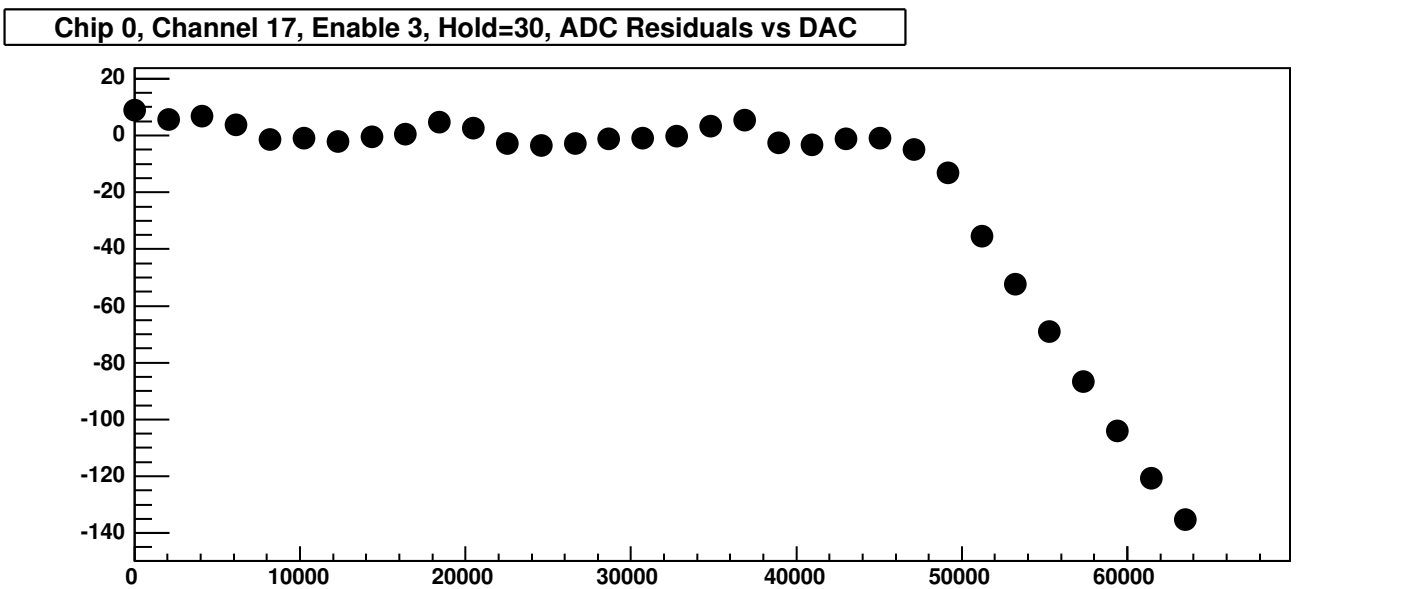

Chip 0, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 9645 / 15
p0 1142 ± 0.2858
p1 0.6583 ± 2.891e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

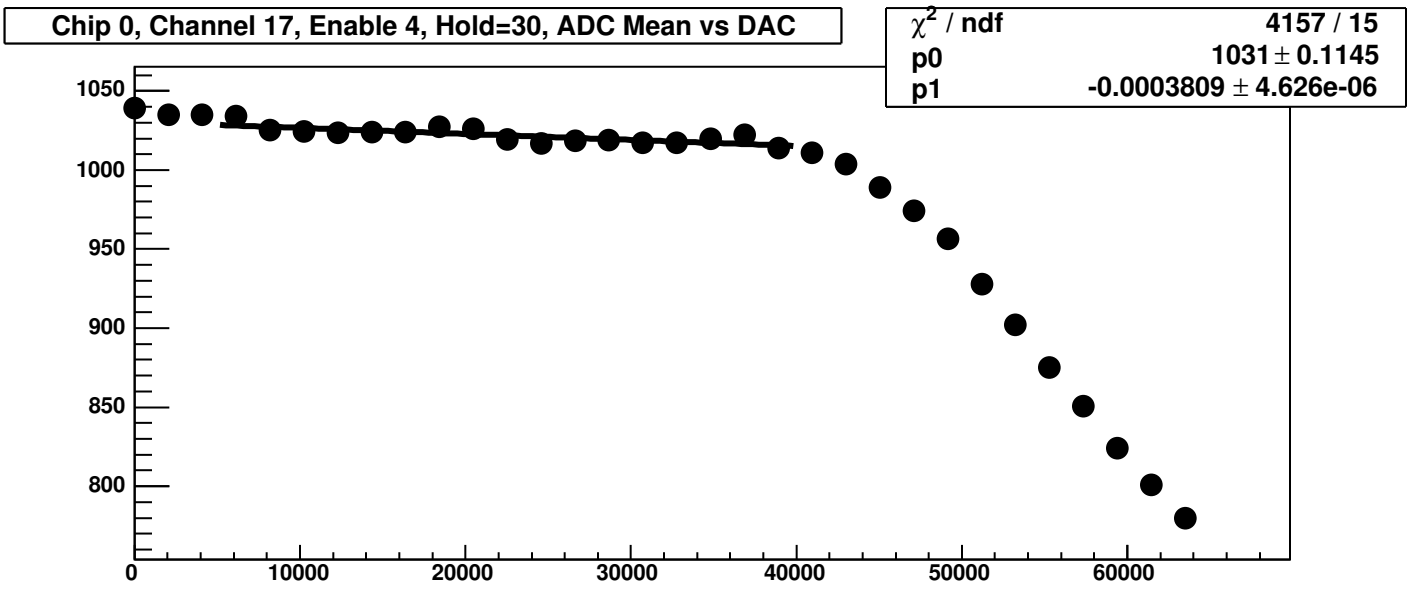

Chip 0, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

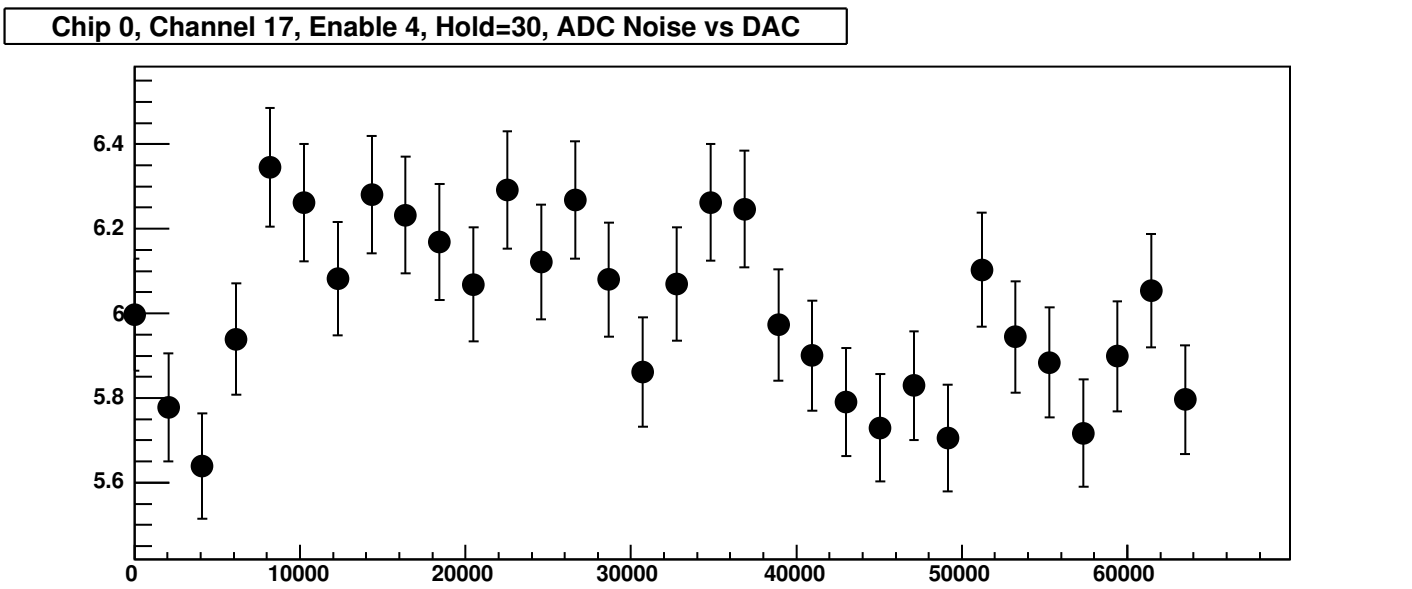

Chip 0, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

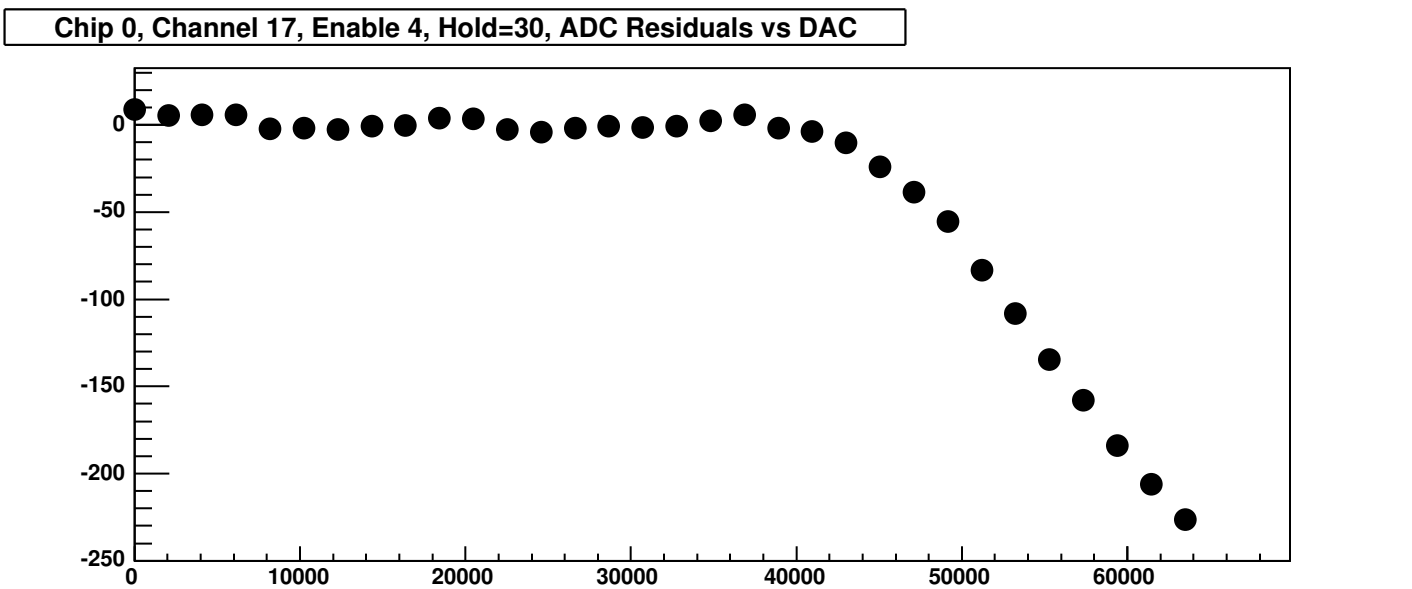

Chip 0, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3721 / 15
p0 1030 ± 0.1131
p1 -0.0006543 ± 4.499e-06
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

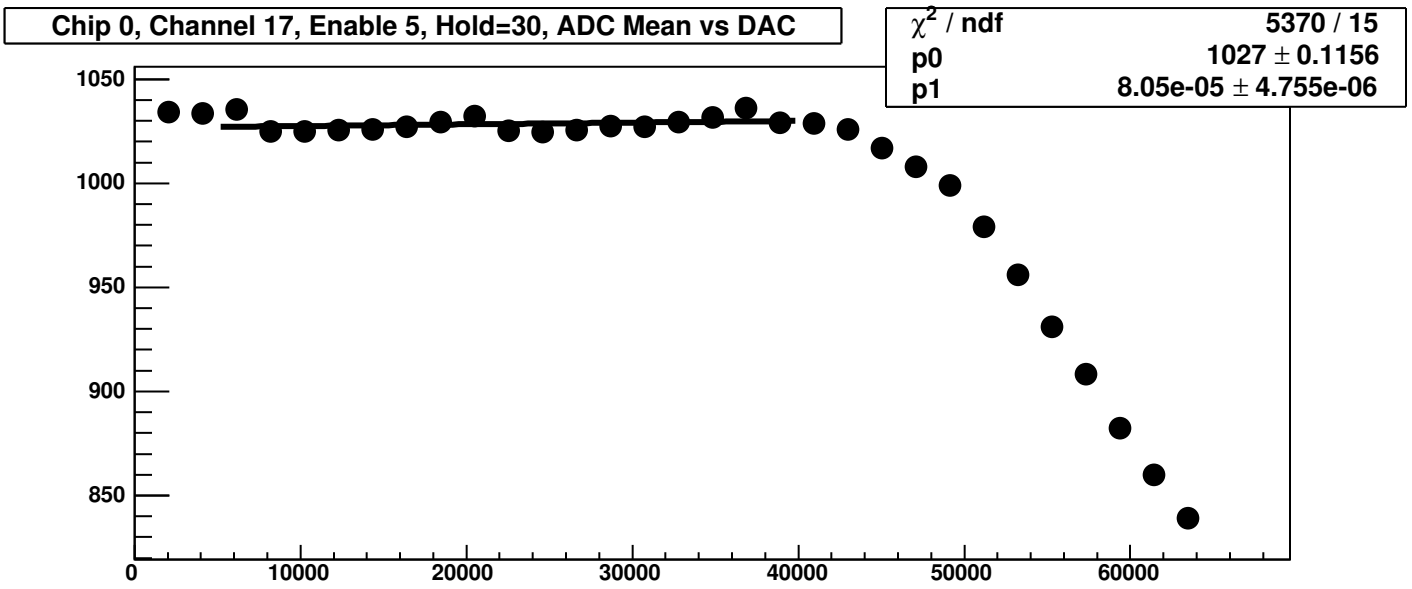

Chip 0, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

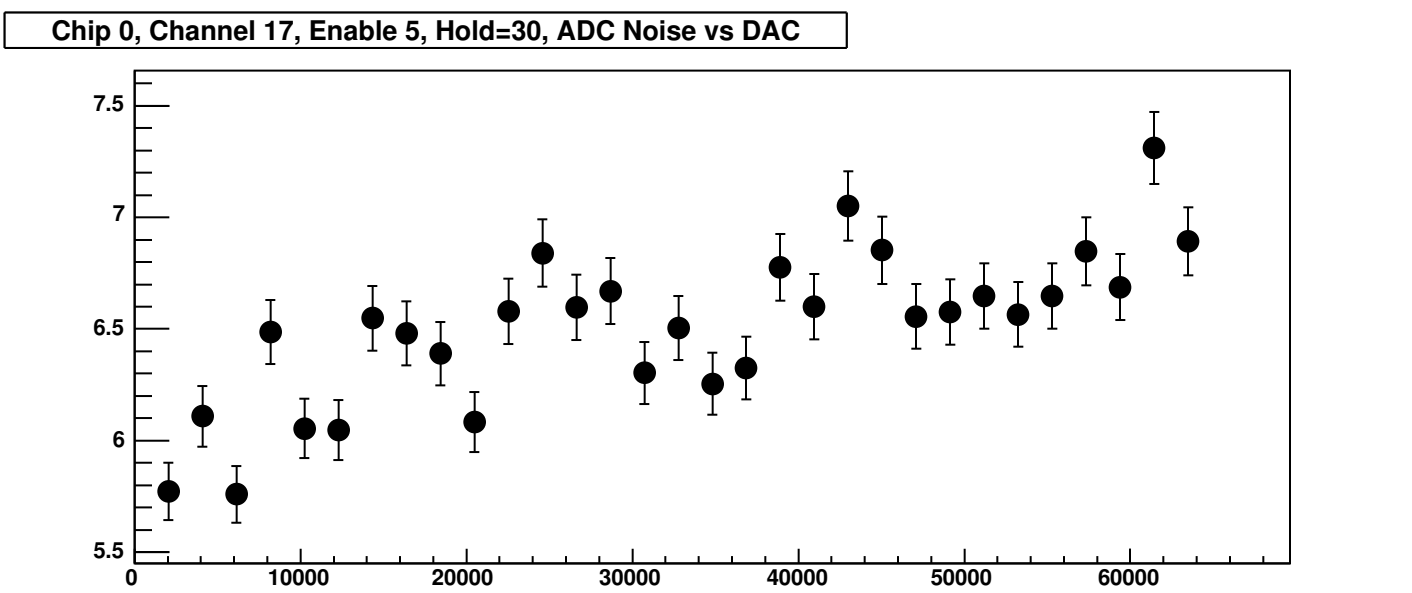

Chip 0, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

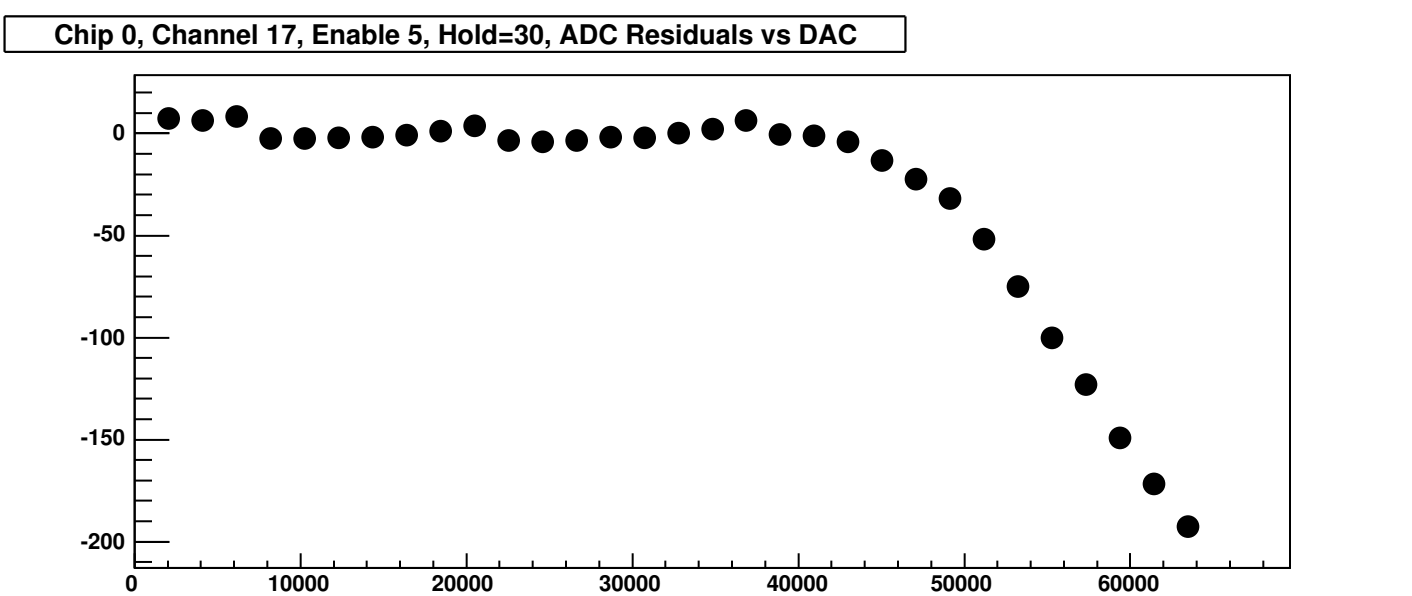

Chip 0, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4157 / 15
p0 1031 ± 0.1145
p1 -0.0003809 ± 4.626e-06
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

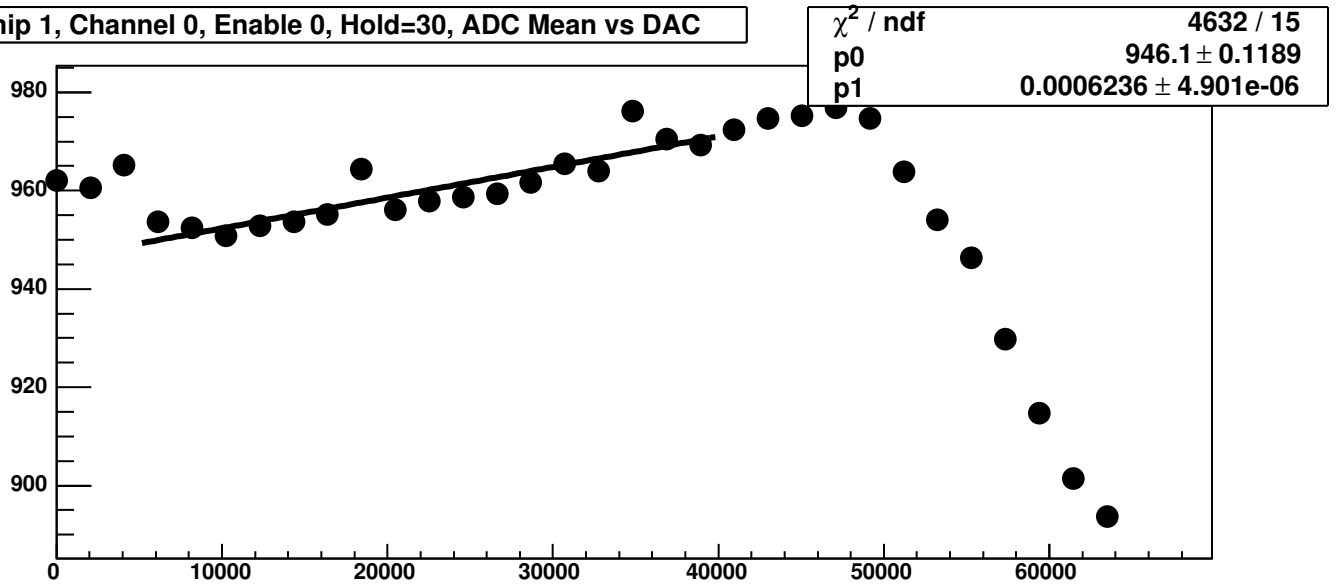

Chip 0, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5370 / 15
p0 1027 ± 0.1156
p1 8.05e-05 ± 4.755e-06
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

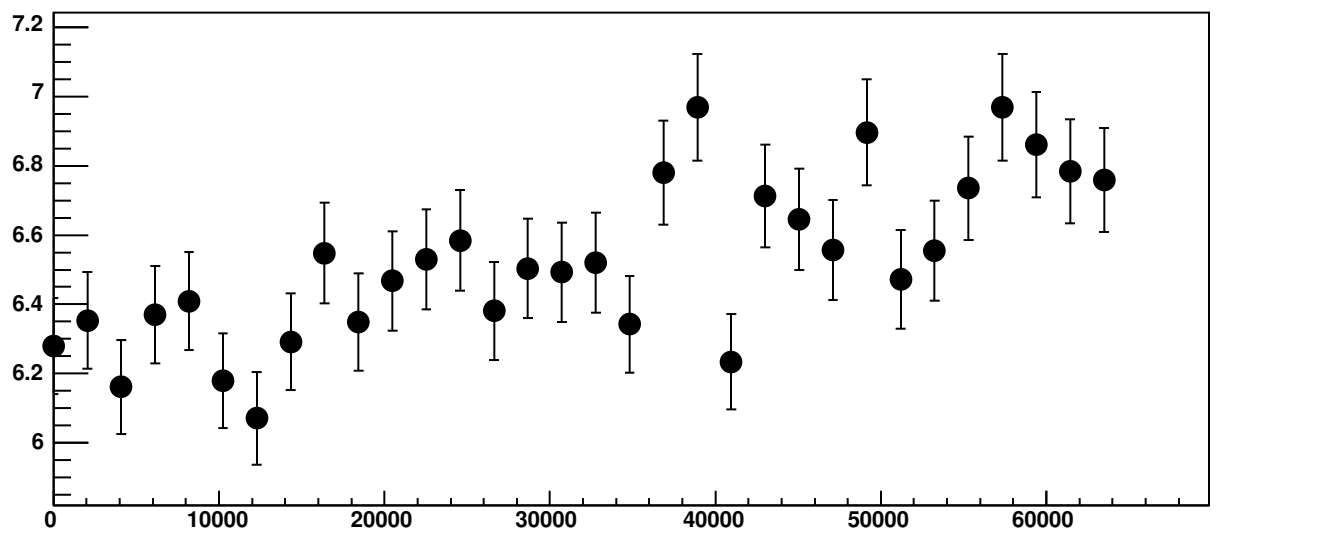

Chip 0, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

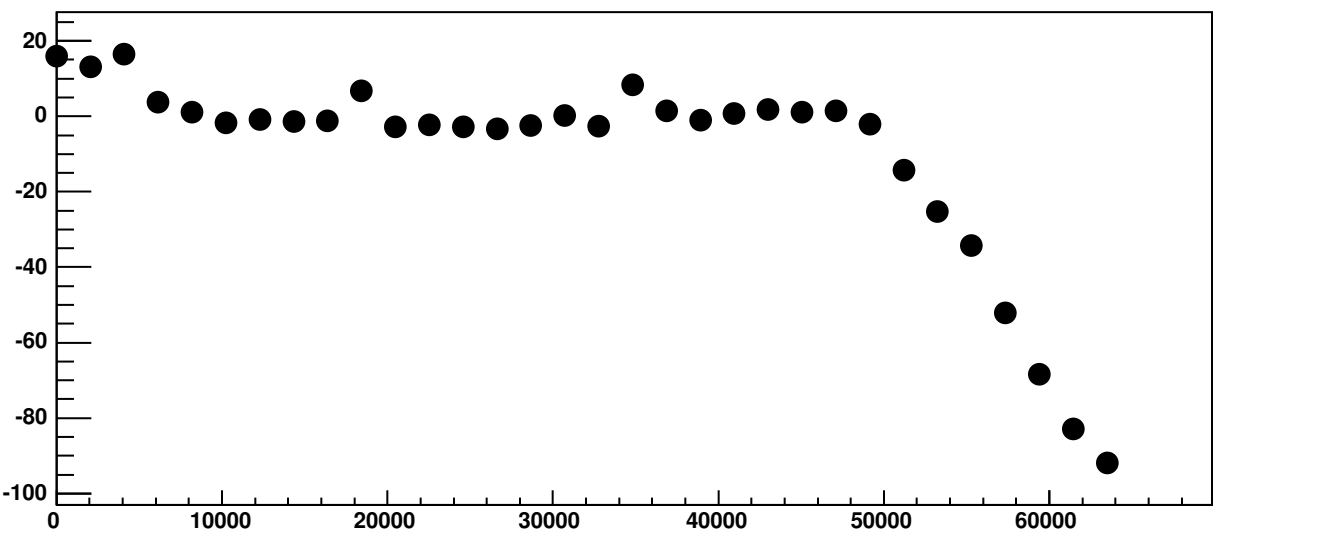
Chip 1, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC

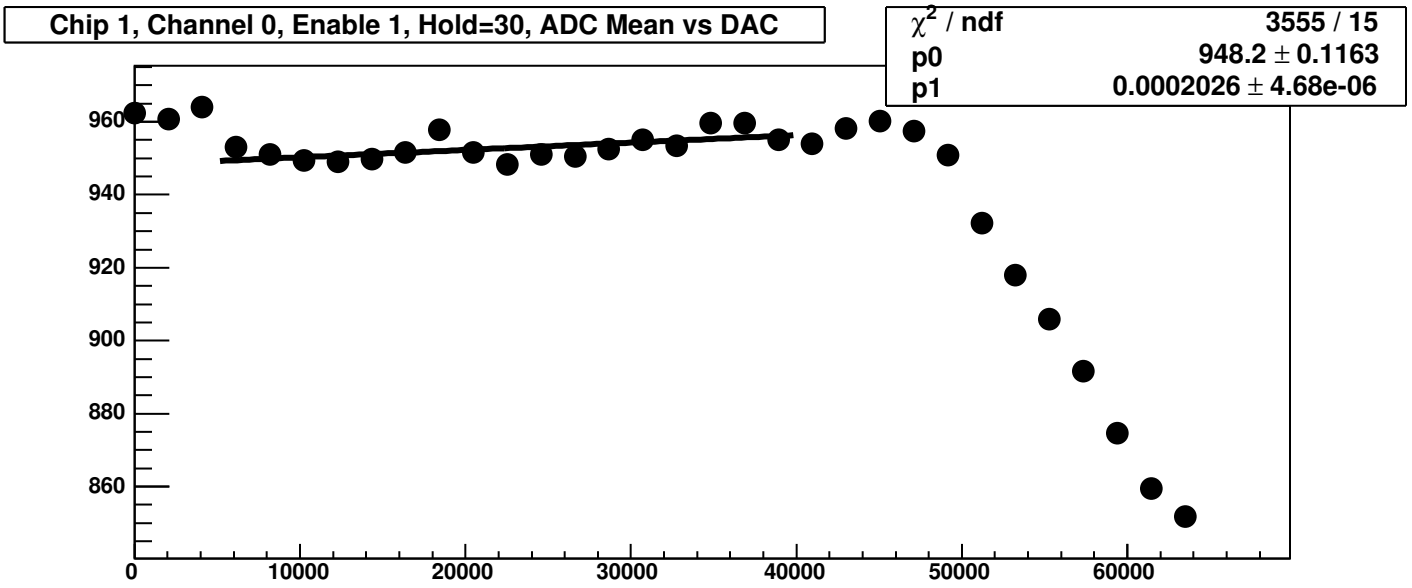


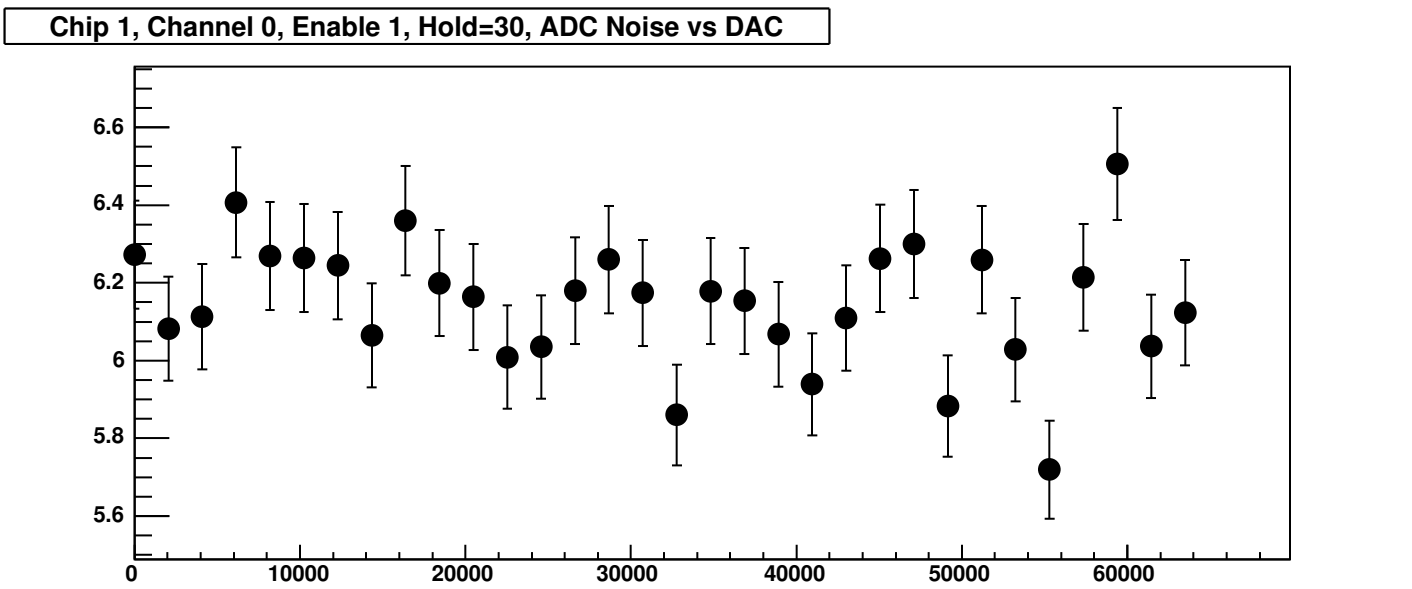
Chip 1, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC

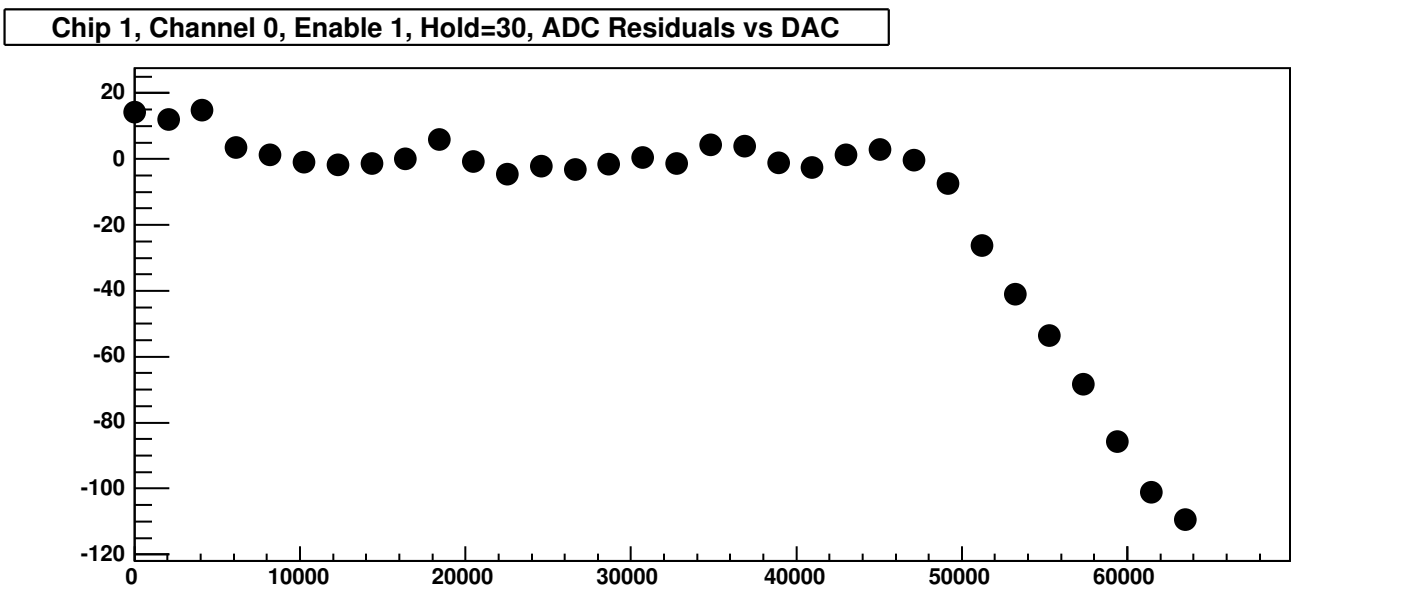


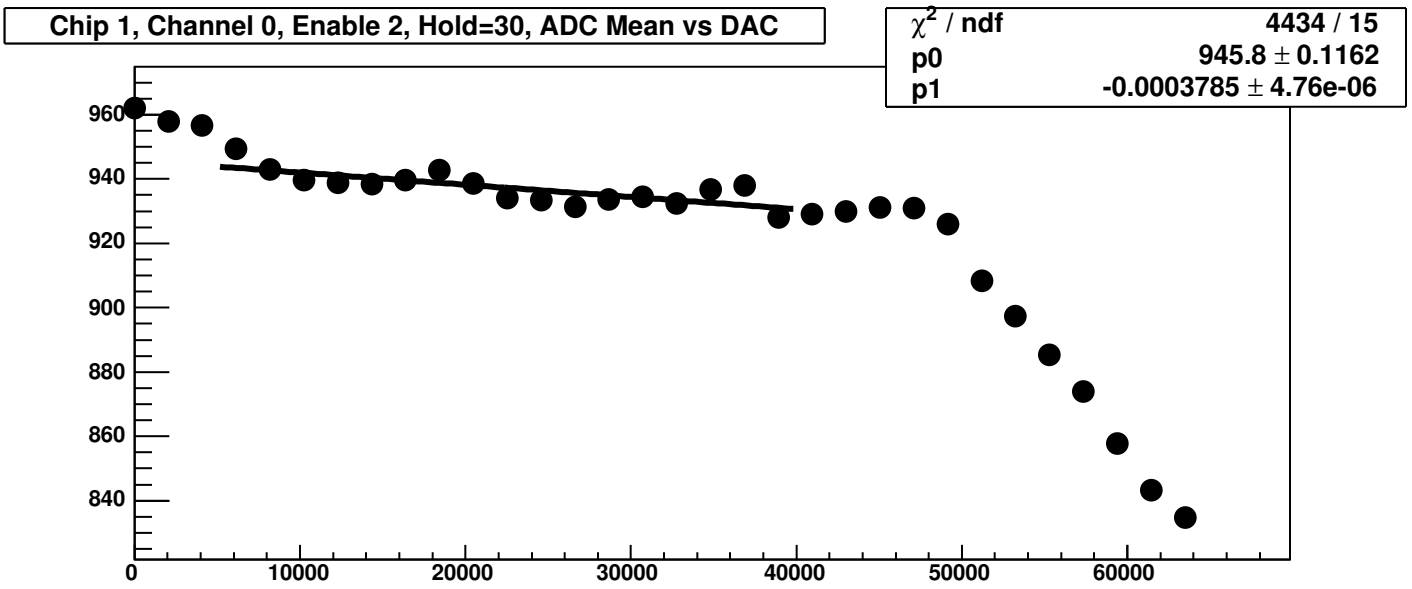
Chip 1, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC

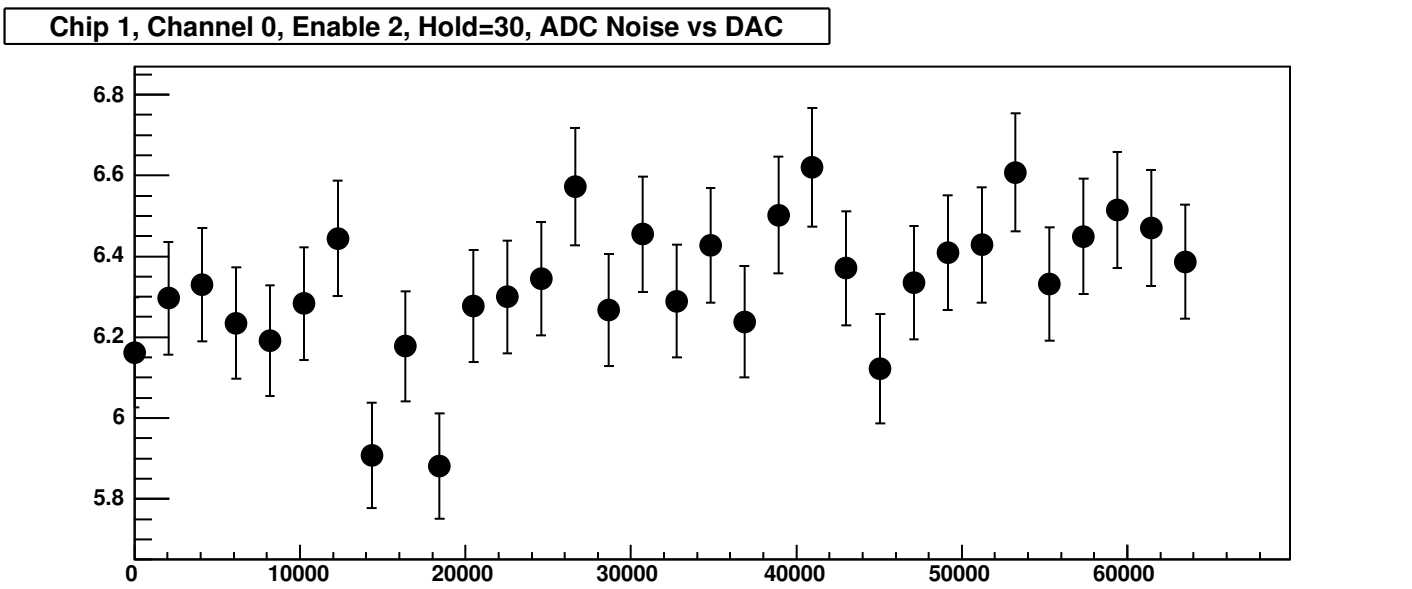


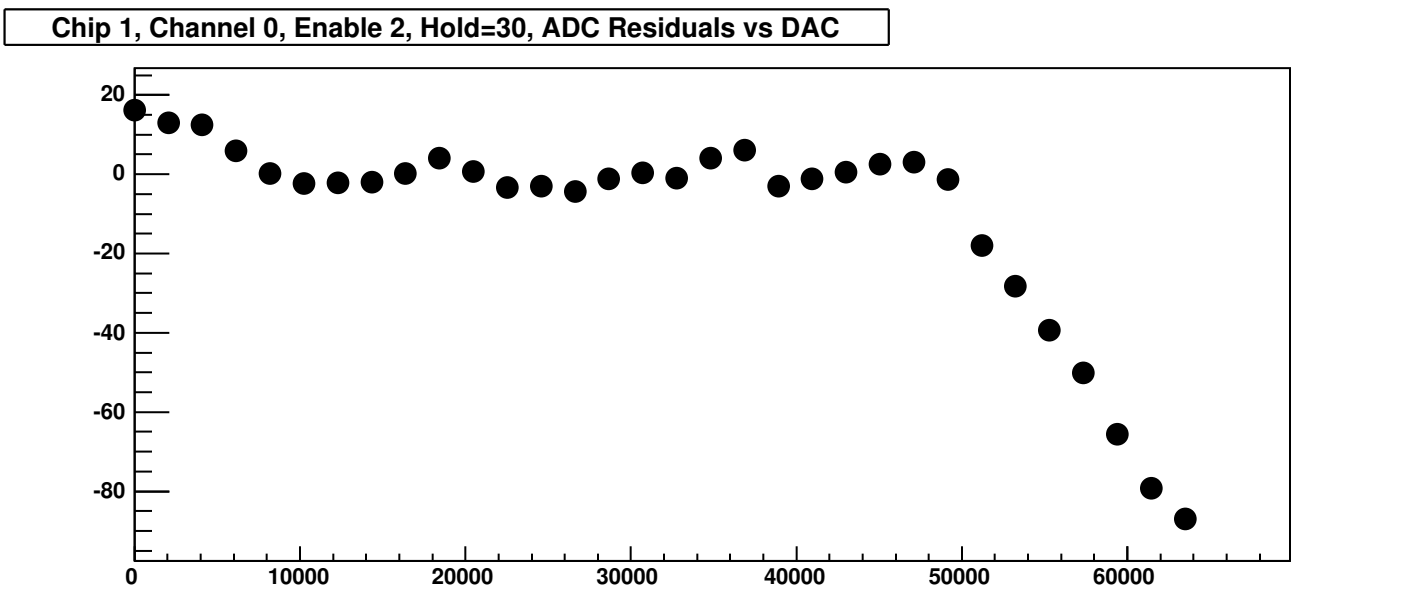

Chip 1, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3555 / 15
p0 948.2 ± 0.1163
p1 0.0002026 ± 4.68e-06
960
940
920
900
880
860
0
10000
20000
30000
40000
50000
60000

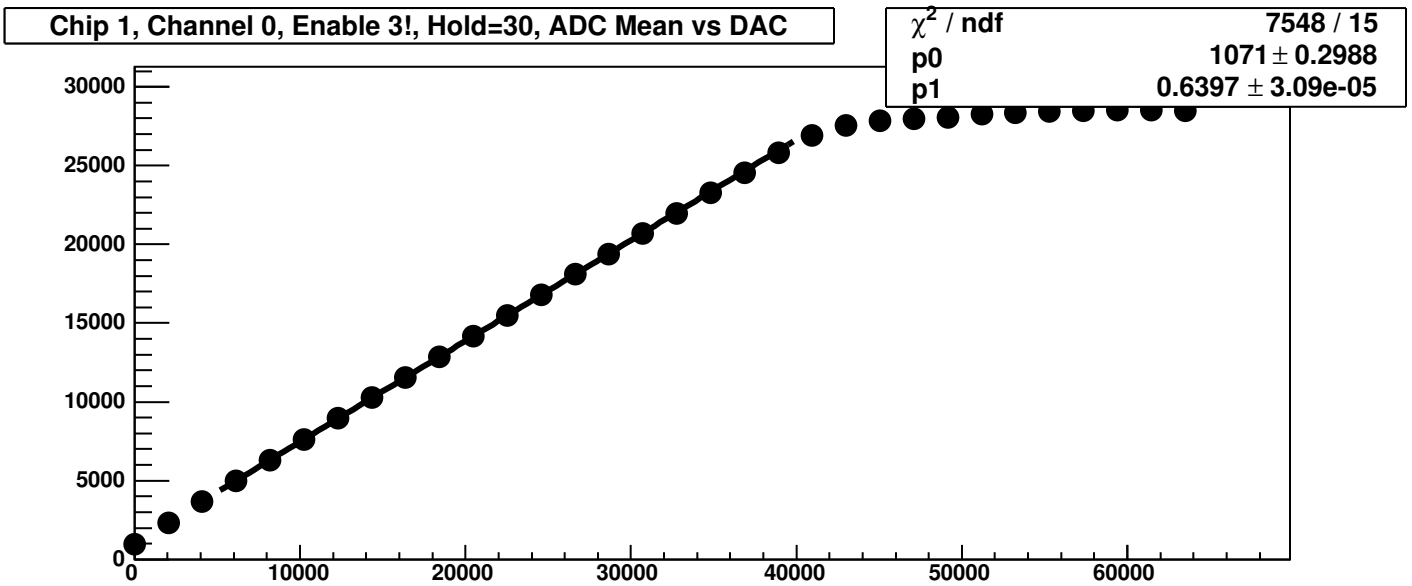

Chip 1, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

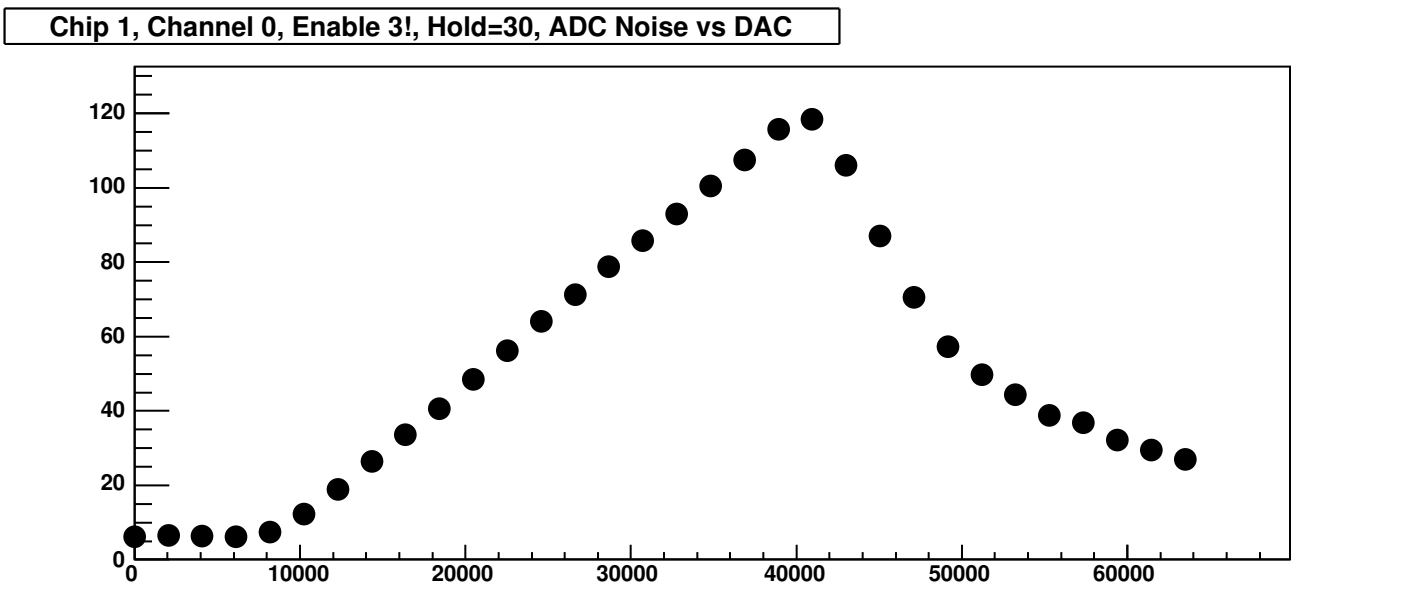

Chip 1, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

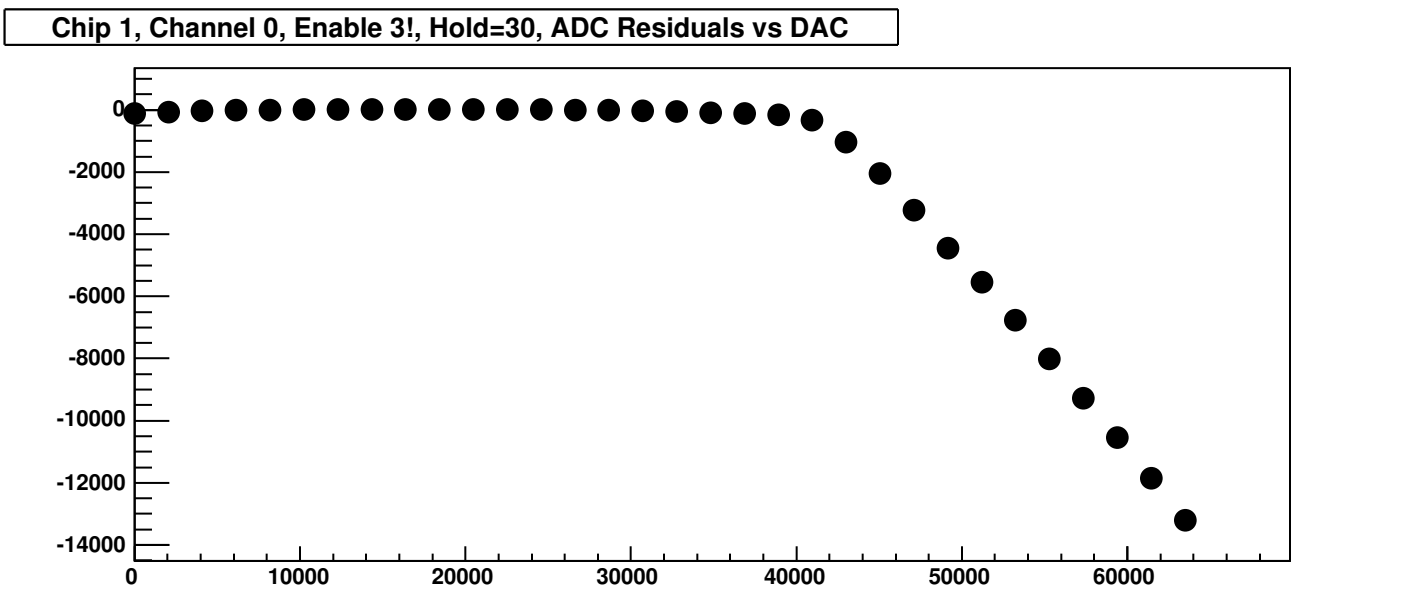

Chip 1, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4434 / 15
p0 945.8 ± 0.1162
p1 -0.0003785 ± 4.76e-06
960
940
920
900
880
860
840
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

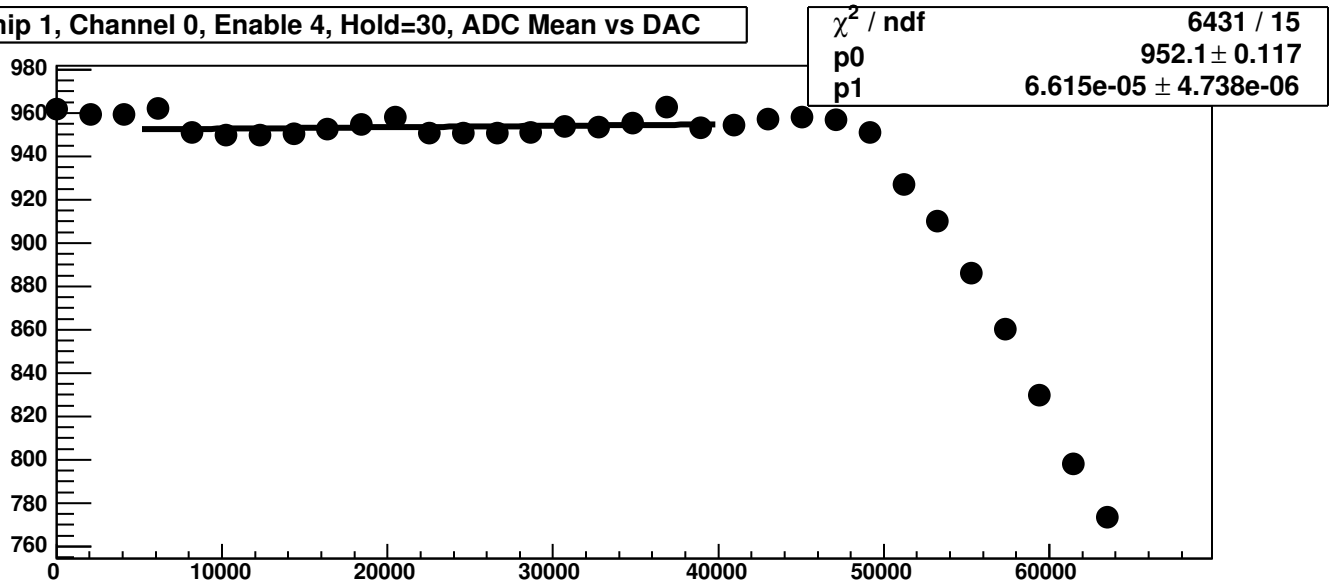

Chip 1, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 7548 / 15
p0 1071 ± 0.2988
p1 0.6397 ± 3.09e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

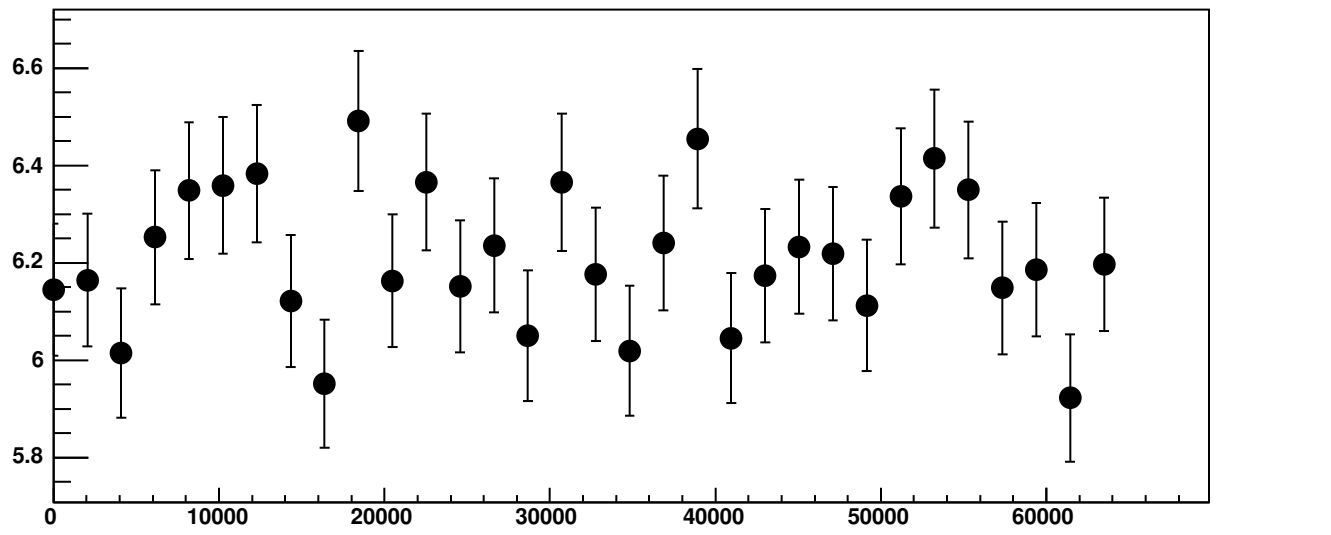

Chip 1, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

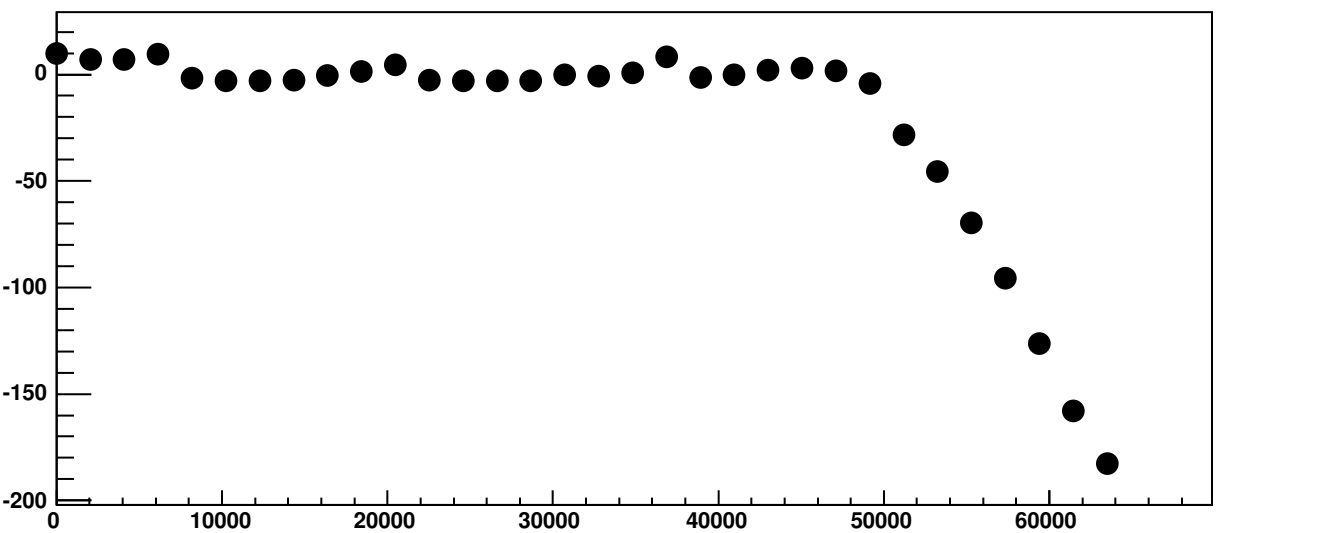
Chip 1, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC

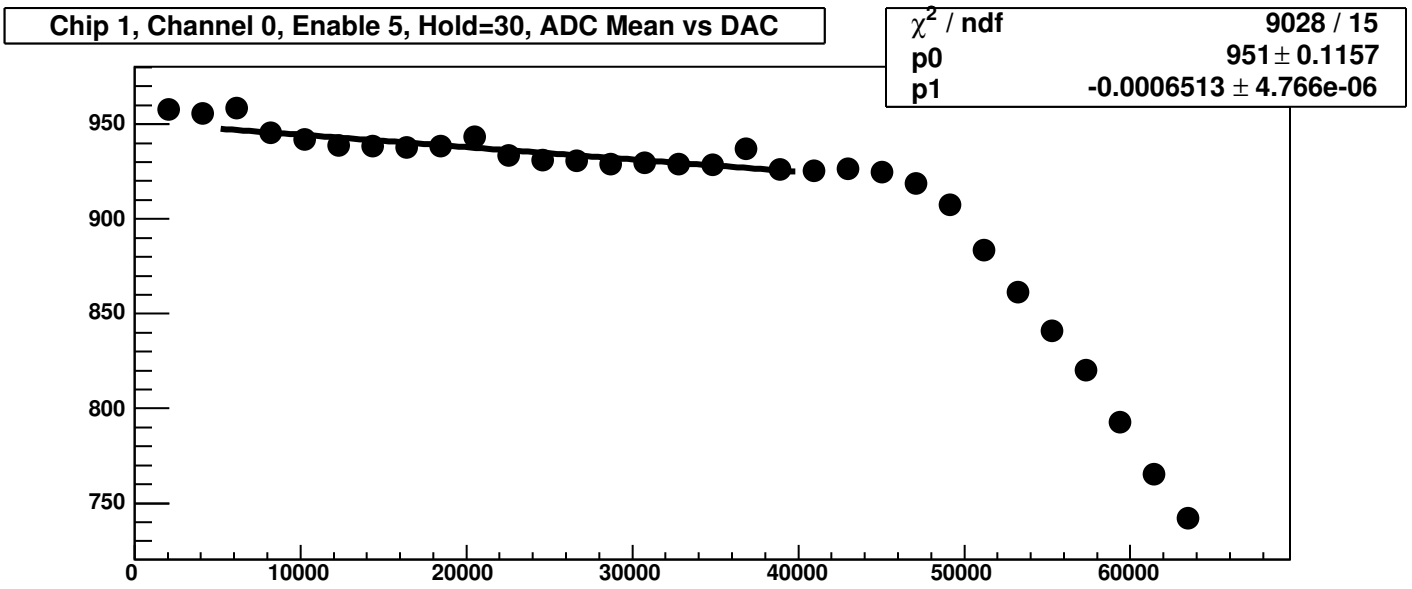


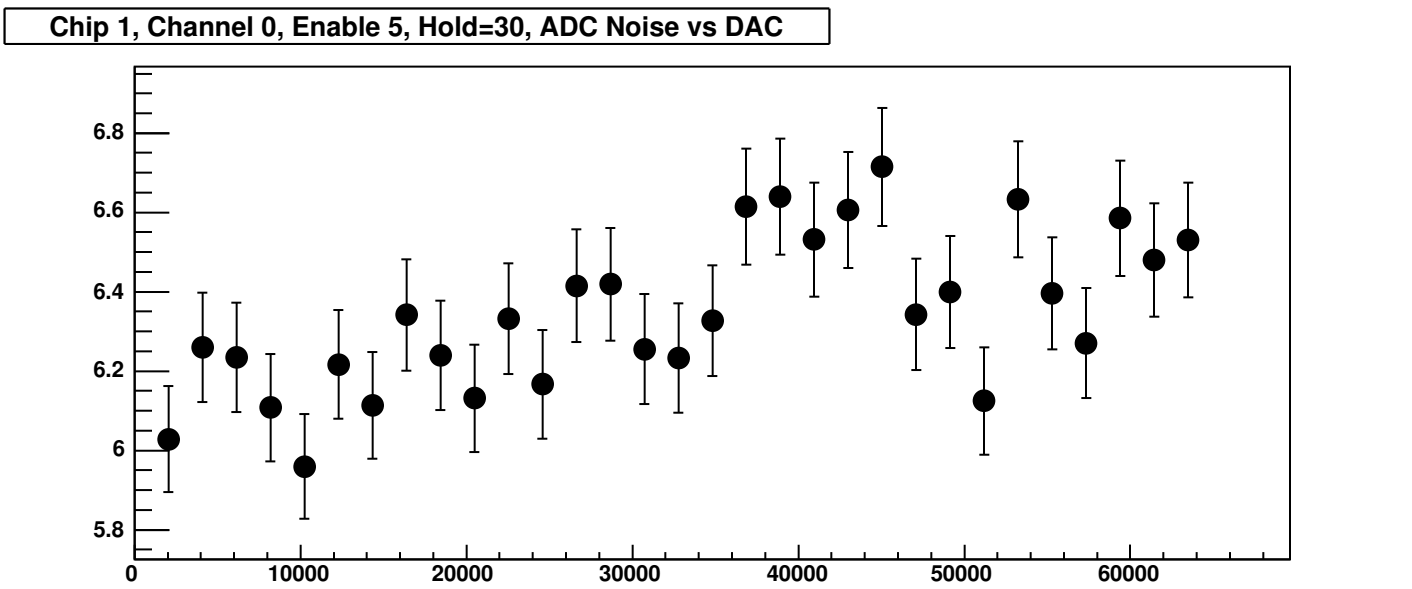
Chip 1, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC

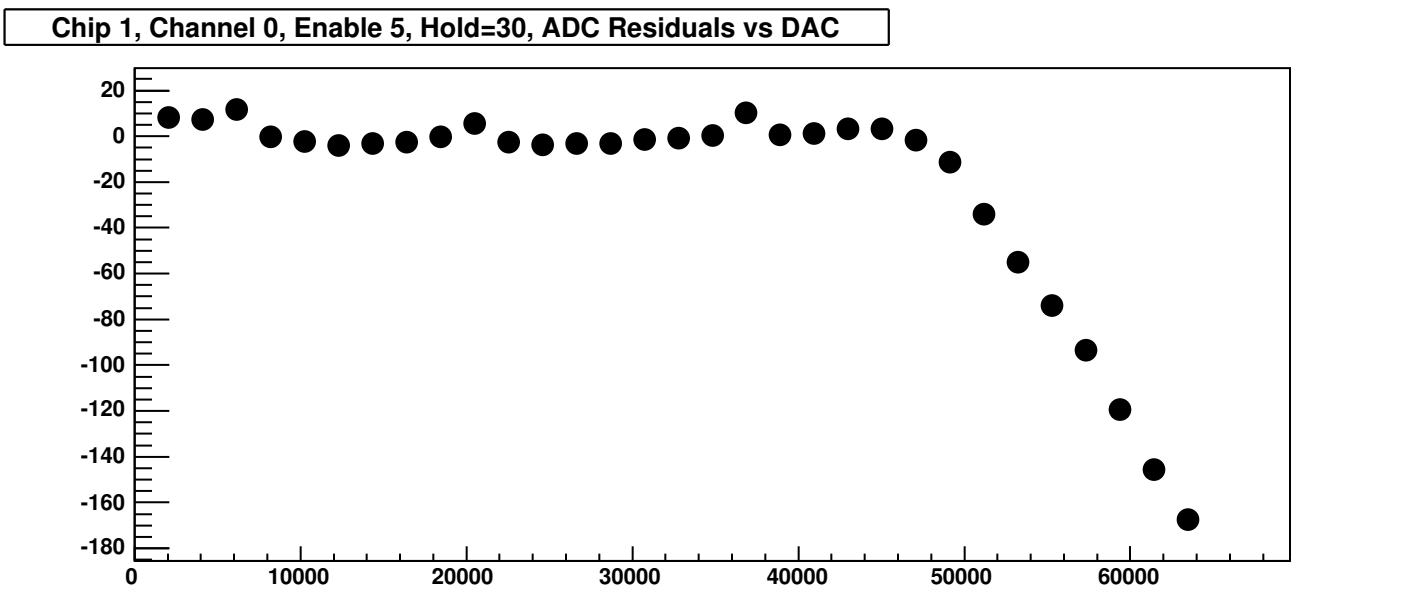


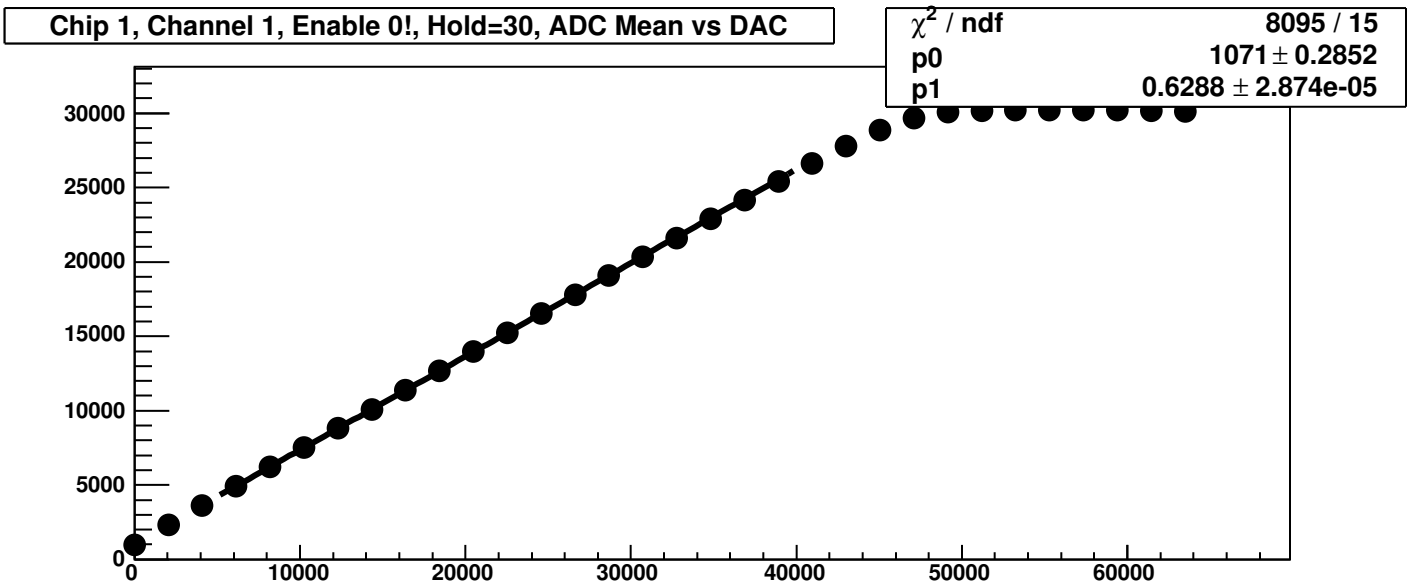
Chip 1, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC

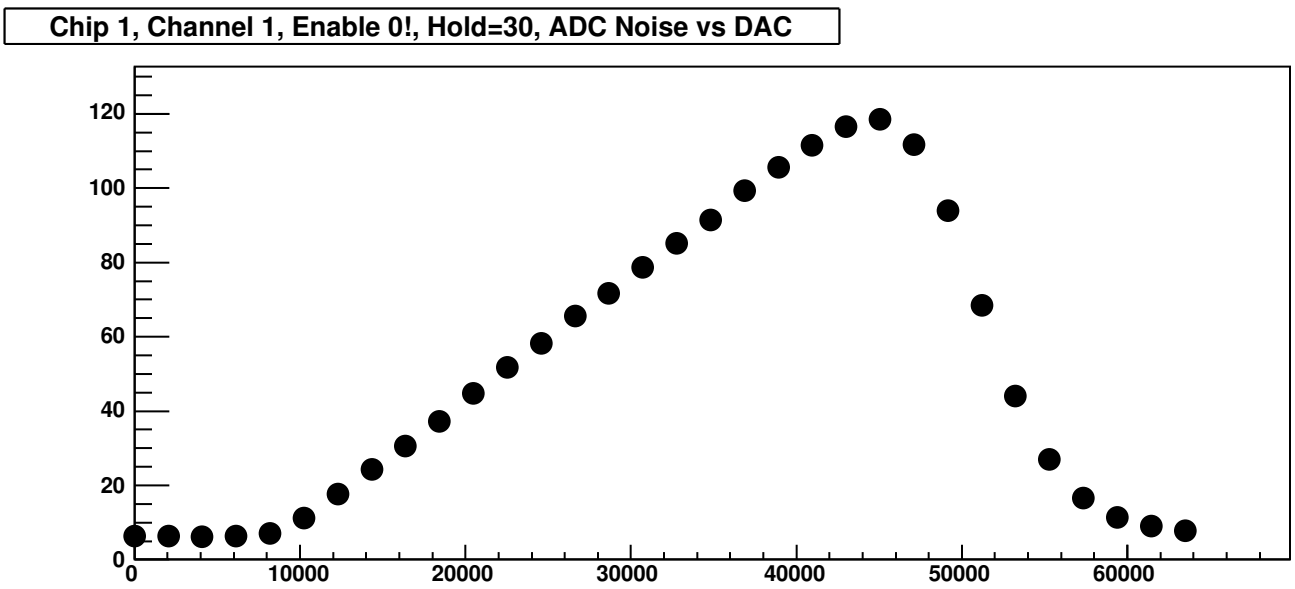


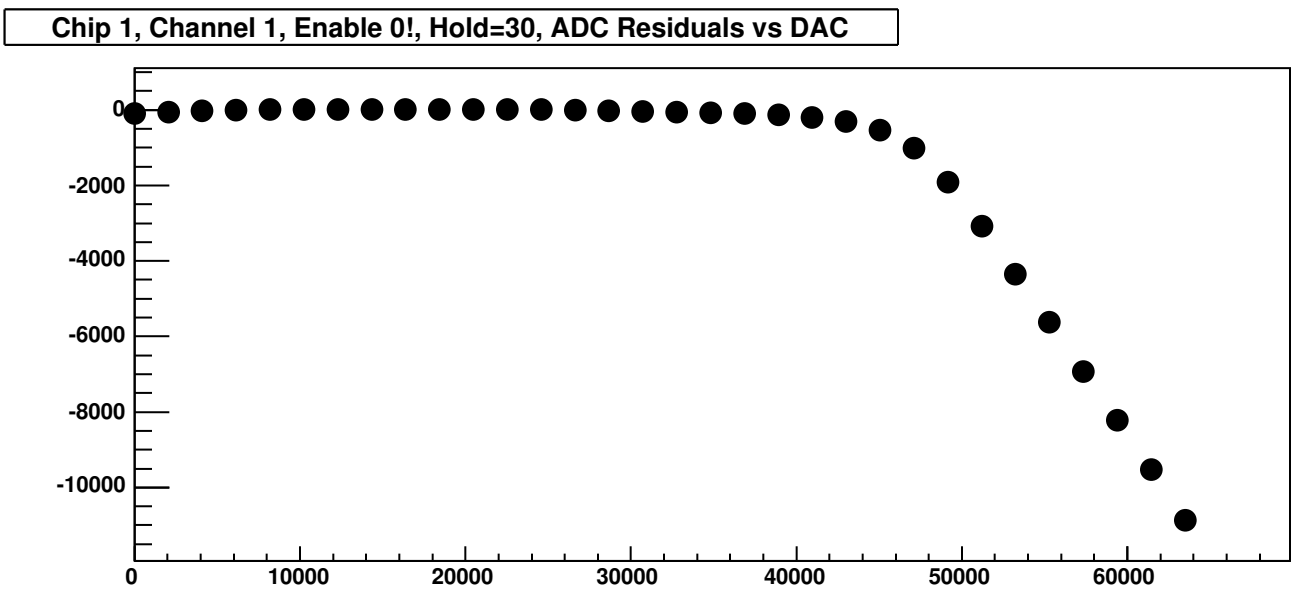

Chip 1, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9028 / 15
p0 951 ± 0.1157
p1 -0.0006513 ± 4.766e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

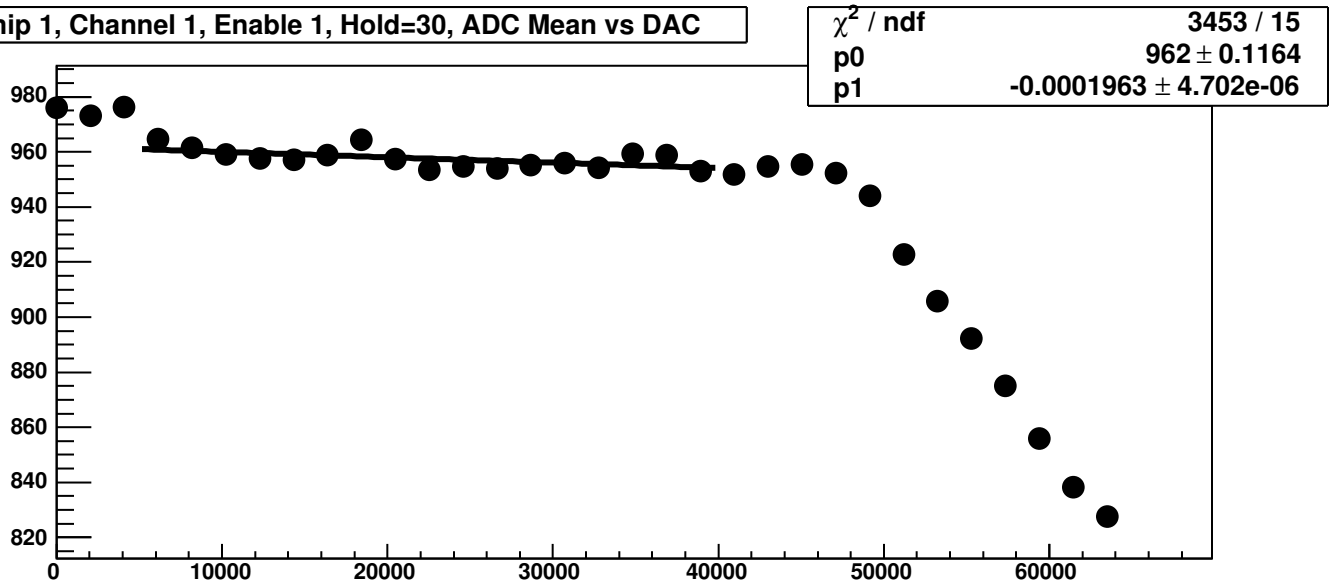

Chip 1, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 8095 / 15
p0 1071 ± 0.2852
p1 0.6288 ± 2.874e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

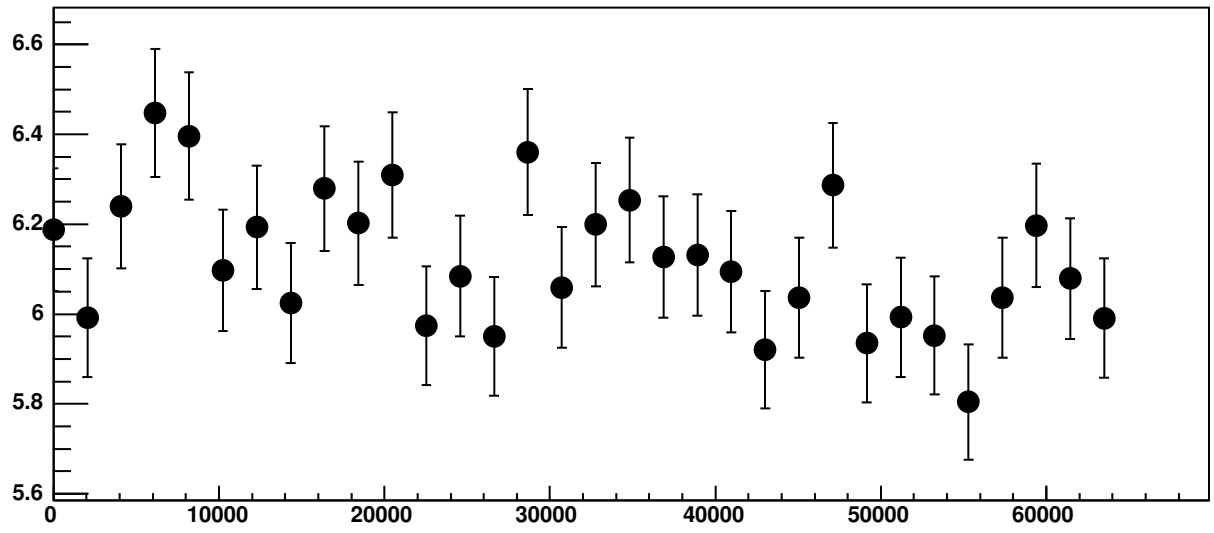

Chip 1, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

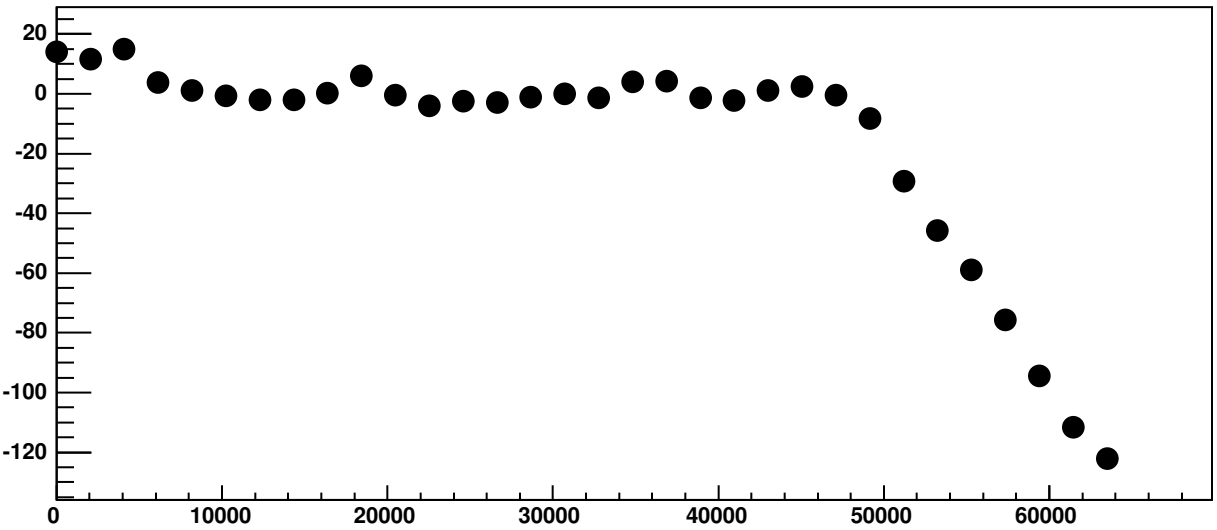
Chip 1, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

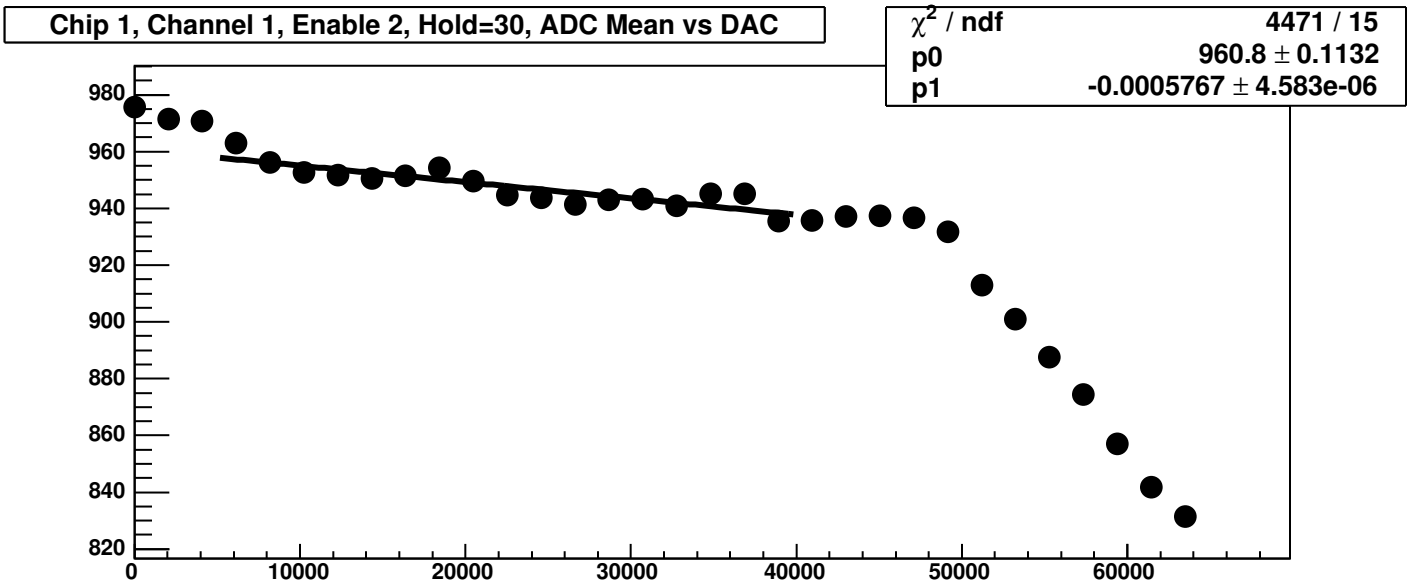


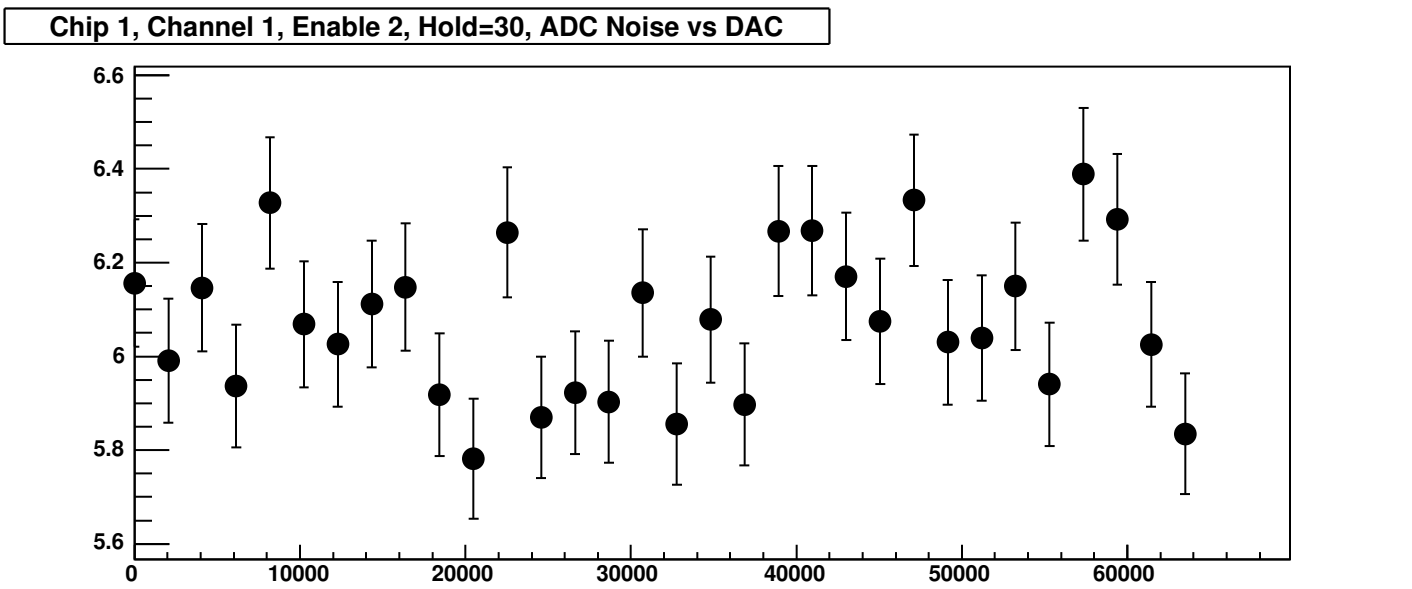
Chip 1, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC

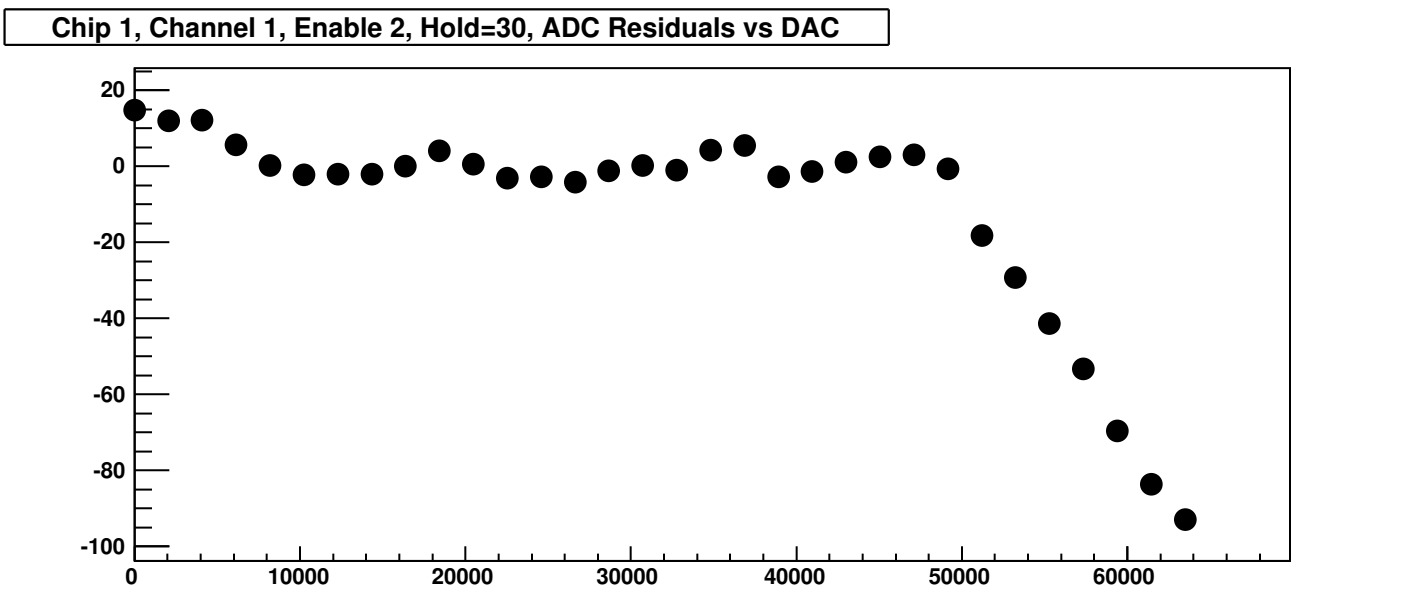


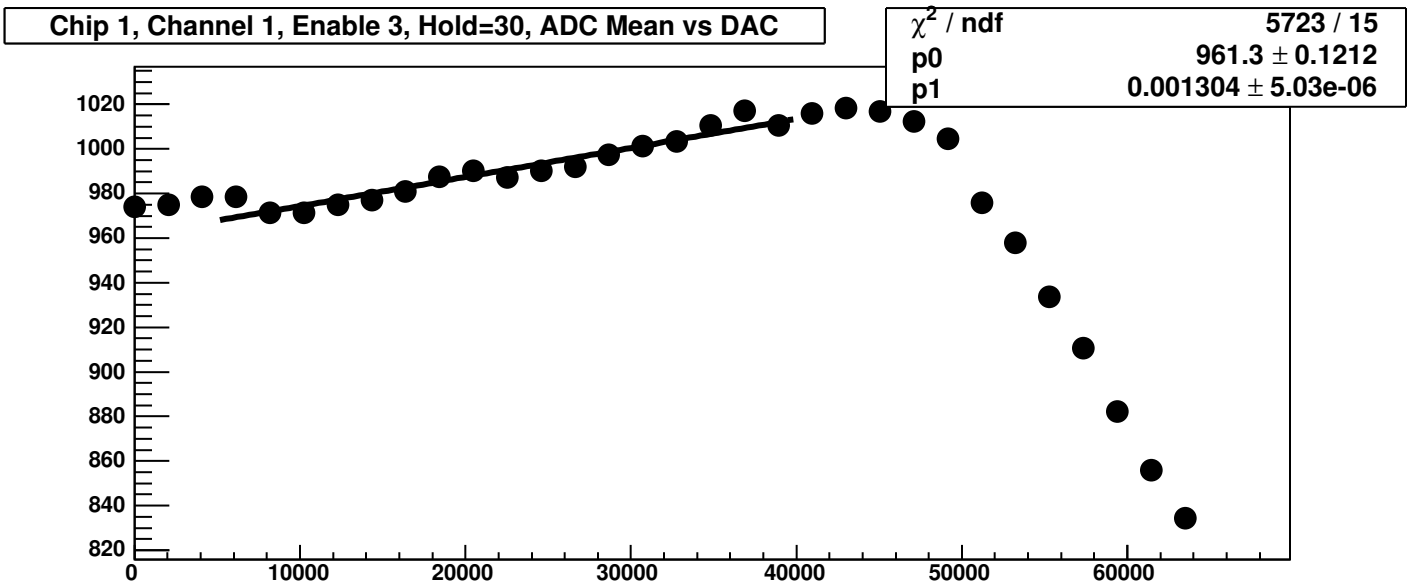
Chip 1, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

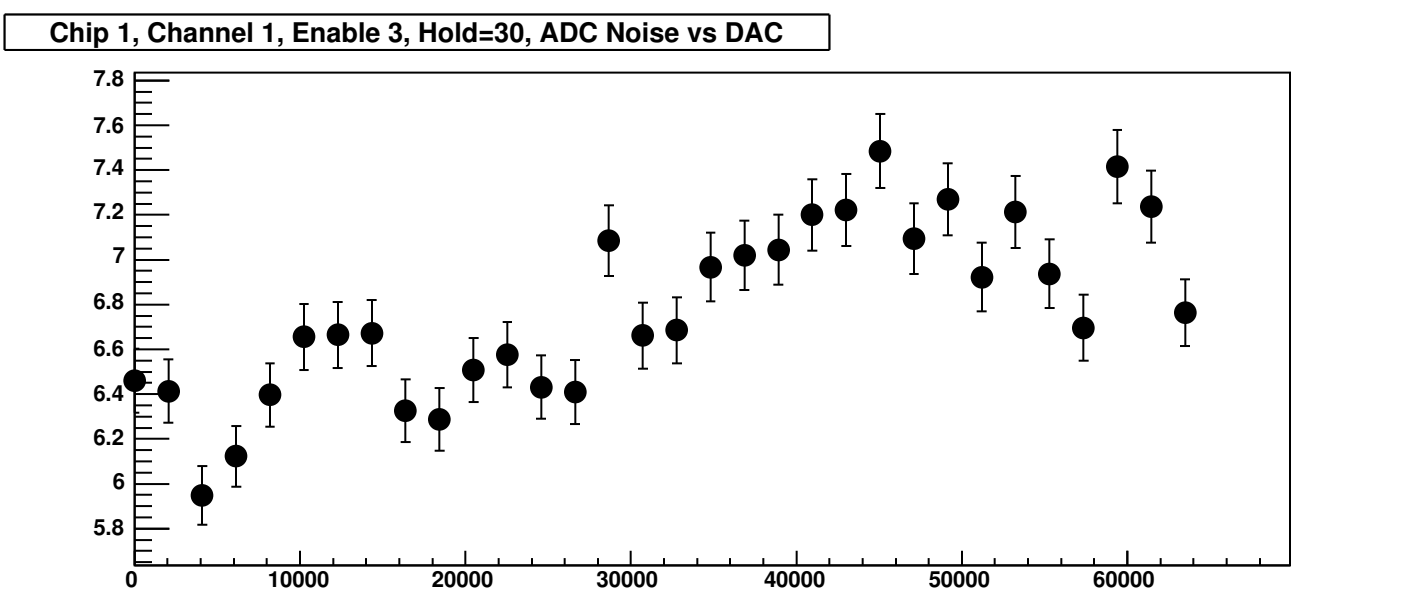


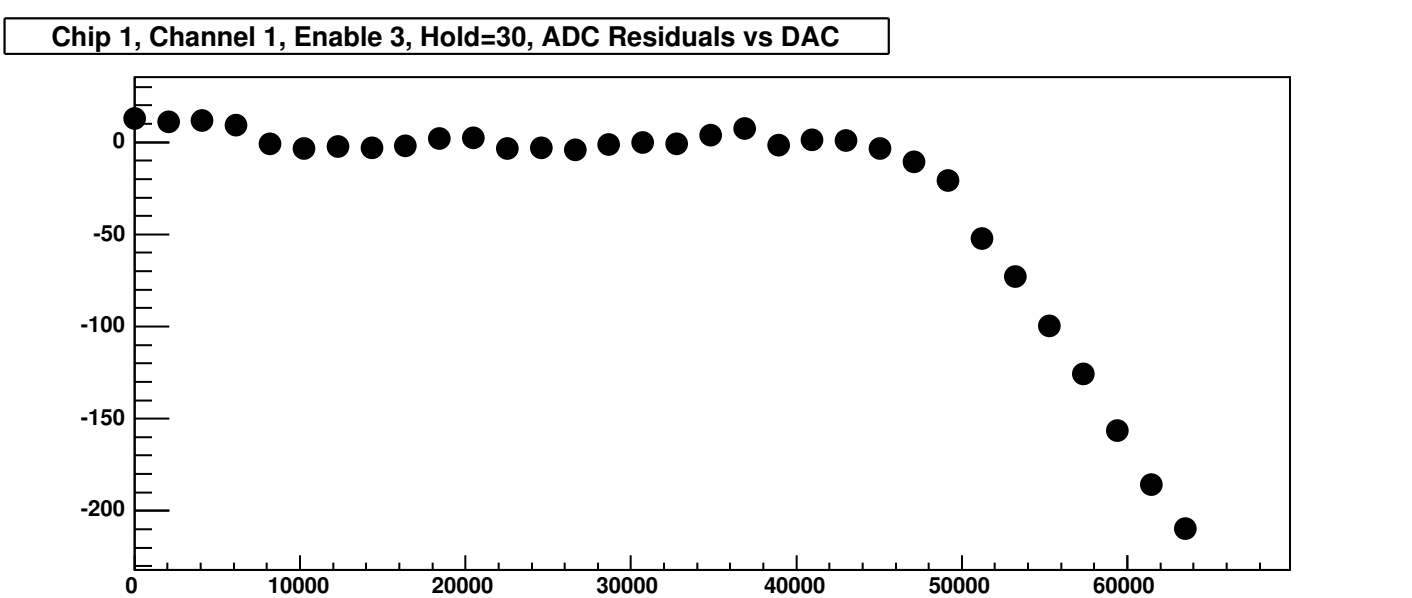

Chip 1, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4471 / 15
p0 960.8 ± 0.1132
p1 -0.0005767 ± 4.583e-06
980
960
940
920
900
880
860
840
820
0
10000
20000
30000
40000
50000
60000

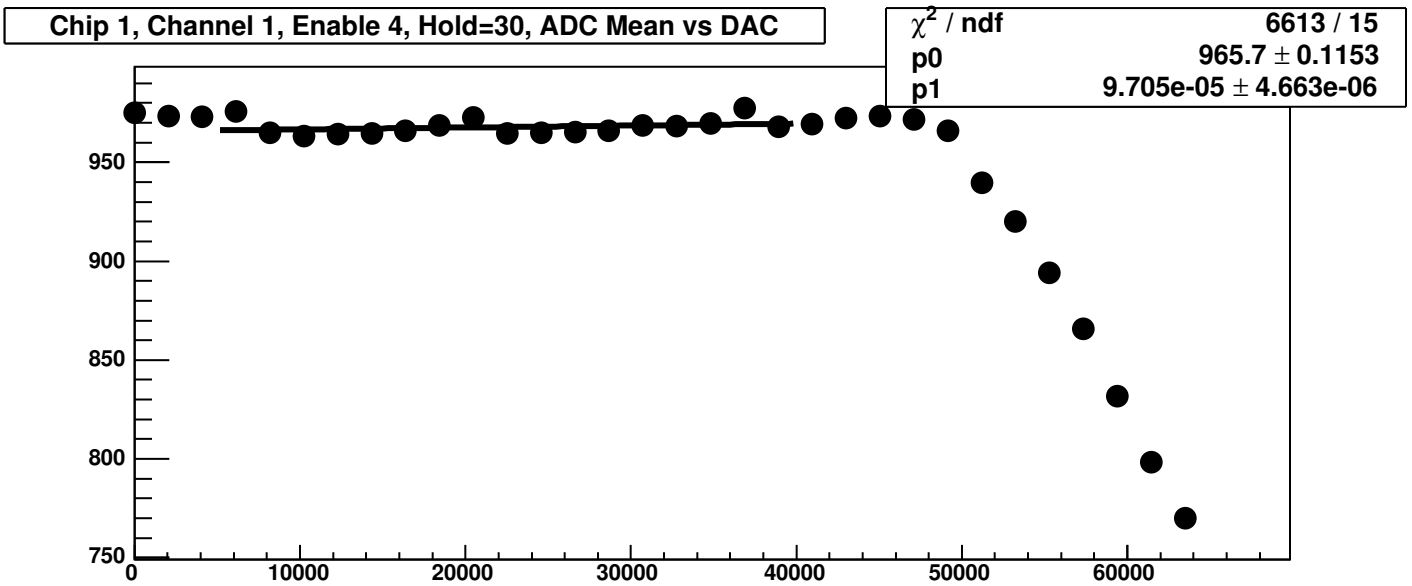

Chip 1, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

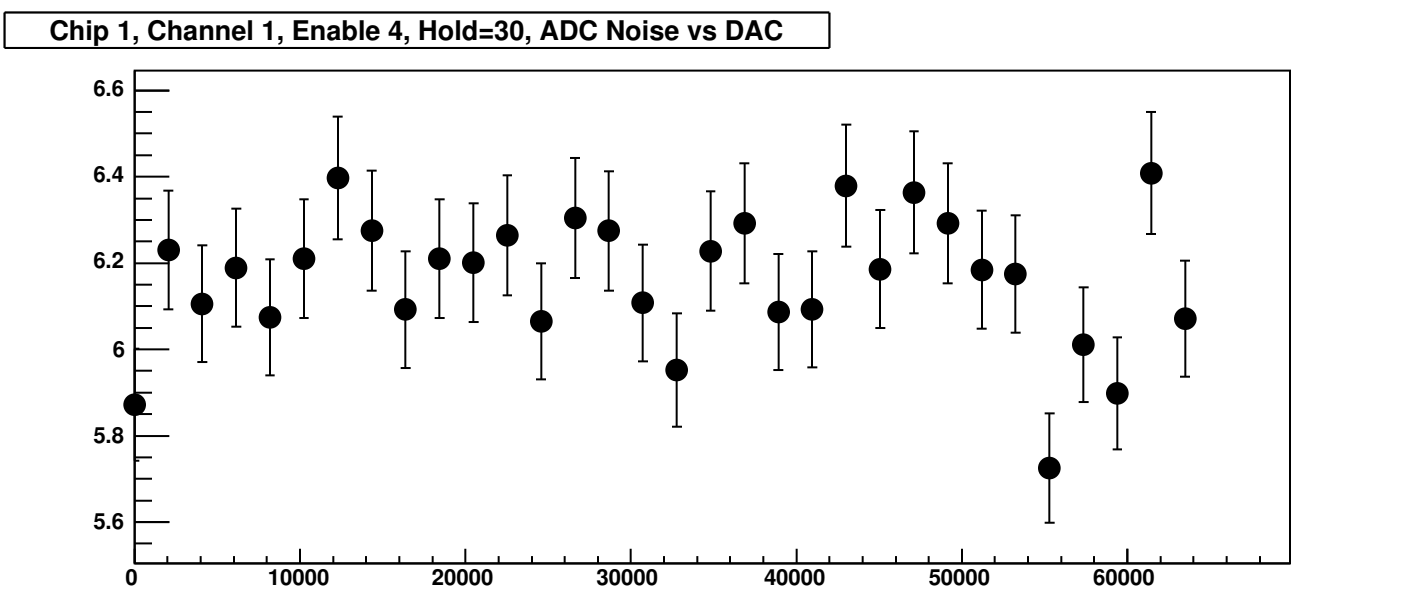

Chip 1, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

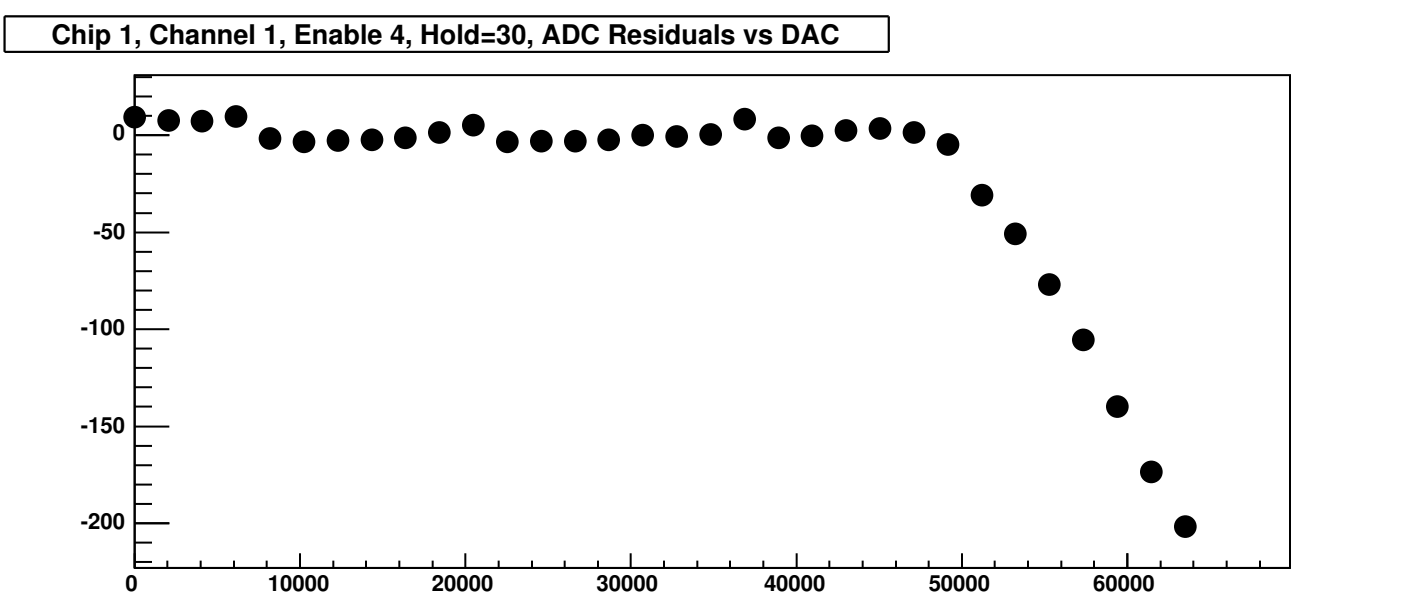

Chip 1, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5723 / 15
p0 961.3 ± 0.1212
p1 0.001304 ± 5.03e-06
1020
1000
980
960
940
920
900
880
860
840
820
0
10000
20000
30000
40000
50000
60000

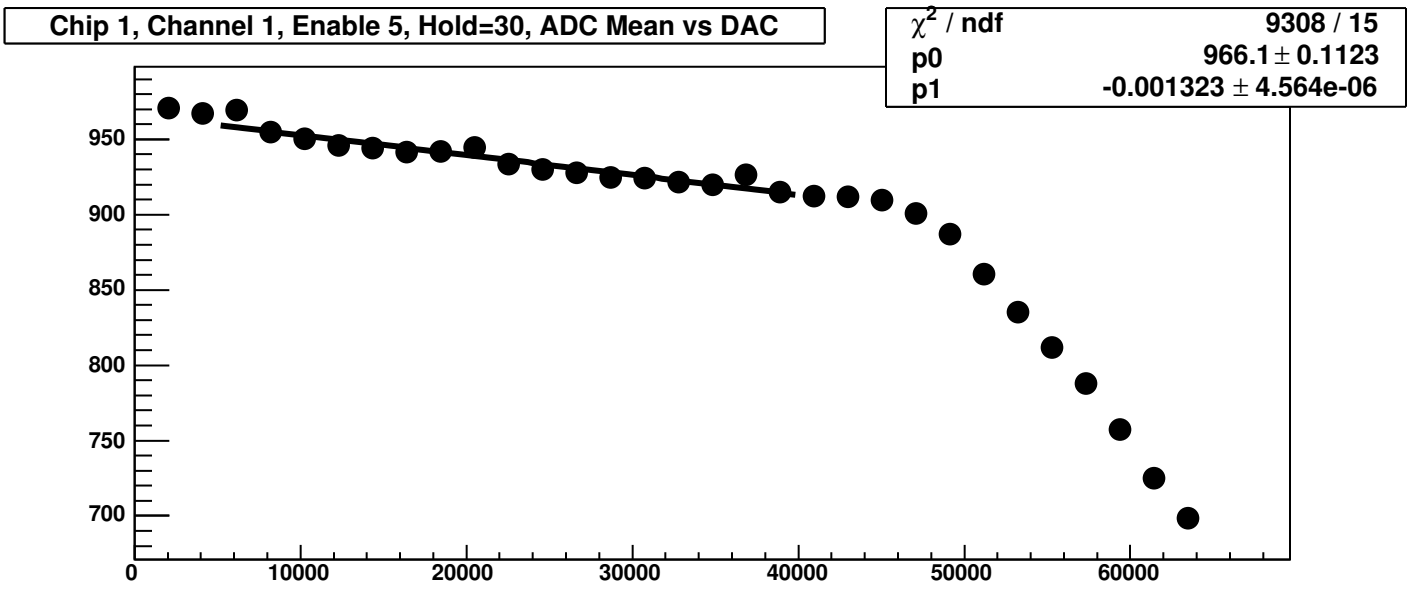

Chip 1, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

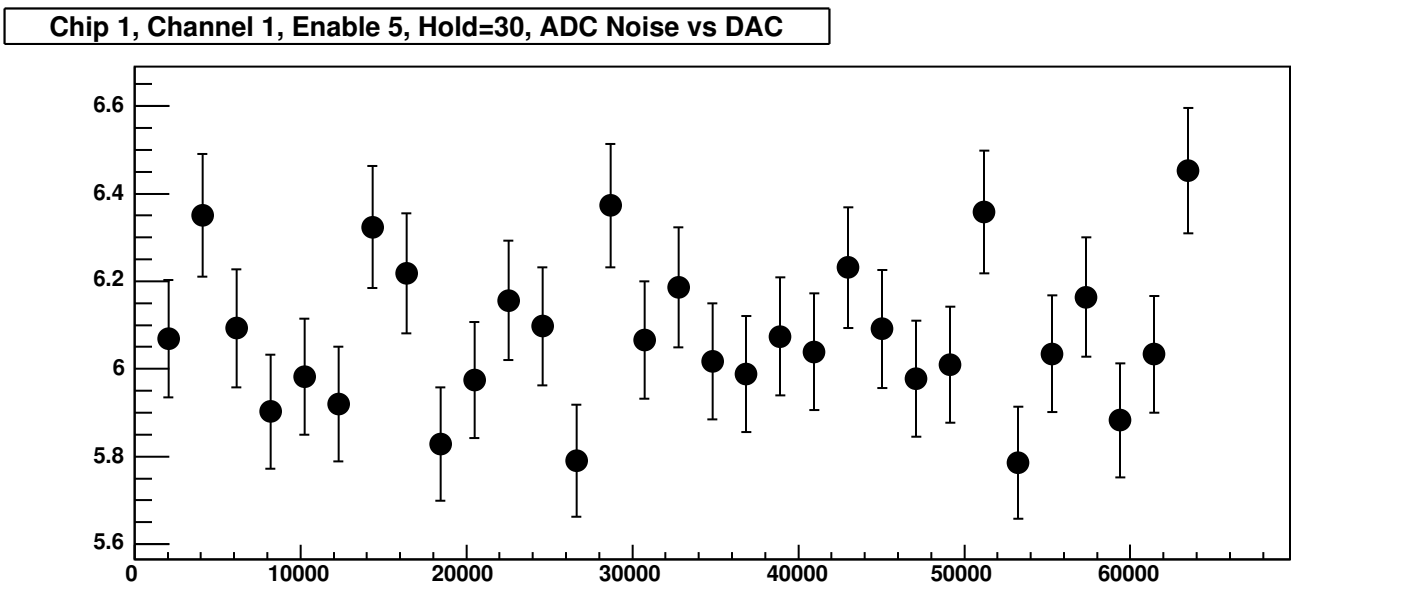

Chip 1, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

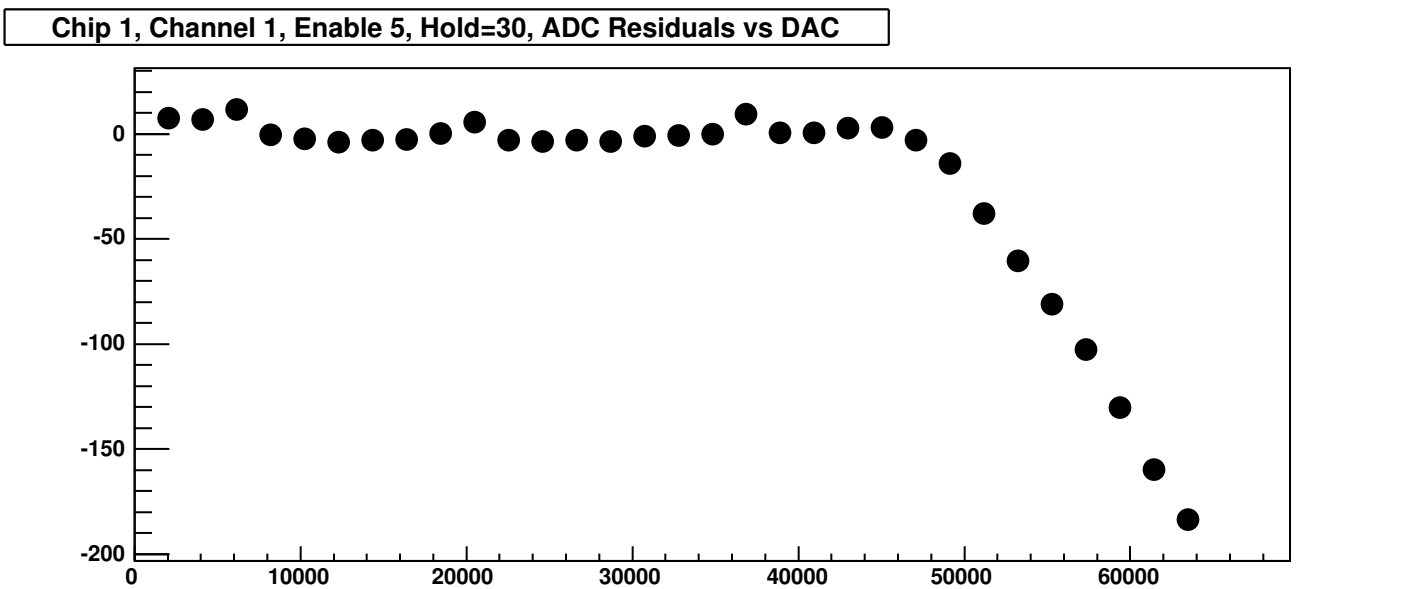

Chip 1, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6613 / 15
p0 965.7 ± 0.1153
p1 9.705e-05 ± 4.663e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

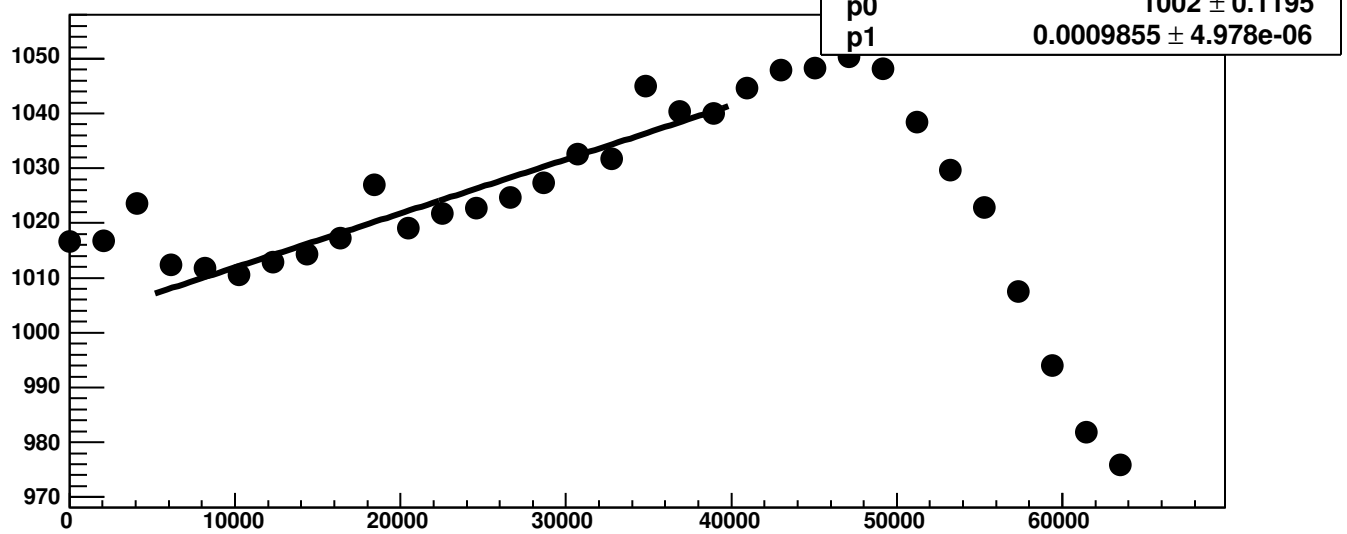

Chip 1, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9308 / 15
p0 966.1 ± 0.1123
p1 -0.001323 ± 4.564e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

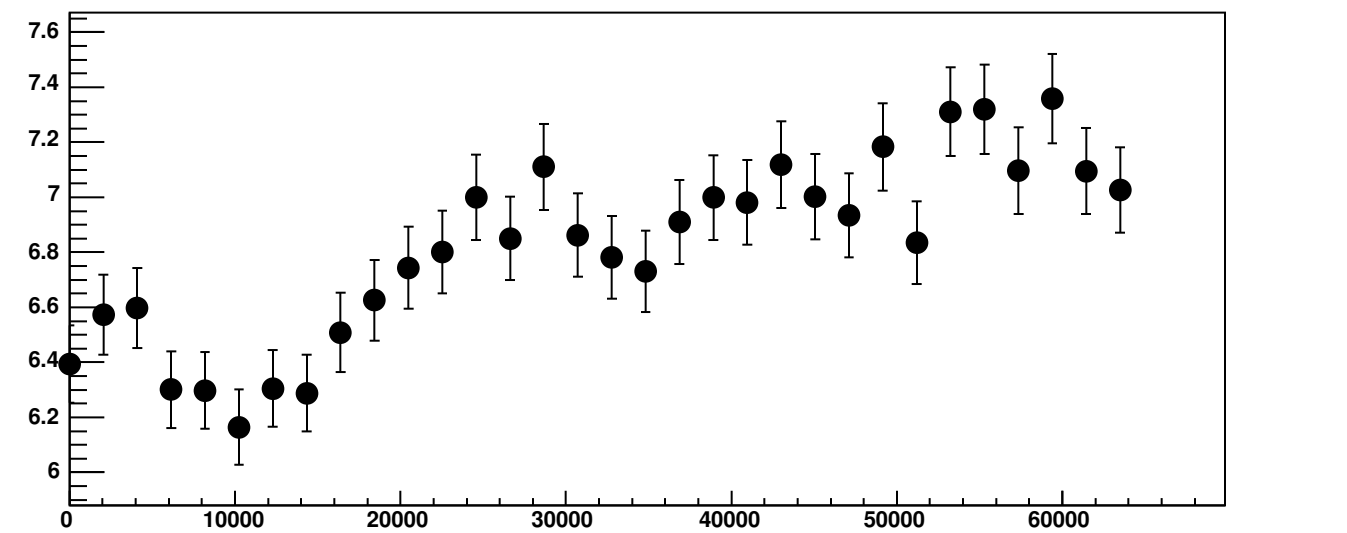

Chip 1, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

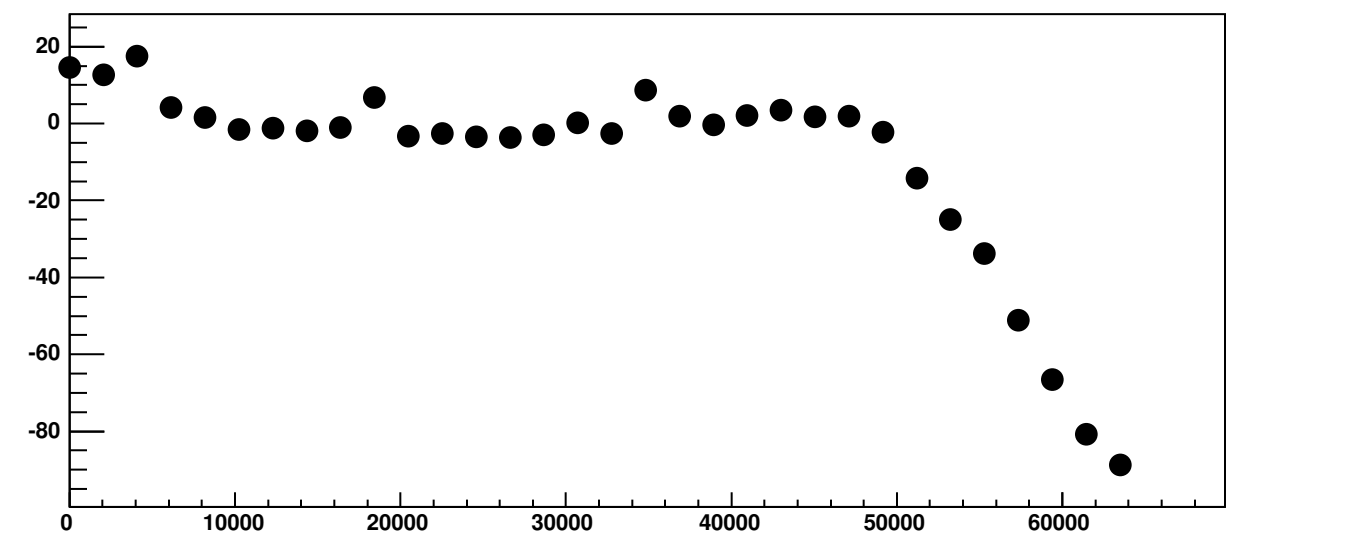
Chip 1, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC

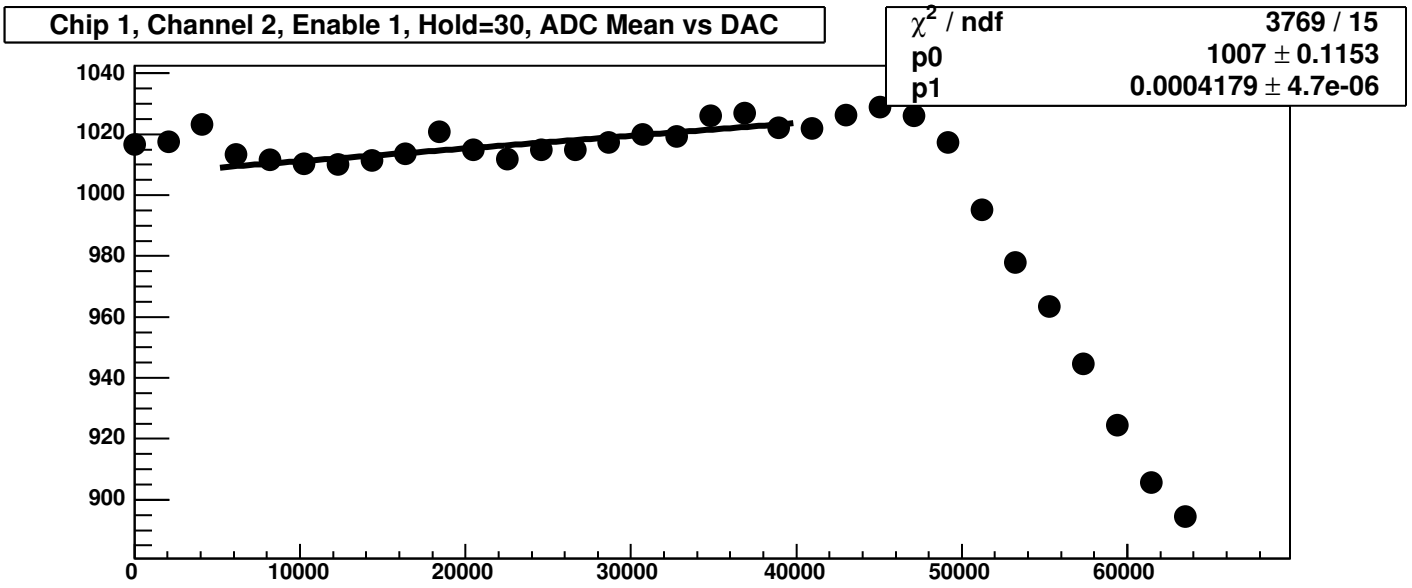


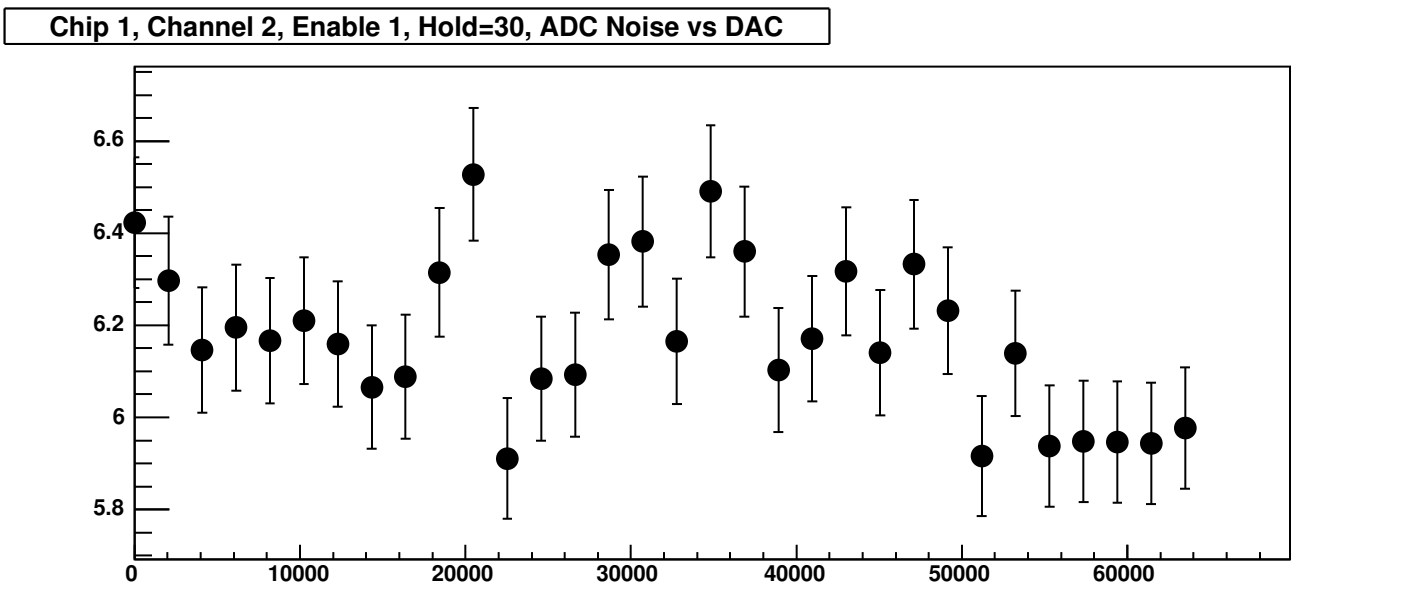
Chip 1, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC

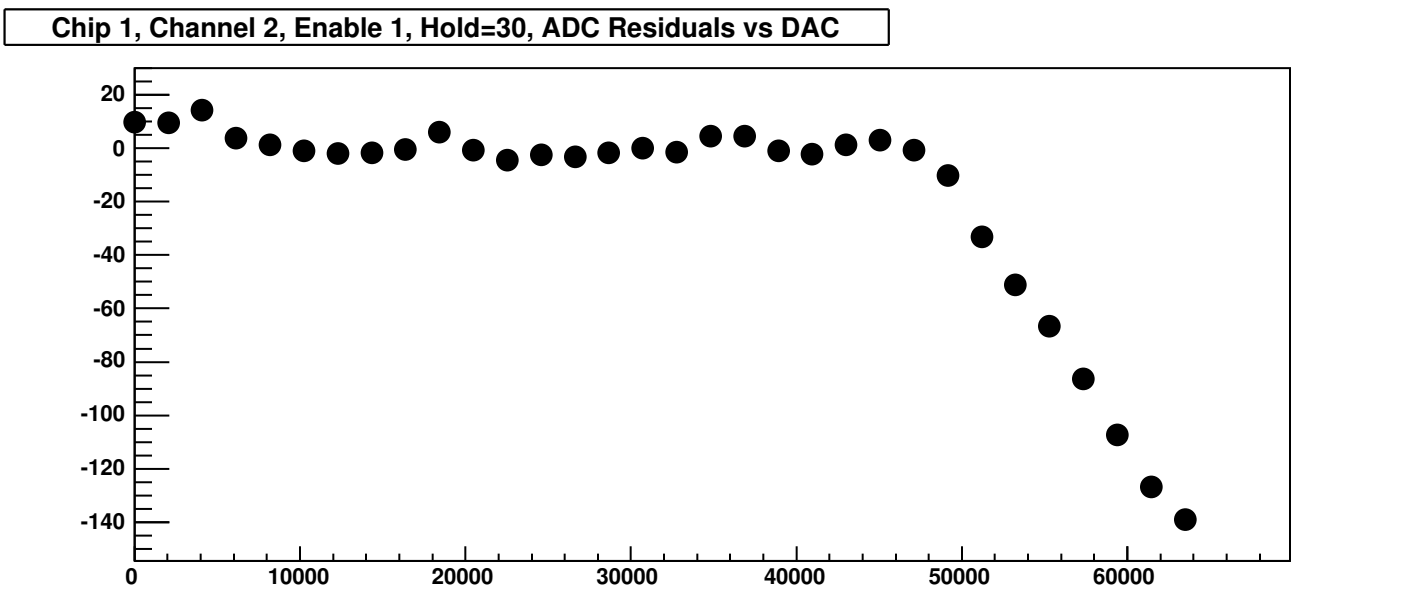


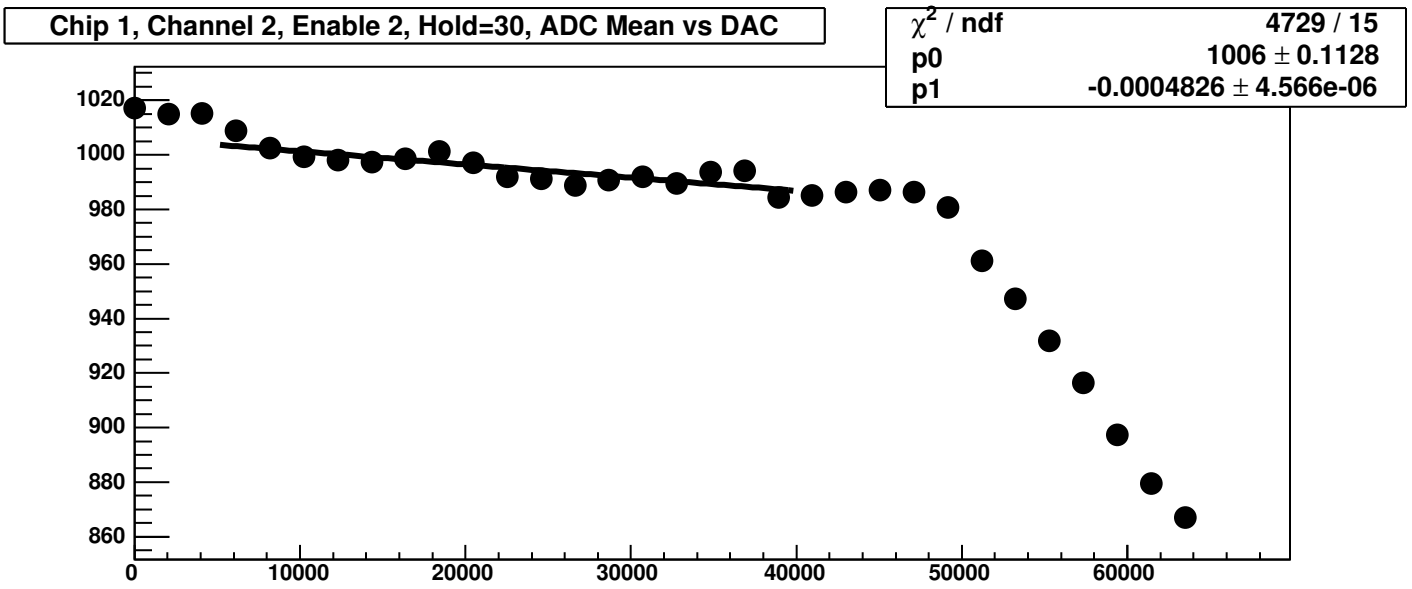
Chip 1, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC

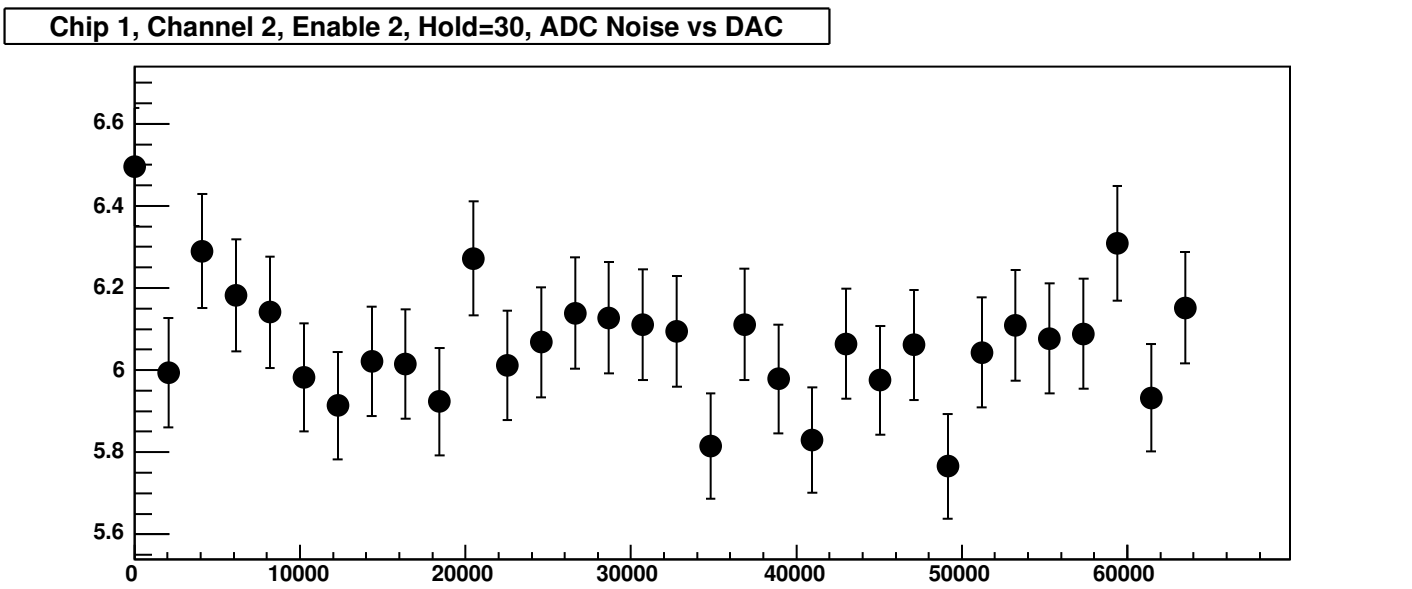


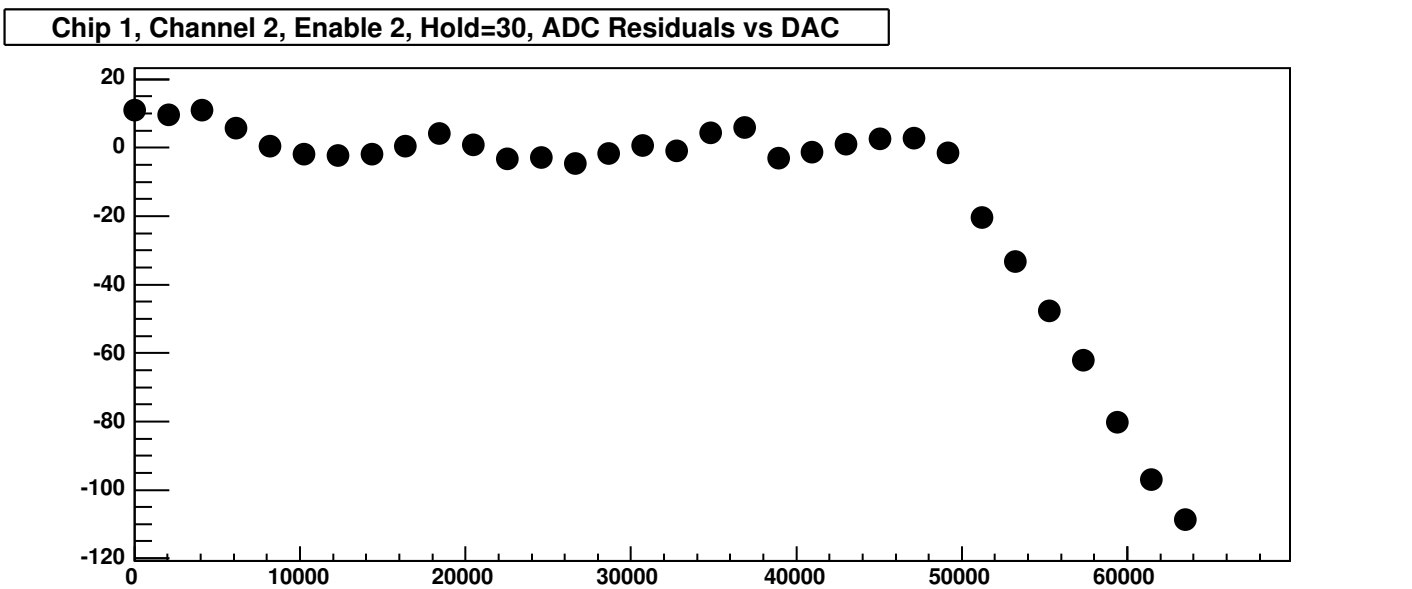

Chip 1, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3769 / 15
p0 1007 ± 0.1153
p1 0.0004179 ± 4.7e-06
1040
1020
1000
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000

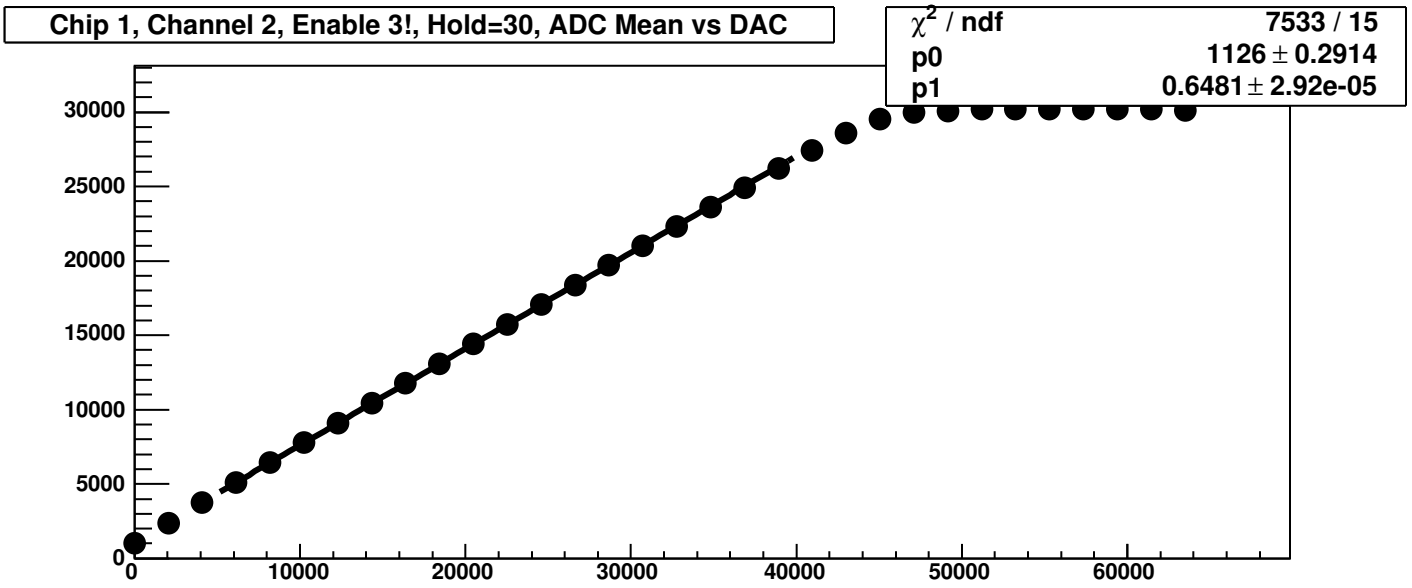

Chip 1, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

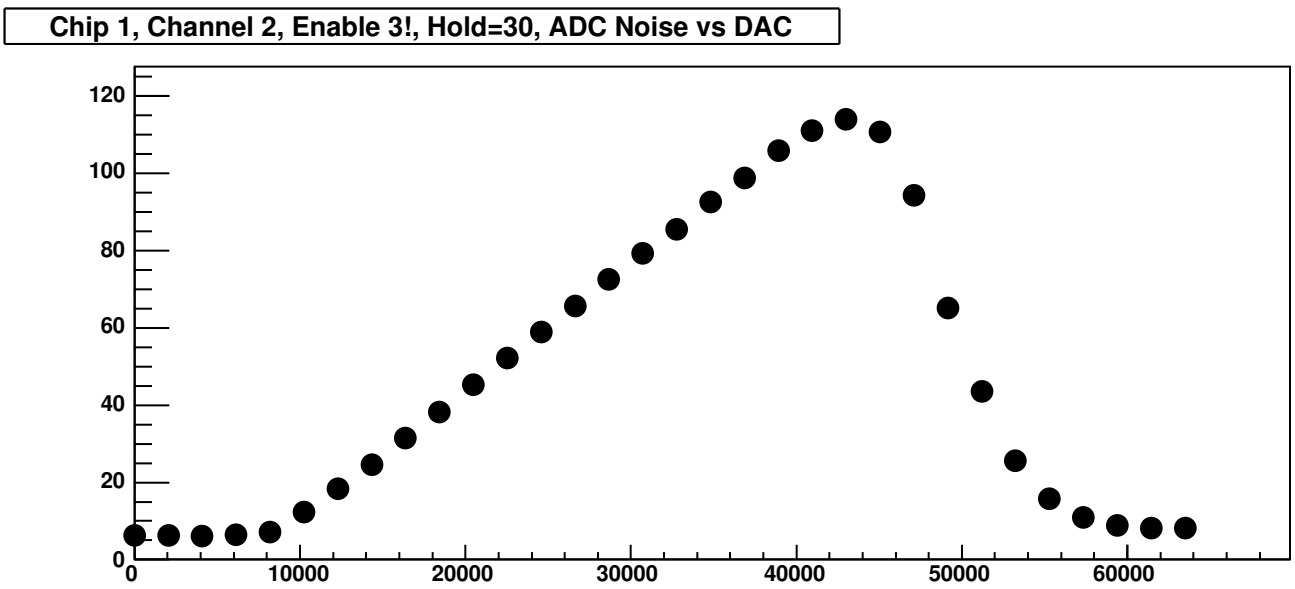

Chip 1, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

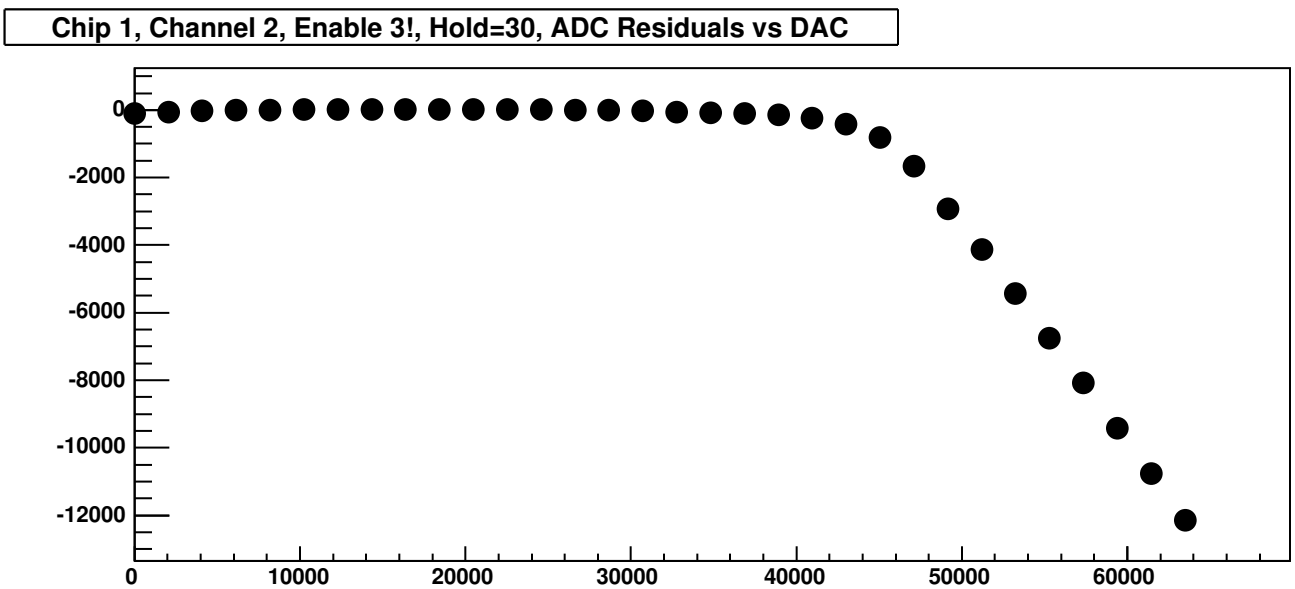

Chip 1, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4729 / 15
p0 1006 ± 0.1128
p1 -0.0004826 ± 4.566e-06
1020
1000
980
960
940
920
900
880
860
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

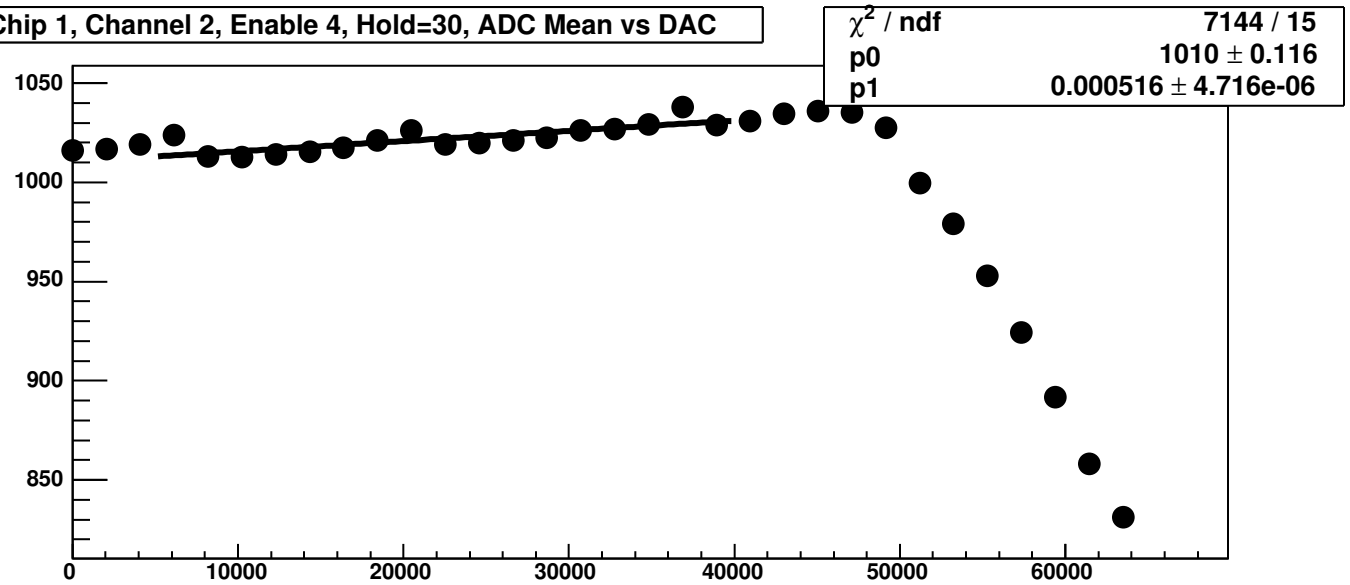

Chip 1, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 7533 / 15
p0 1126 ± 0.2914
p1 0.6481 ± 2.92e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

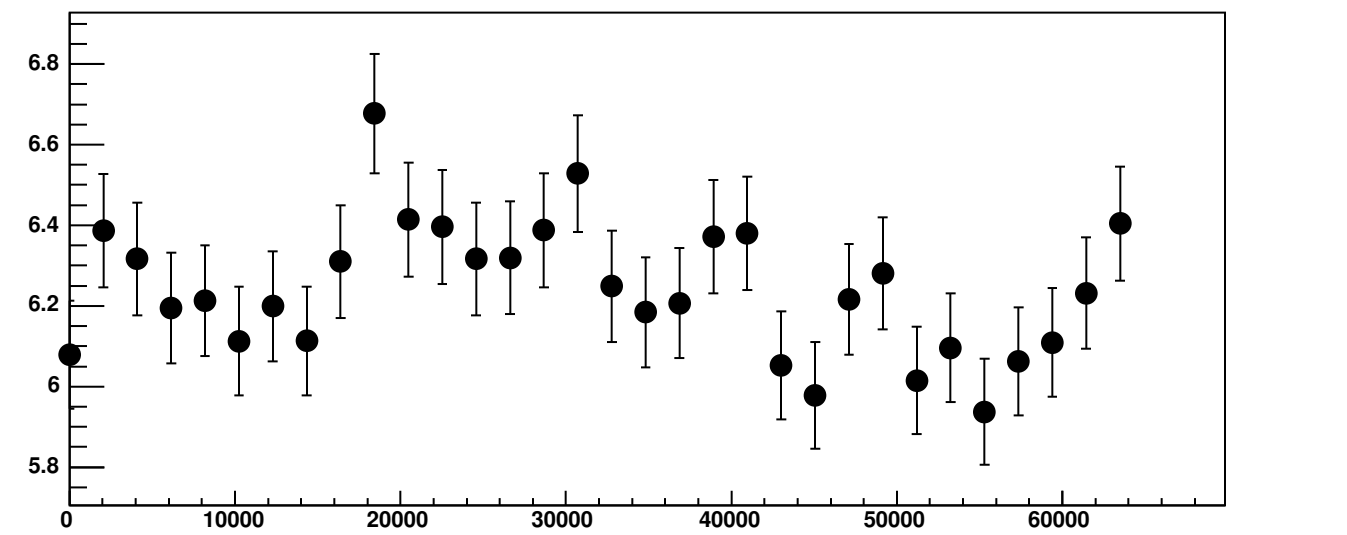

Chip 1, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

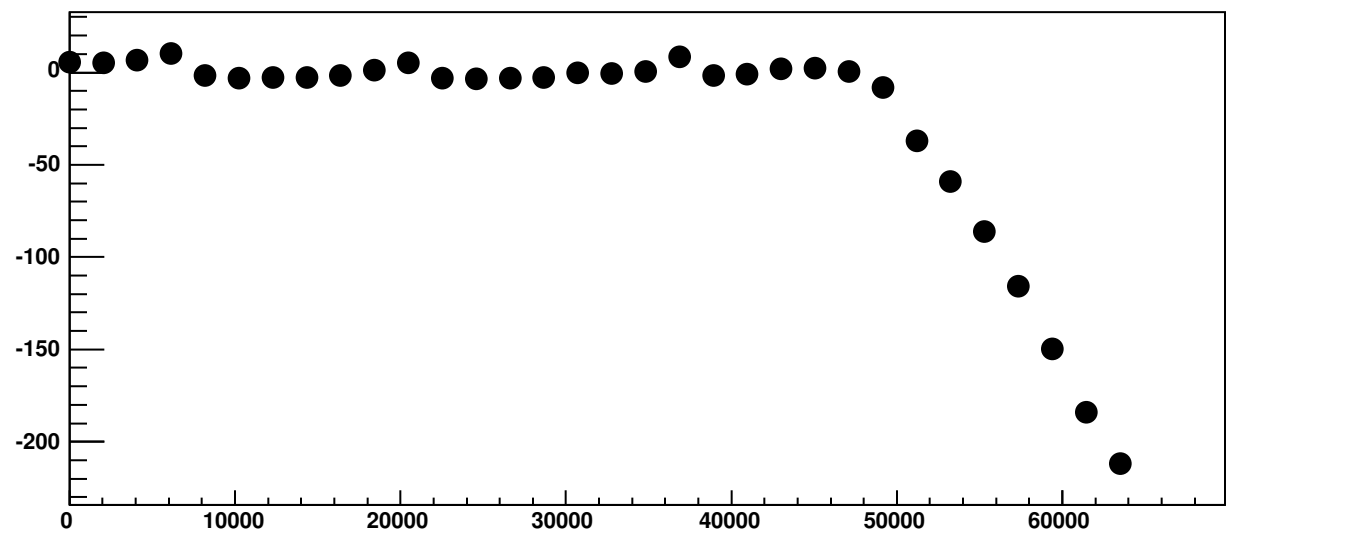
Chip 1, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

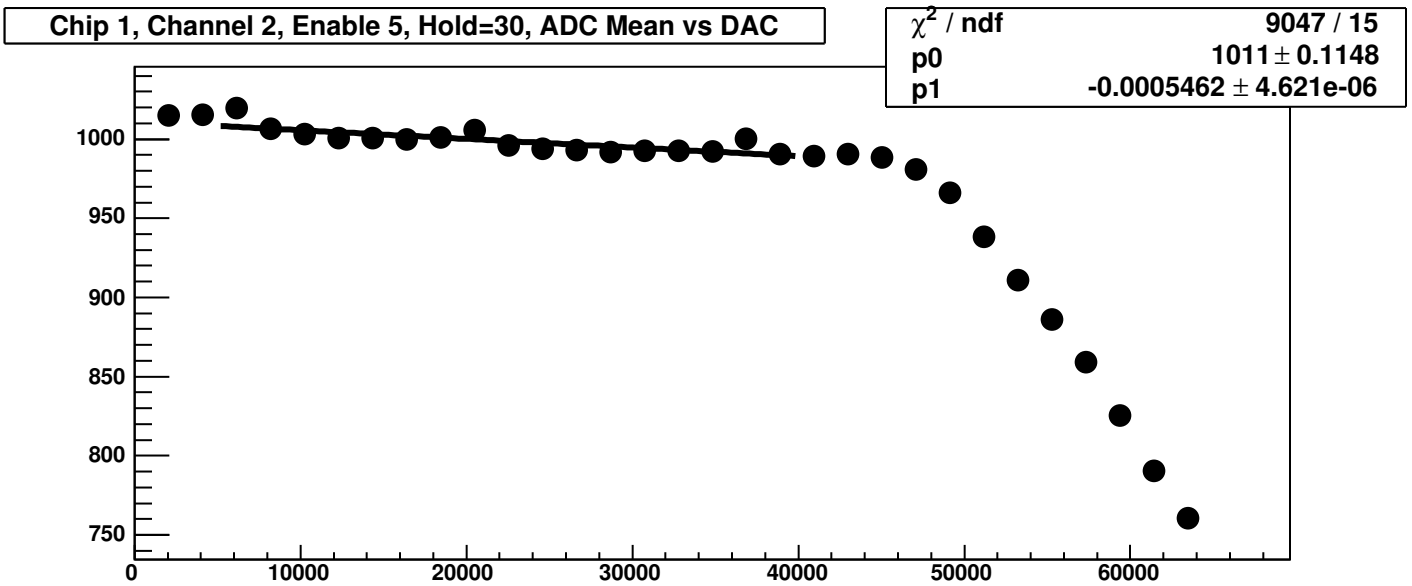


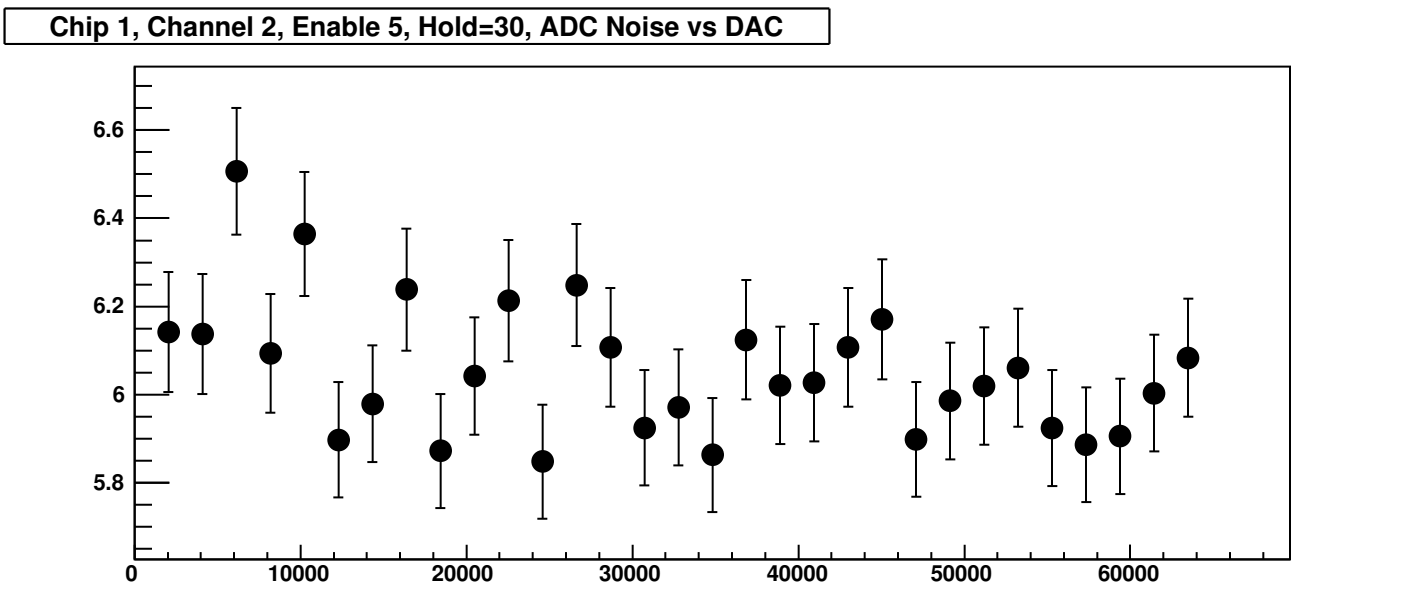
Chip 1, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC

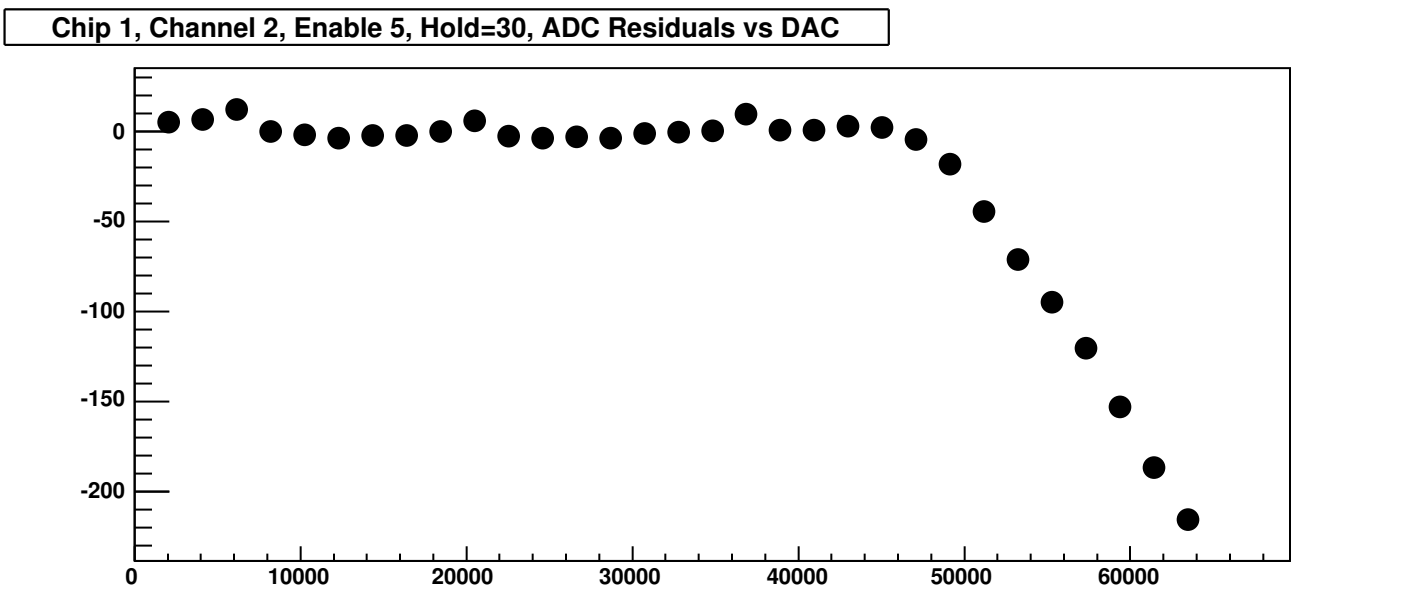


Chip 1, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

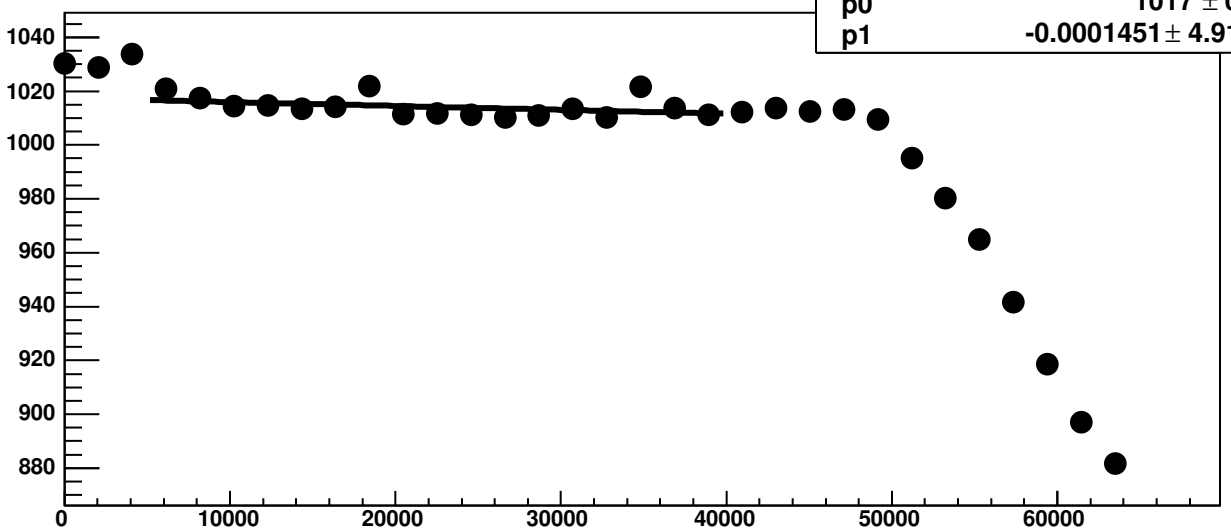



Chip 1, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9047 / 15
p0 1011 ± 0.1148
p1 -0.0005462 ± 4.621e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

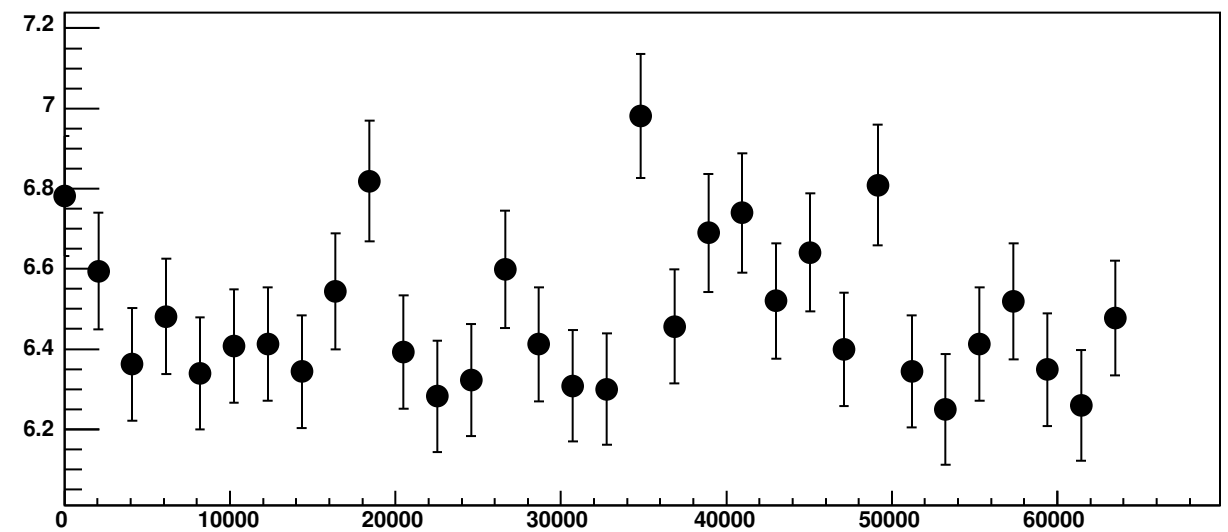

Chip 1, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

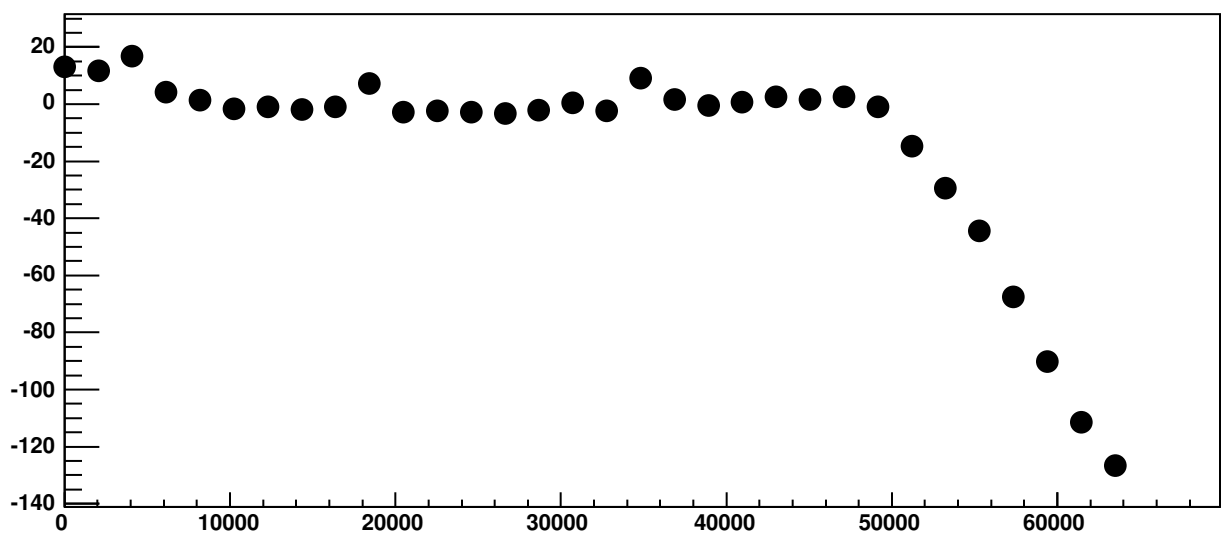
Chip 1, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC

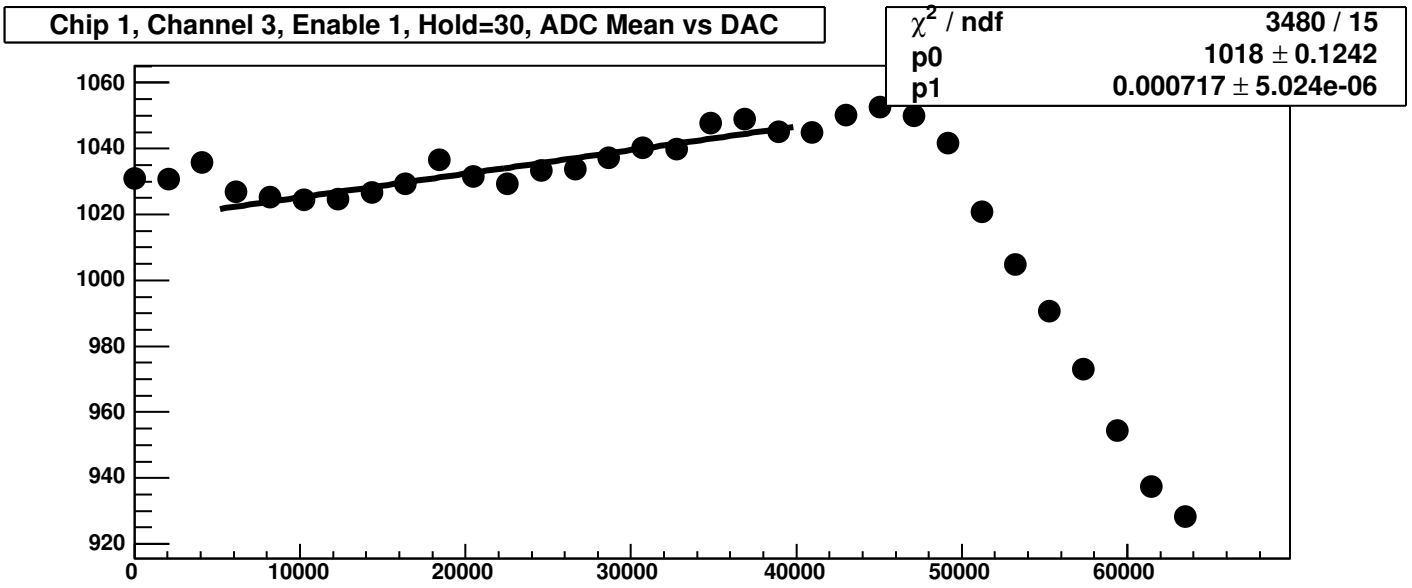


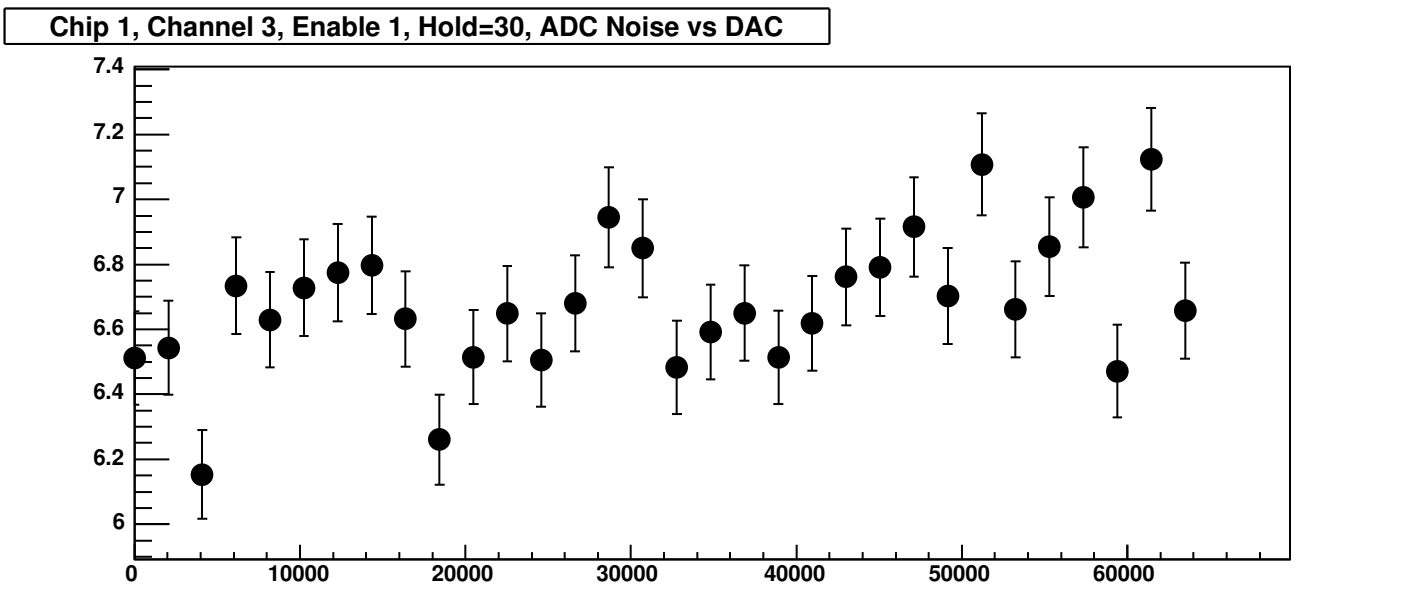
Chip 1, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC

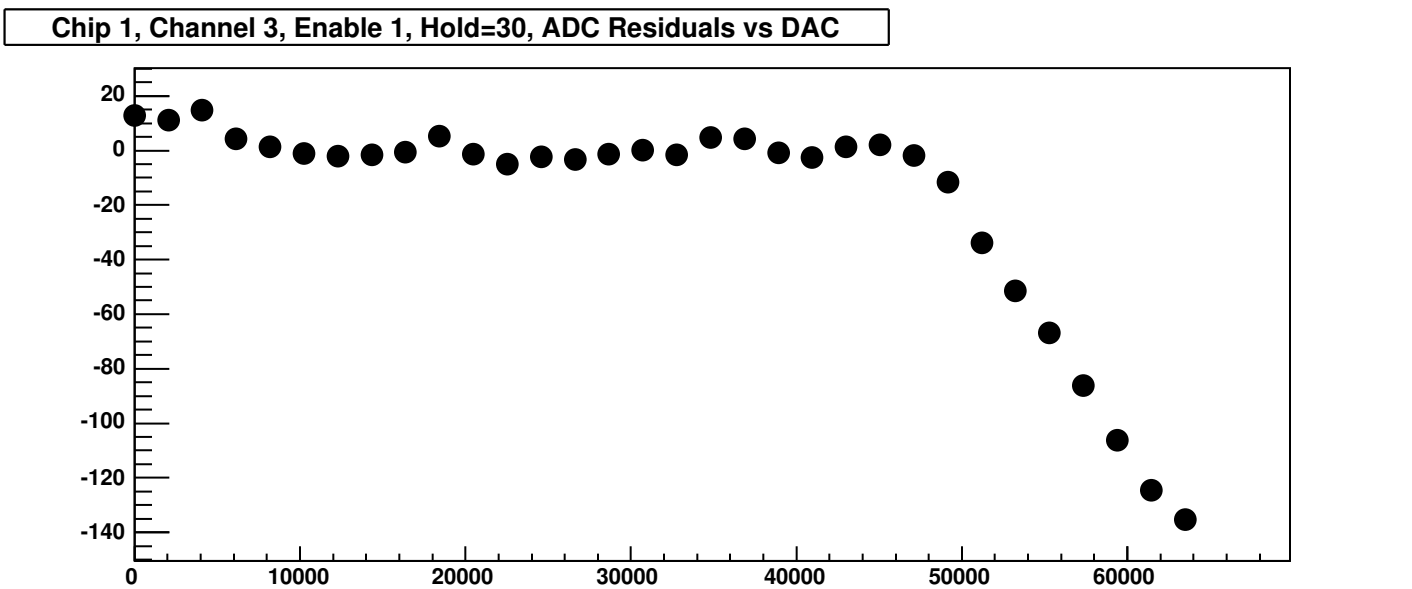


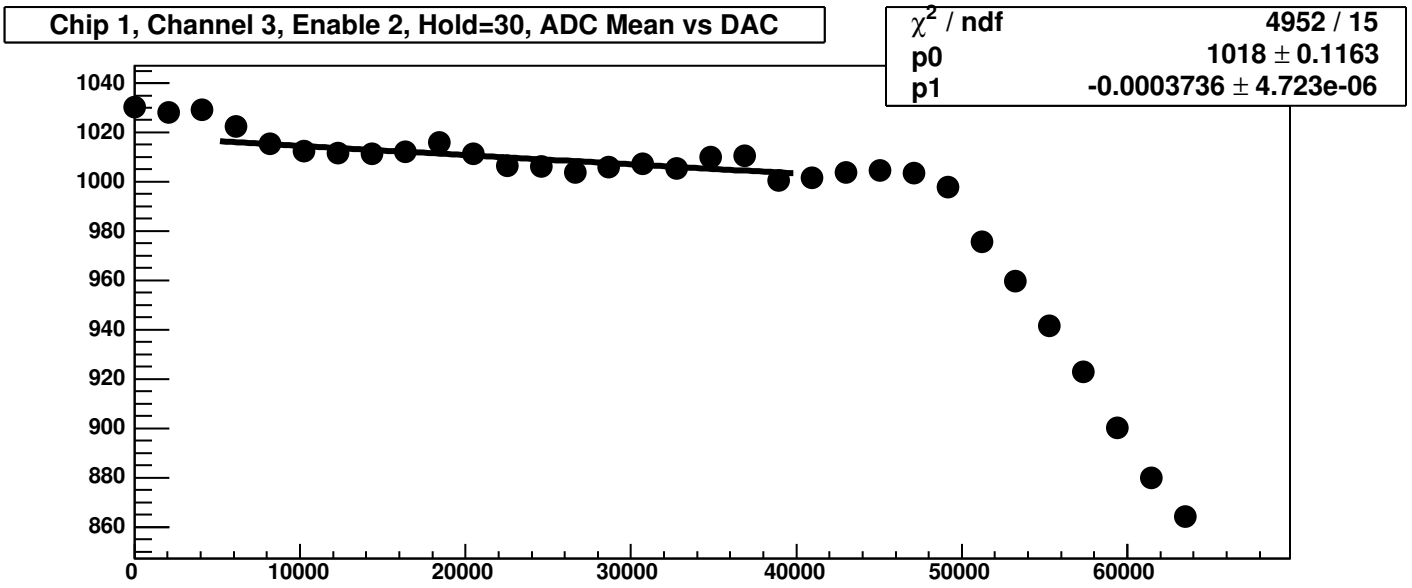
Chip 1, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

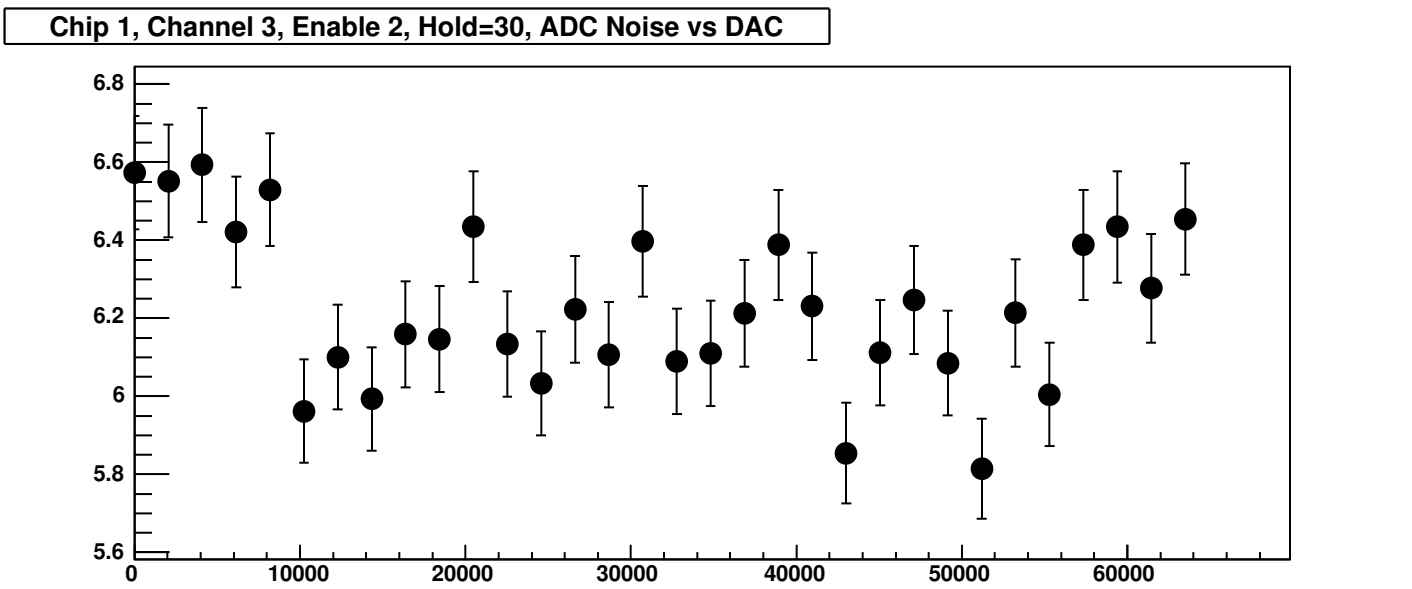


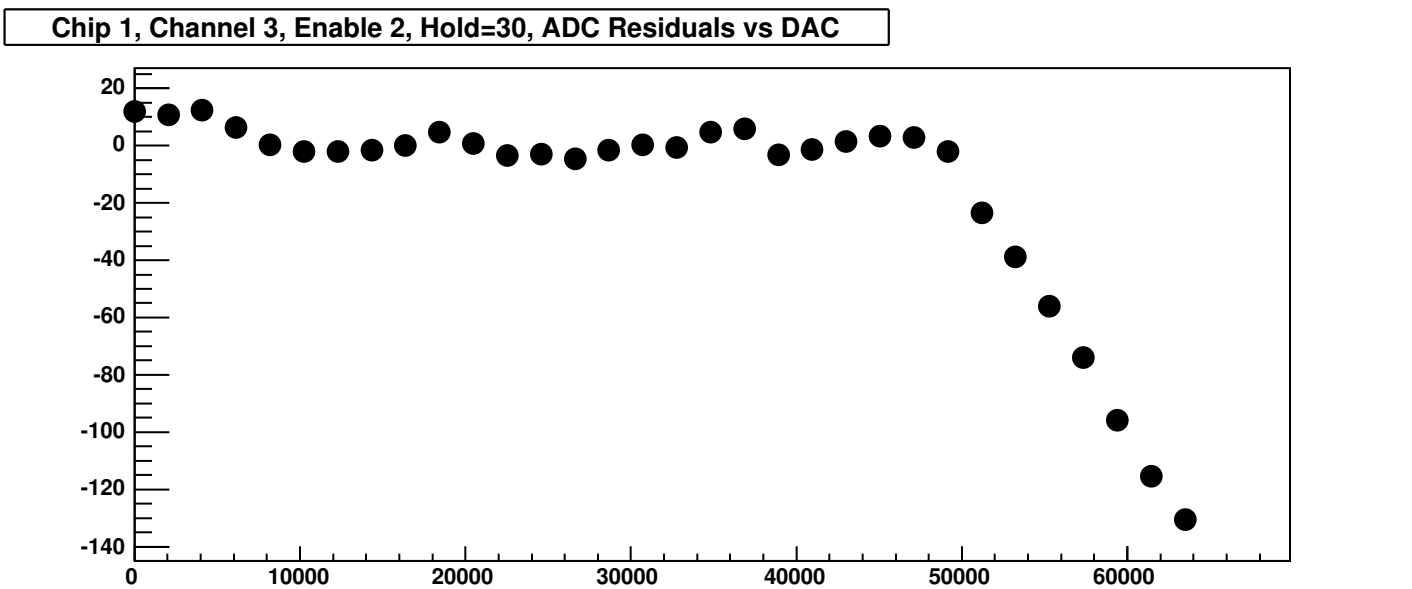

Chip 1, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3480 / 15
p0 1018 ± 0.1242
p1 0.000717 ± 5.024e-06
1060
1040
1020
1000
980
960
940
920
0
10000
20000
30000
40000
50000
60000

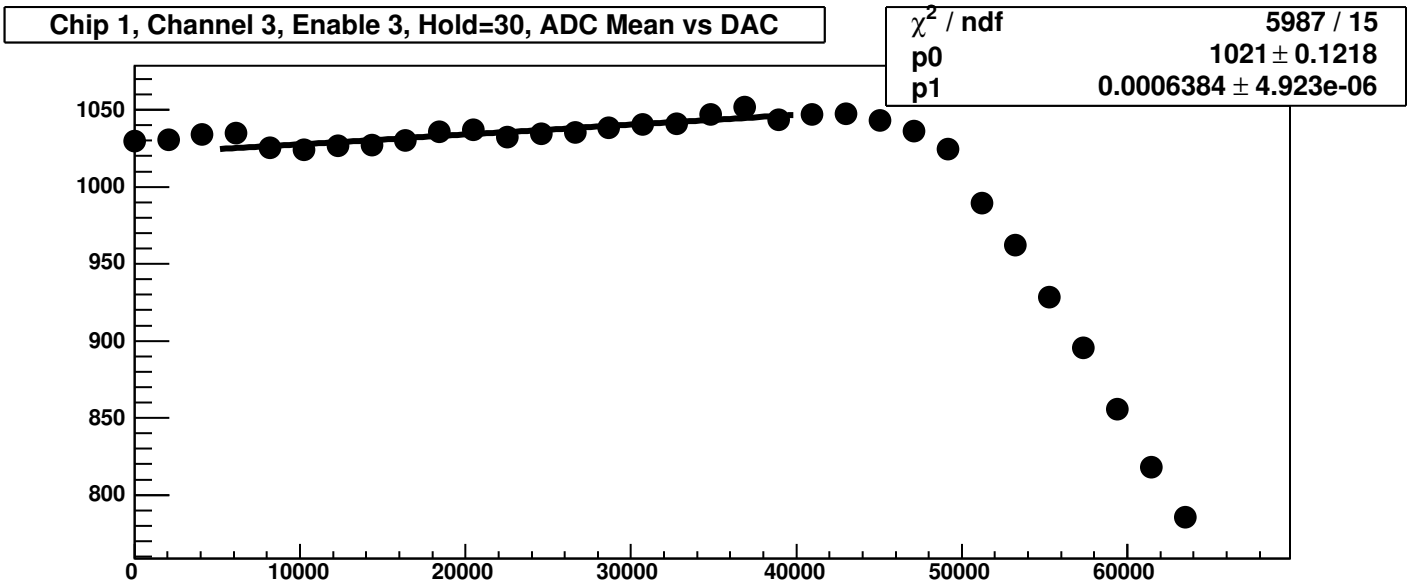

Chip 1, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

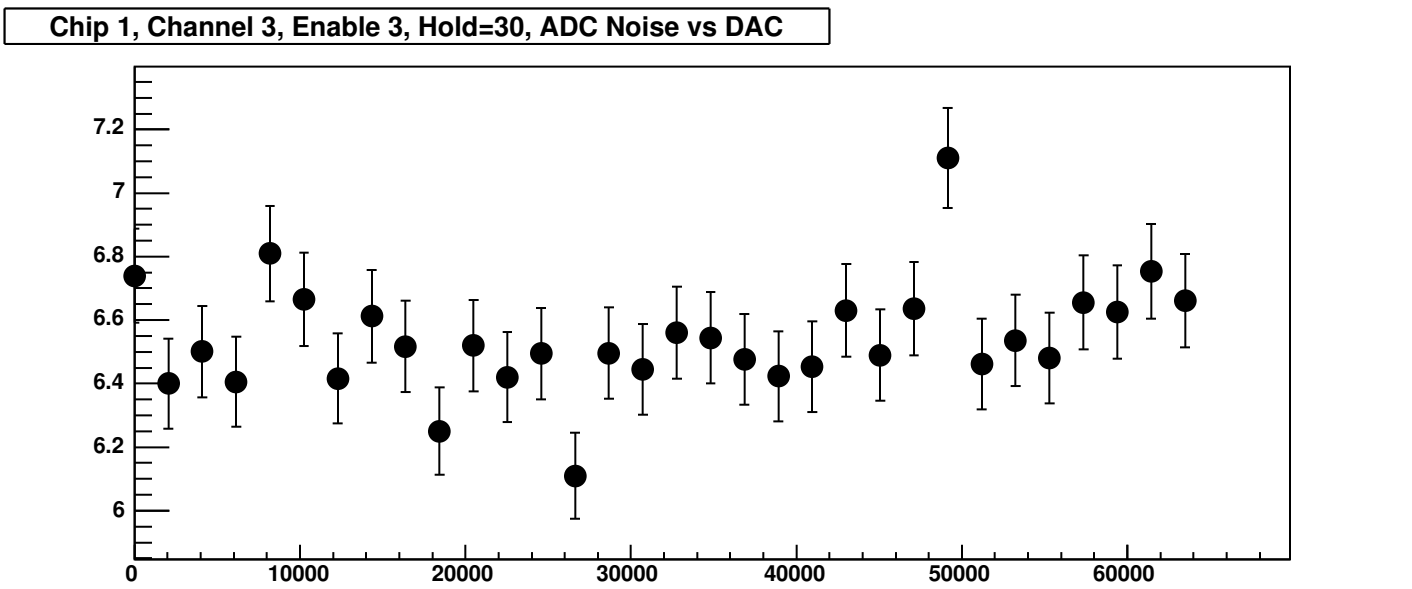

Chip 1, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

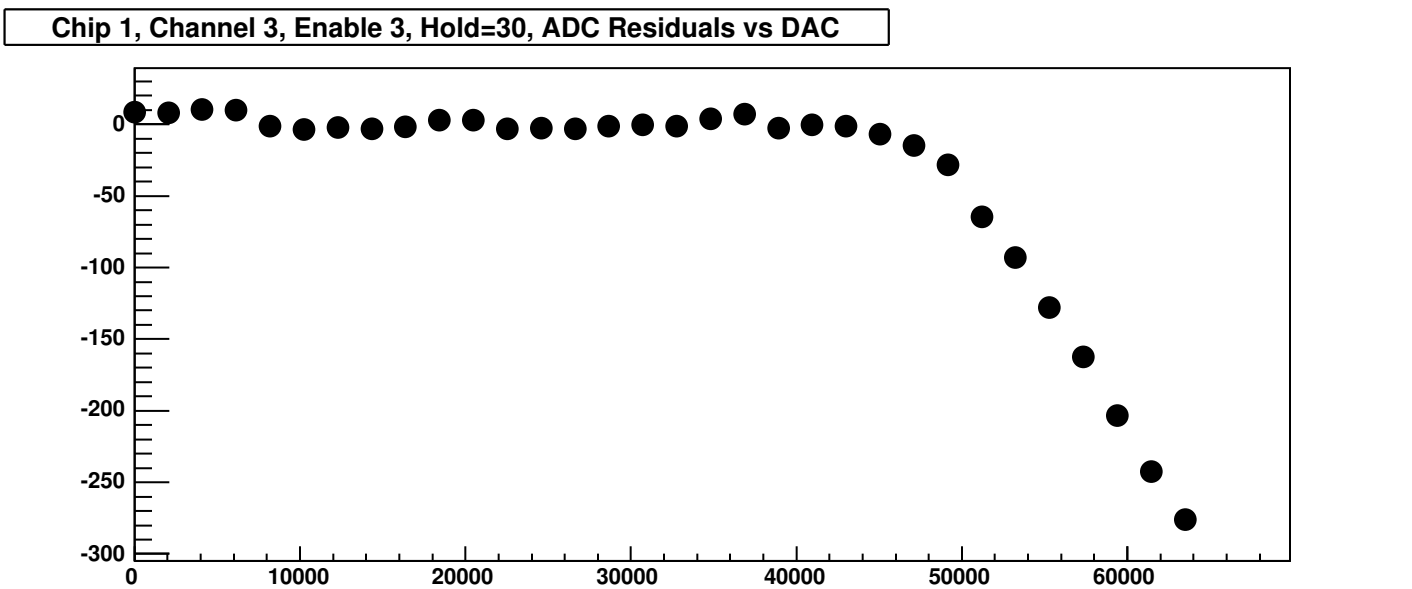

Chip 1, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4952 / 15
p0 1018 ± 0.1163
p1 -0.0003736 ± 4.723e-06
1040
1020
1000
980
960
940
920
900
880
860
0
10000
20000
30000
40000
50000
60000

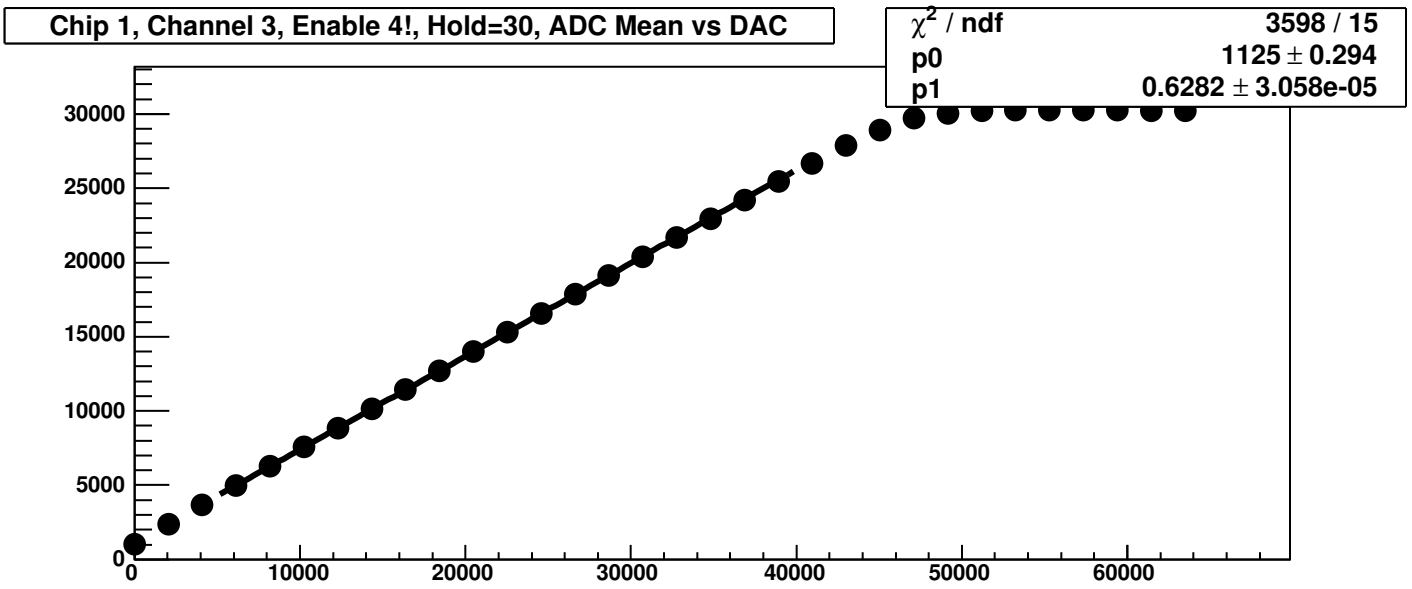

Chip 1, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

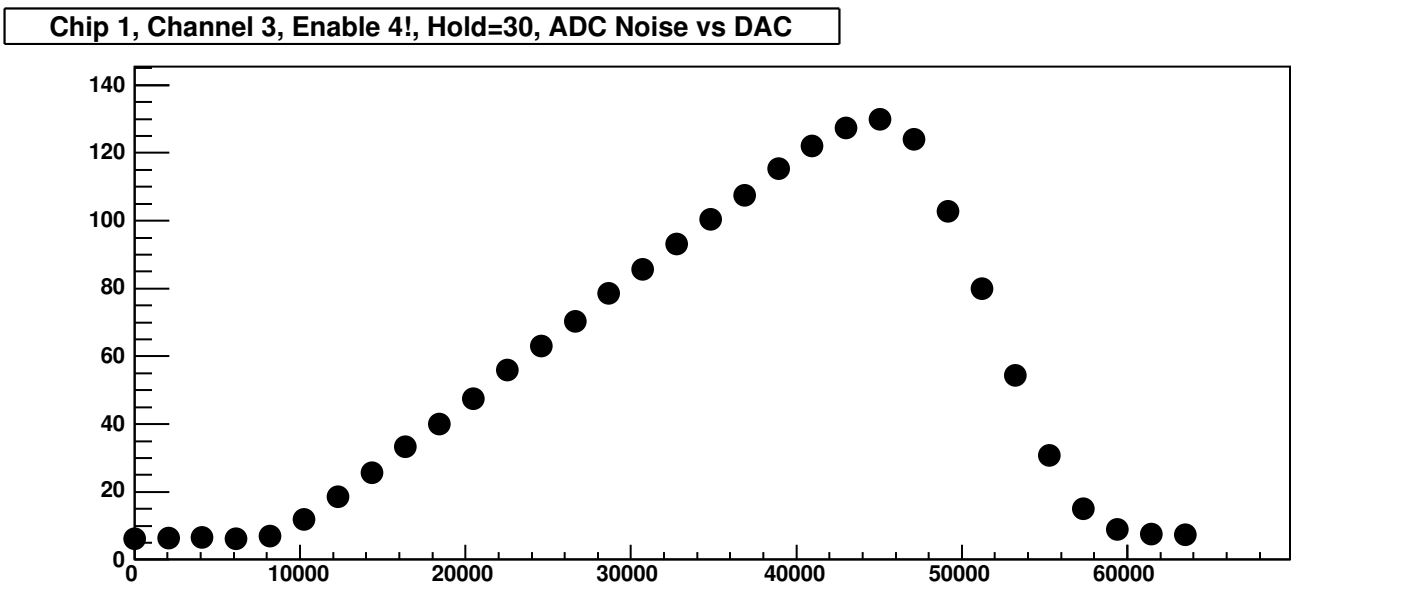

Chip 1, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

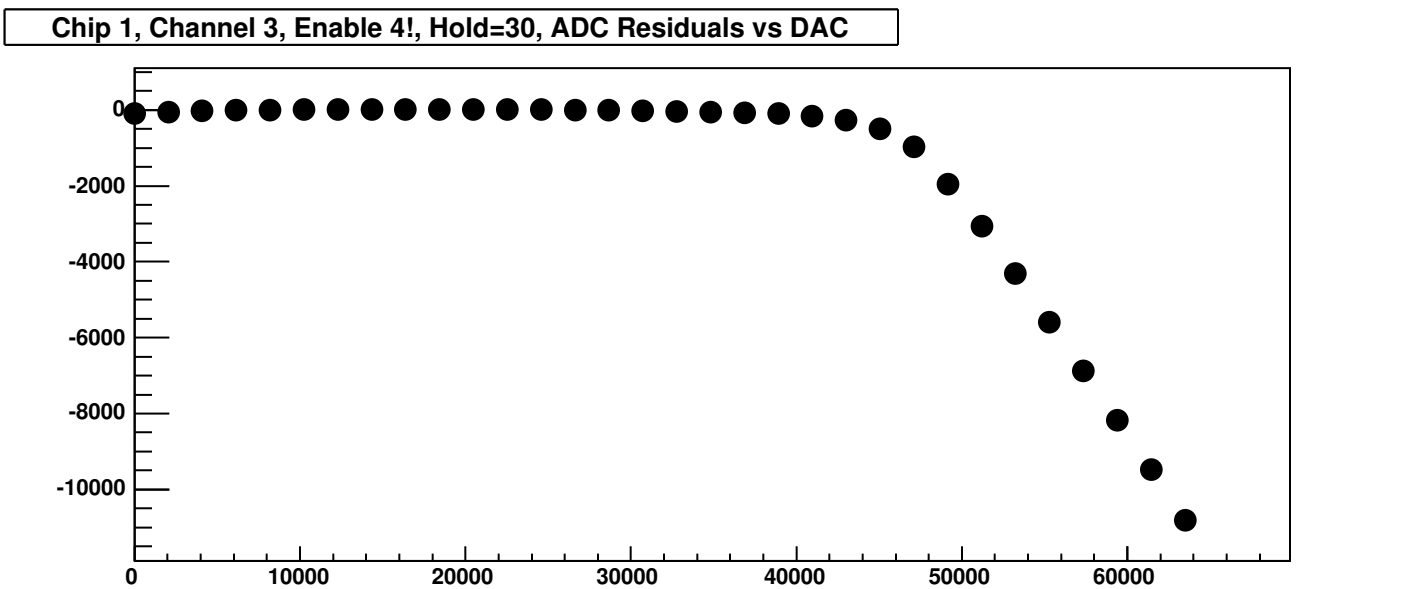

Chip 1, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5987 / 15
p0 1021 ± 0.1218
p1 0.0006384 ± 4.923e-06
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

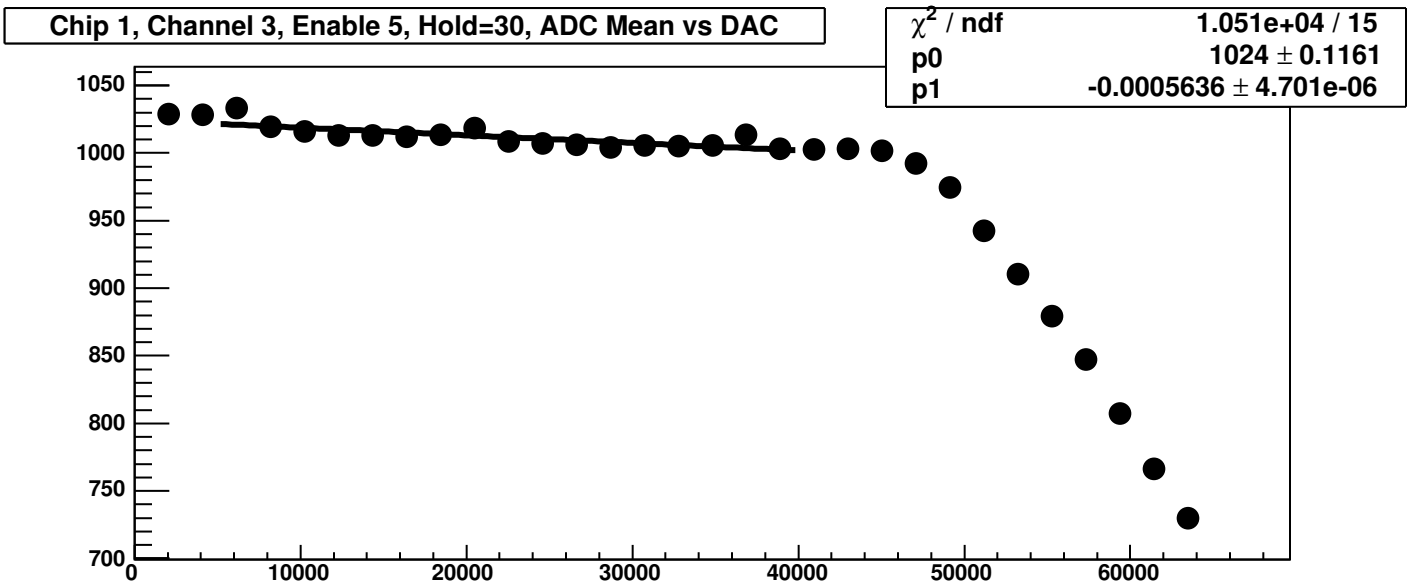

Chip 1, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

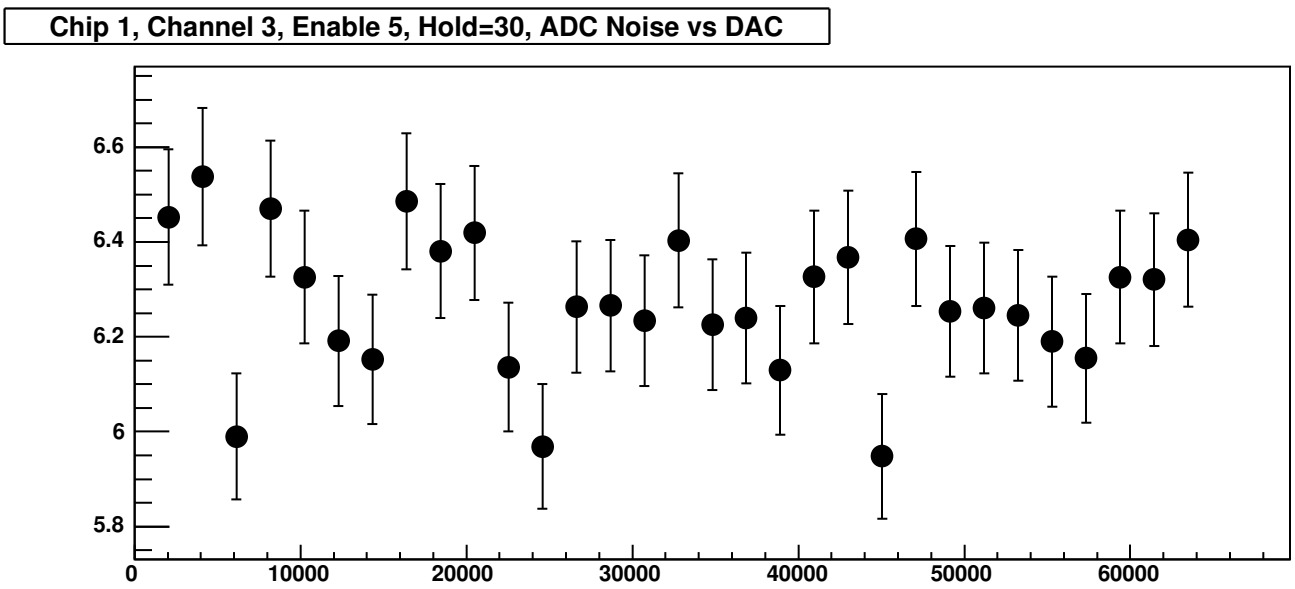

Chip 1, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

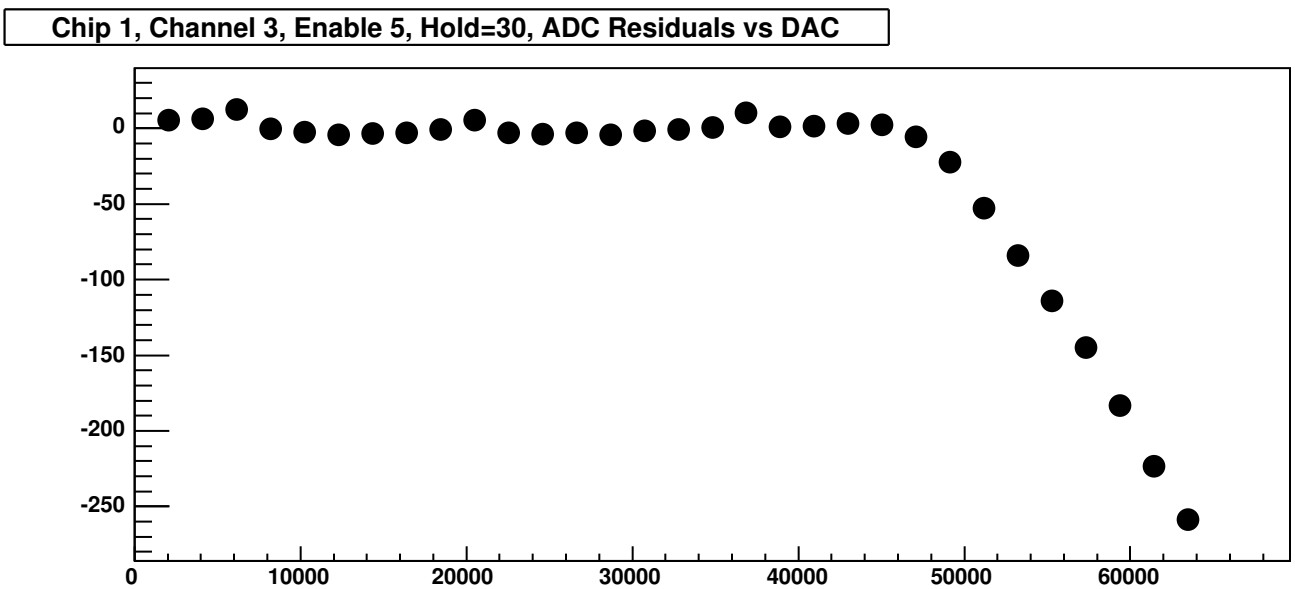

Chip 1, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 3598 / 15
p0 1125 ± 0.294
p1 0.6282 ± 3.058e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

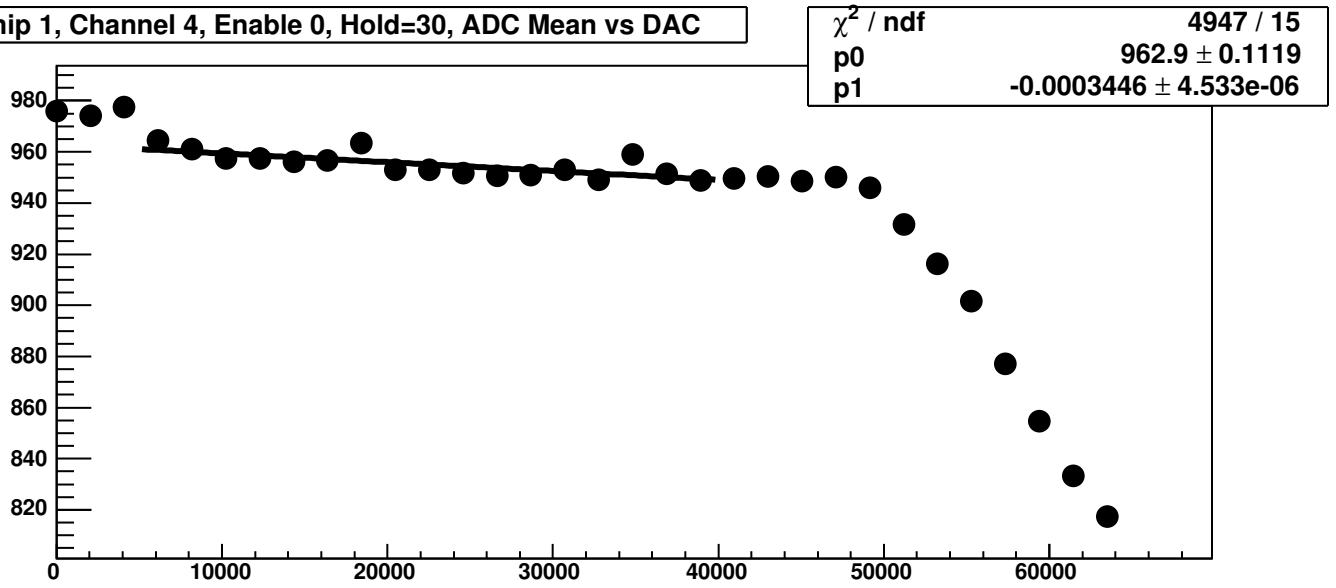

Chip 1, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.051e+04 / 15
p0 1024 ± 0.1161
p1 -0.0005636 ± 4.701e-06
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

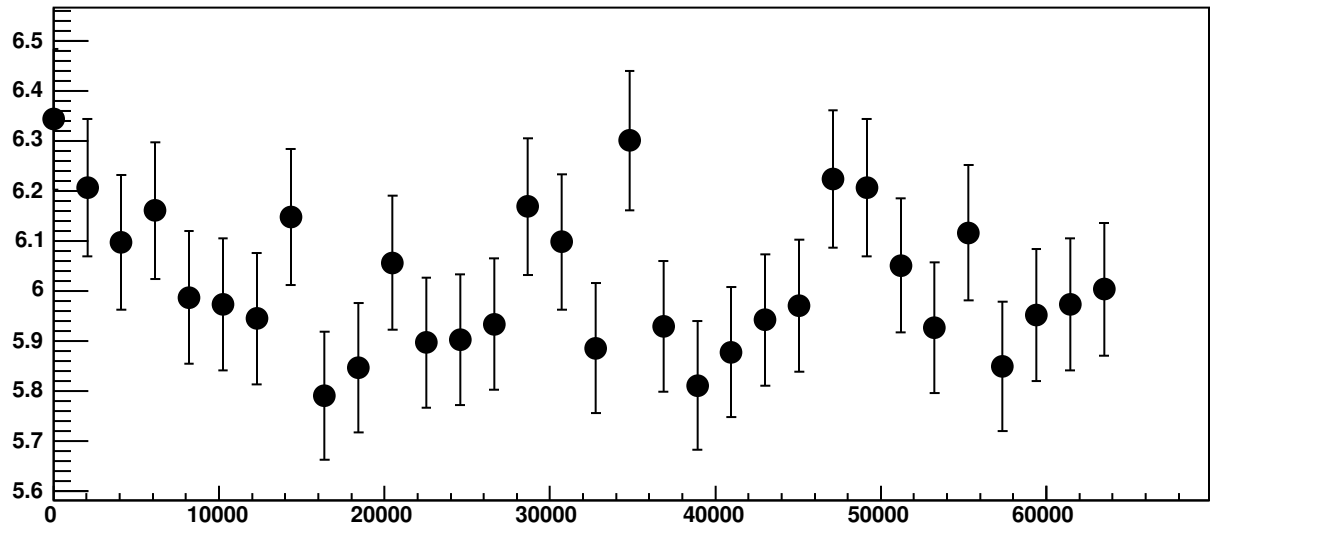

Chip 1, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

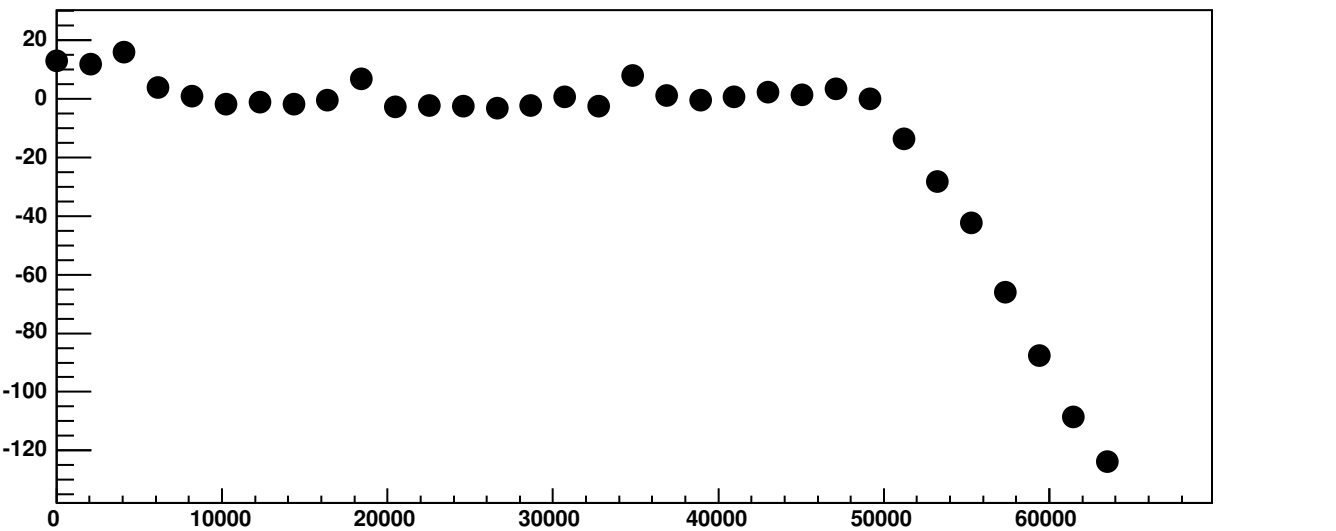
Chip 1, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

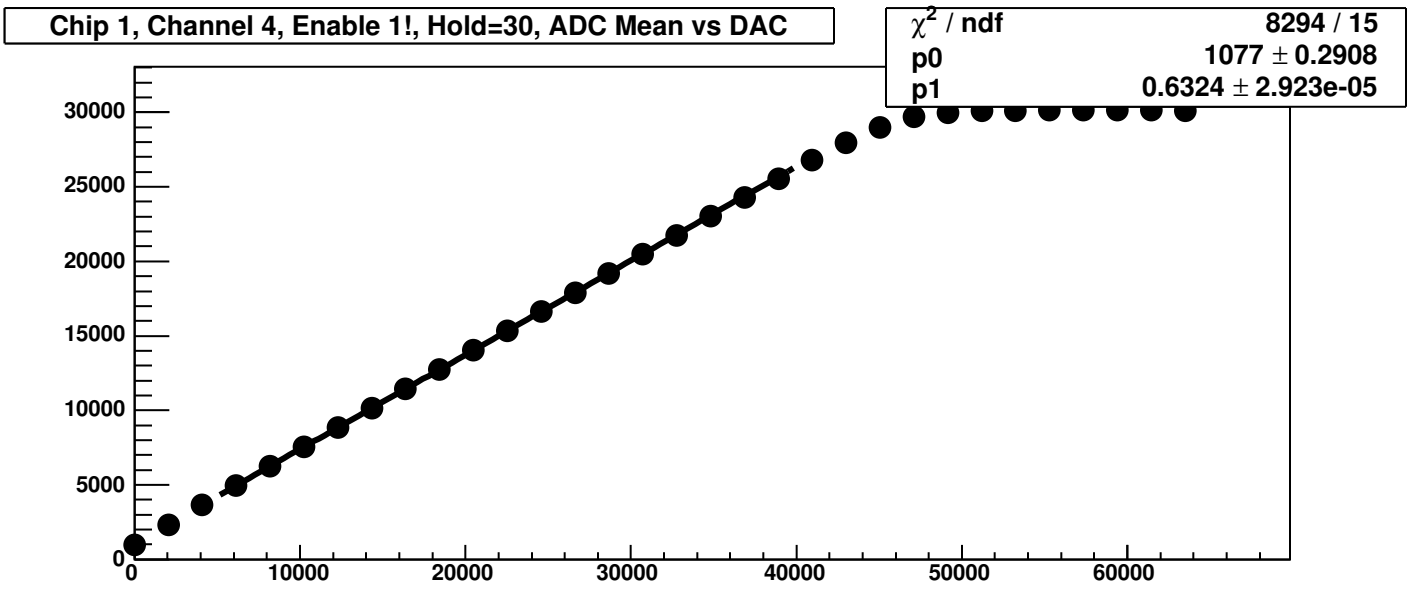


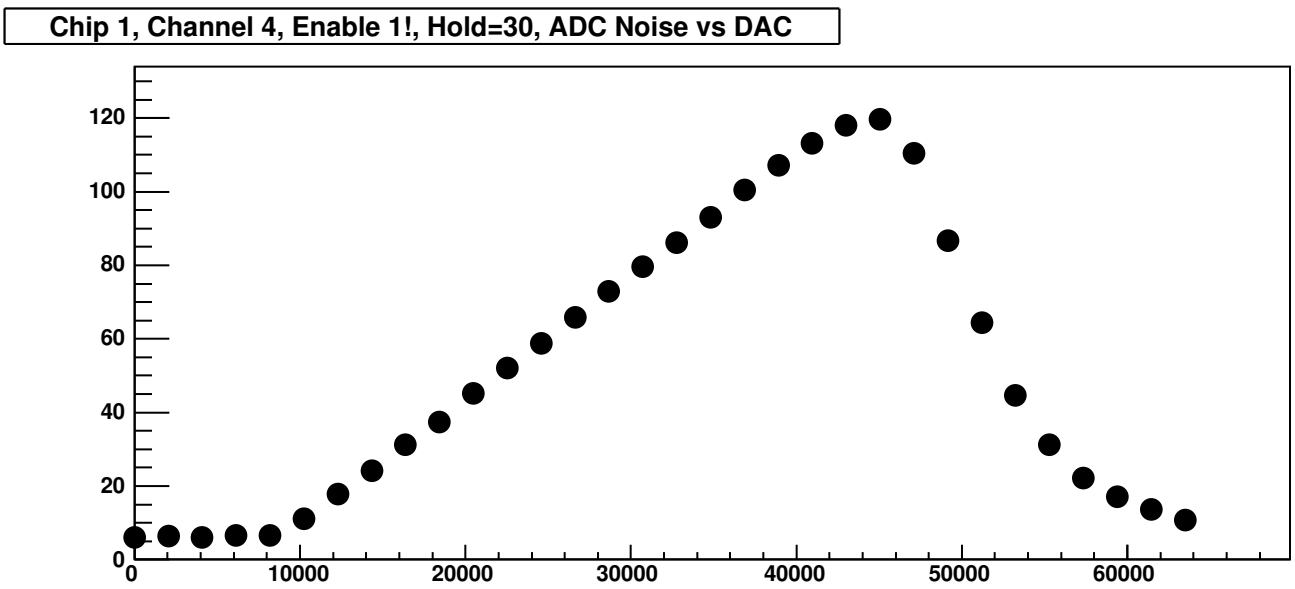
Chip 1, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC

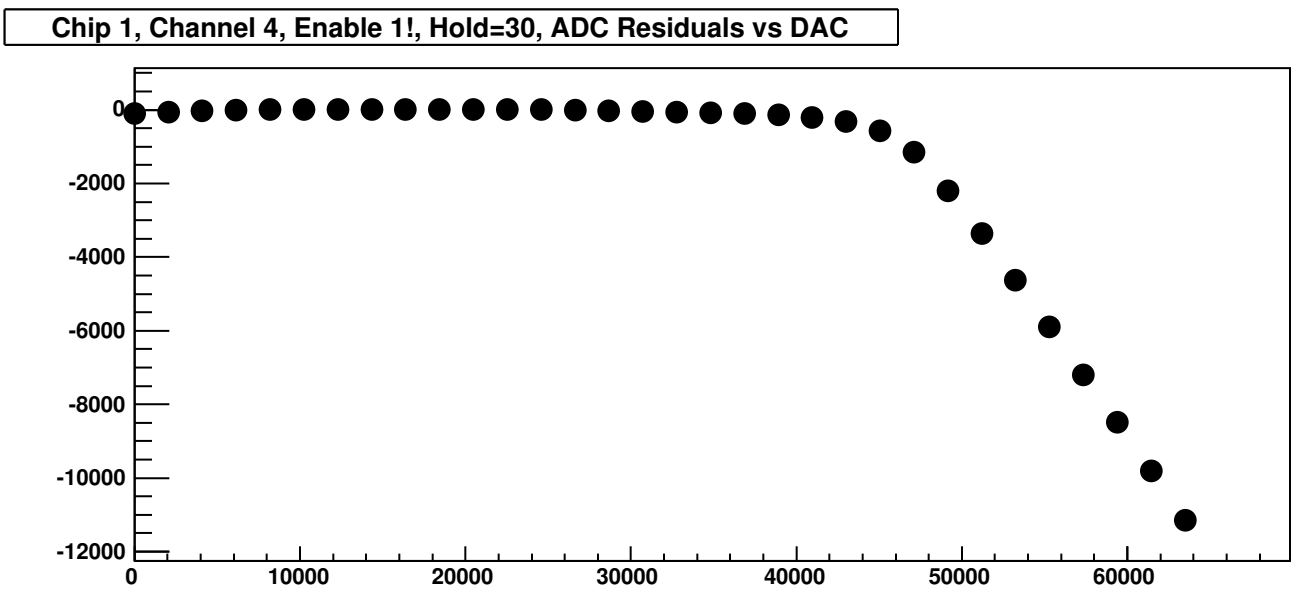


Chip 1, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC



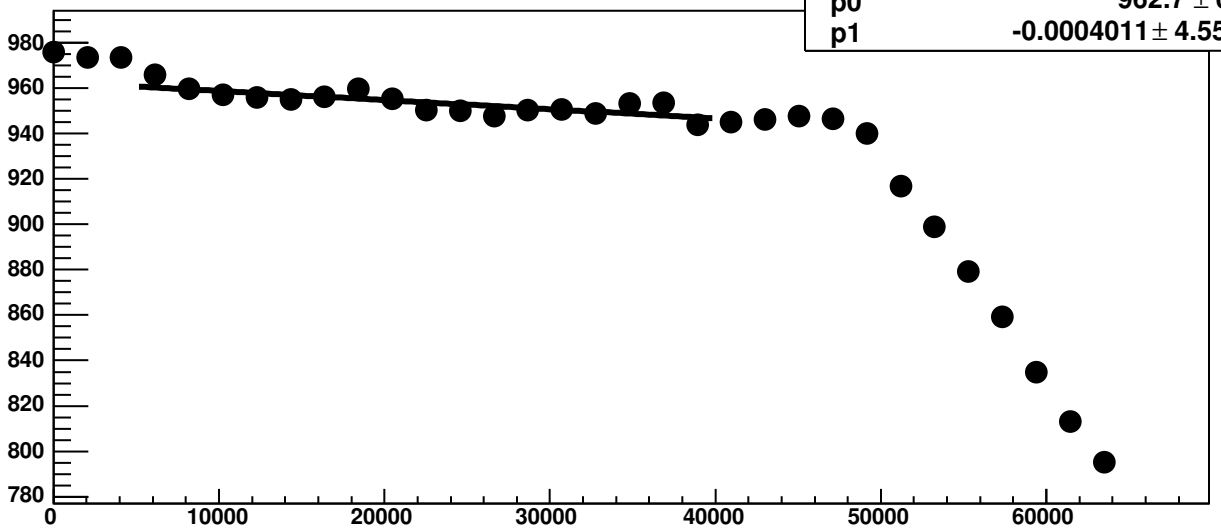

Chip 1, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 8294 / 15
p0 1077 ± 0.2908
p1 0.6324 ± 2.923e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

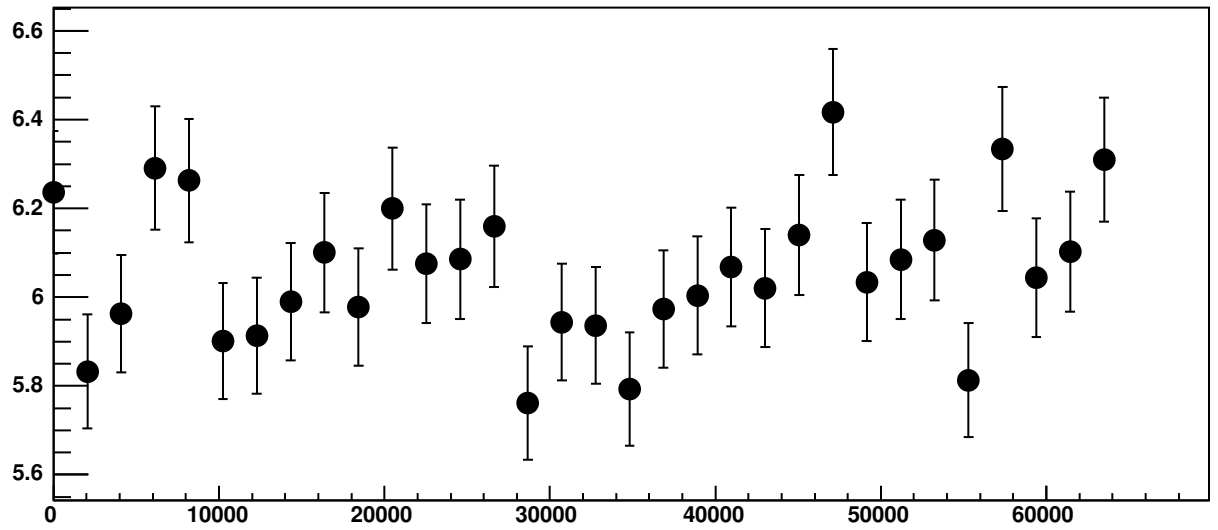

Chip 1, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

Chip 1, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

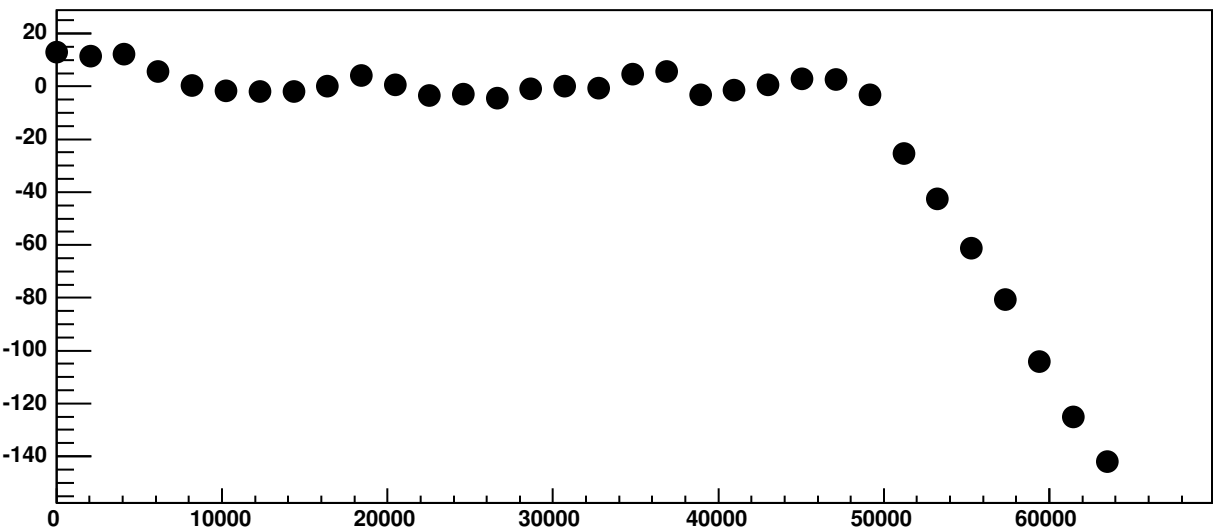
χ^2 / ndf	4608 / 15
p0	962.7 ± 0.1132
p1	-0.0004011 ± 4.555e-06



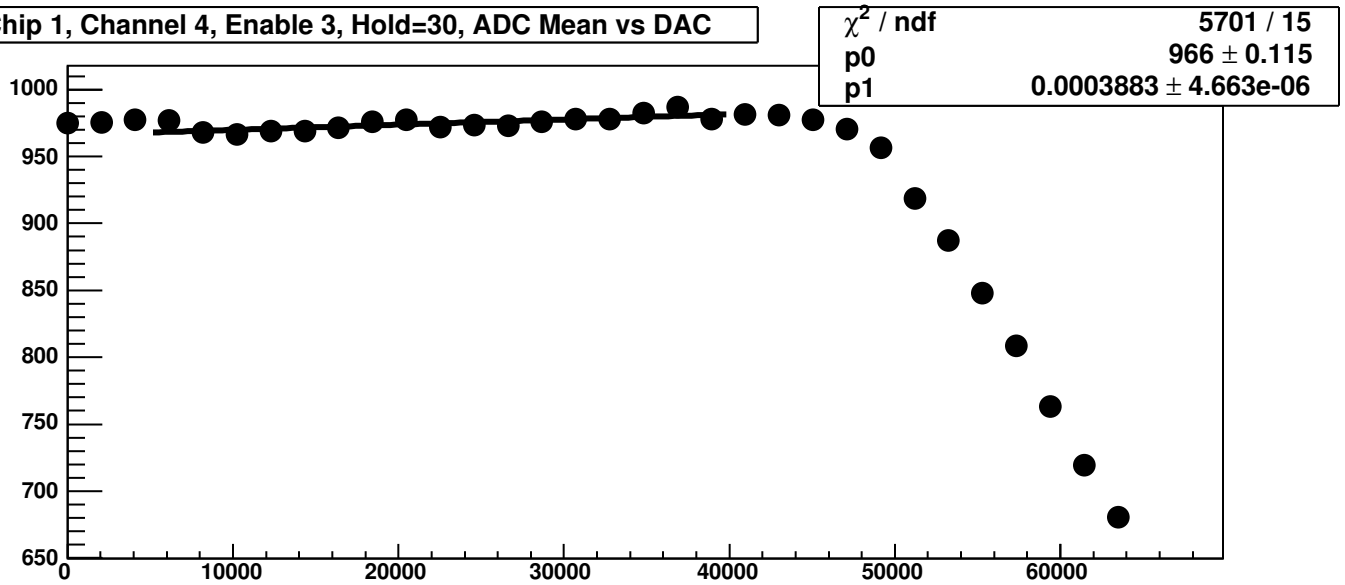
Chip 1, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



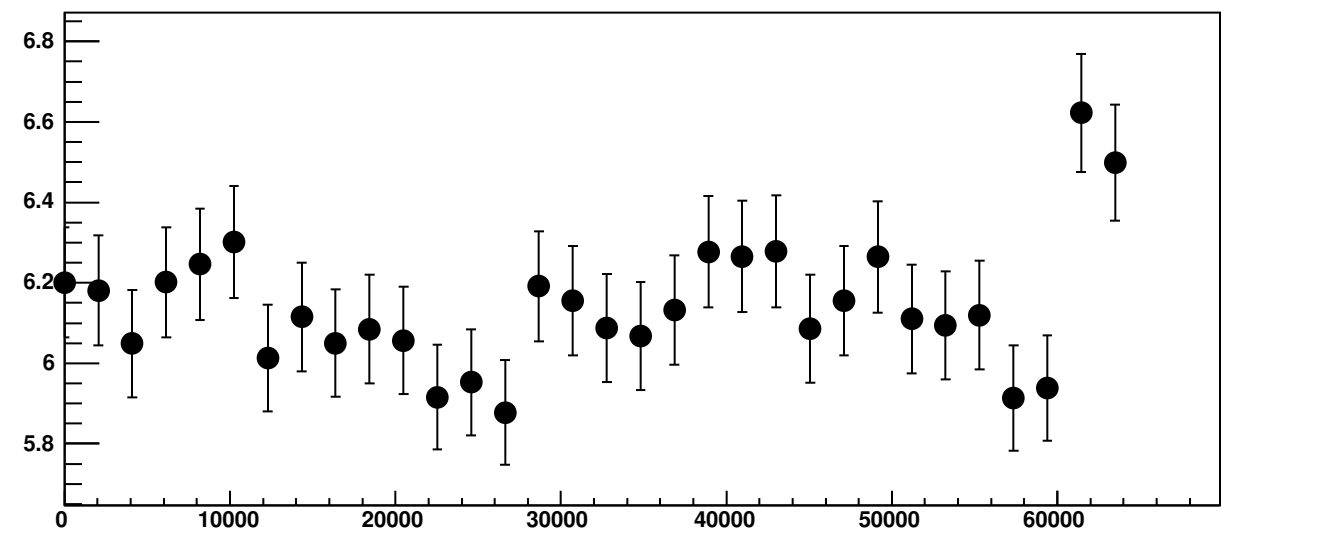
Chip 1, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC



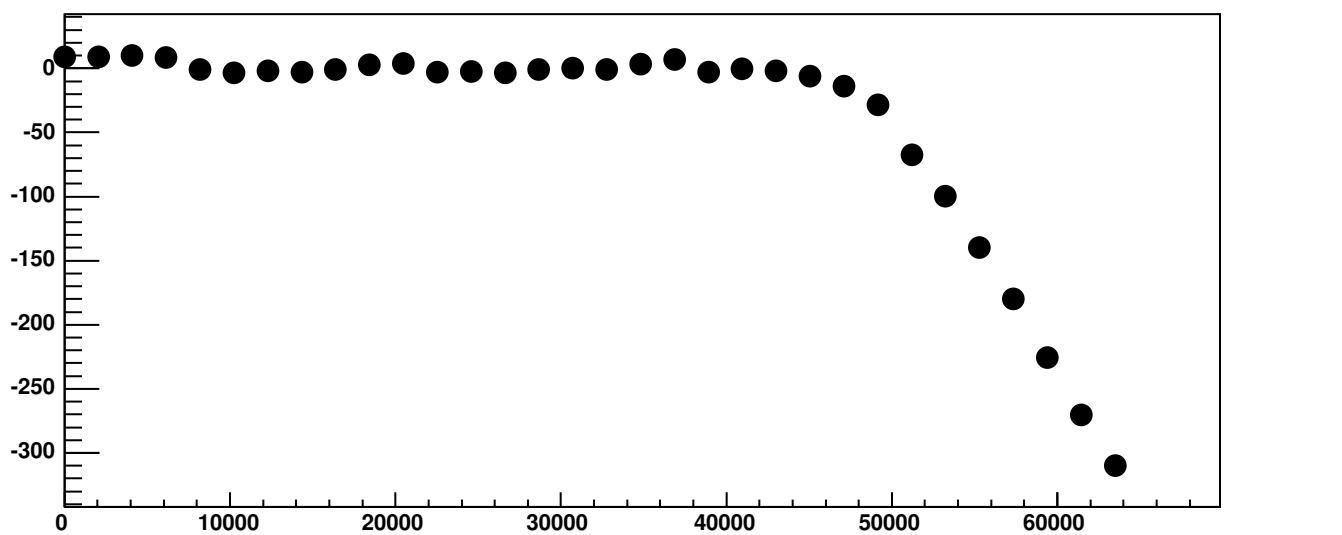
Chip 1, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC

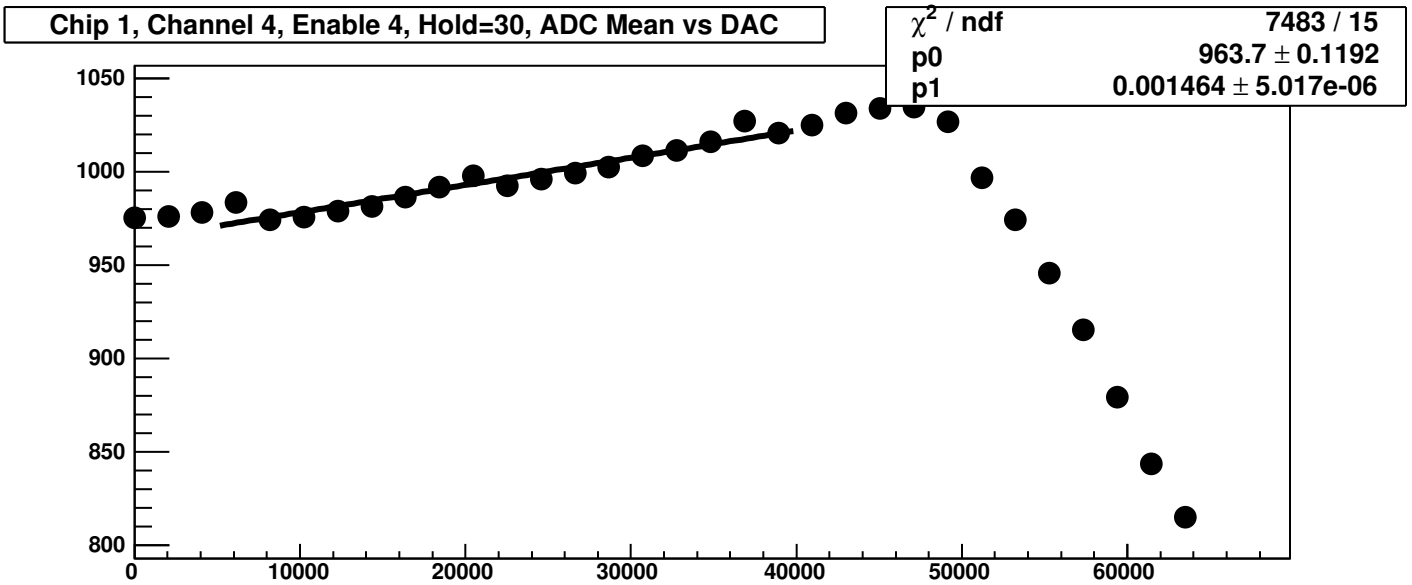


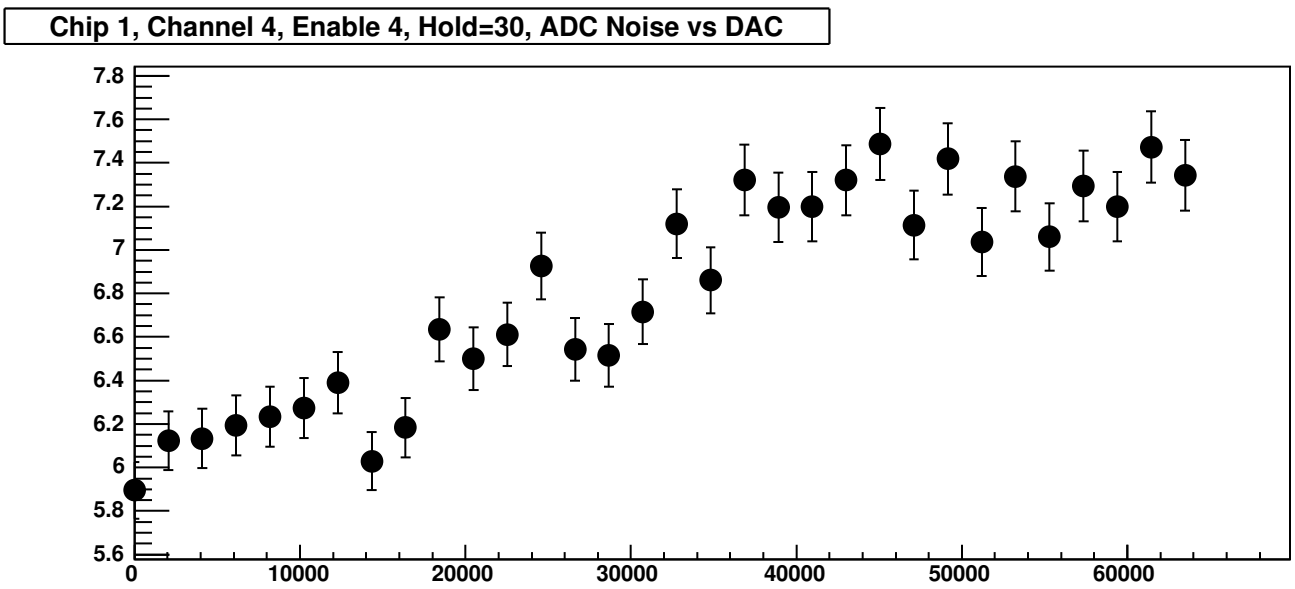
Chip 1, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC

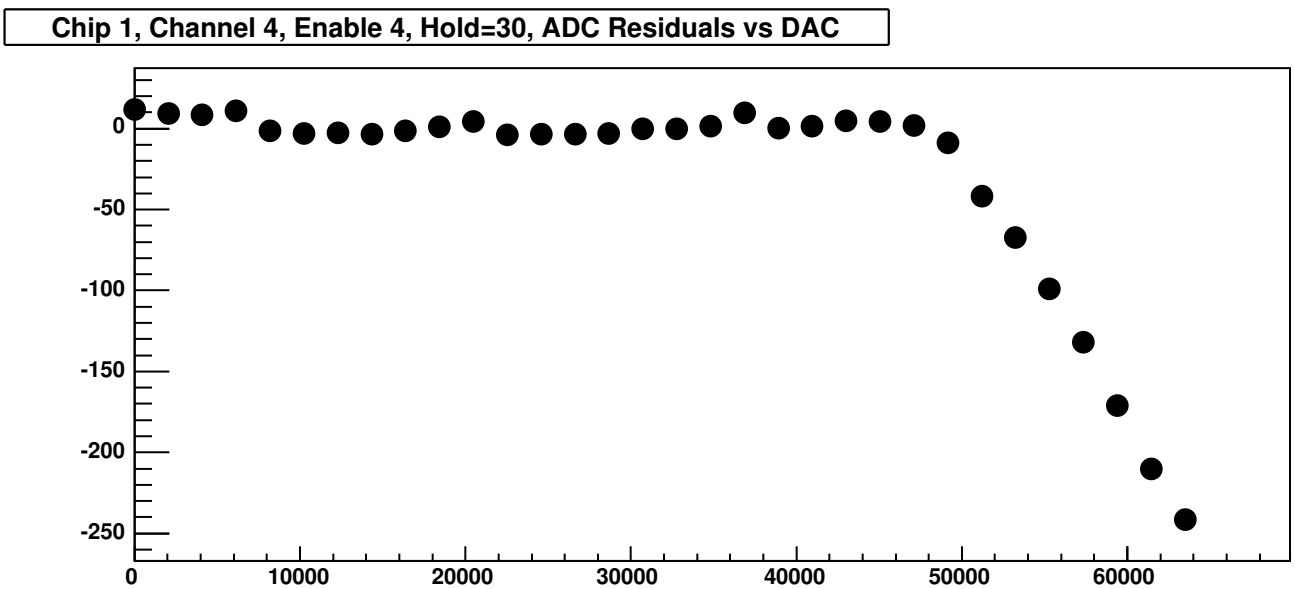


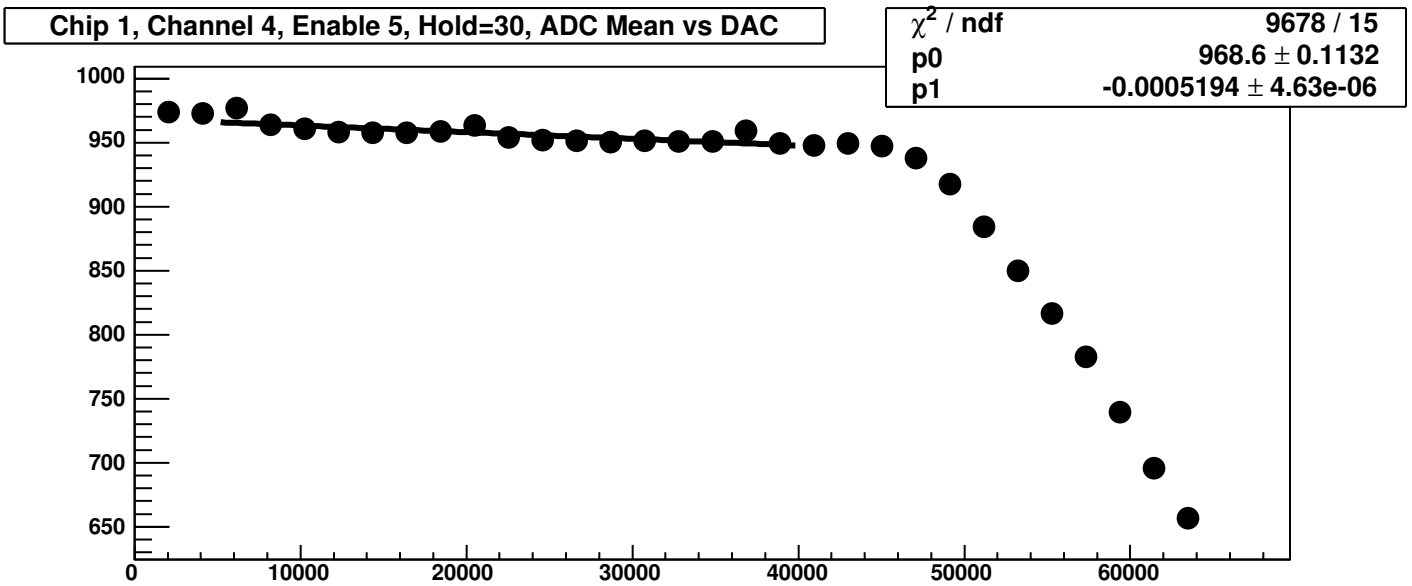
Chip 1, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC

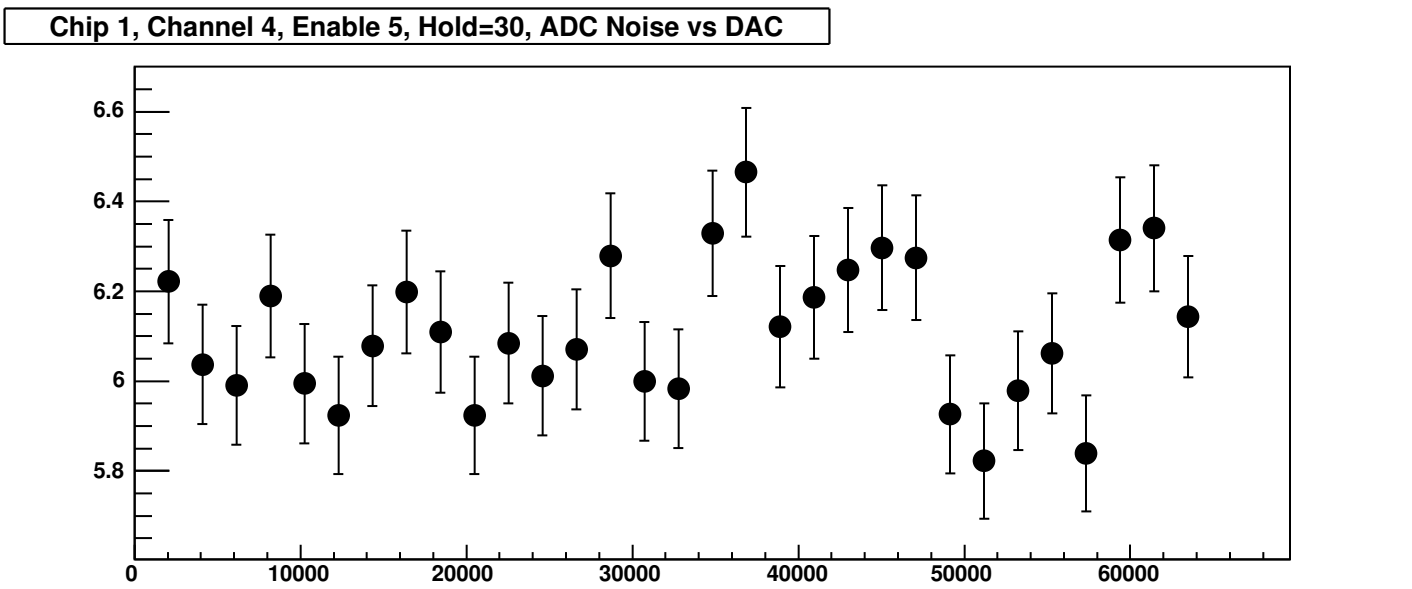


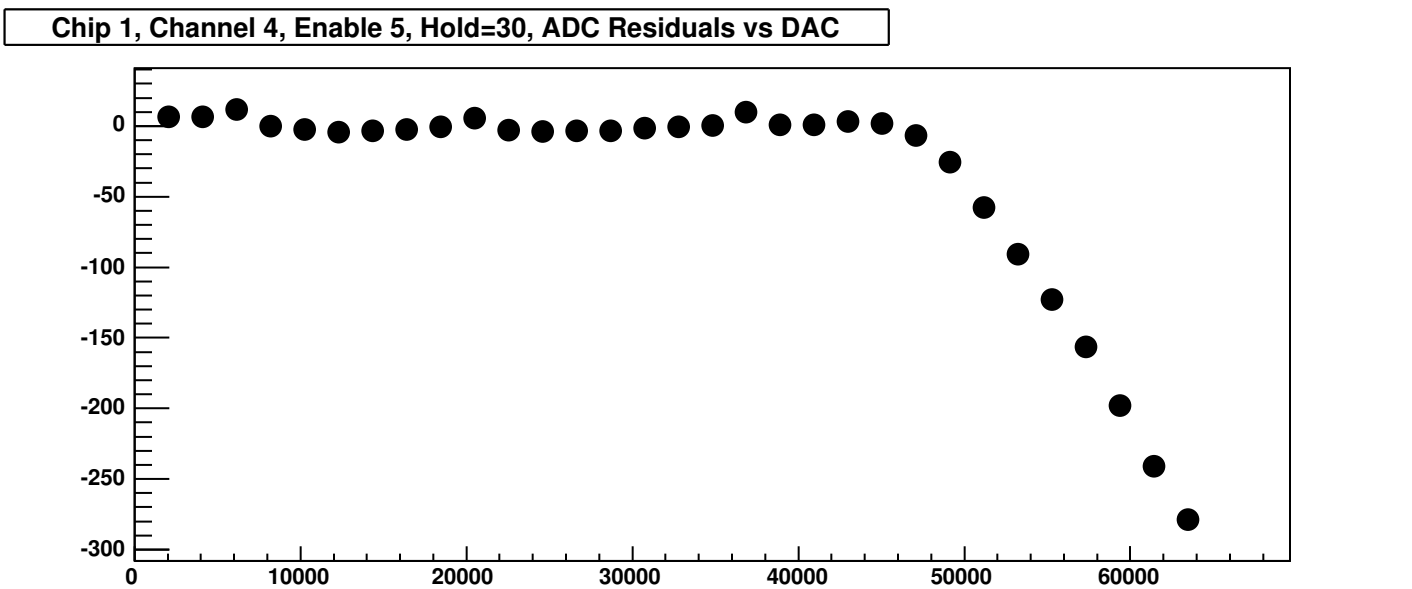

Chip 1, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7483 / 15
p0 963.7 ± 0.1192
p1 0.001464 ± 5.017e-06
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

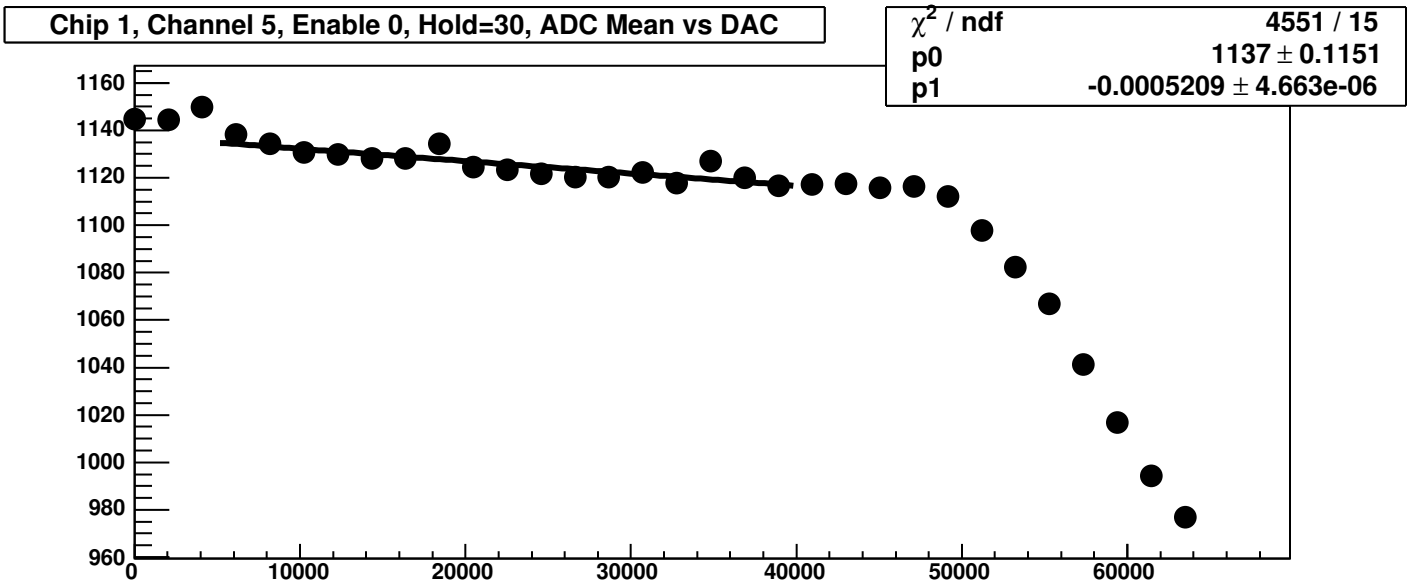

Chip 1, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

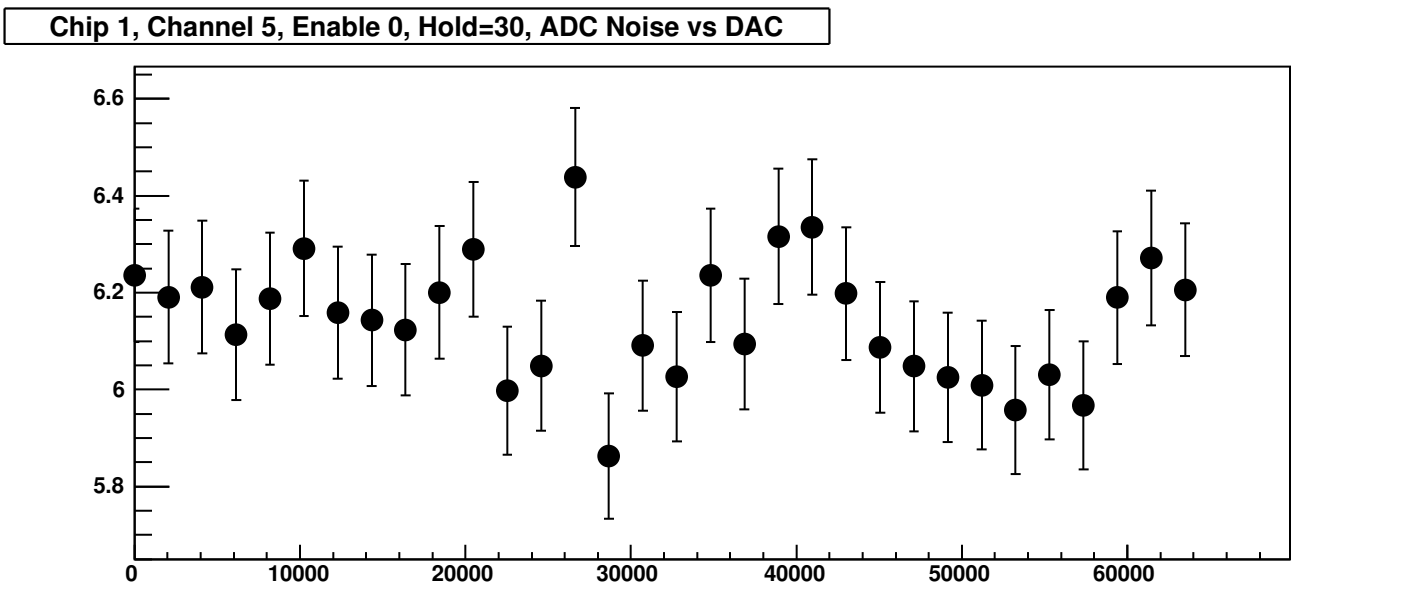

Chip 1, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

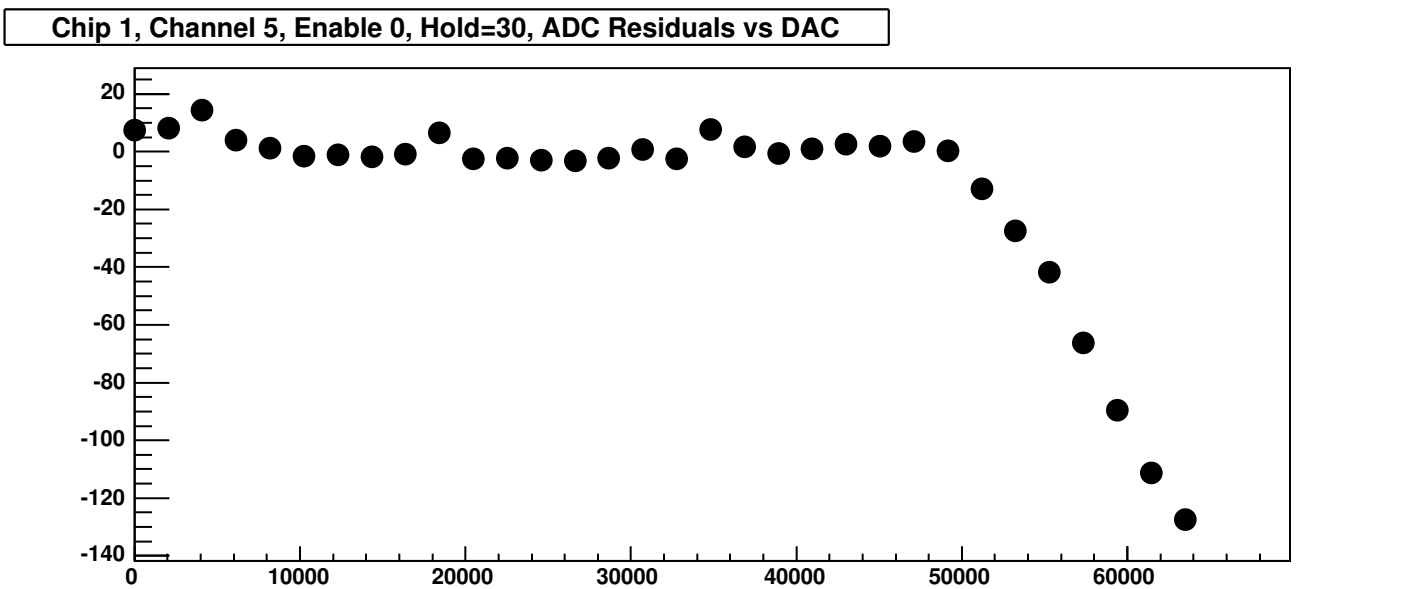

Chip 1, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9678 / 15
p0 968.6 ± 0.1132
p1 -0.0005194 ± 4.63e-06
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

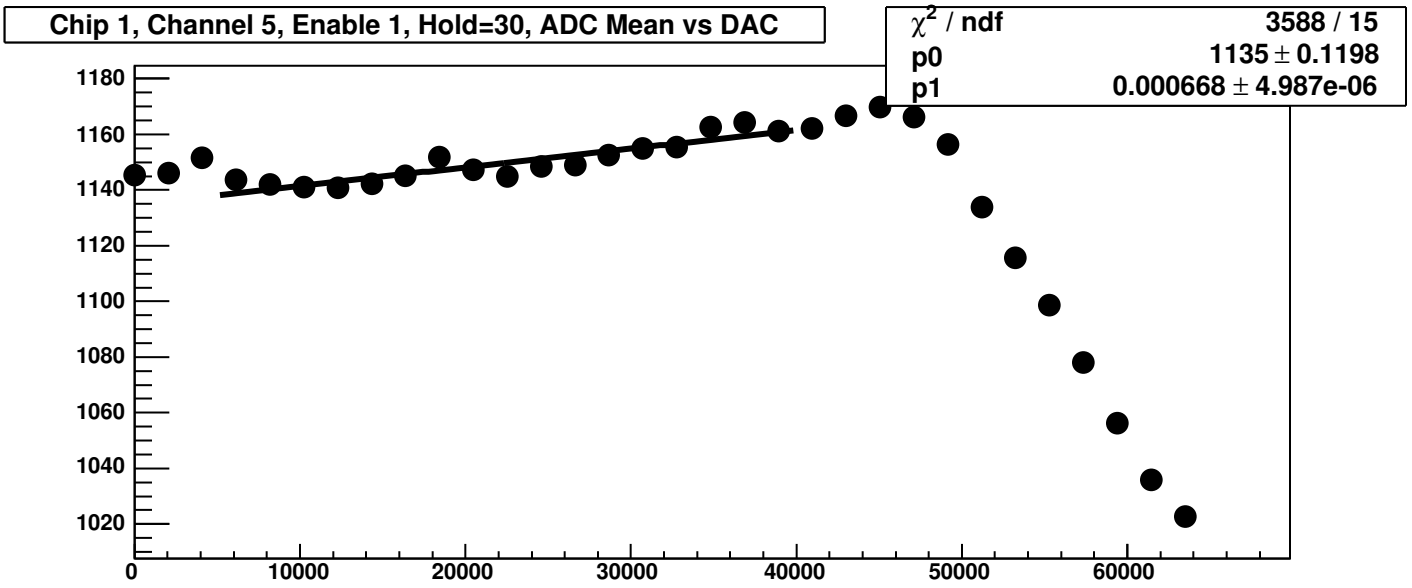

Chip 1, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

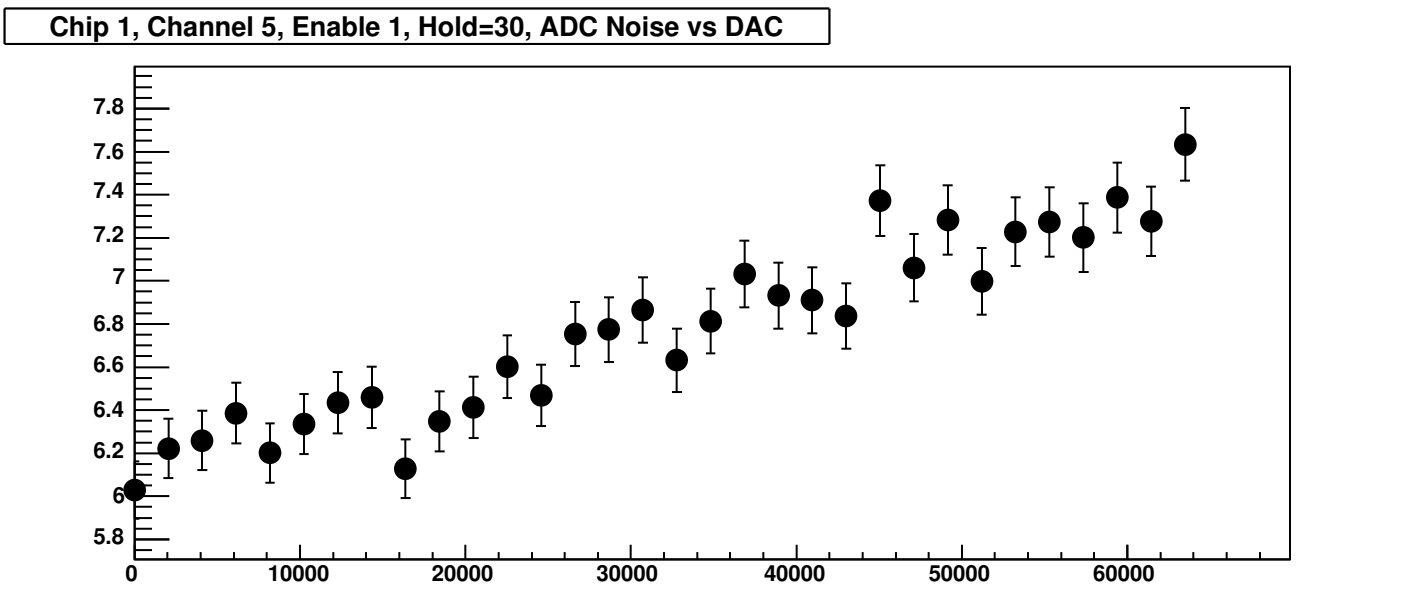

Chip 1, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

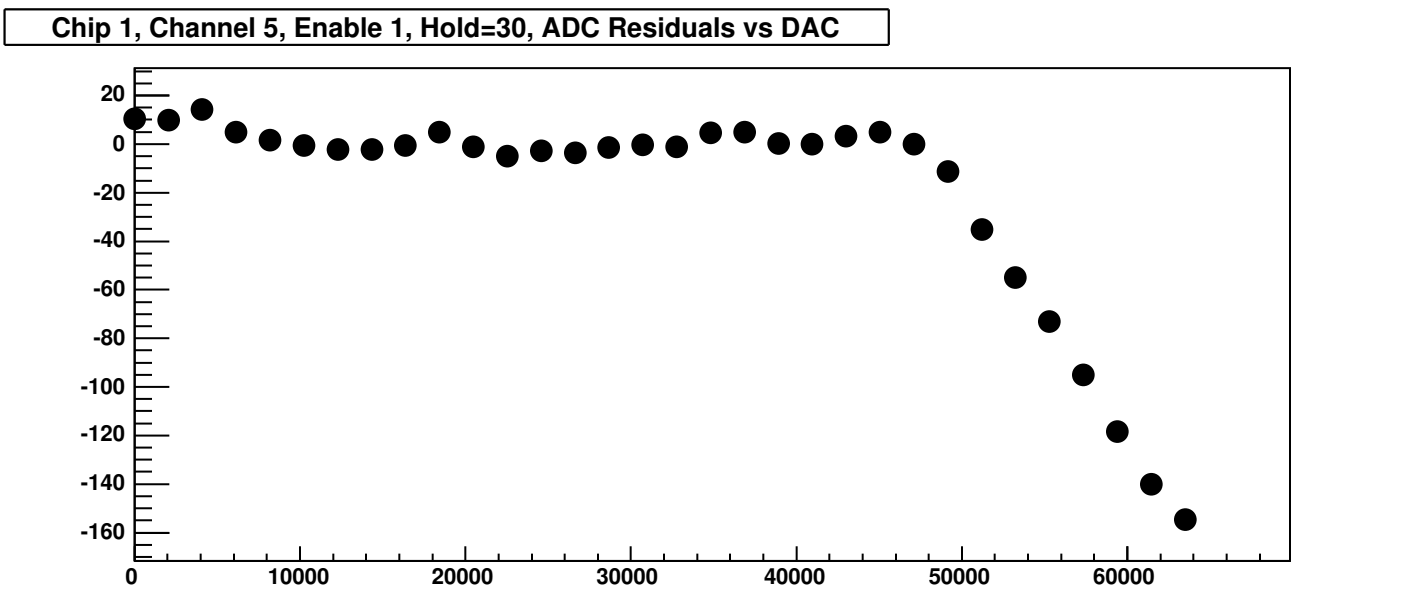

Chip 1, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4551 / 15
p0 1137 ± 0.1151
p1 -0.0005209 ± 4.663e-06
1160
1140
1120
1100
1080
1060
1040
1020
1000
980
960
0
10000
20000
30000
40000
50000
60000

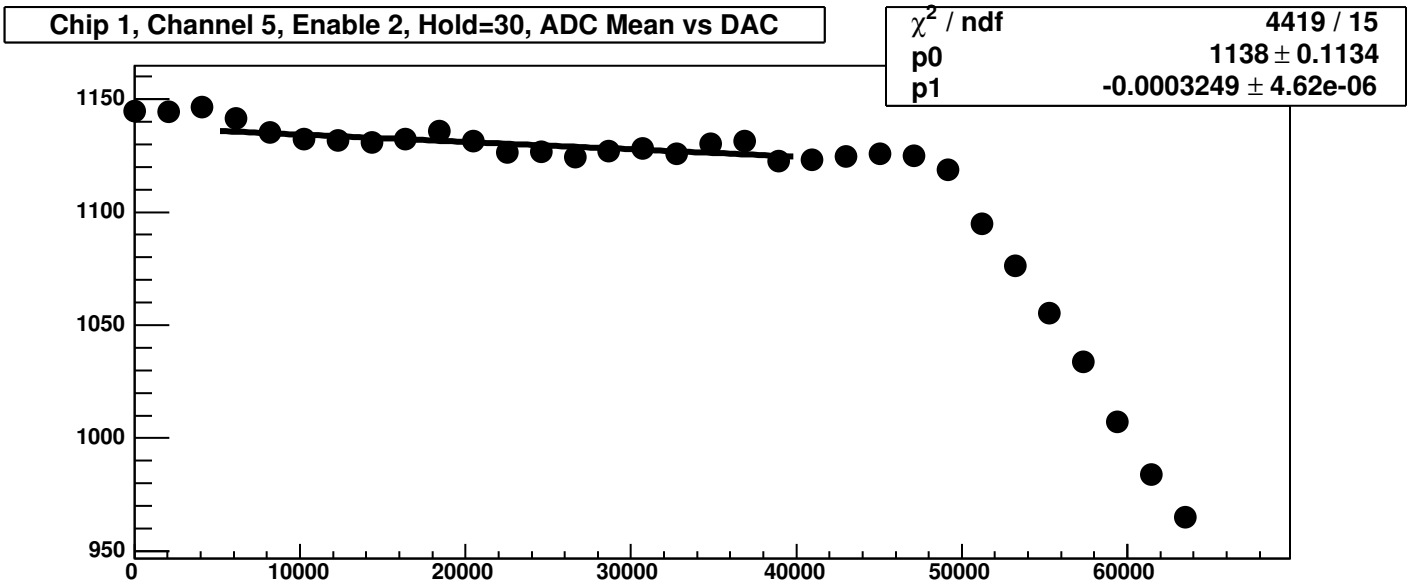

Chip 1, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

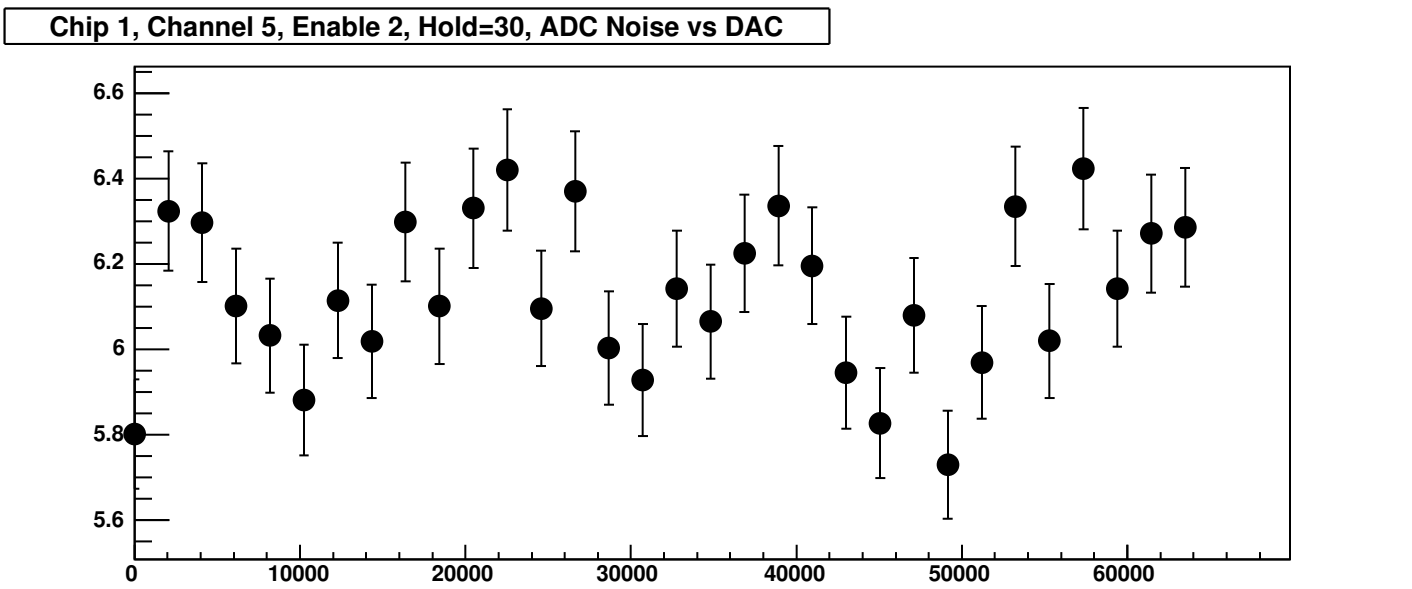

Chip 1, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

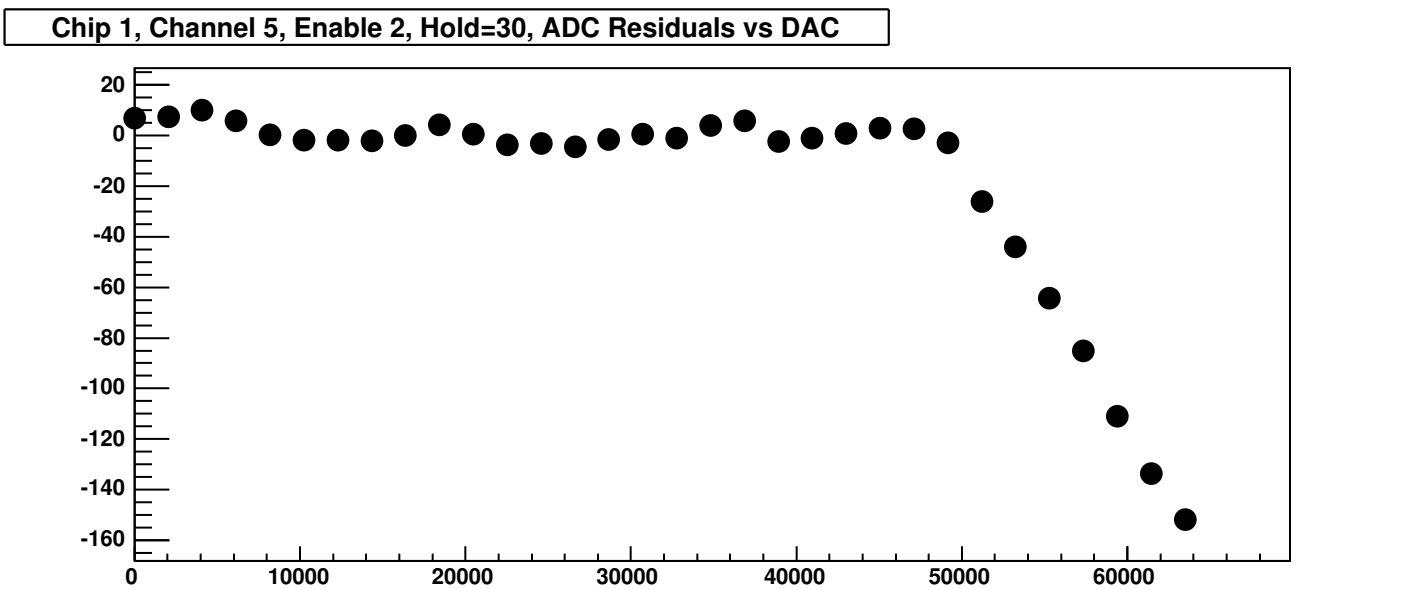

Chip 1, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3588 / 15
p0 1135 ± 0.1198
p1 0.000668 ± 4.987e-06
1180
1160
1140
1120
1100
1080
1060
1040
1020
0
10000
20000
30000
40000
50000
60000

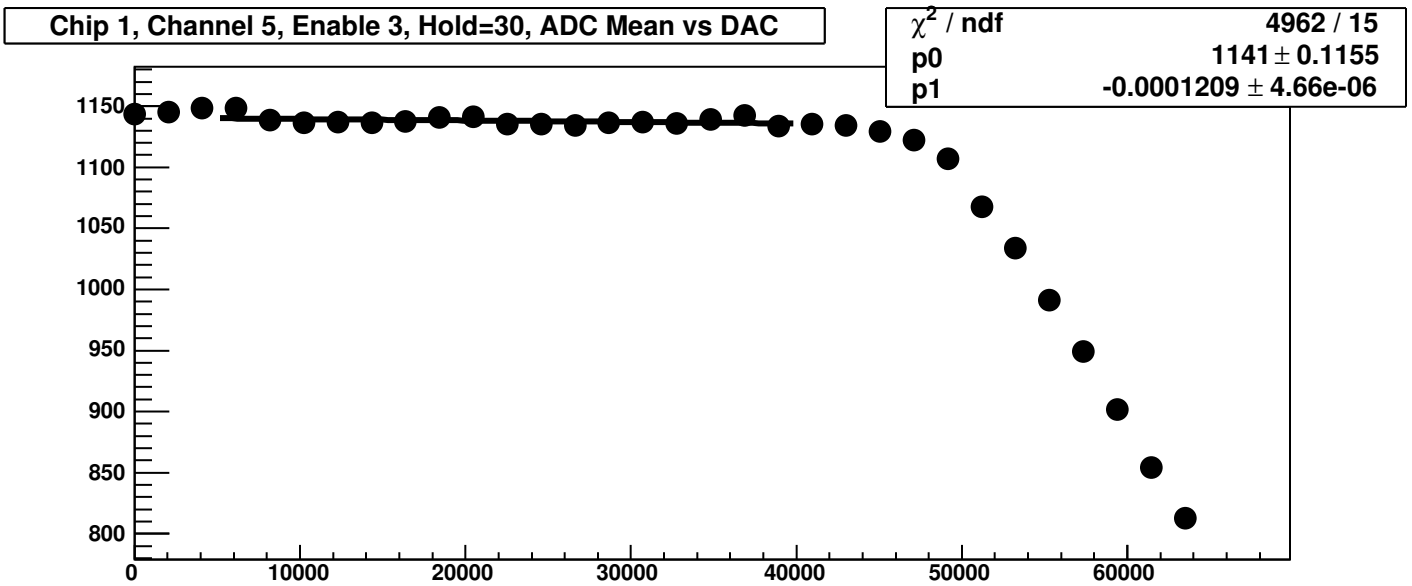

Chip 1, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

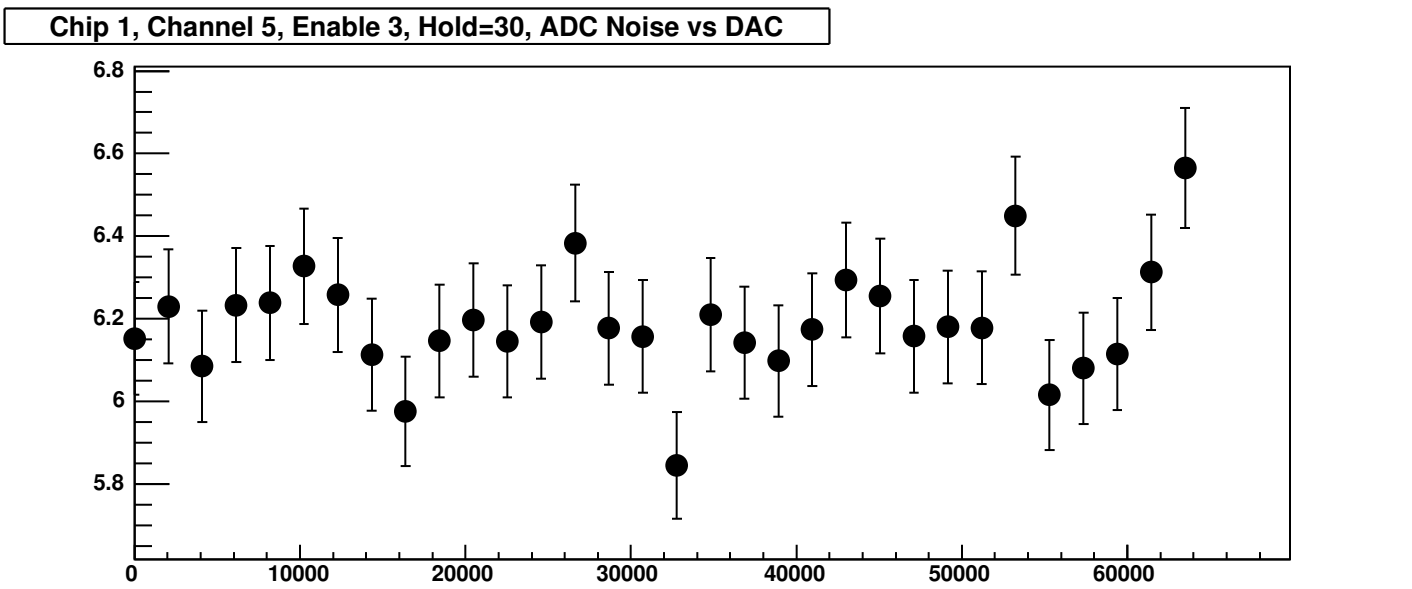

Chip 1, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

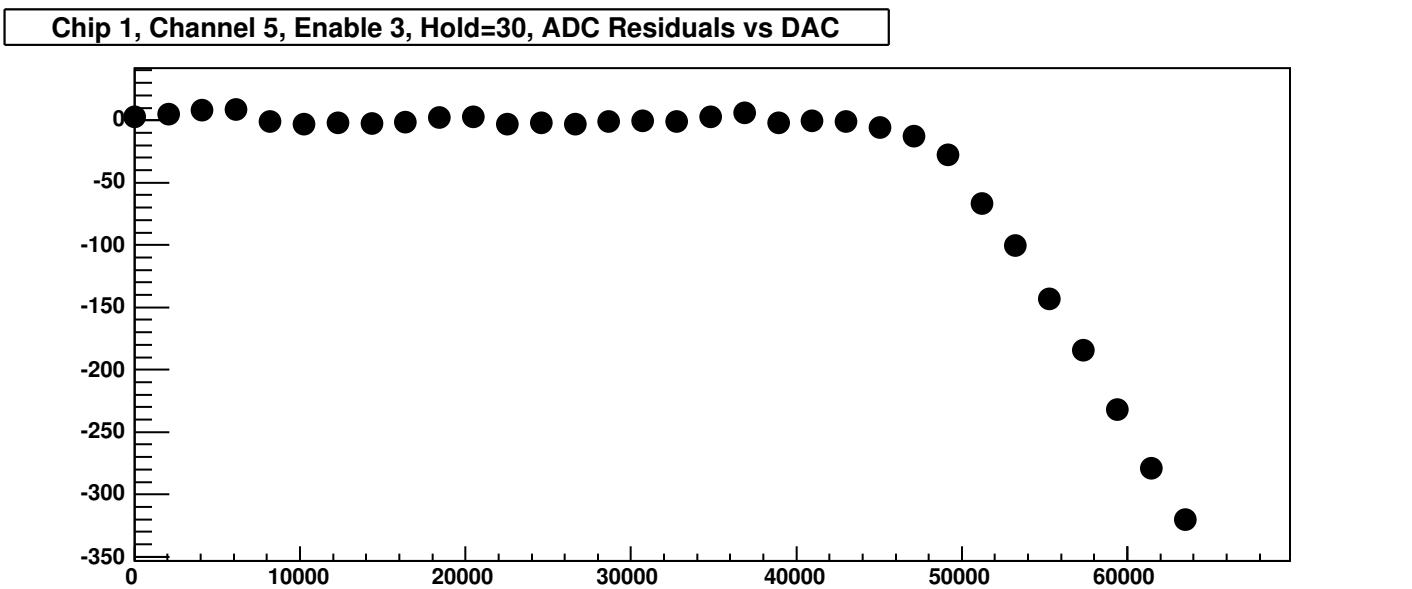

Chip 1, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4419 / 15
p0 1138 ± 0.1134
p1 -0.0003249 ± 4.62e-06
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

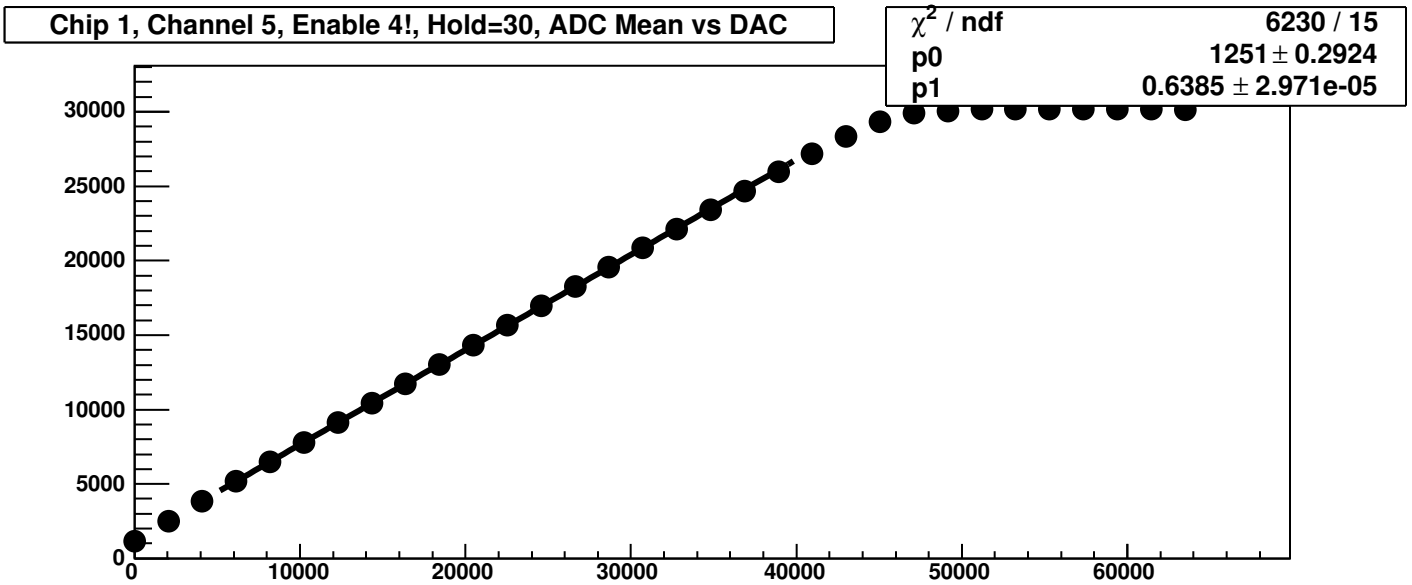

Chip 1, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

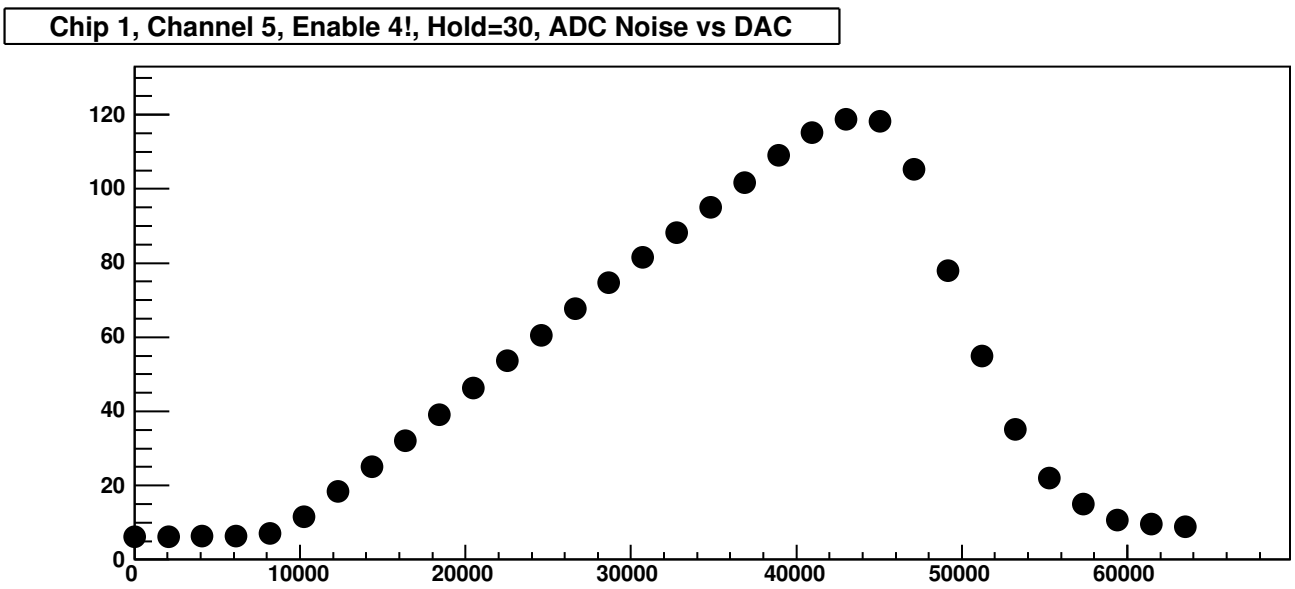

Chip 1, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

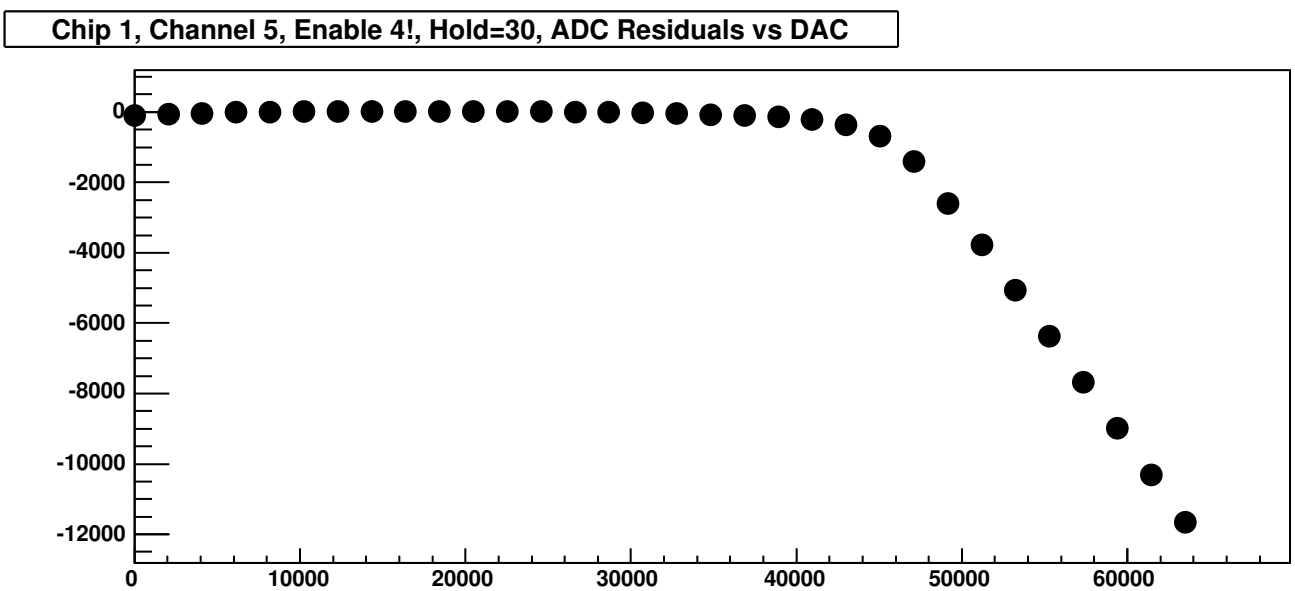

Chip 1, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4962 / 15
p0 1141 ± 0.1155
p1 -0.0001209 ± 4.66e-06
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

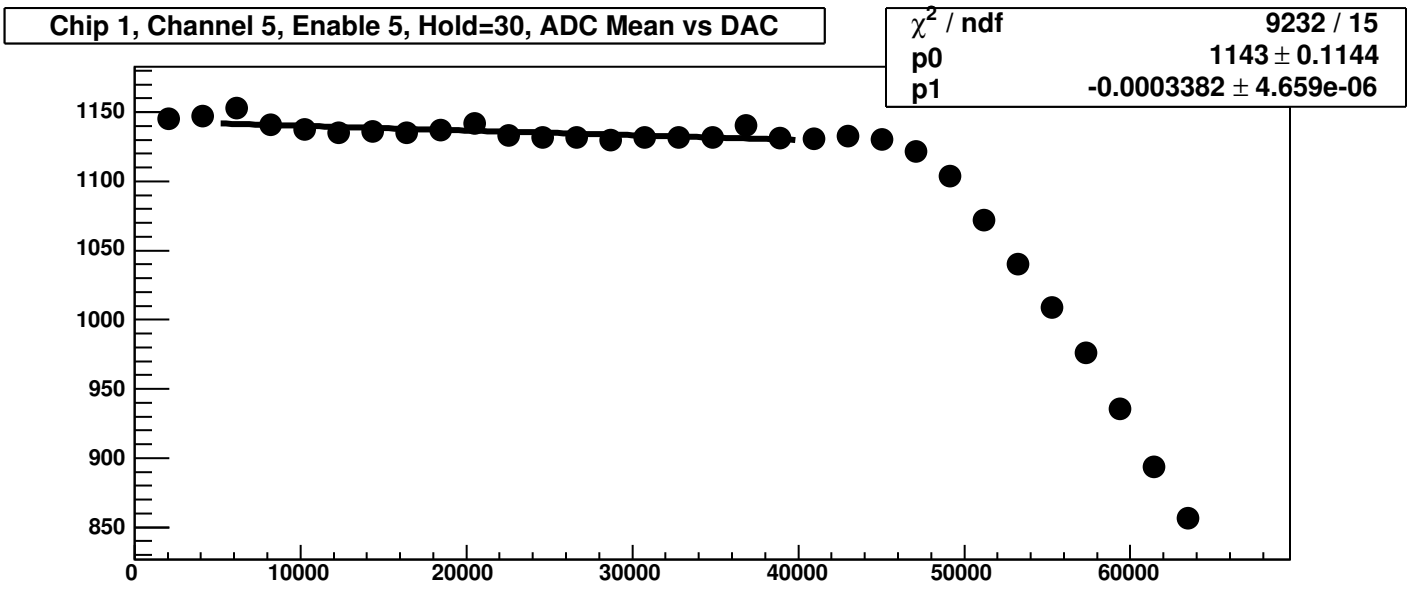

Chip 1, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

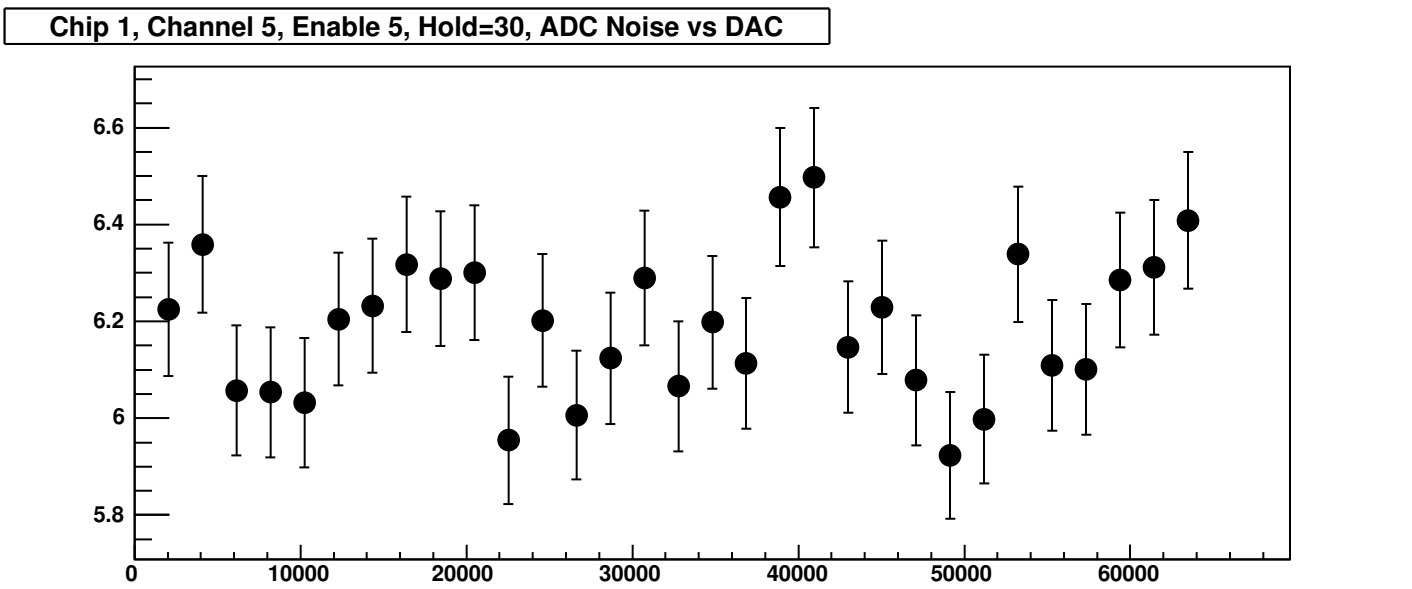

Chip 1, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

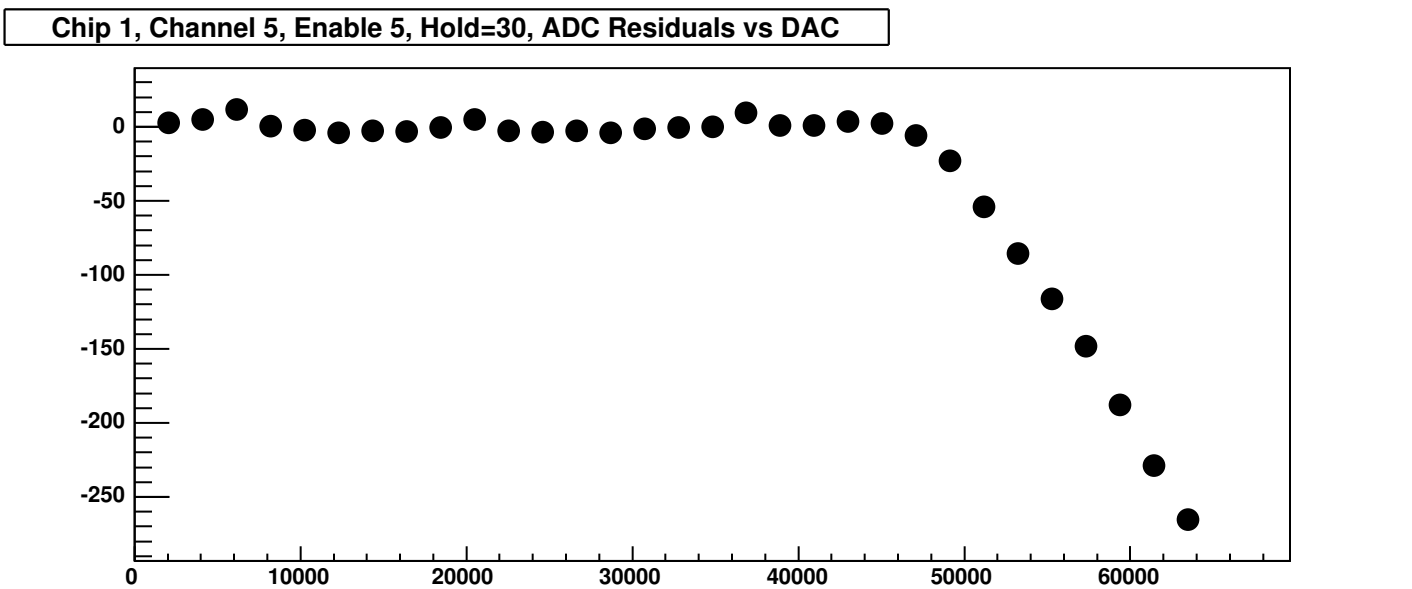

Chip 1, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 6230 / 15
p0 1251 ± 0.2924
p1 0.6385 ± 2.971e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

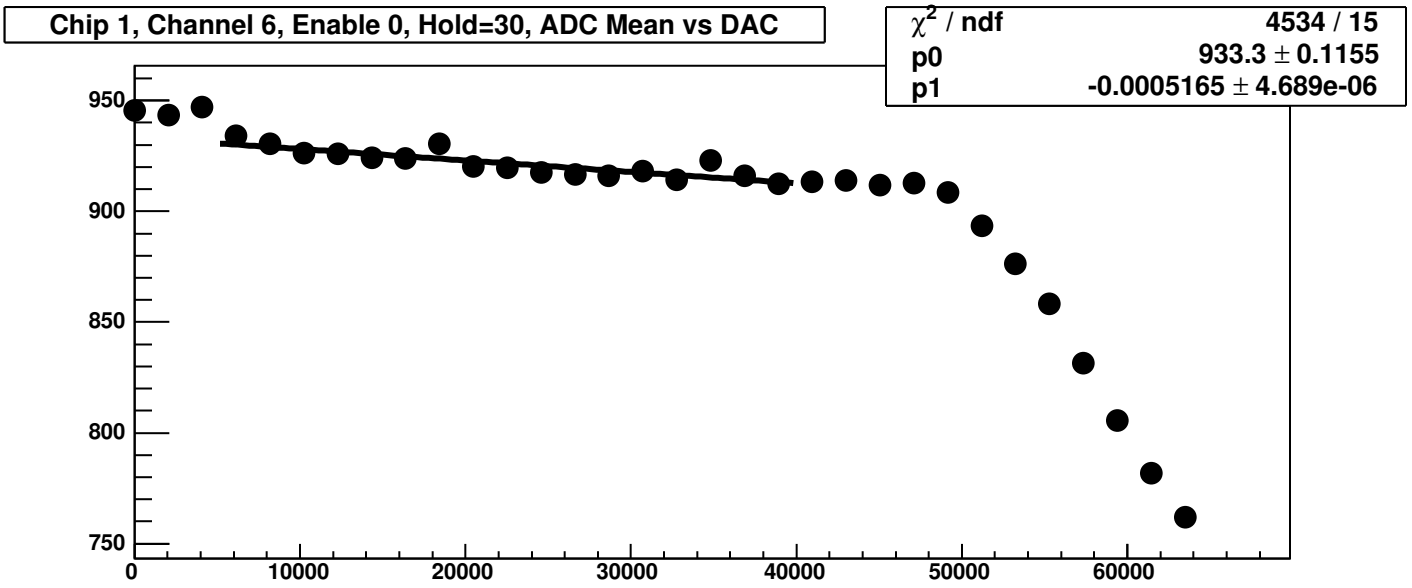

Chip 1, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

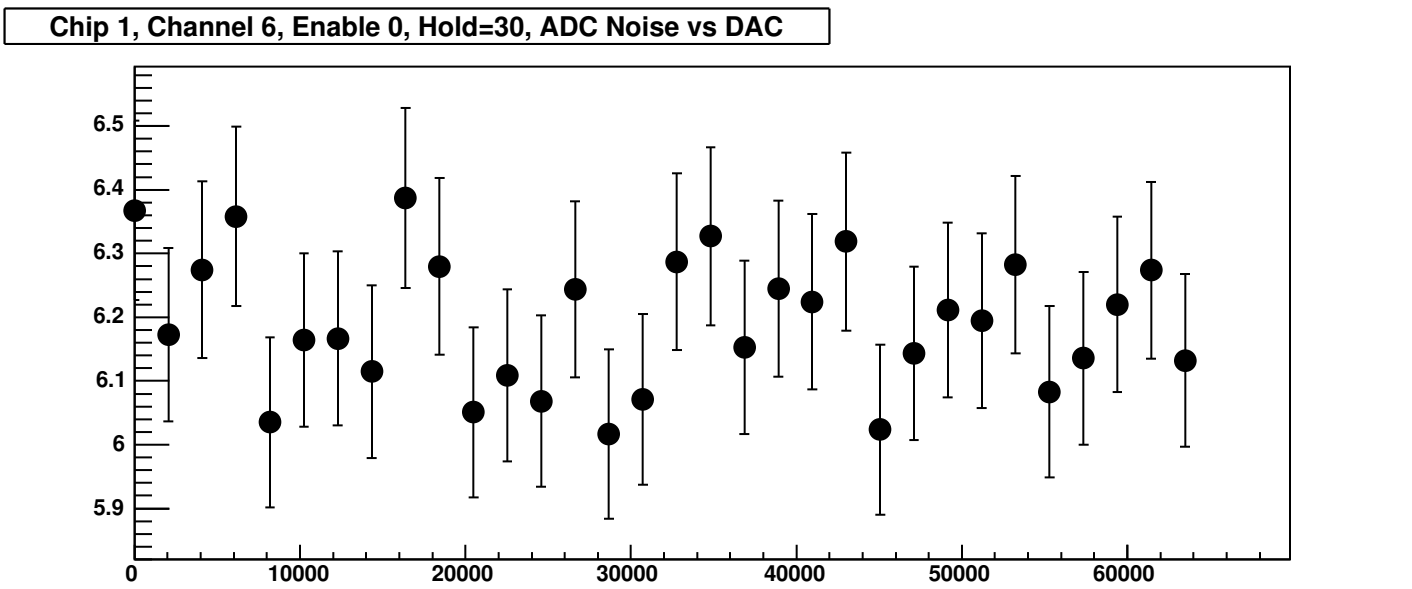

Chip 1, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

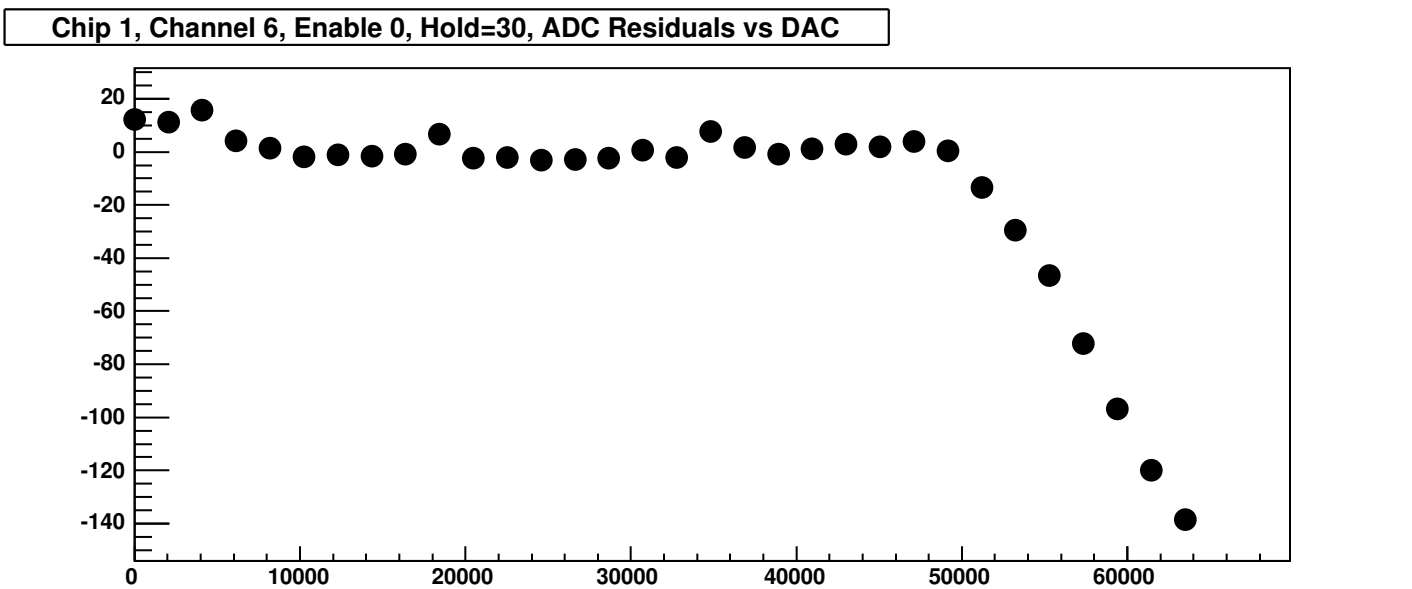

Chip 1, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9232 / 15
p0 1143 ± 0.1144
p1 -0.0003382 ± 4.659e-06
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

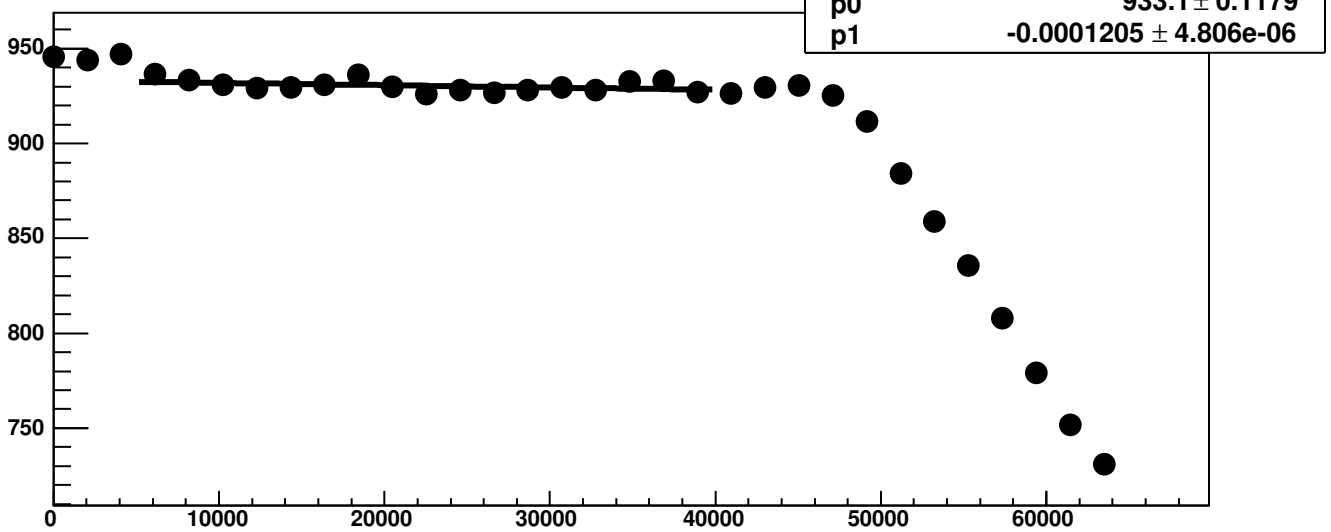

Chip 1, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4534 / 15
p0 933.3 ± 0.1155
p1 -0.0005165 ± 4.689e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

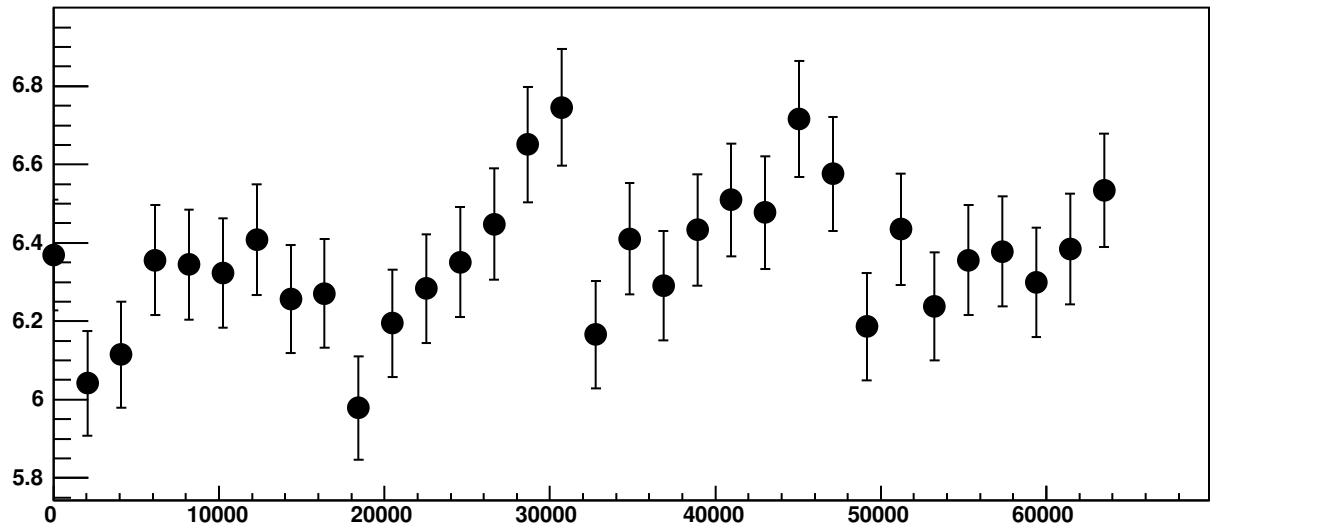

Chip 1, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
6.5
6.4
6.3
6.2
6.1
6
5.9
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

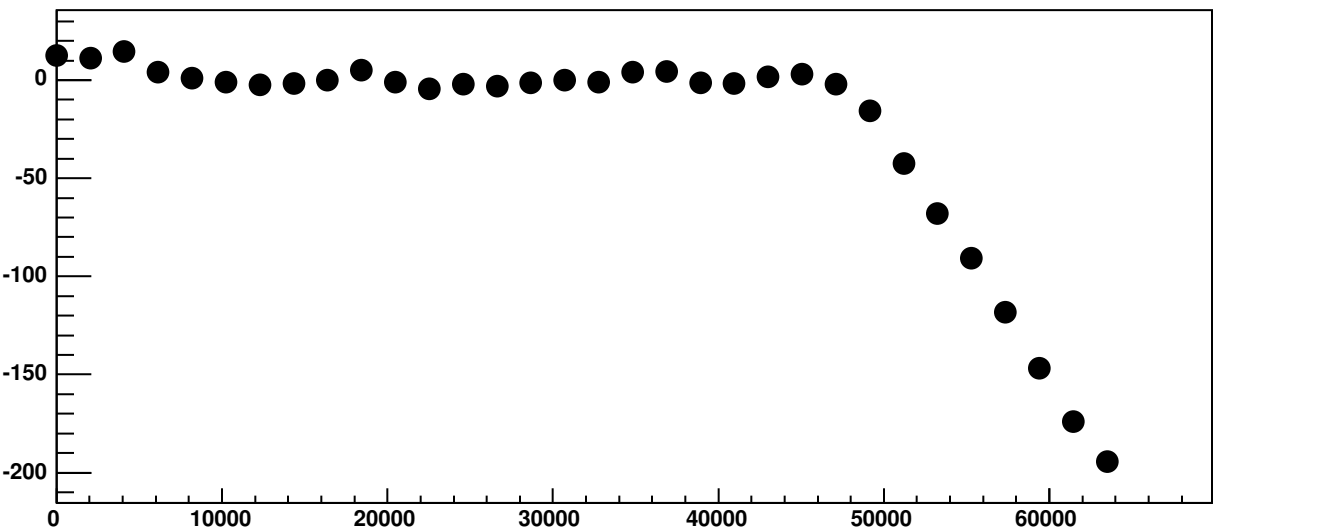
Chip 1, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

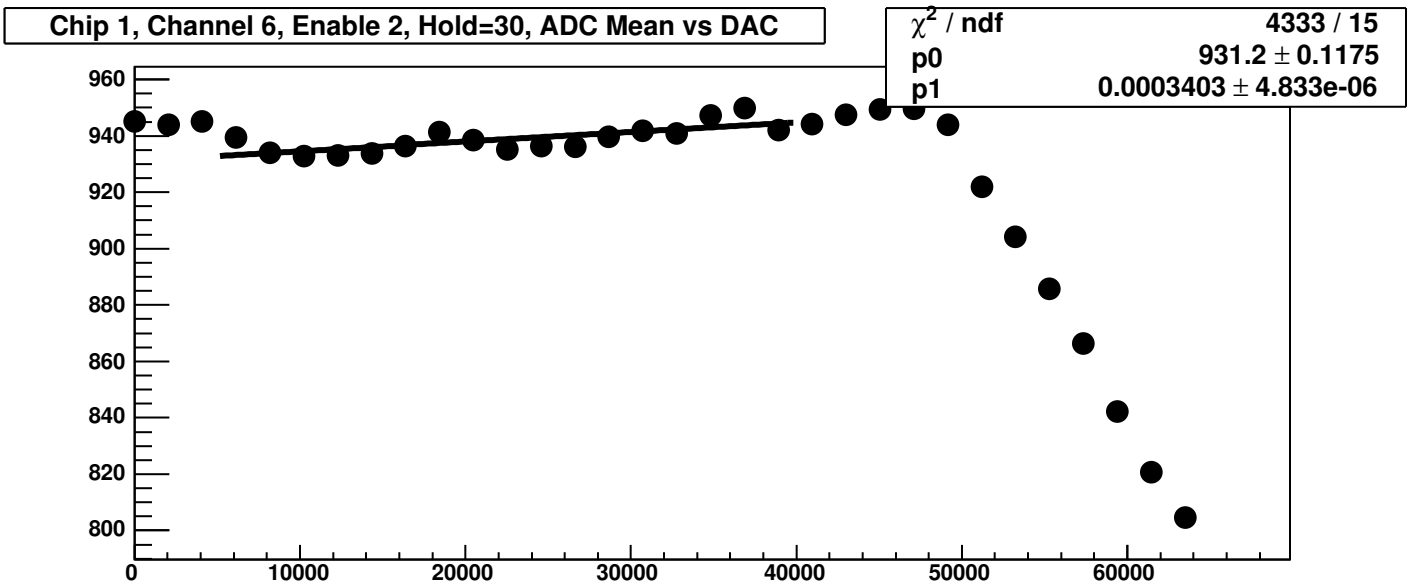


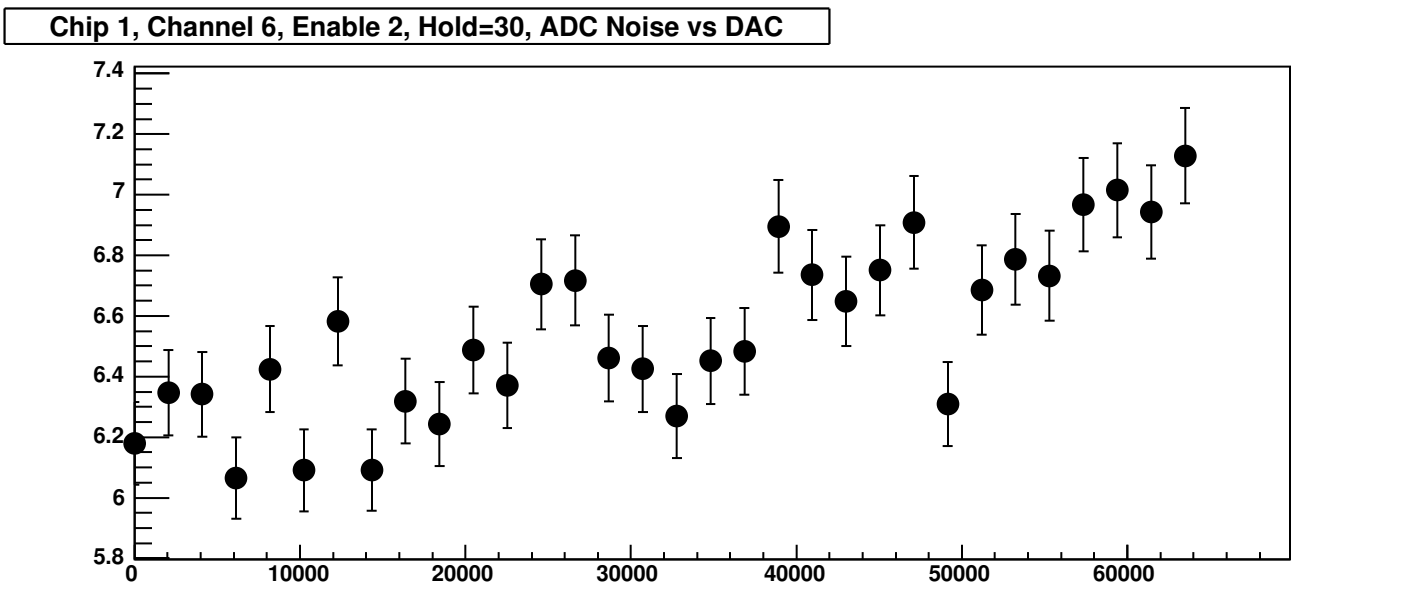
Chip 1, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

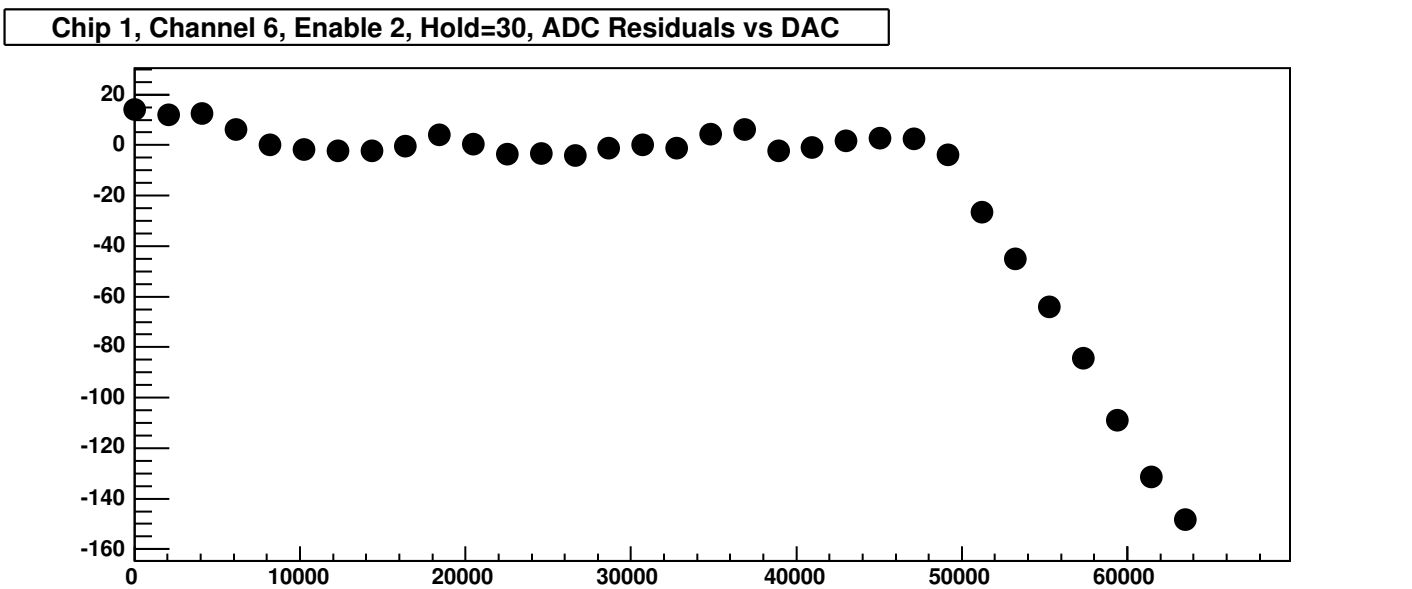


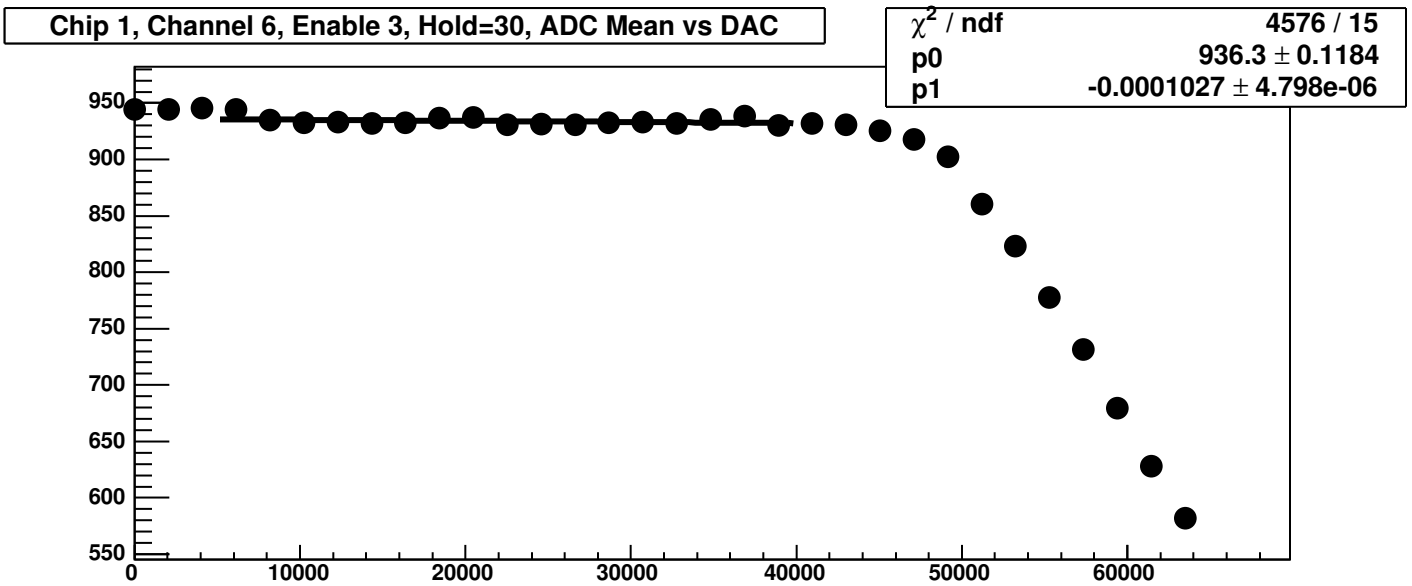
Chip 1, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

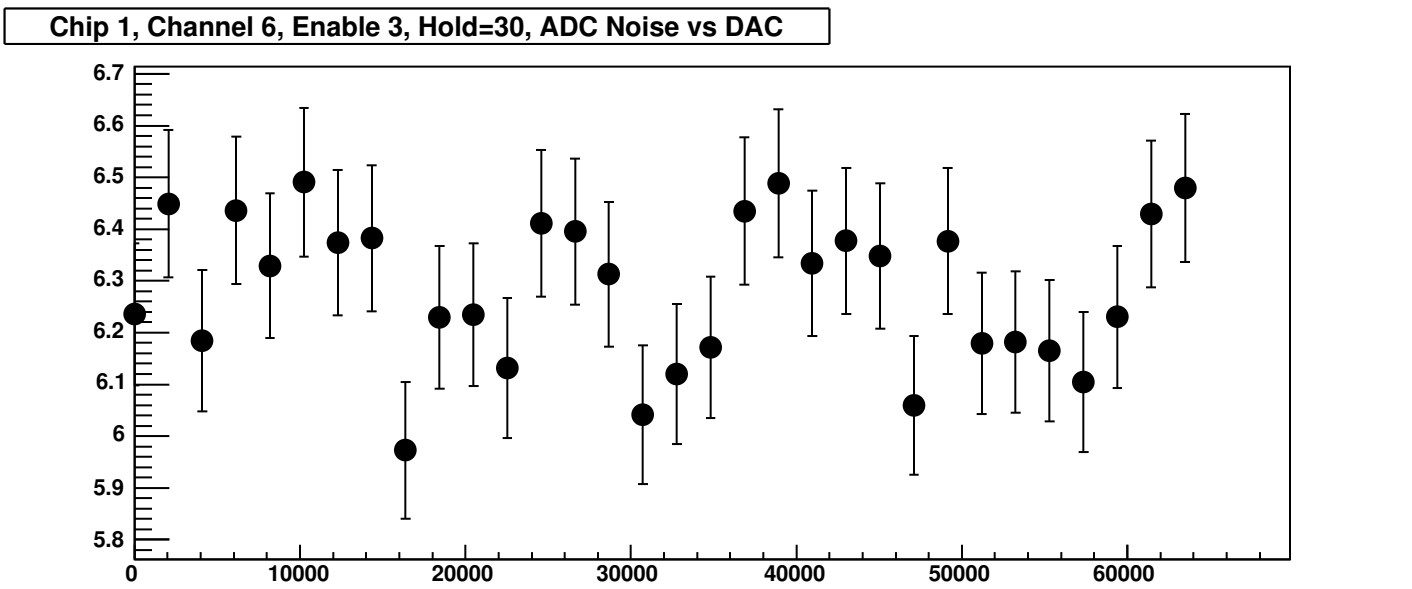


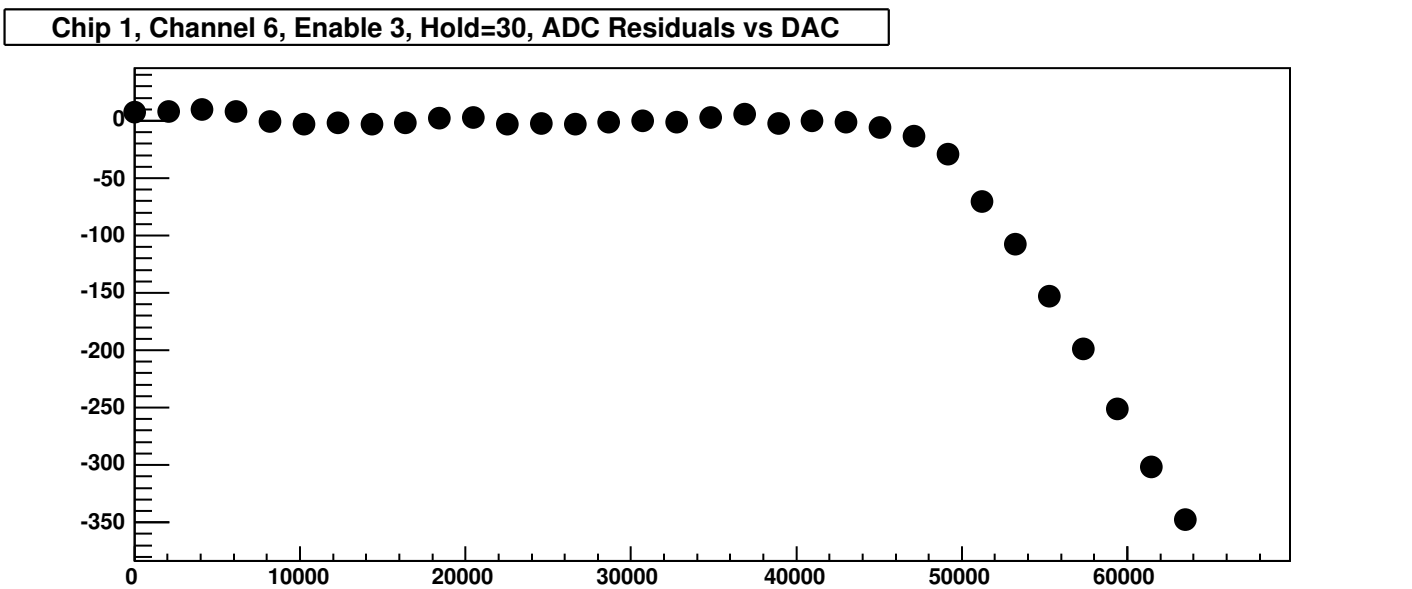

Chip 1, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4333 / 15
p0 931.2 ± 0.1175
p1 0.0003403 ± 4.833e-06
960
940
920
900
880
860
840
820
800
0
10000
20000
30000
40000
50000
60000

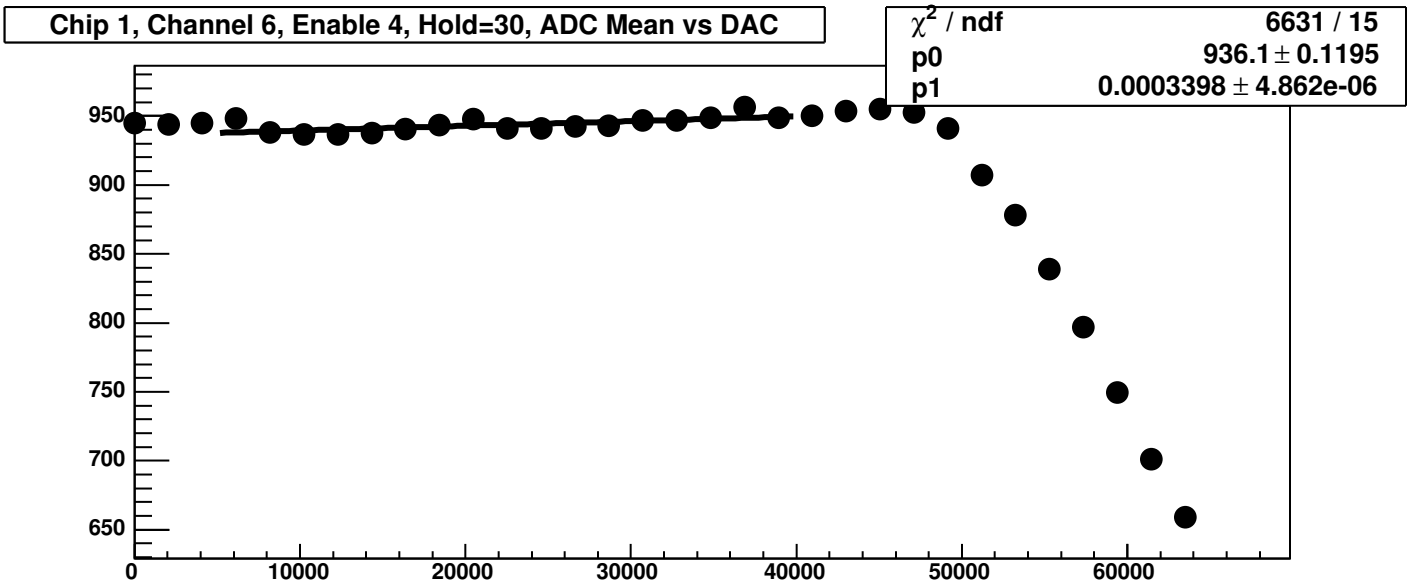

Chip 1, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

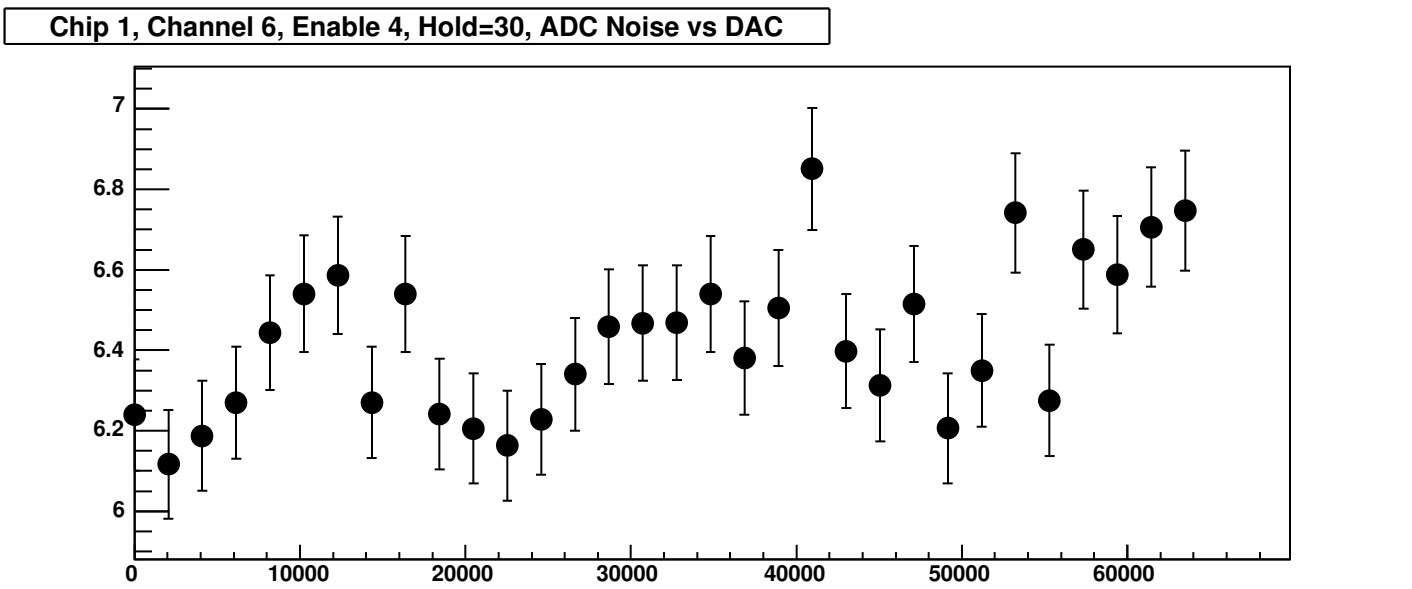

Chip 1, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

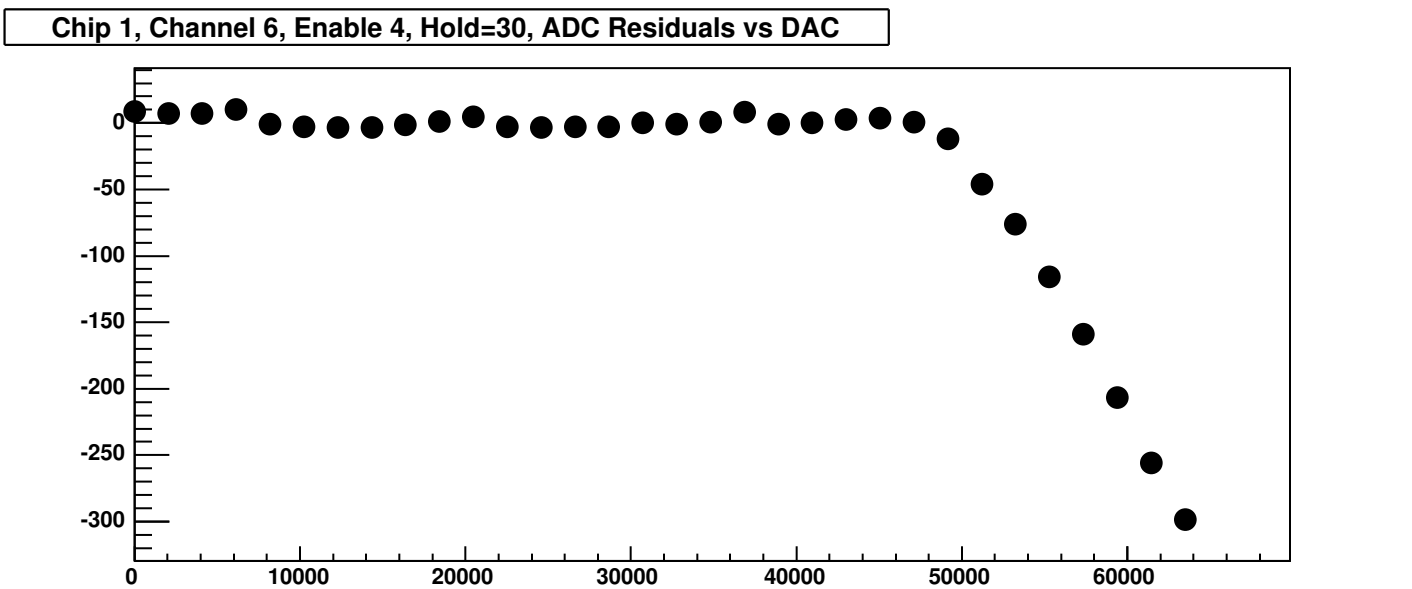

Chip 1, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4576 / 15
p0 936.3 ± 0.1184
p1 -0.0001027 ± 4.798e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

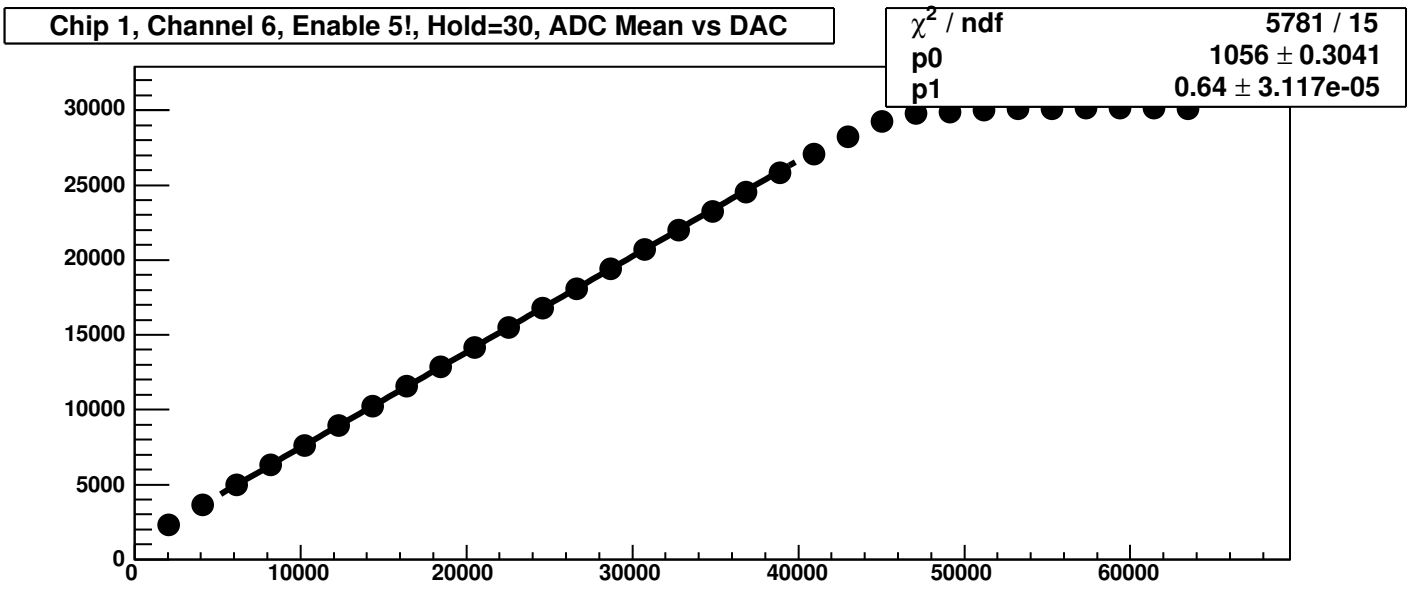

Chip 1, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
6.7
6.6
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
0
10000
20000
30000
40000
50000
60000

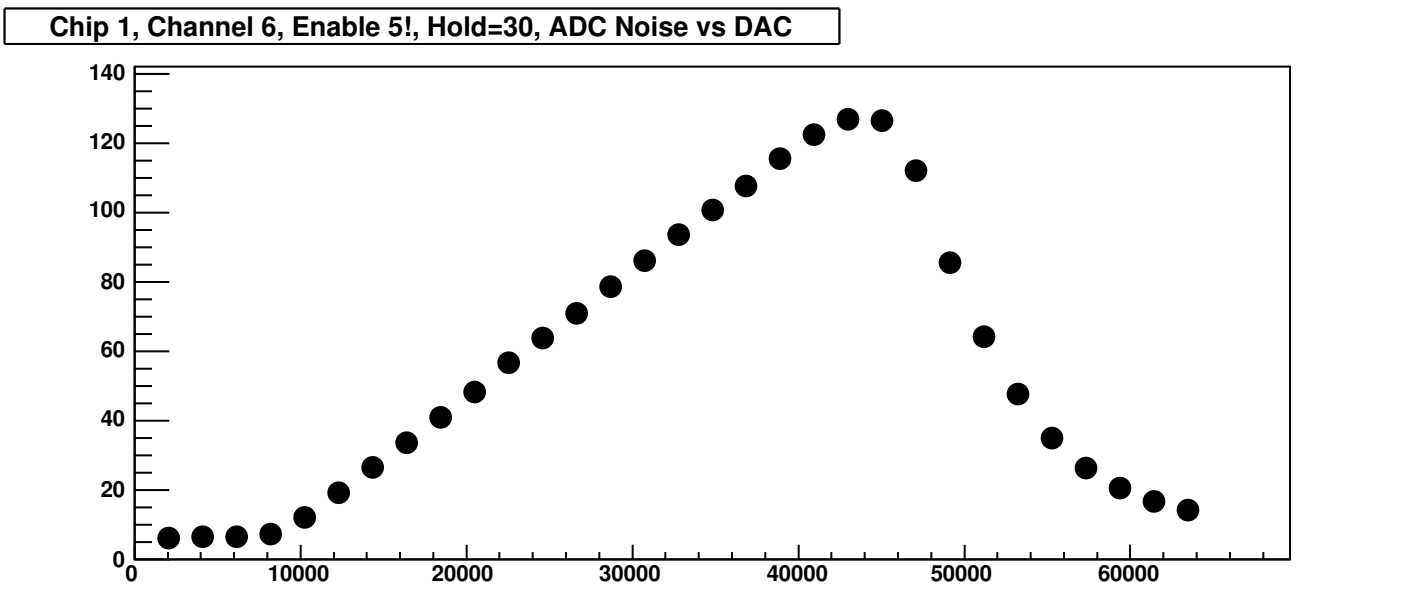

Chip 1, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

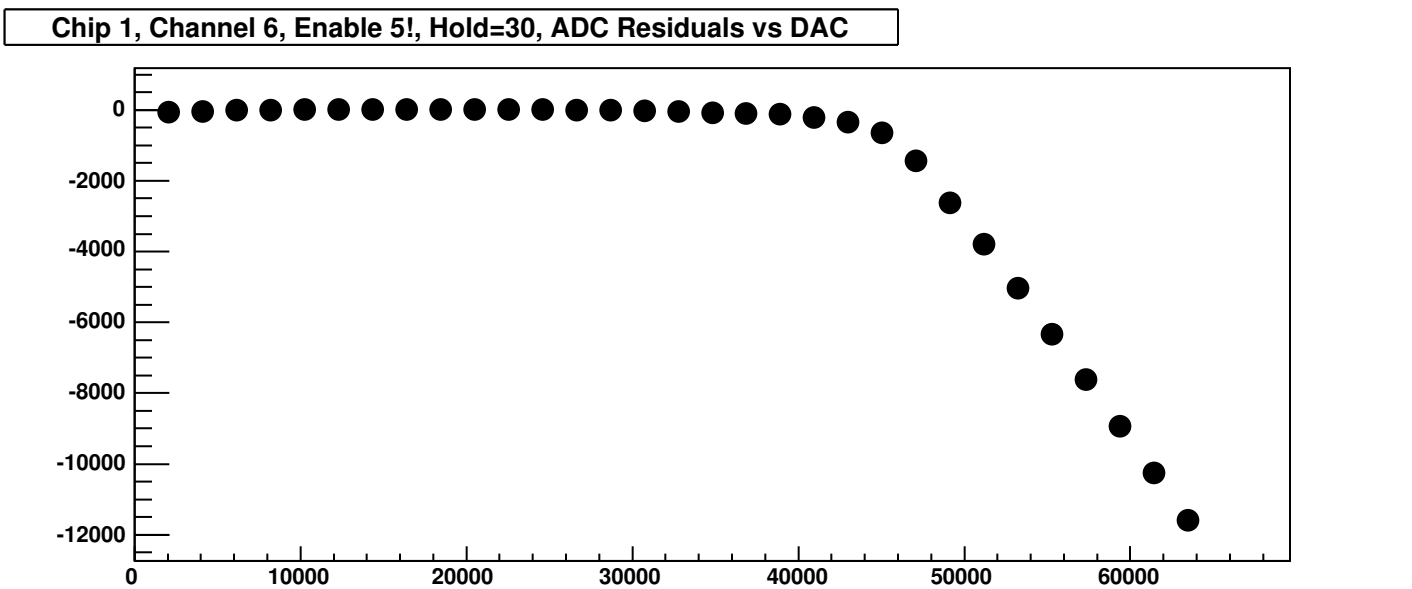

Chip 1, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6631 / 15
p0 936.1 ± 0.1195
p1 0.0003398 ± 4.862e-06
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

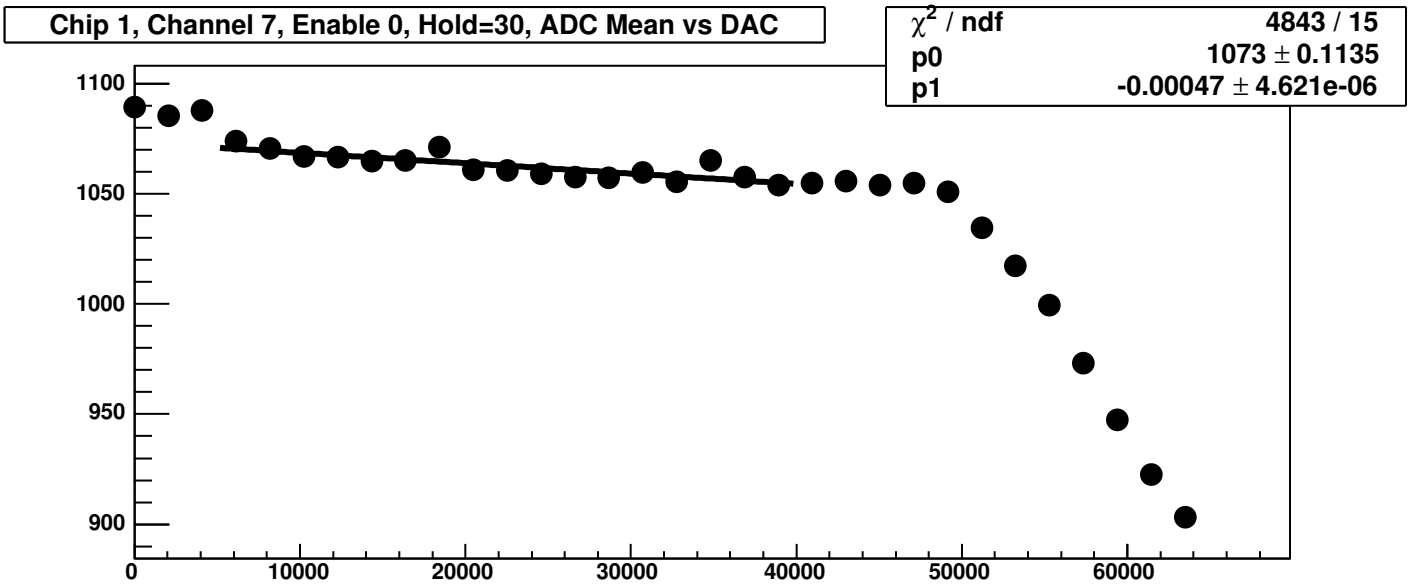

Chip 1, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

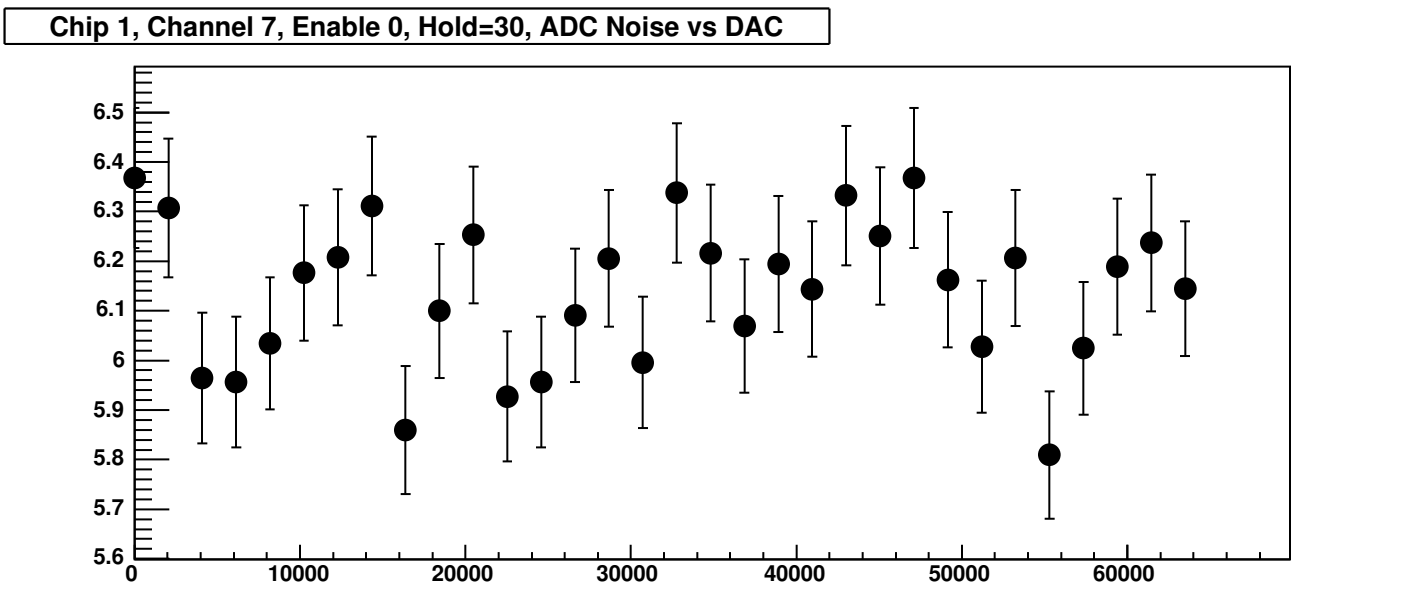

Chip 1, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

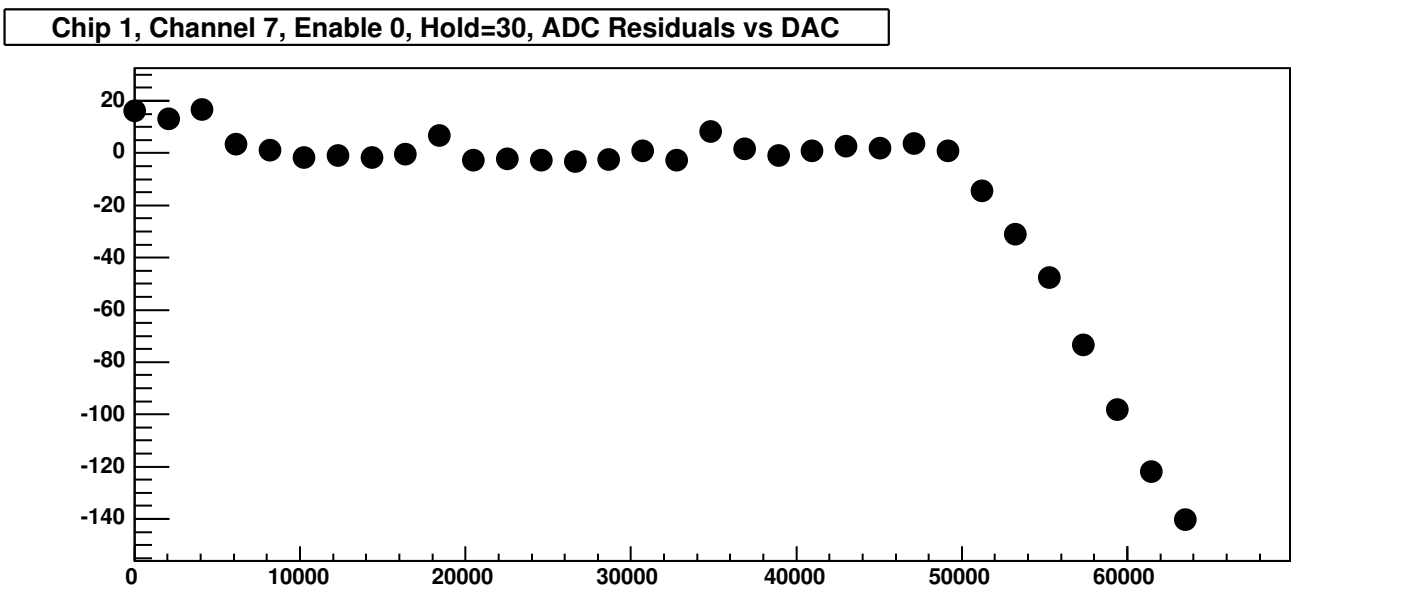

Chip 1, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 5781 / 15
p0 1056 ± 0.3041
p1 0.64 ± 3.117e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

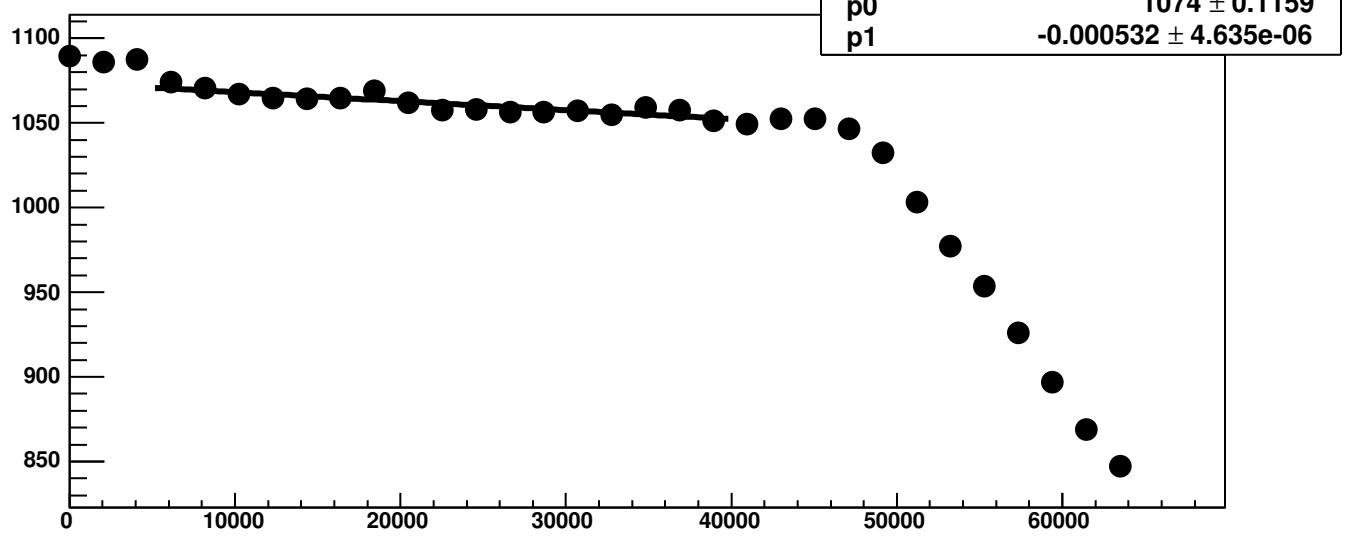

Chip 1, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4843 / 15
p0 1073 ± 0.1135
p1 -0.00047 ± 4.621e-06
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

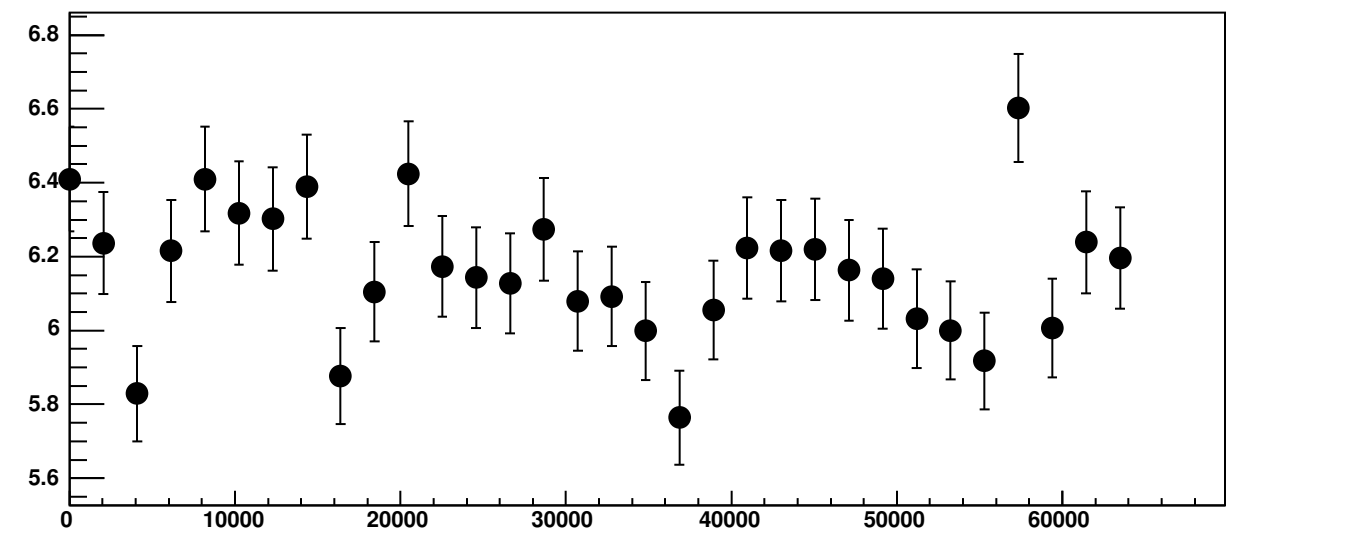

Chip 1, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

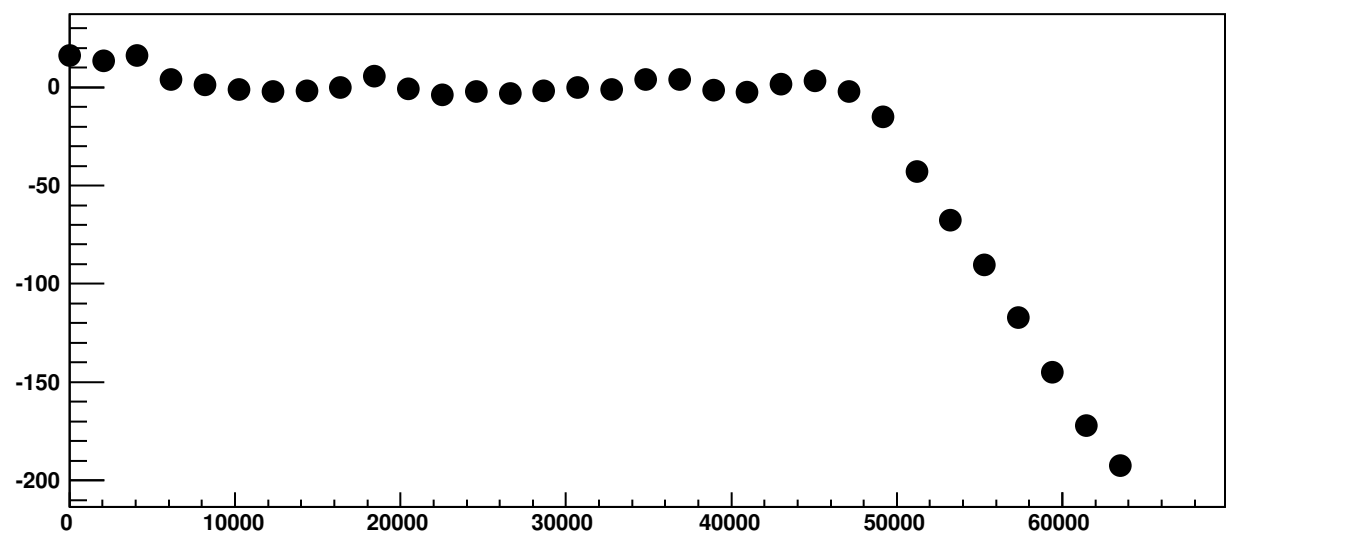
Chip 1, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

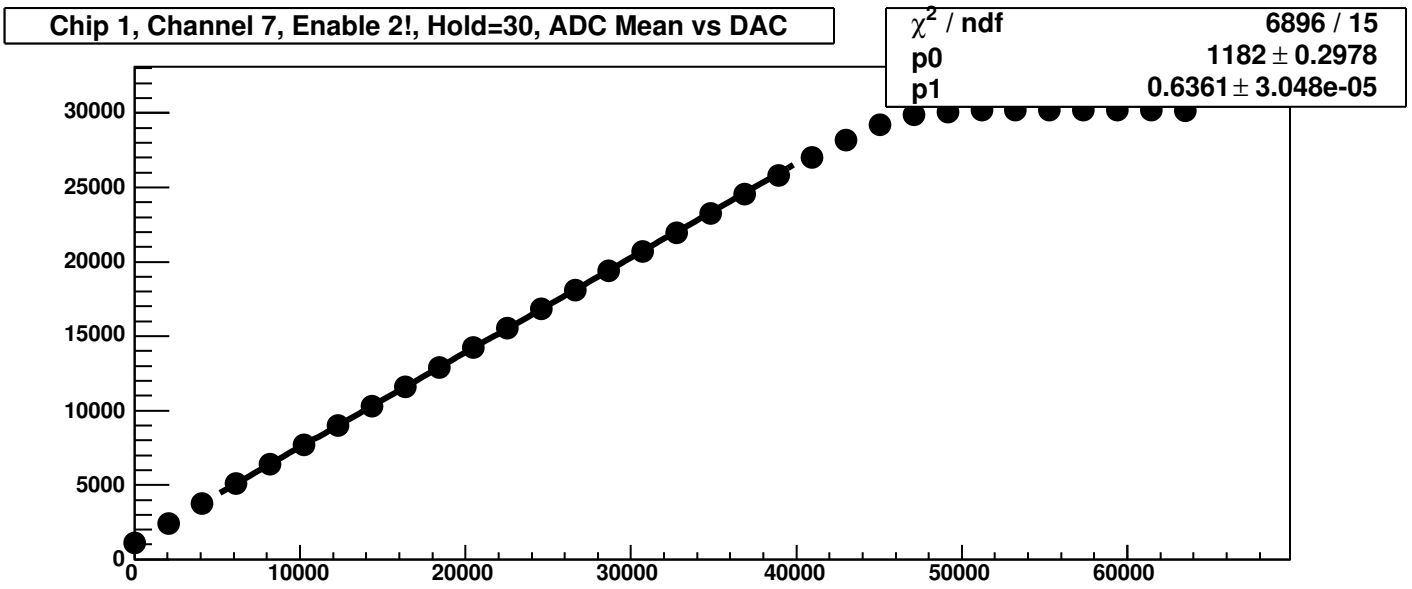


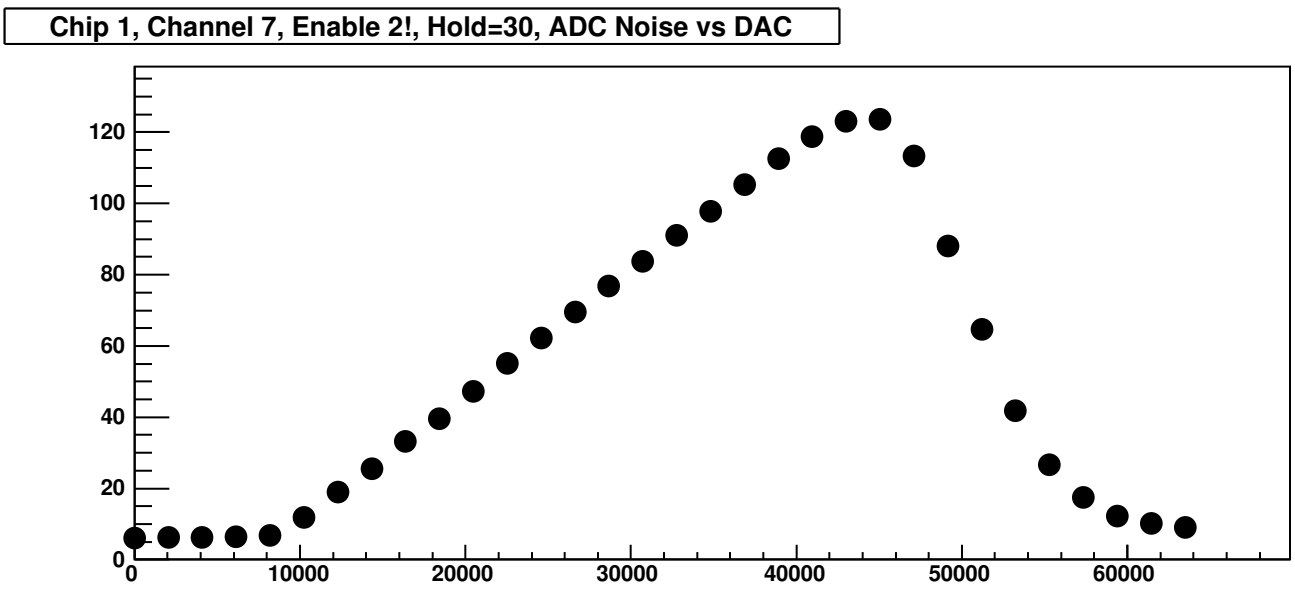
Chip 1, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC

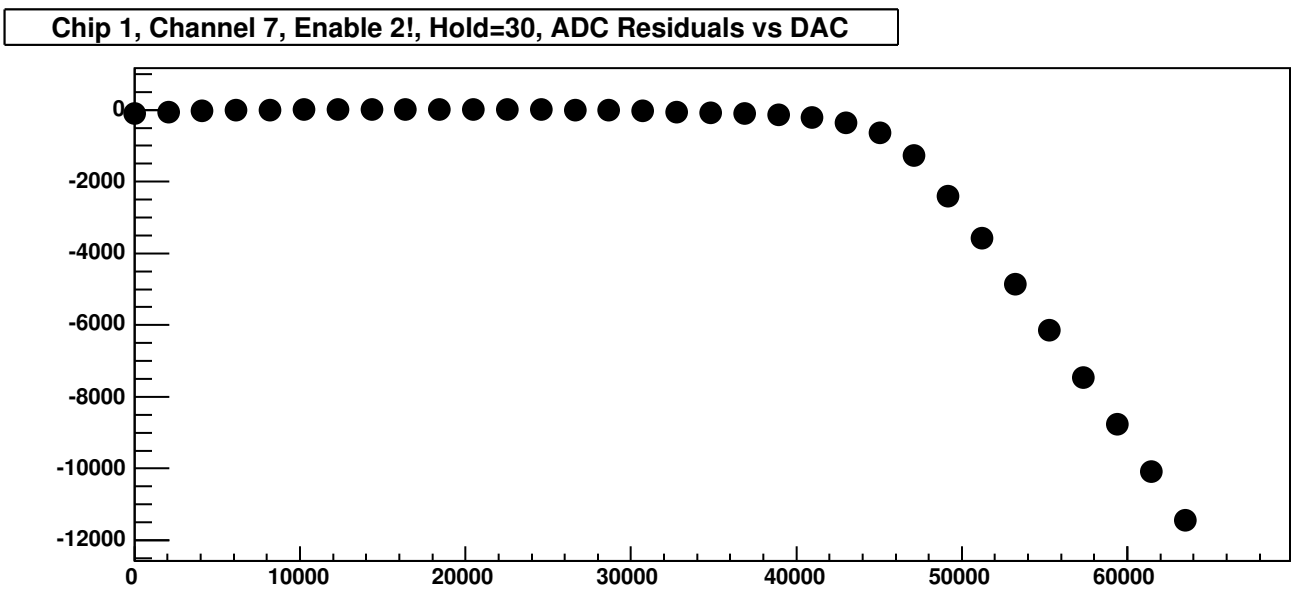


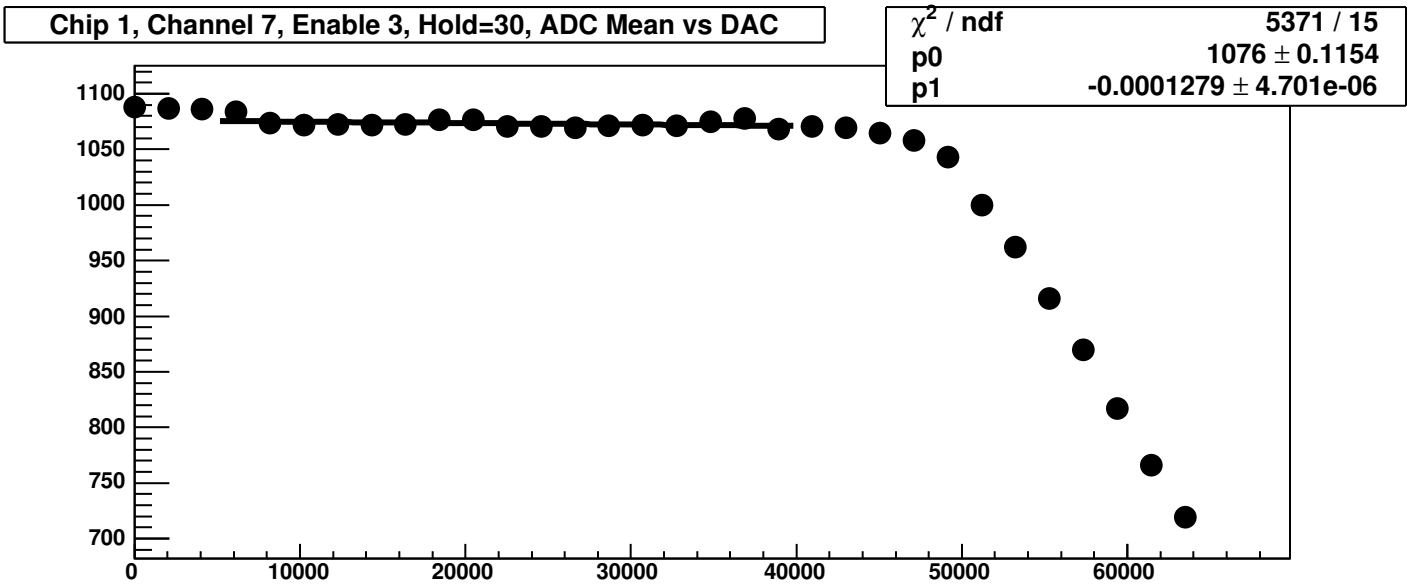
Chip 1, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

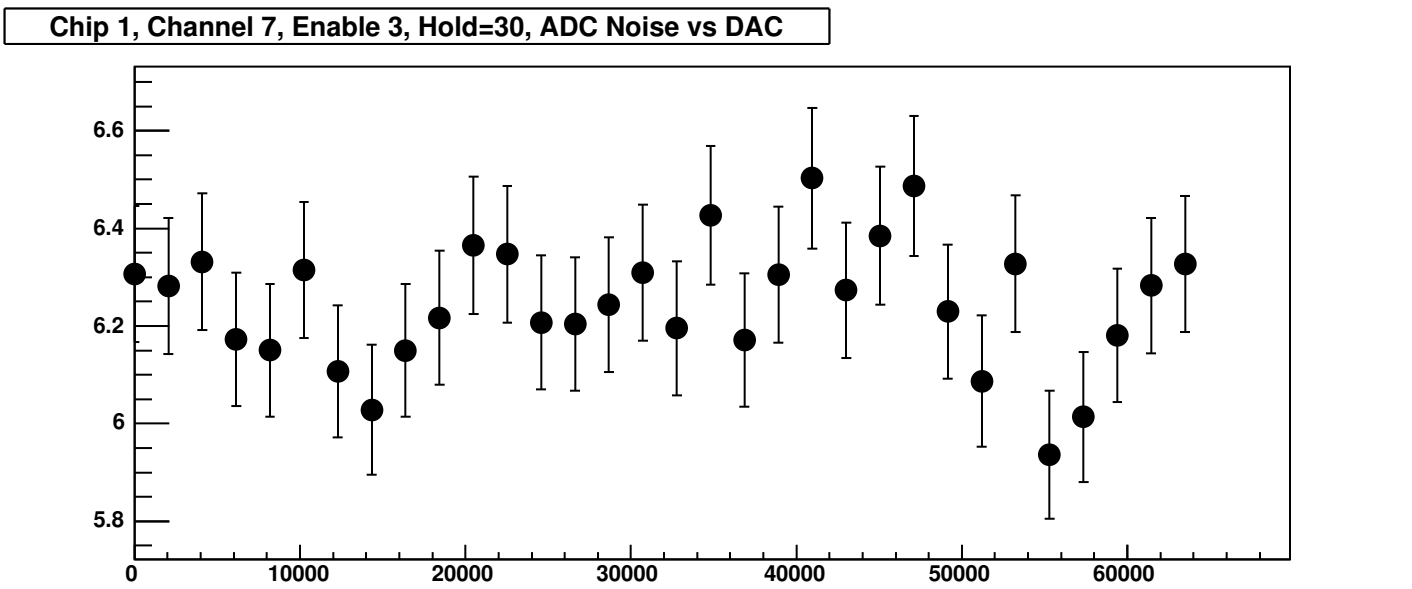


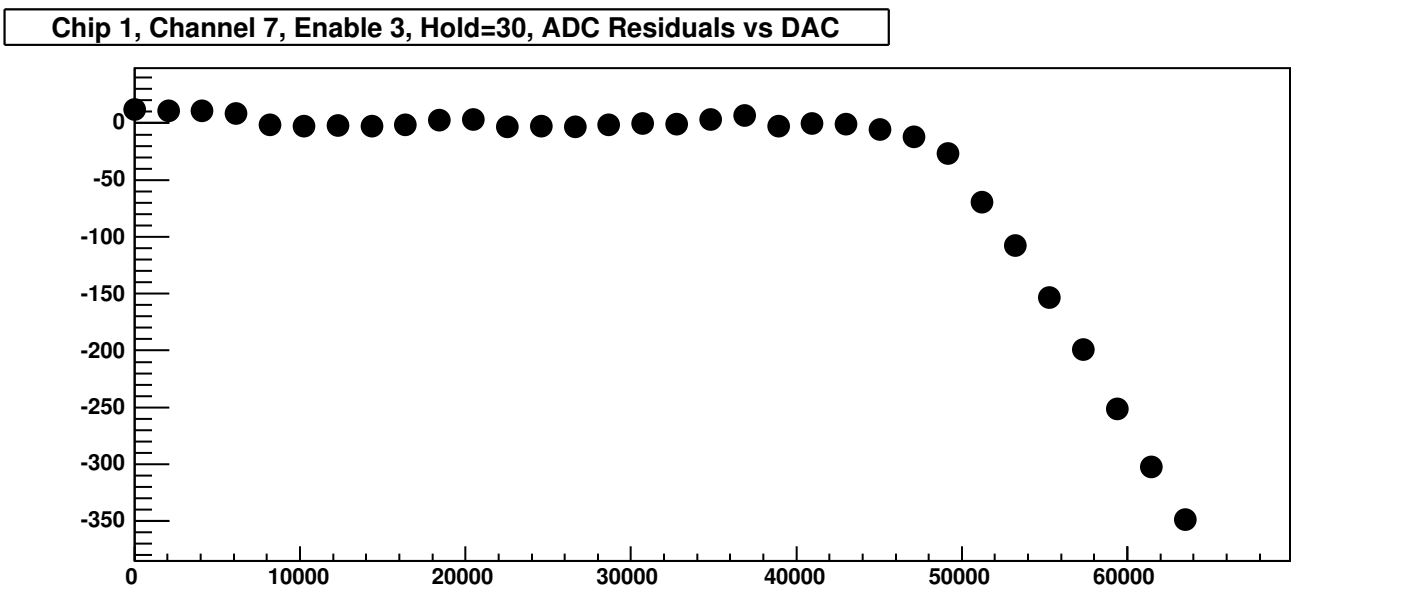

Chip 1, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 6896 / 15
p0 1182 ± 0.2978
p1 0.6361 ± 3.048e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

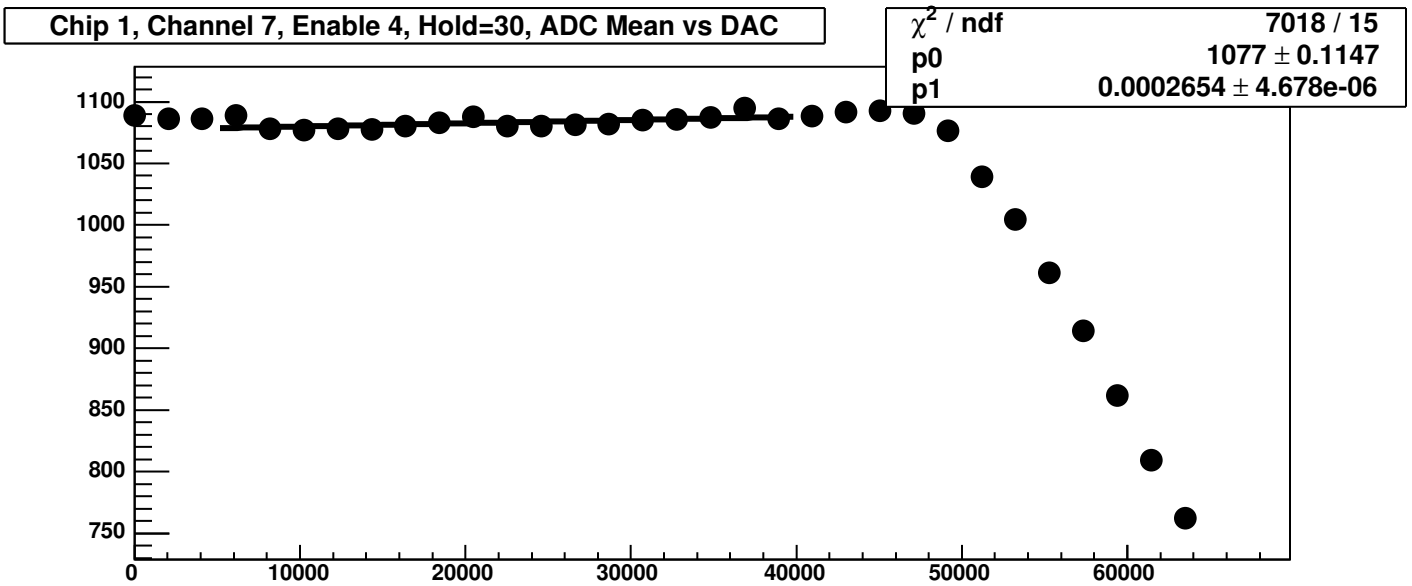

Chip 1, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

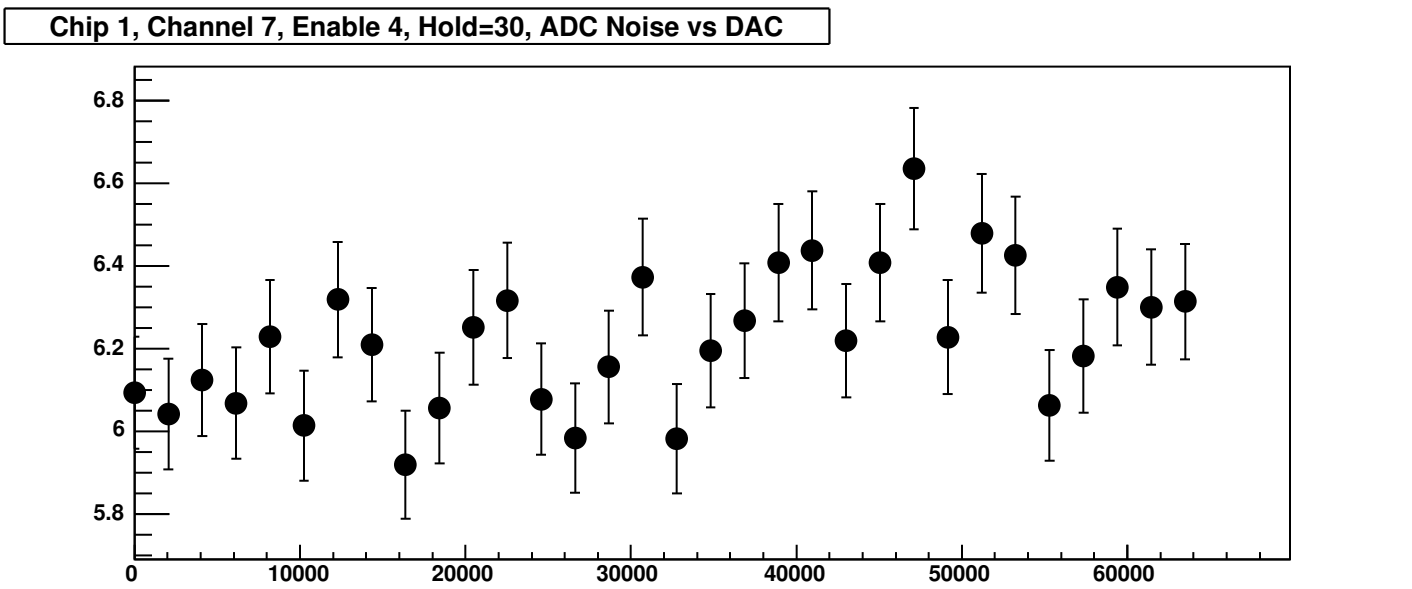

Chip 1, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

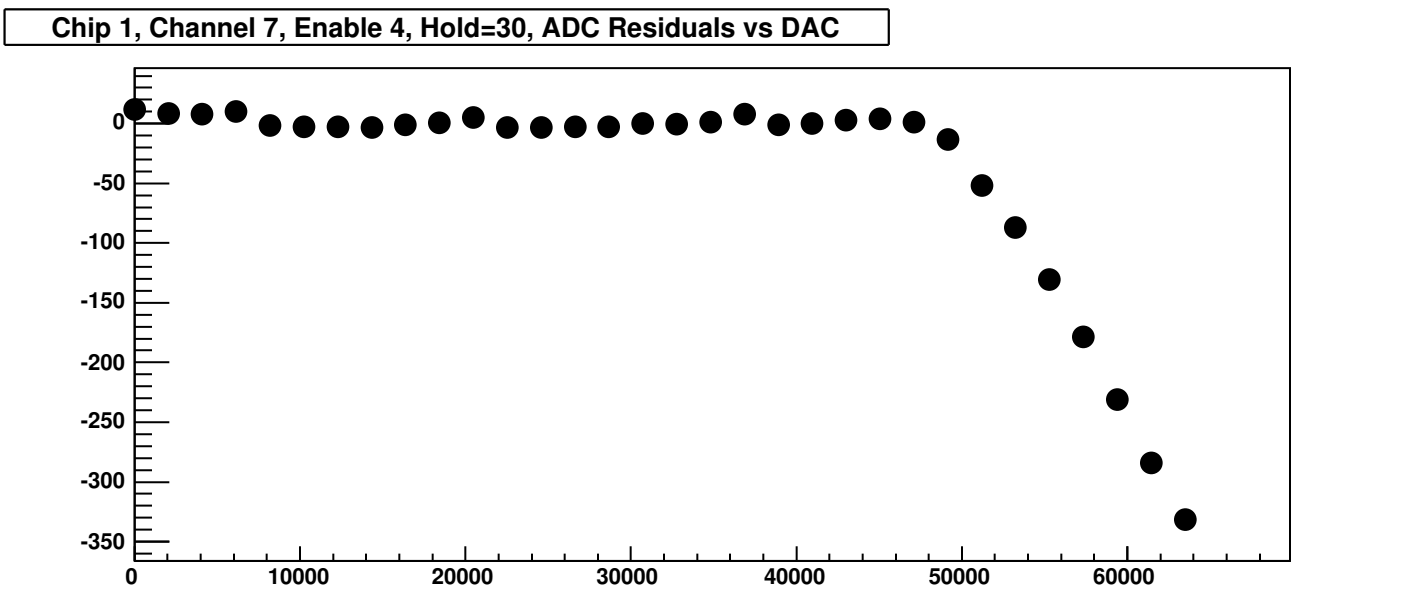

Chip 1, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5371 / 15
p0 1076 ± 0.1154
p1 -0.0001279 ± 4.701e-06
1100
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

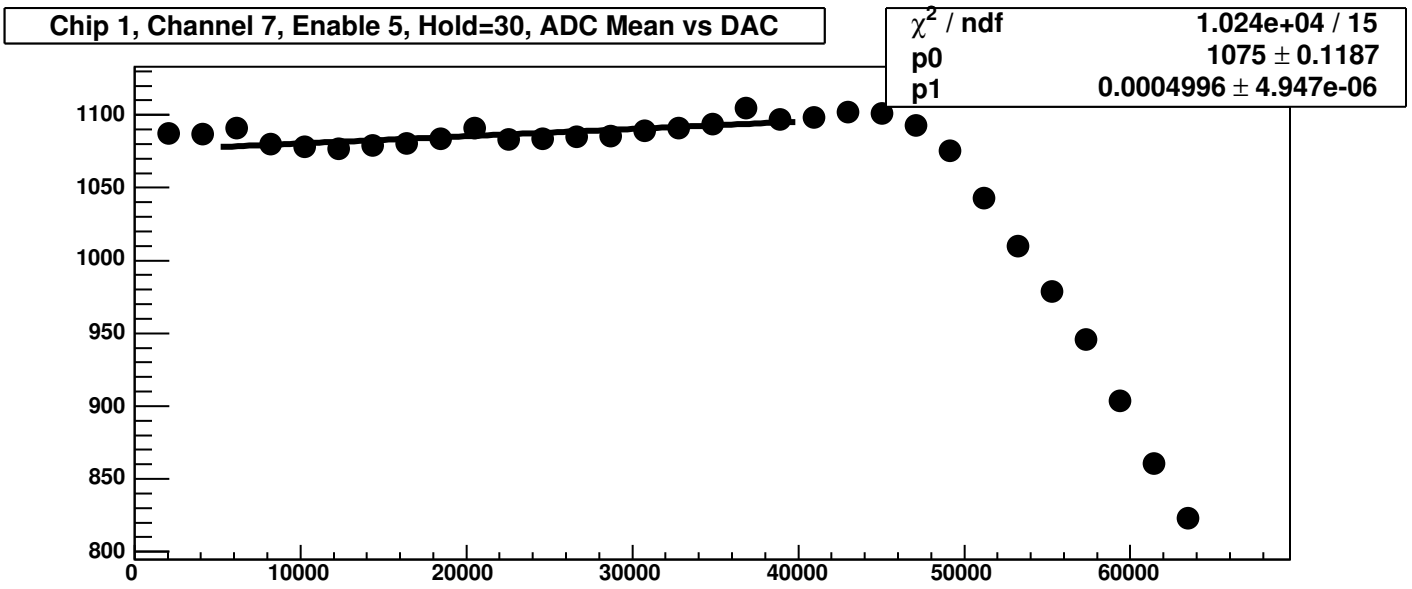

Chip 1, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

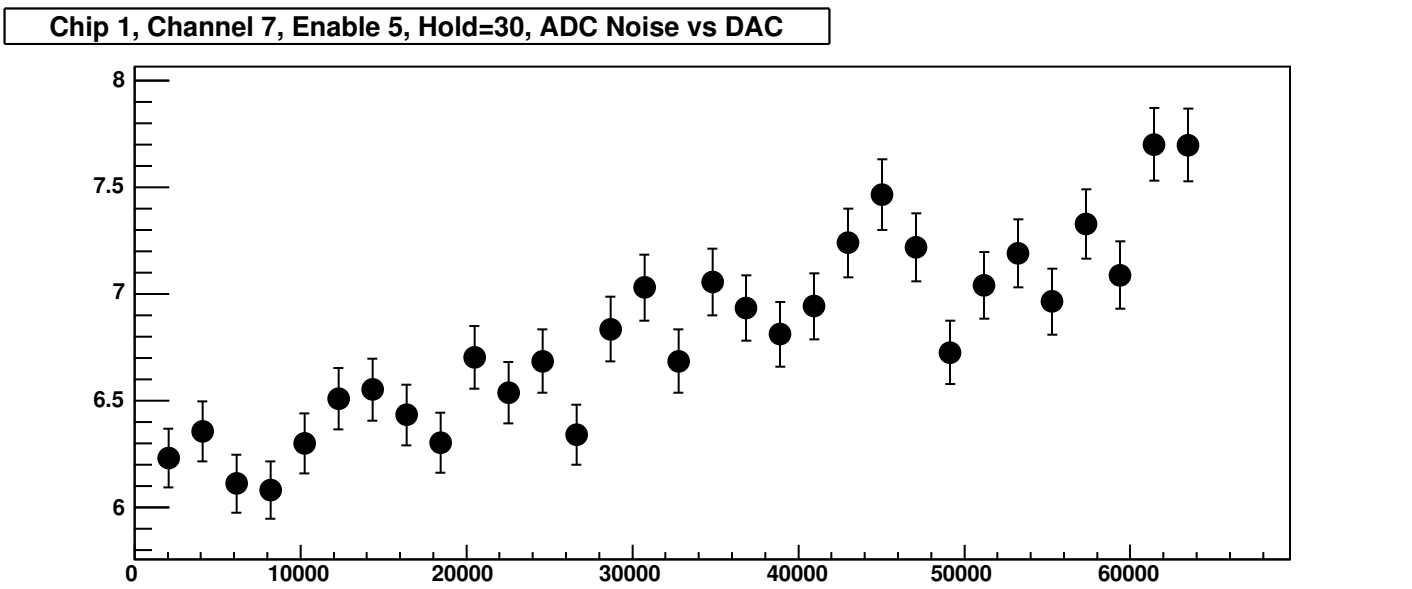

Chip 1, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

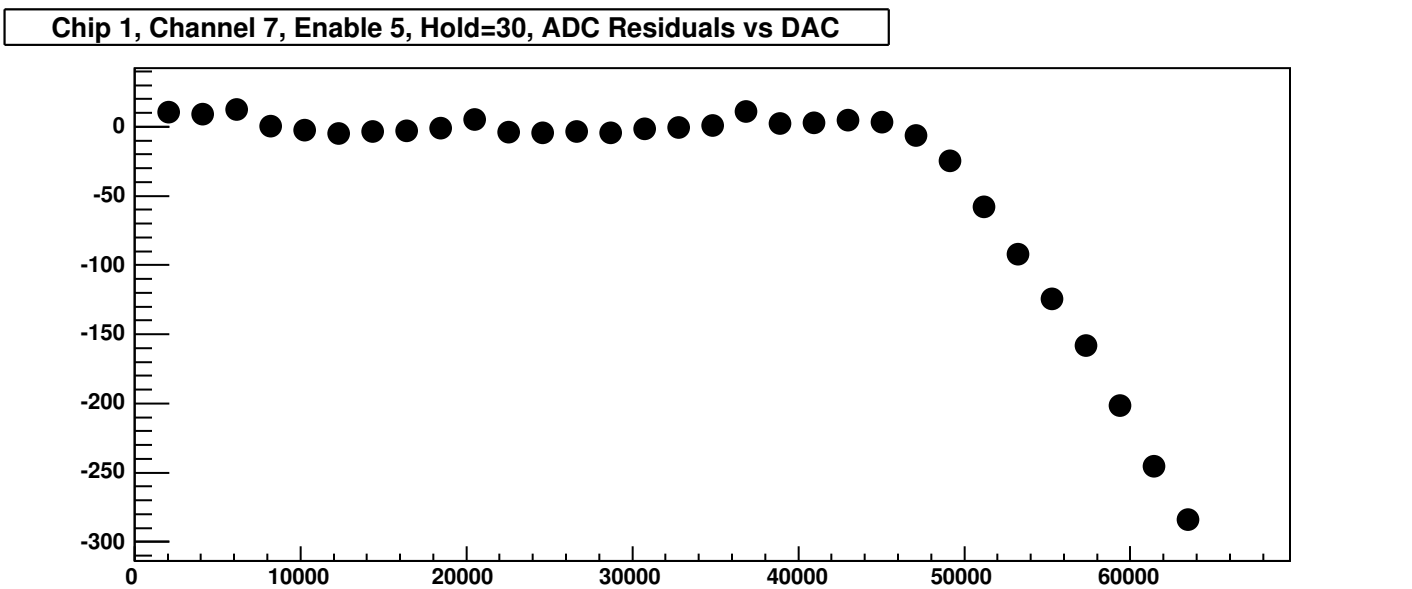

Chip 1, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7018 / 15
p0 1077 ± 0.1147
p1 0.0002654 ± 4.678e-06
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

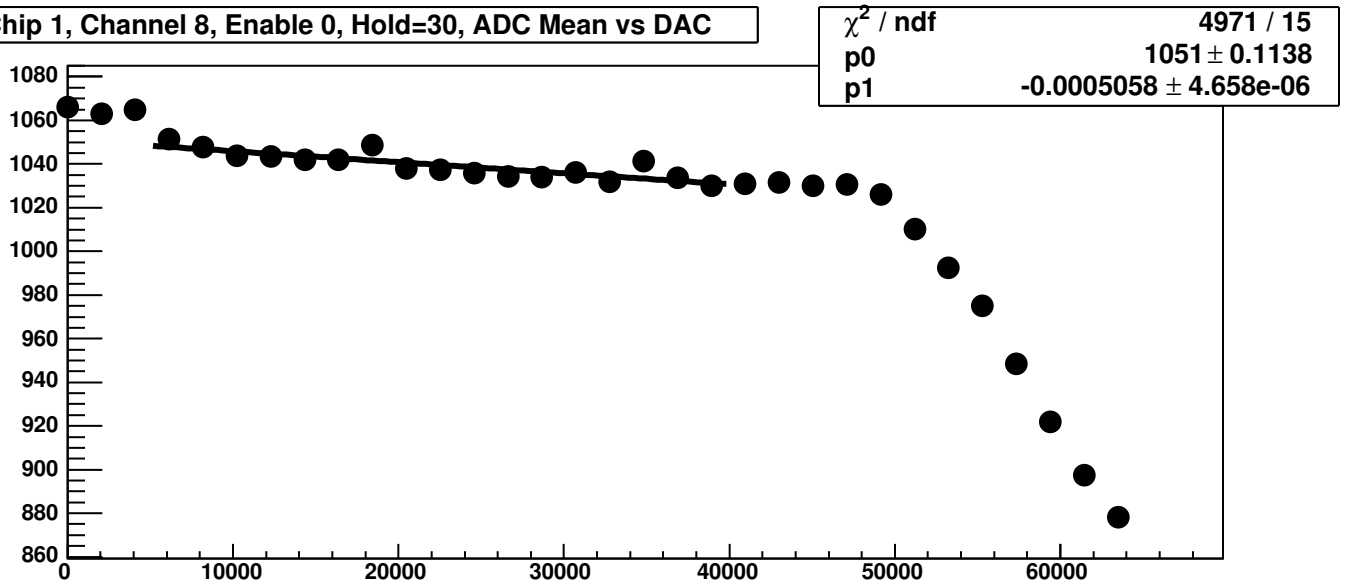

Chip 1, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.024e+04 / 15
p0 1075 ± 0.1187
p1 0.0004996 ± 4.947e-06
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

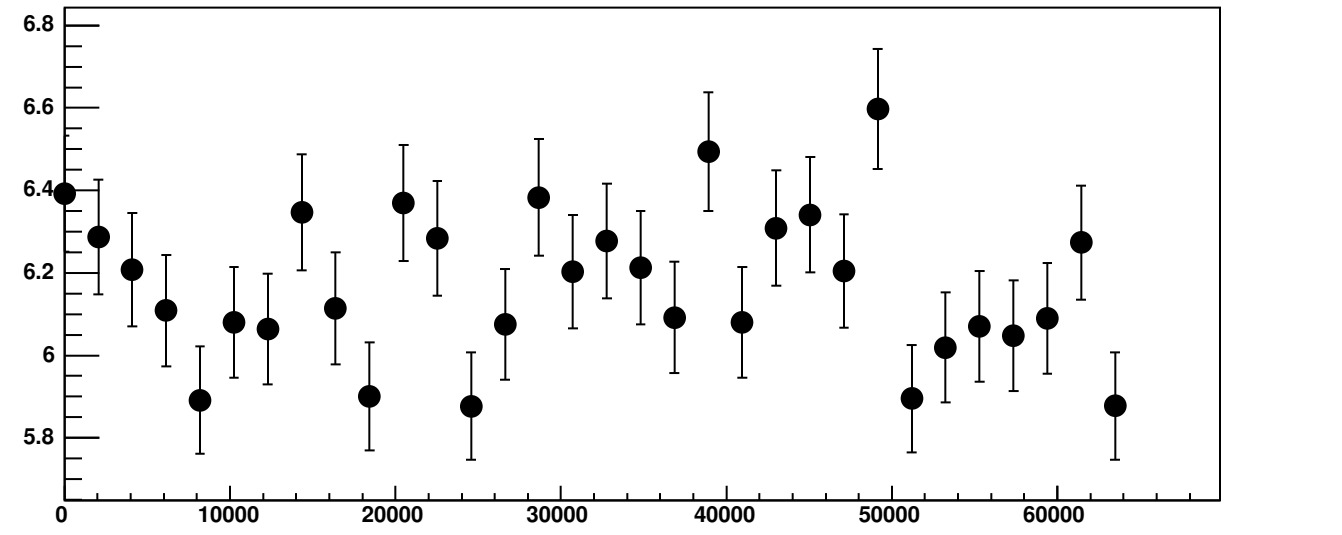

Chip 1, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
8
7.5
7
6.5
6
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

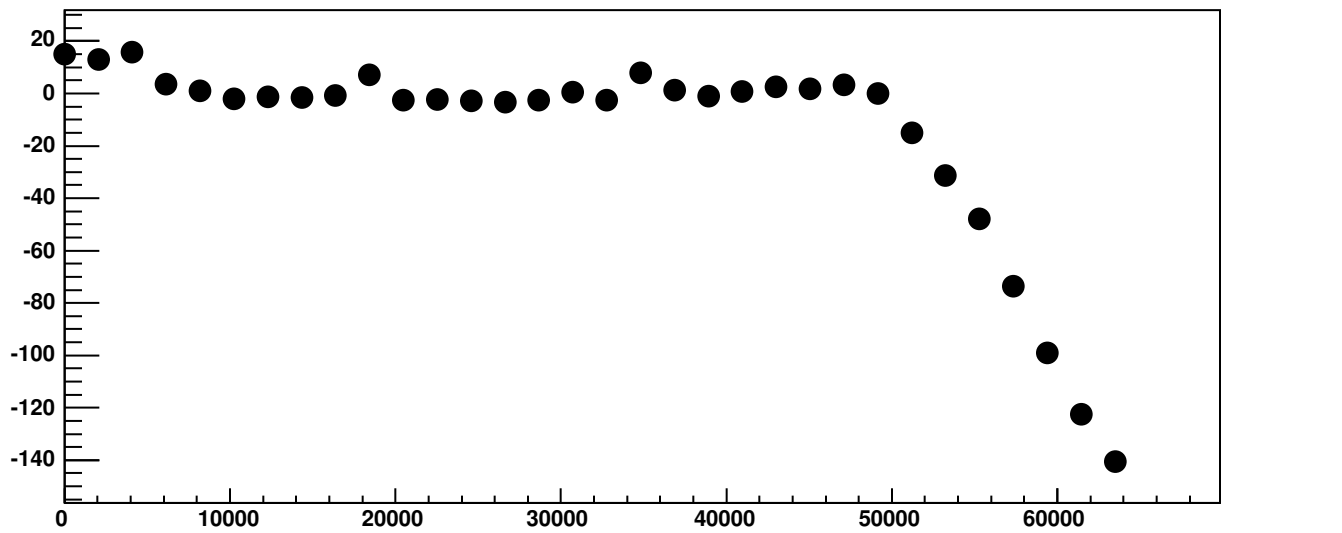
Chip 1, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



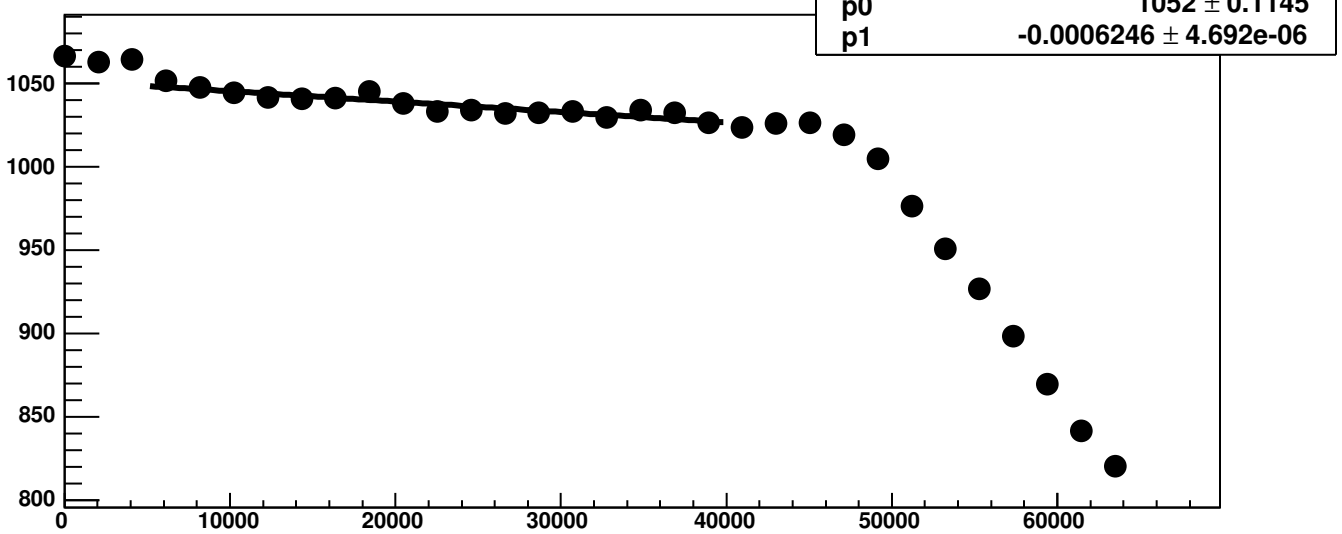
Chip 1, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



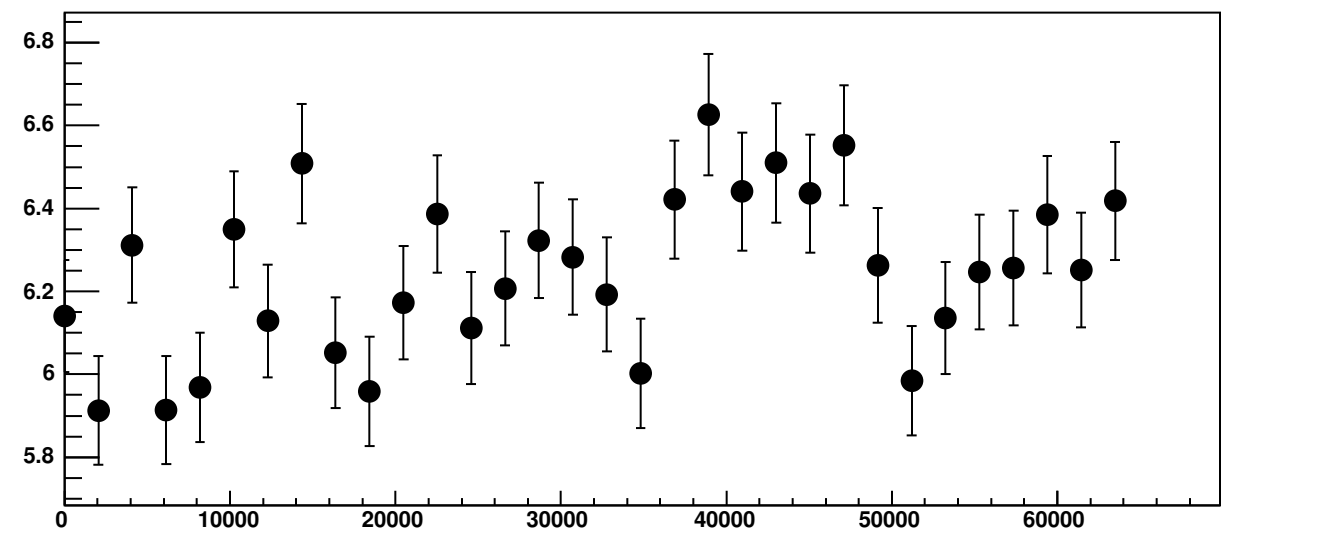
Chip 1, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



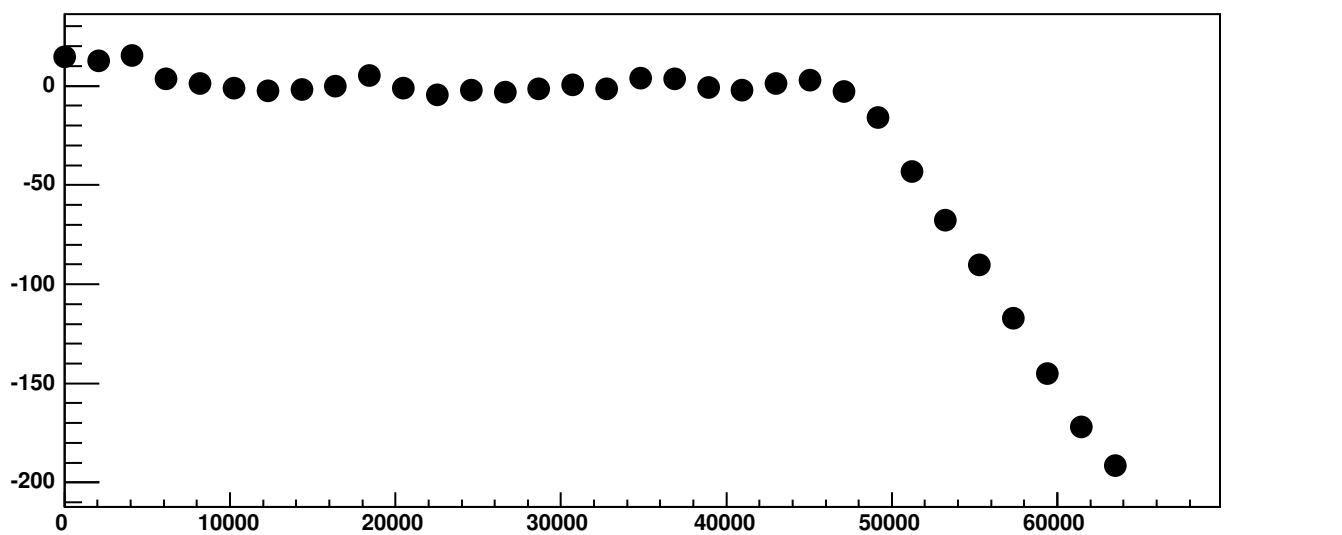
Chip 1, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC

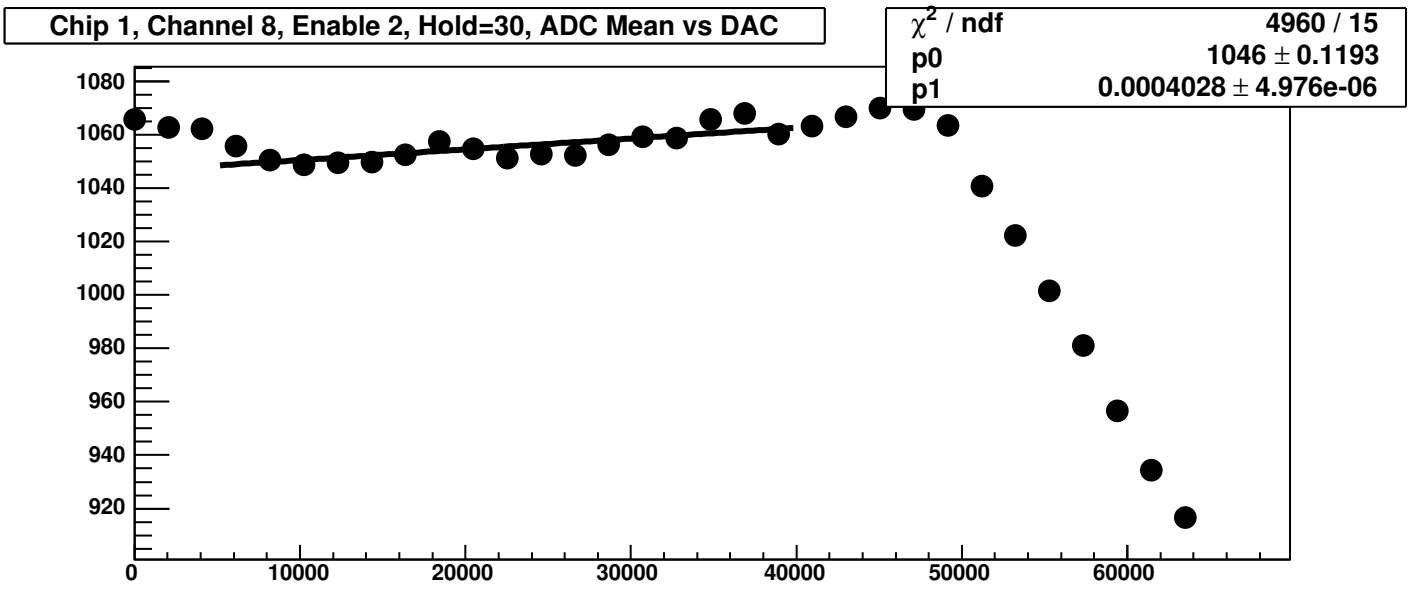


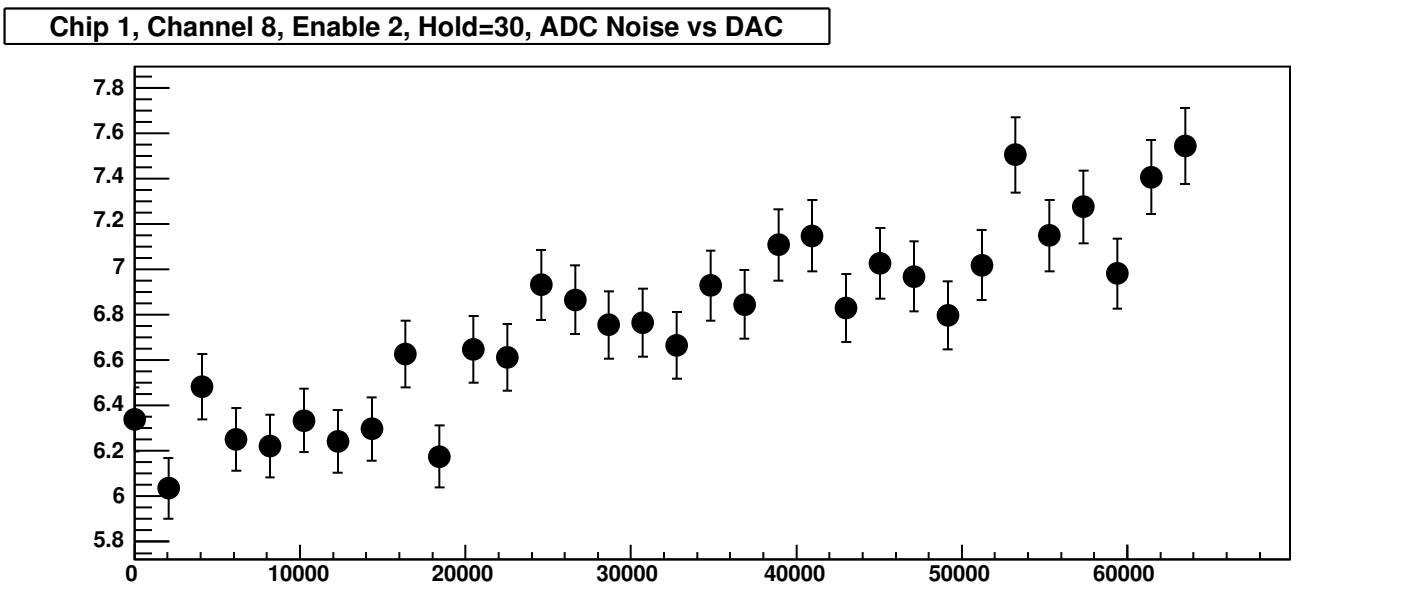
Chip 1, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC

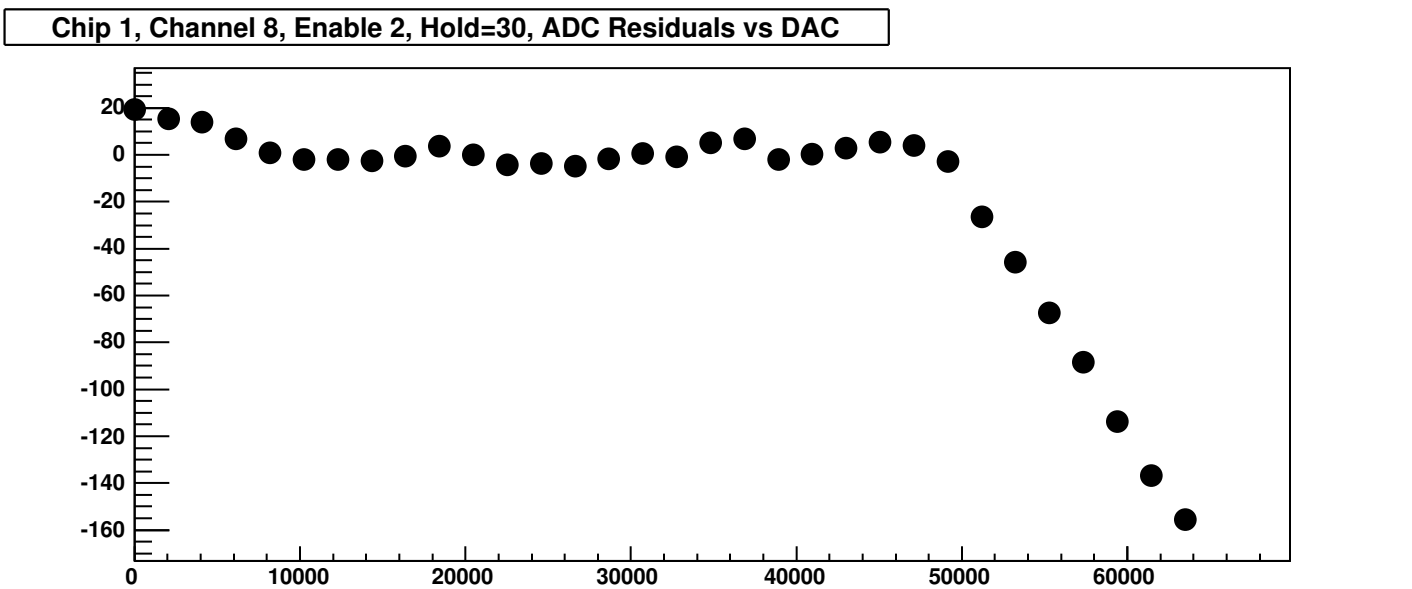


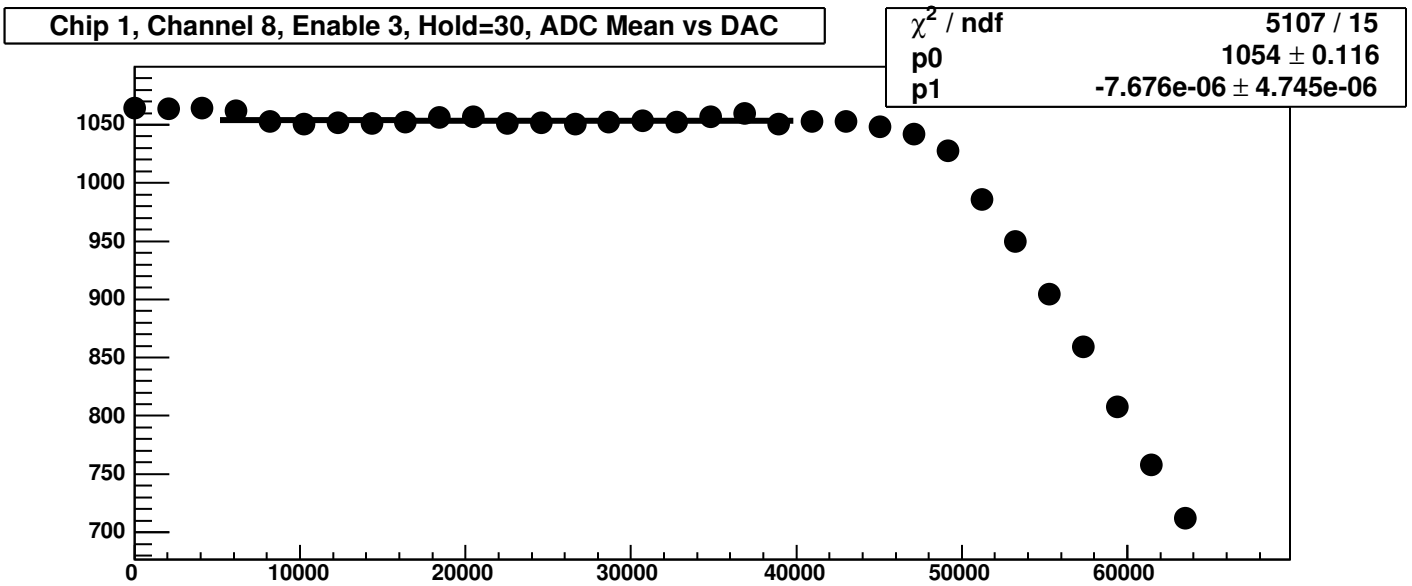
Chip 1, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC

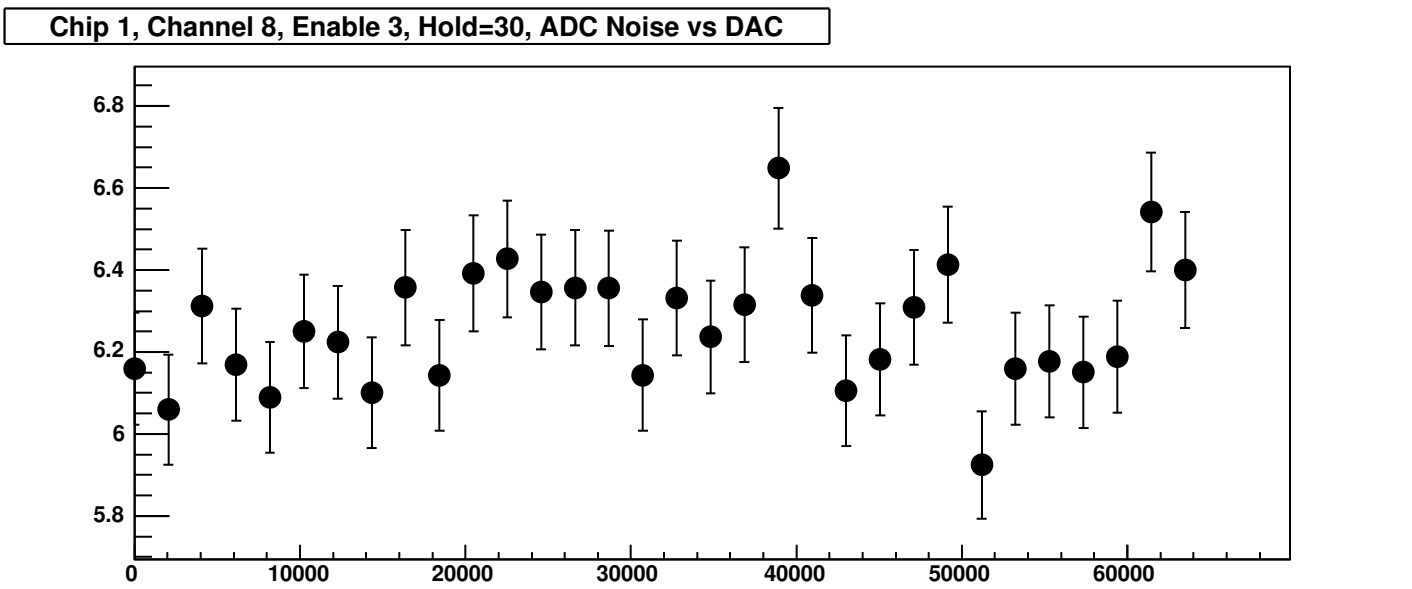


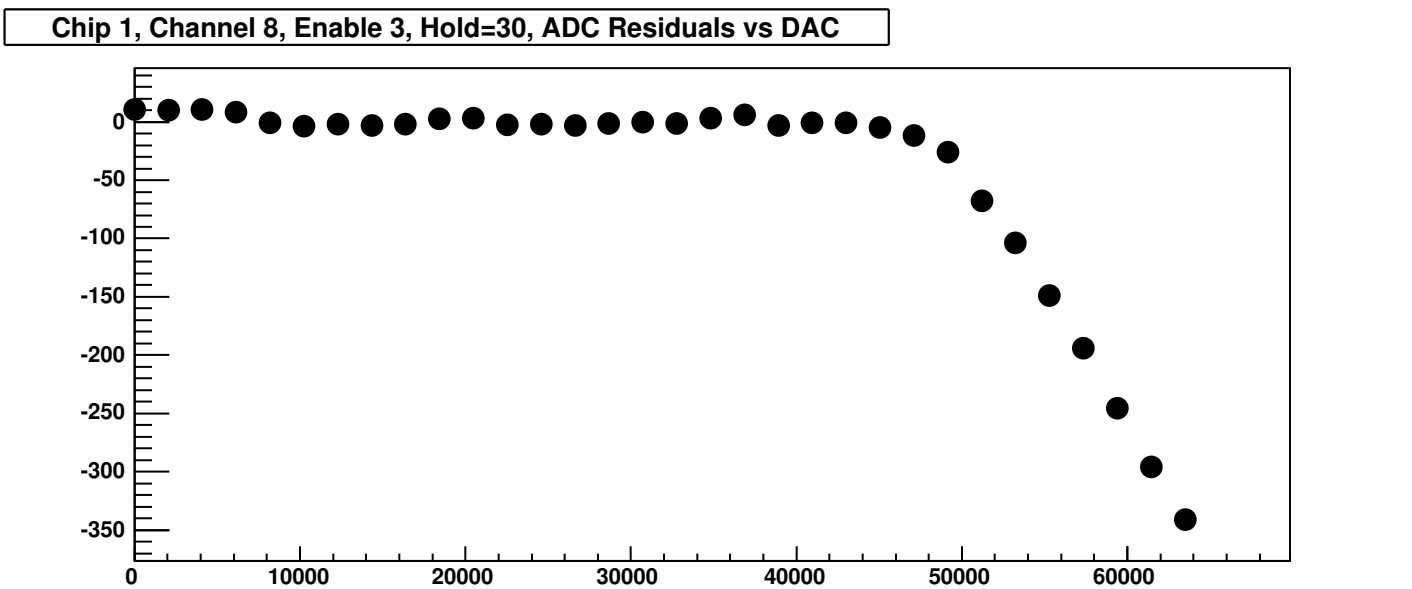

Chip 1, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4960 / 15
p0 1046 ± 0.1193
p1 0.0004028 ± 4.976e-06
1080
1060
1040
1020
1000
980
960
940
920
0
10000
20000
30000
40000
50000
60000

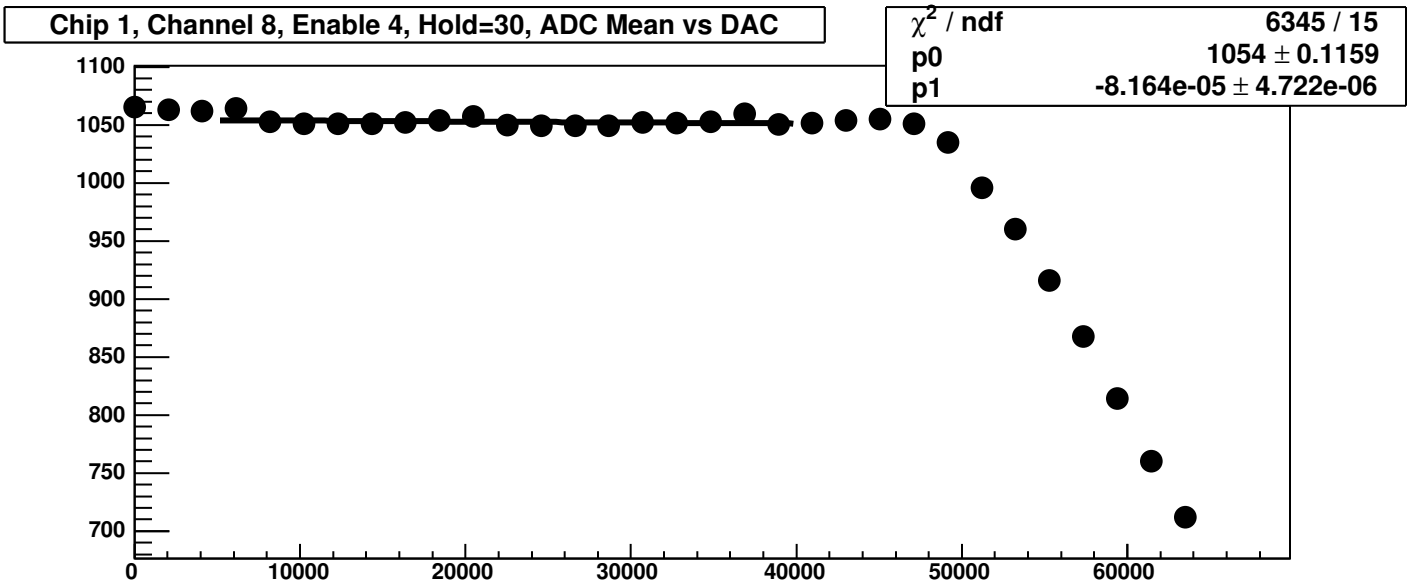

Chip 1, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

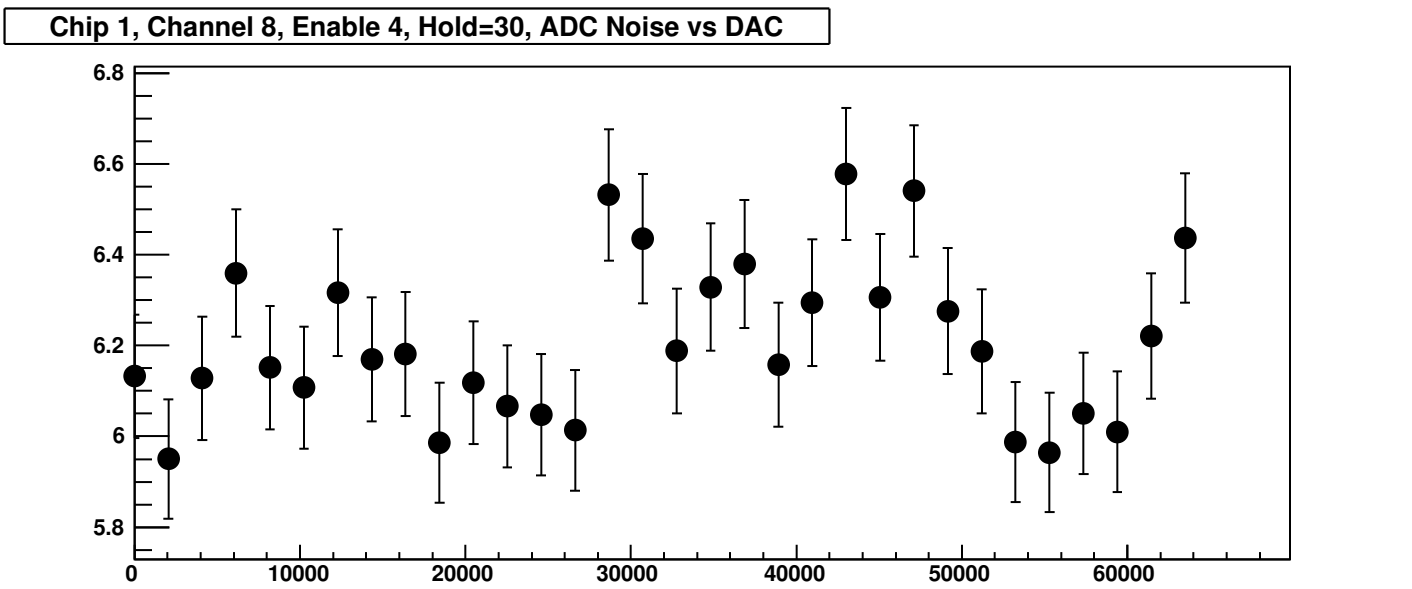

Chip 1, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

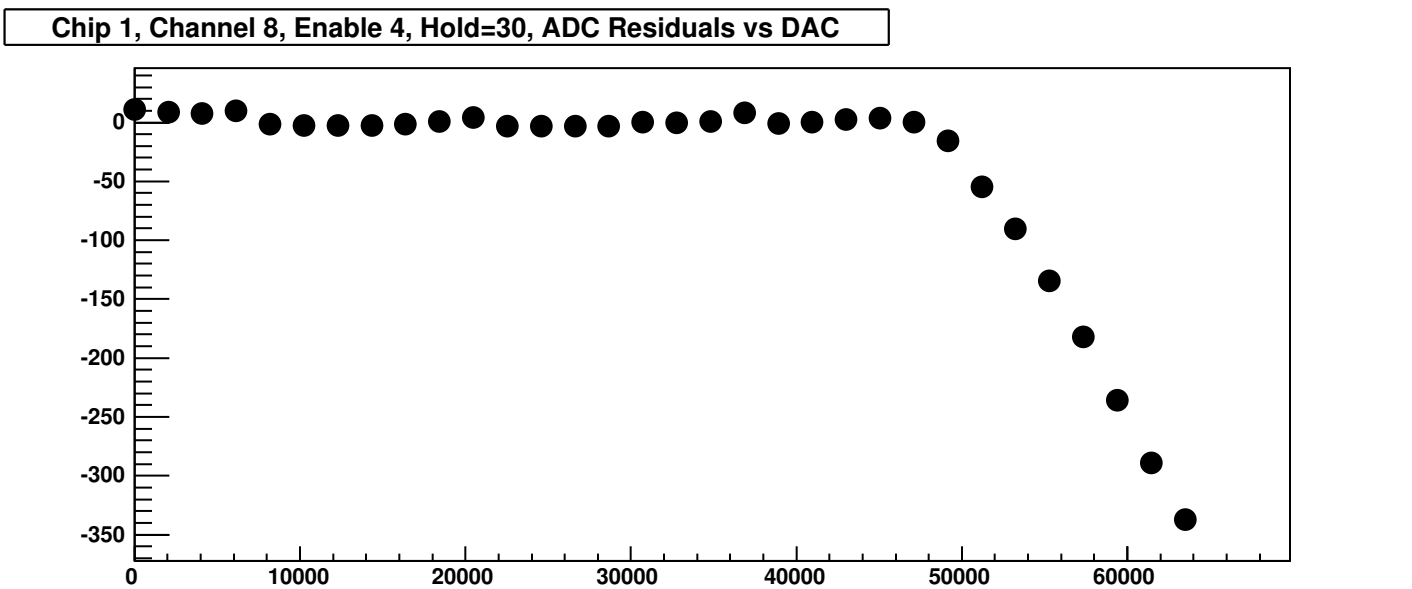

Chip 1, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5107 / 15
p0 1054 ± 0.116
p1 -7.676e-06 ± 4.745e-06
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

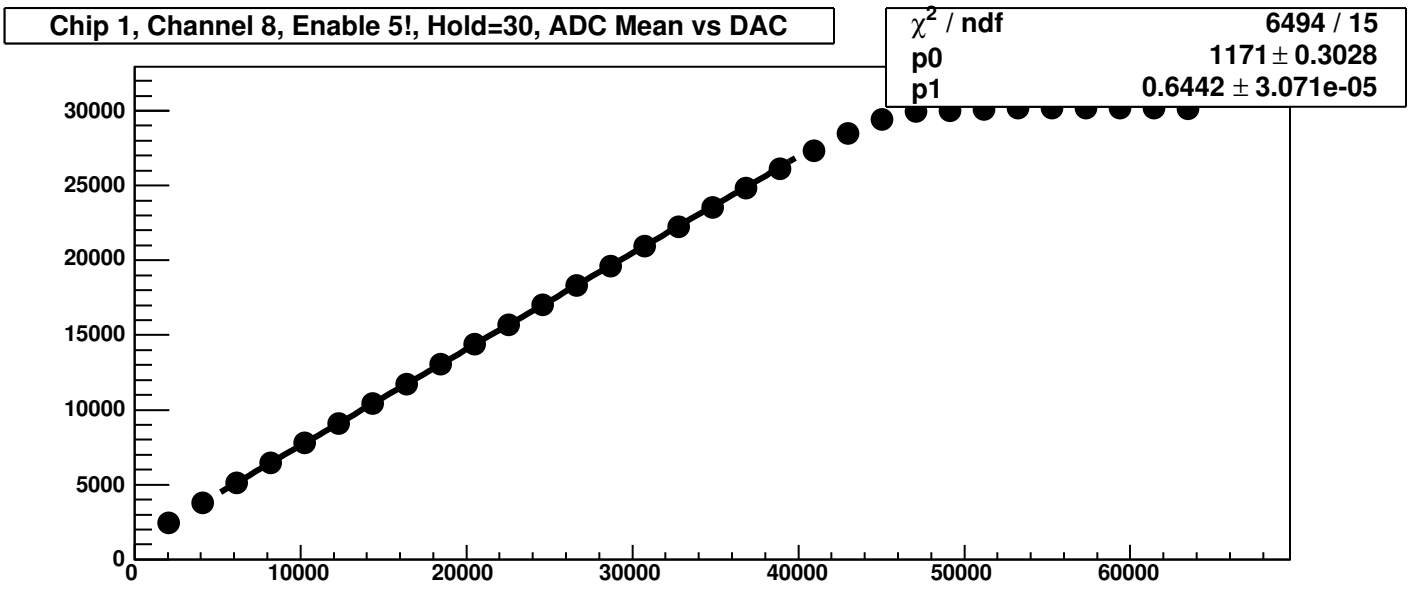

Chip 1, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

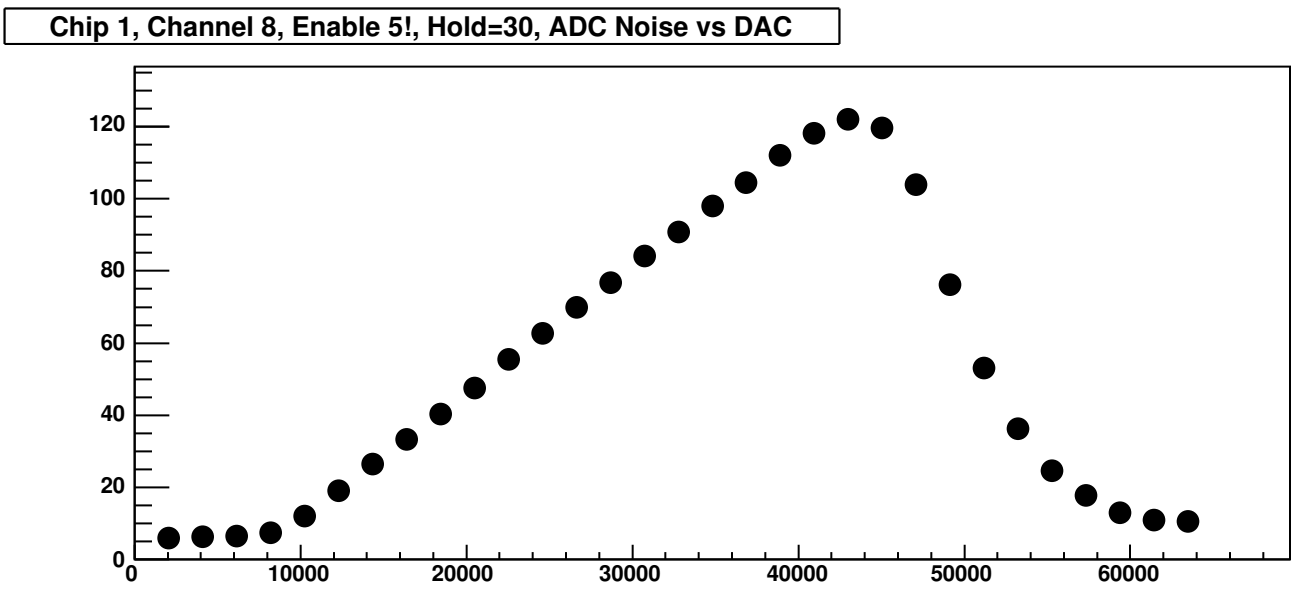

Chip 1, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

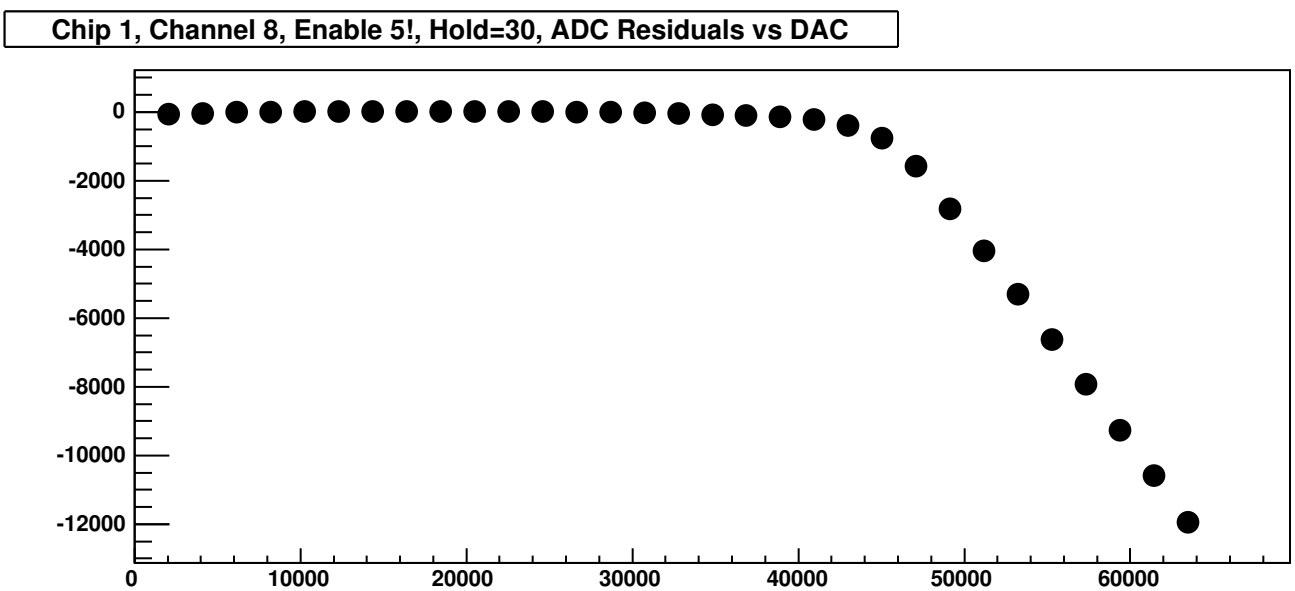

Chip 1, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6345 / 15
p0 1054 ± 0.1159
p1 -8.164e-05 ± 4.722e-06
1100
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

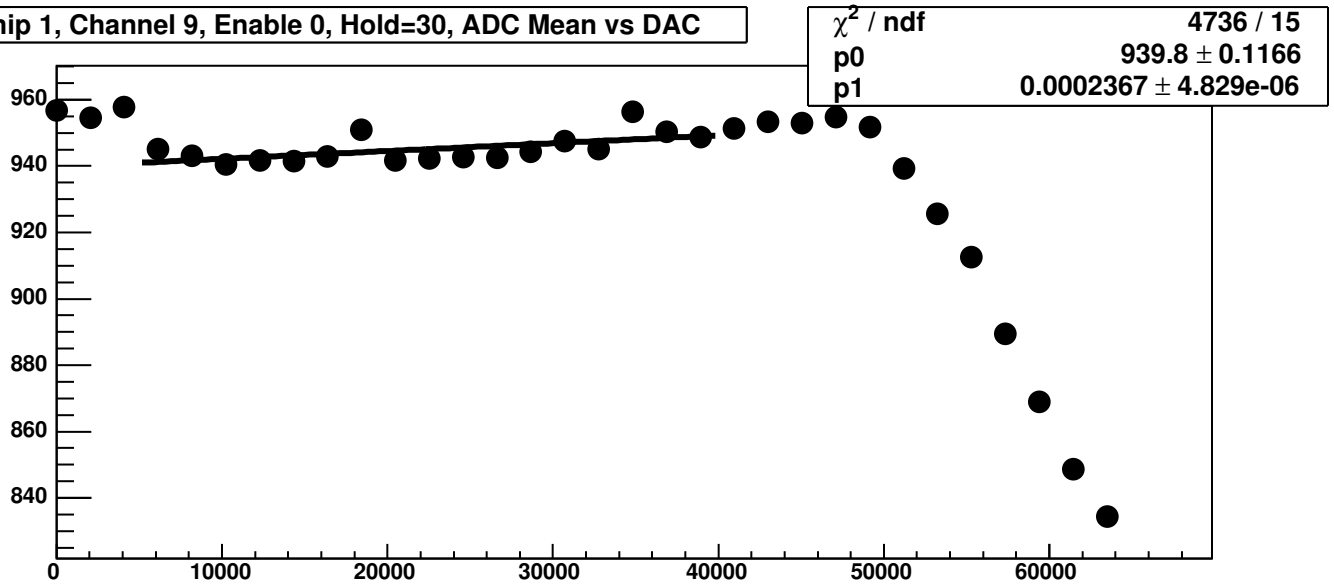

Chip 1, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 6494 / 15
p0 1171 ± 0.3028
p1 0.6442 ± 3.071e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

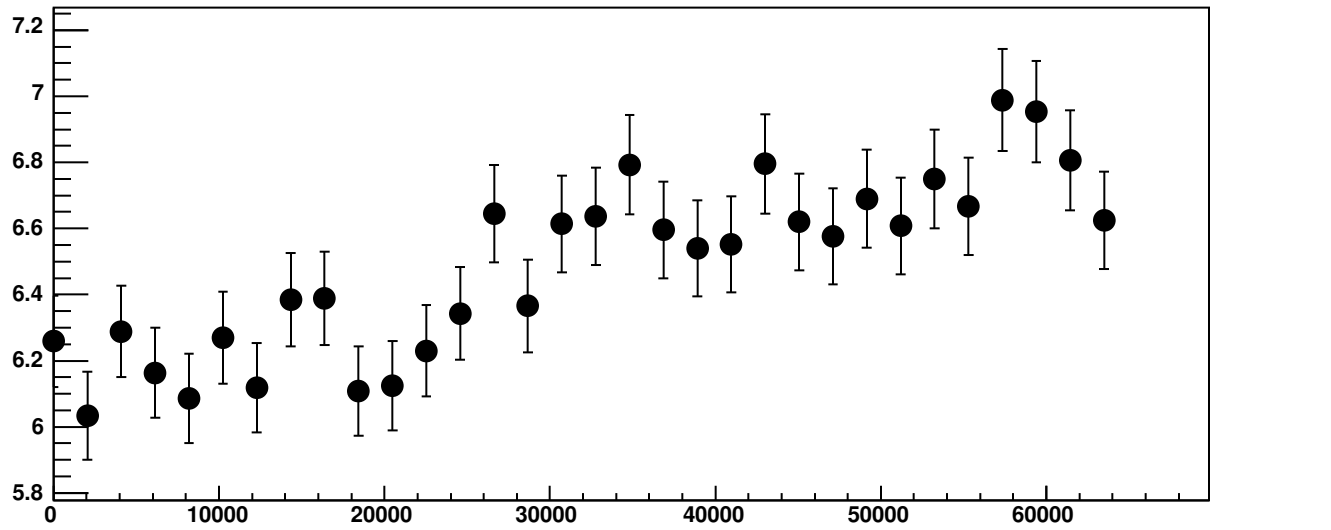

Chip 1, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

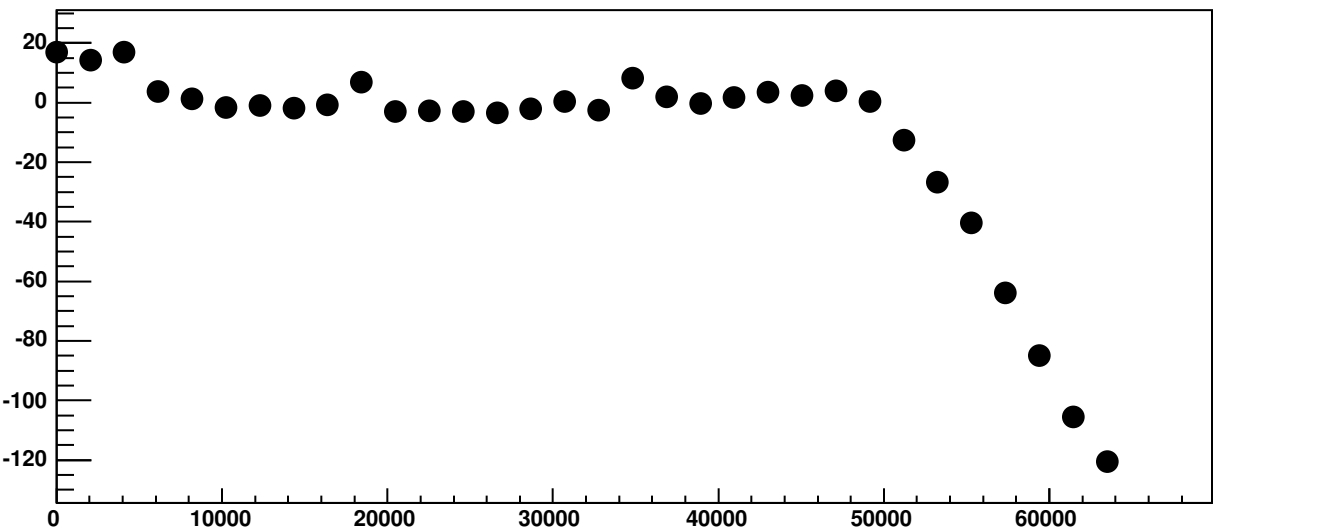
Chip 1, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC



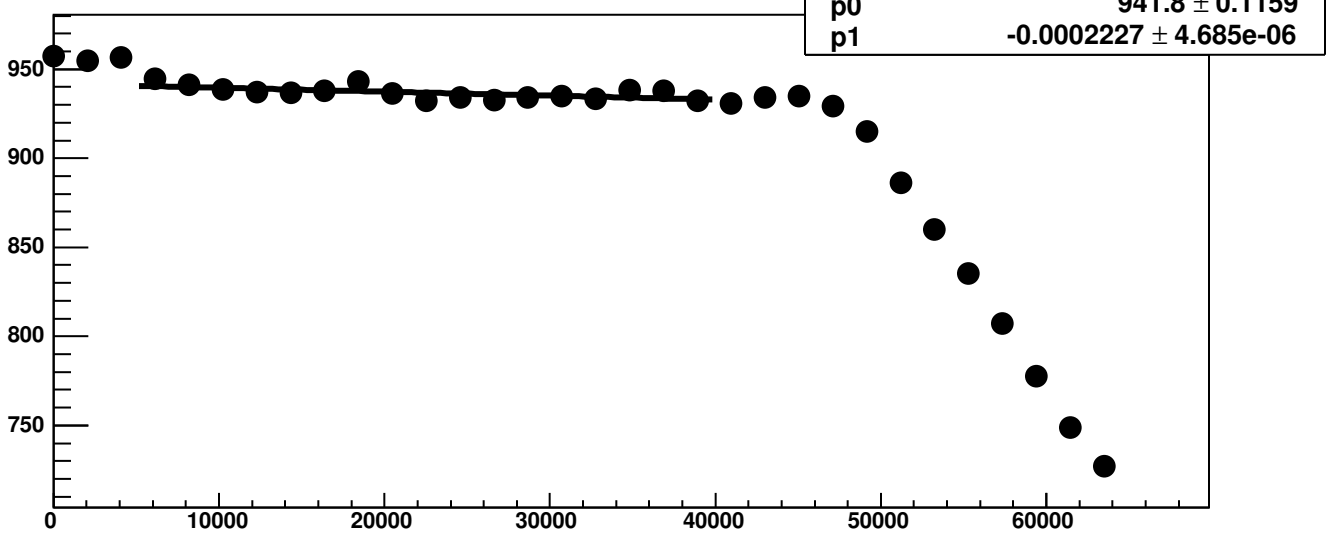
Chip 1, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC



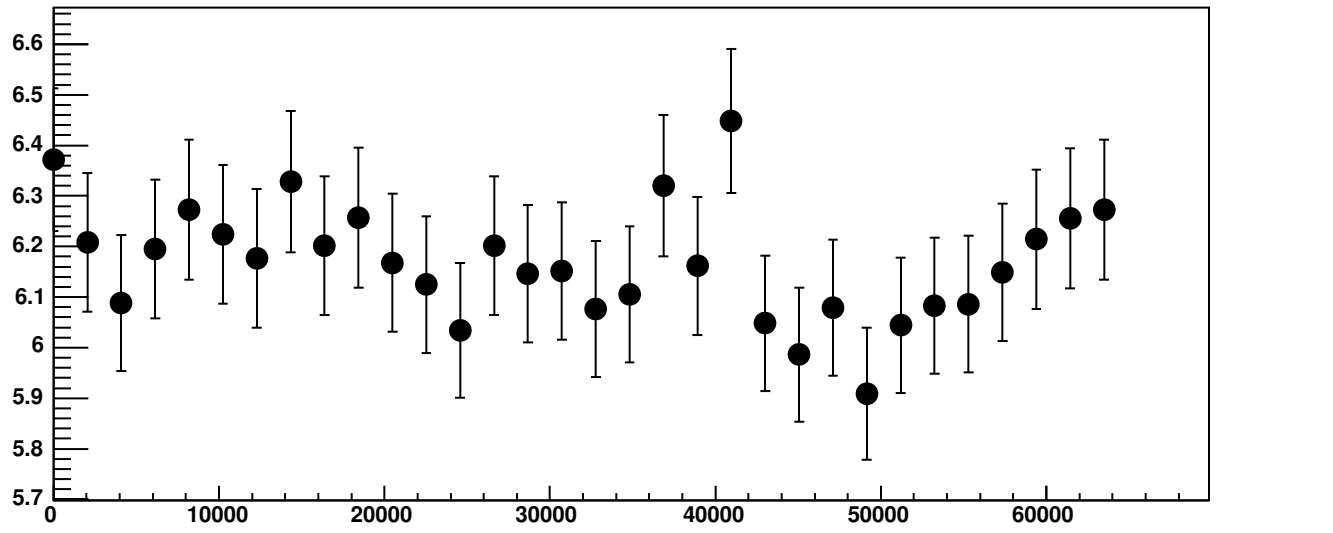
Chip 1, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC



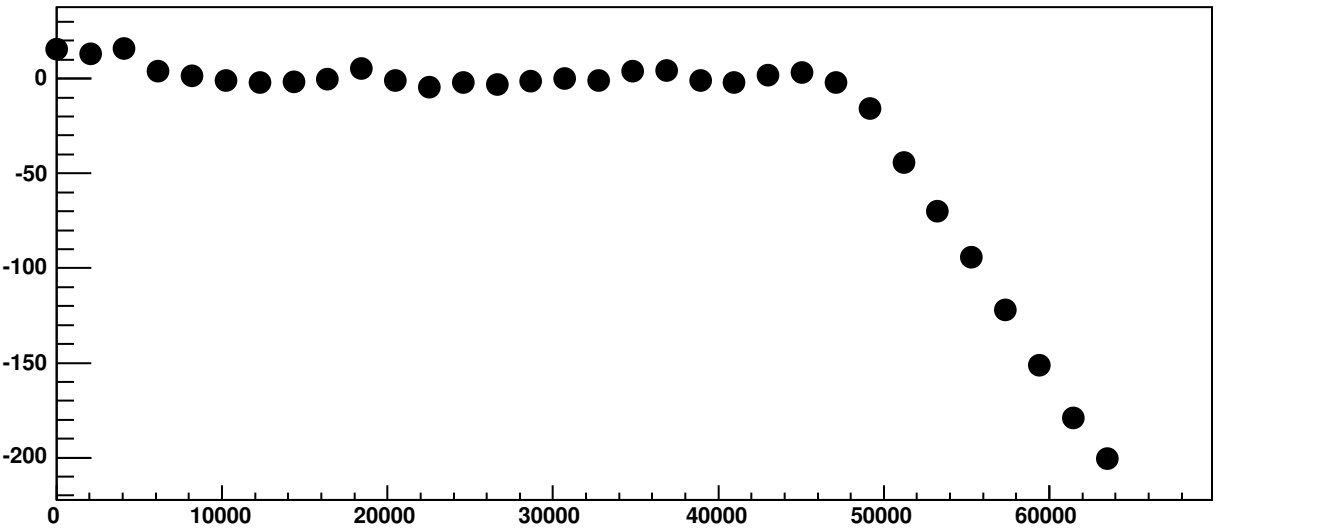
Chip 1, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

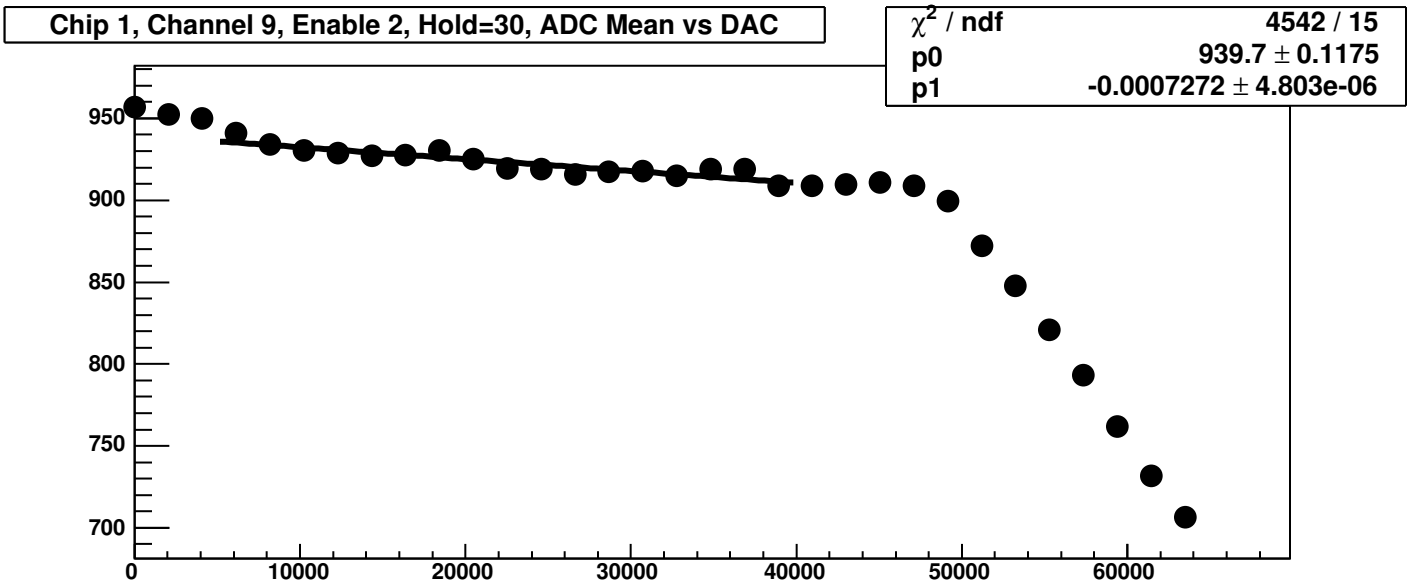


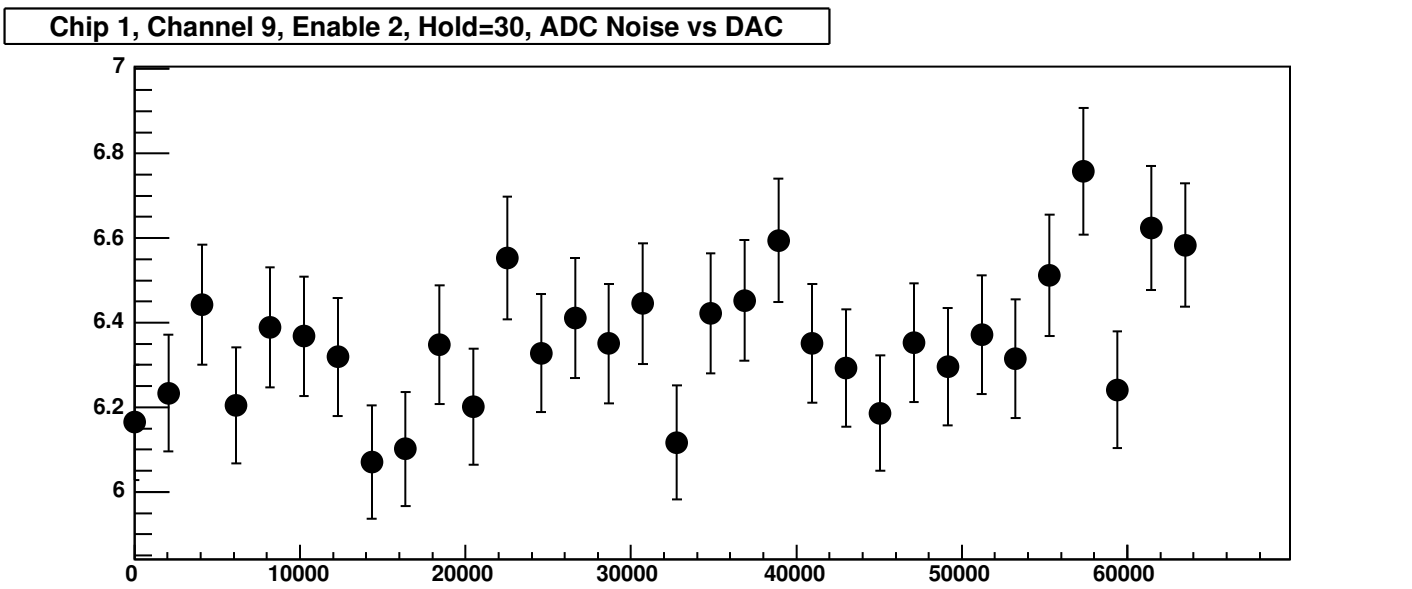
Chip 1, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC

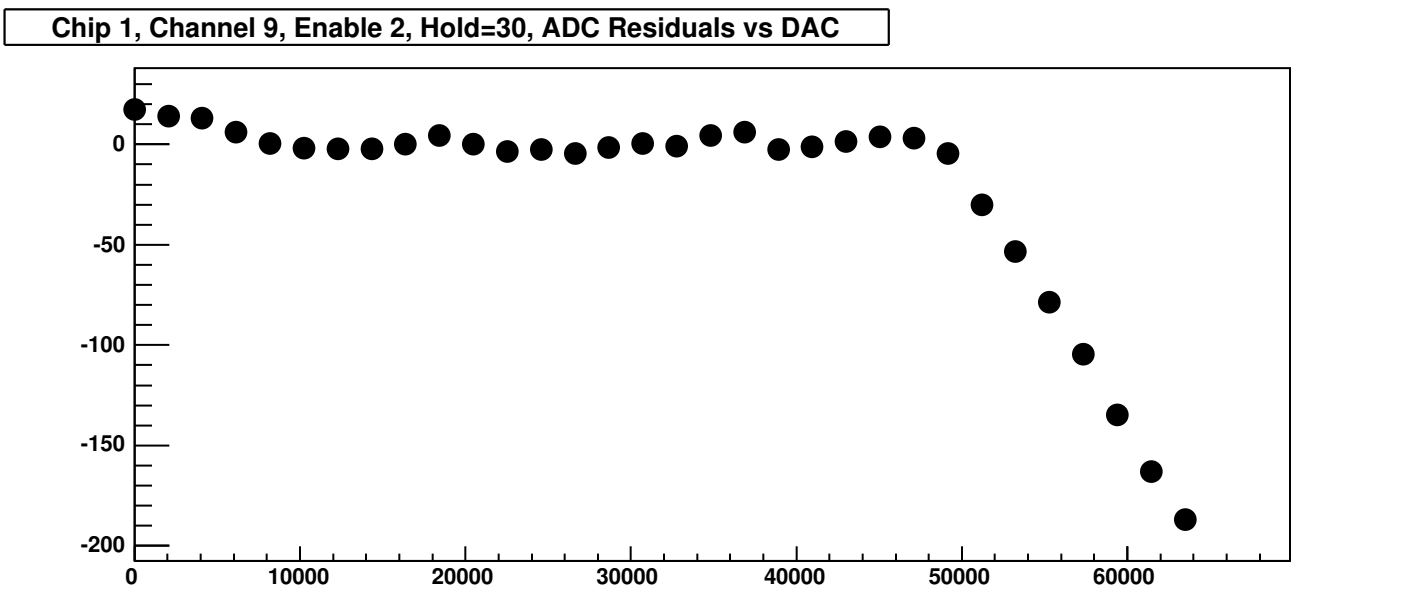


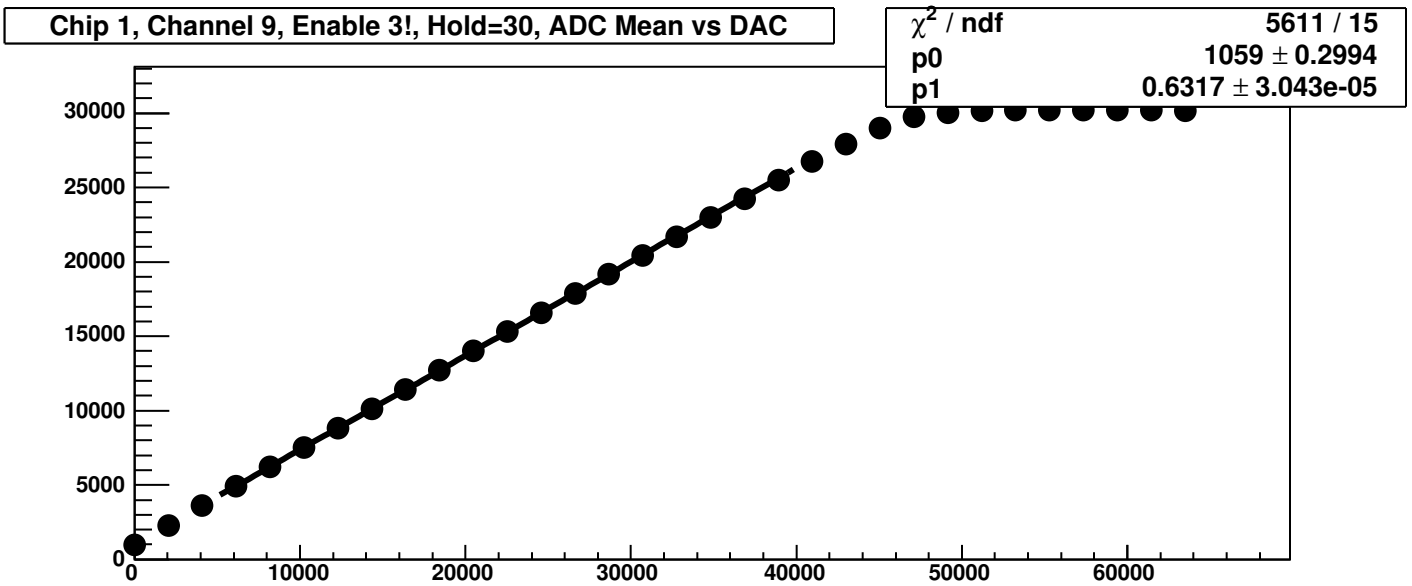
Chip 1, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

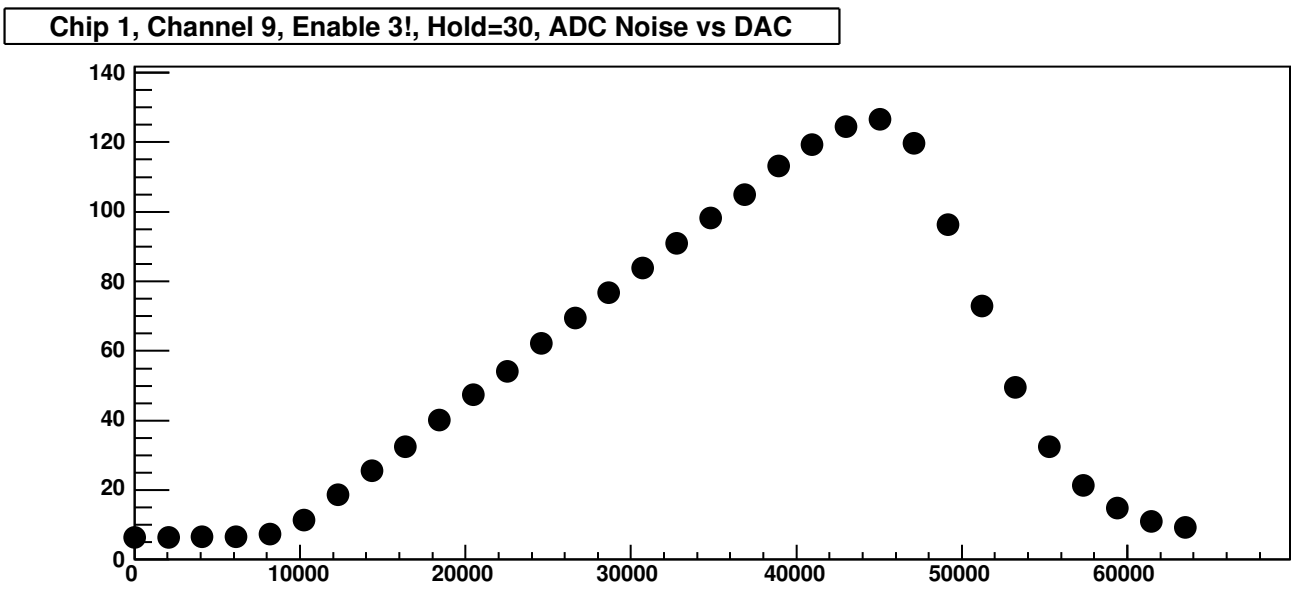


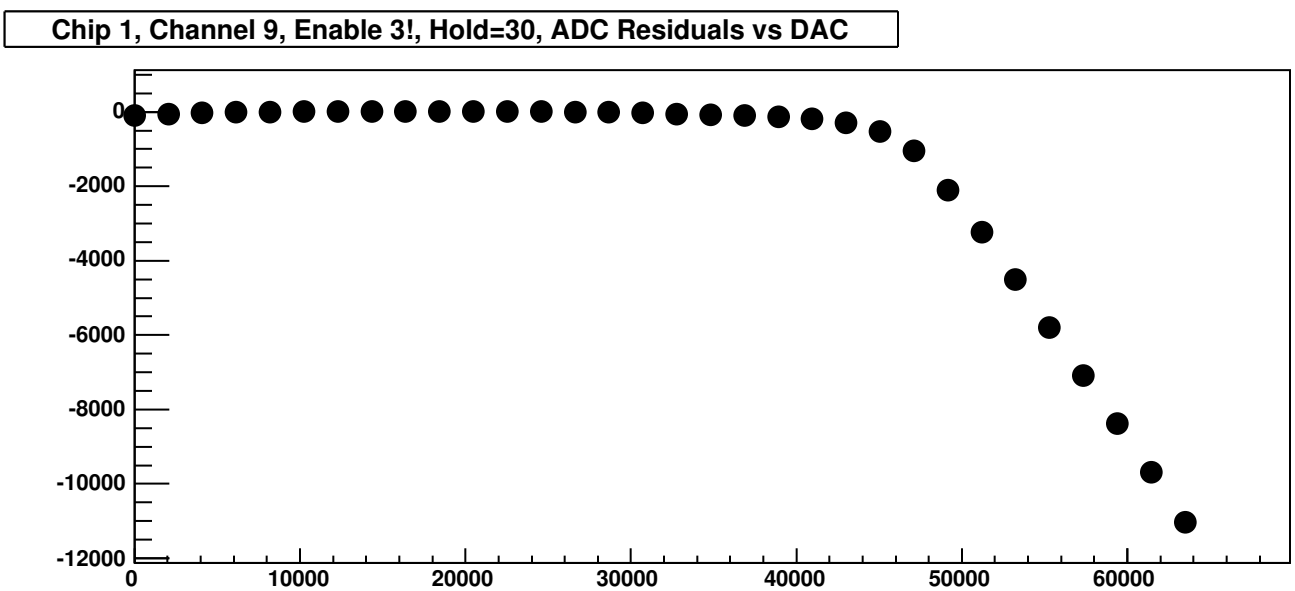

Chip 1, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4542 / 15
p0 939.7 ± 0.1175
p1 -0.0007272 ± 4.803e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

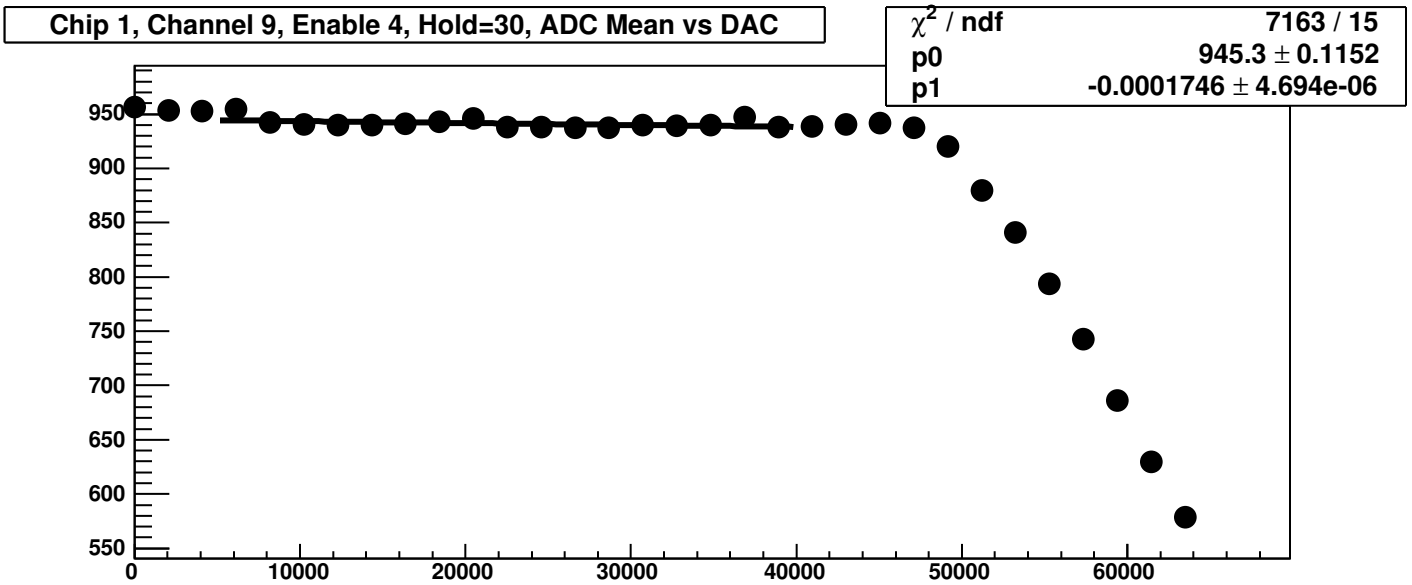

Chip 1, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

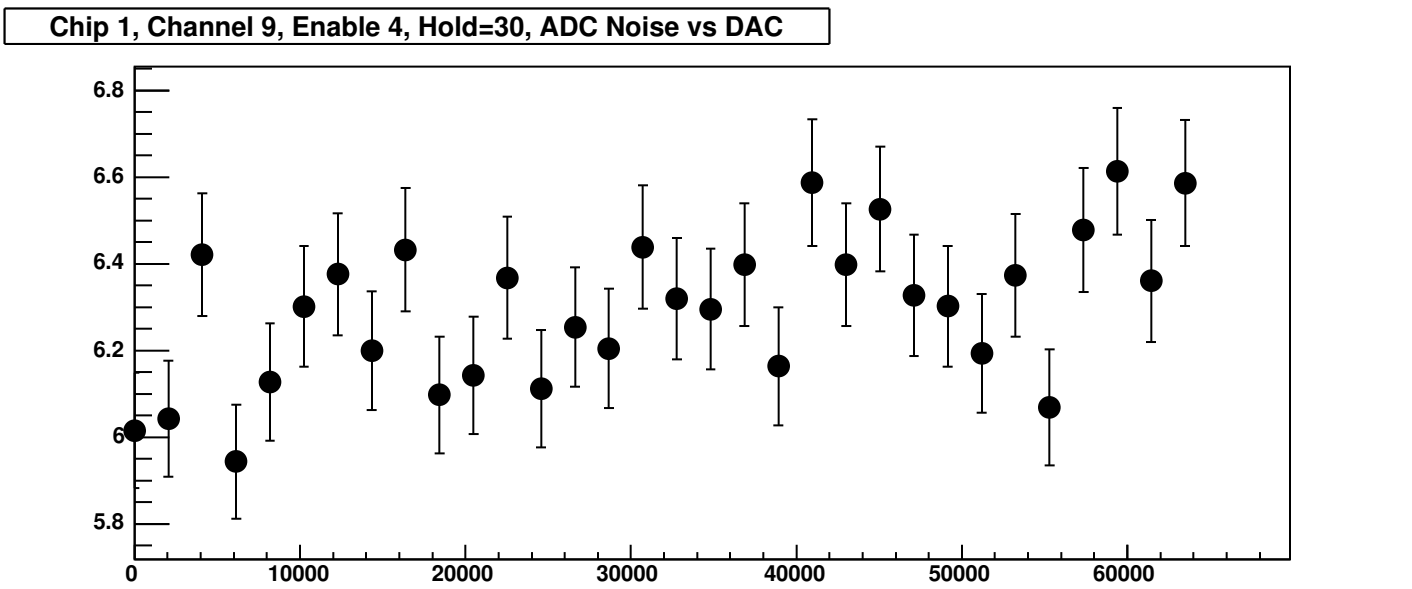

Chip 1, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

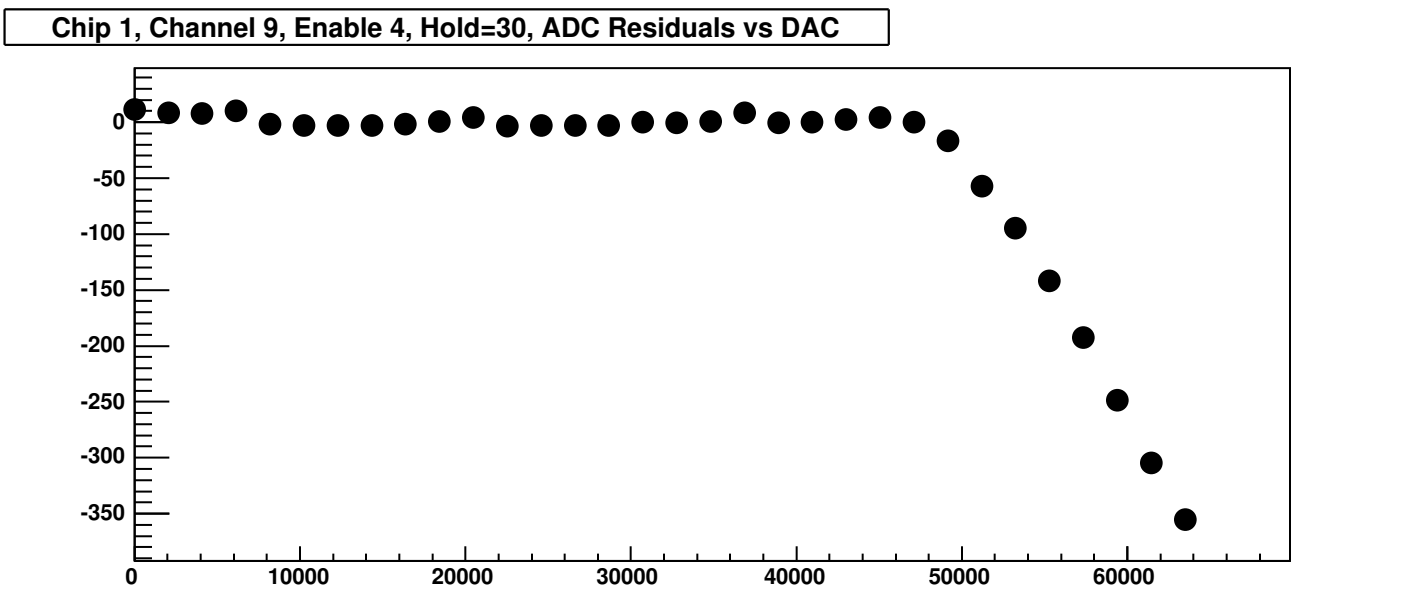

Chip 1, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 5611 / 15
p0 1059 ± 0.2994
p1 0.6317 ± 3.043e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

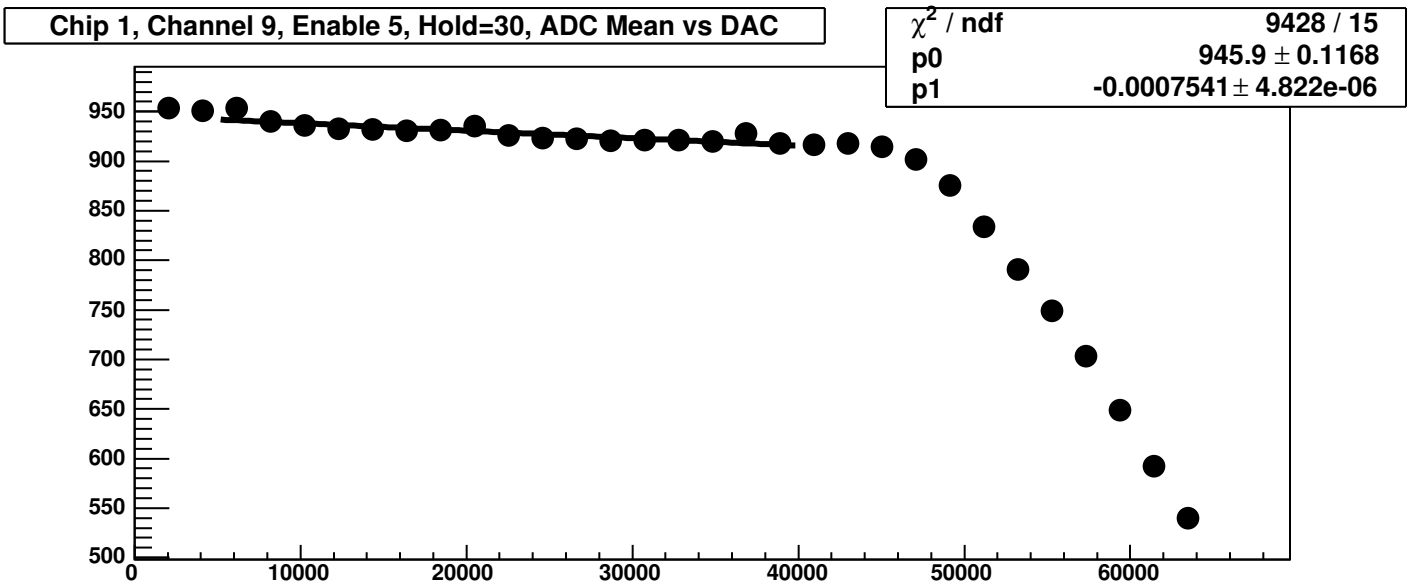

Chip 1, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

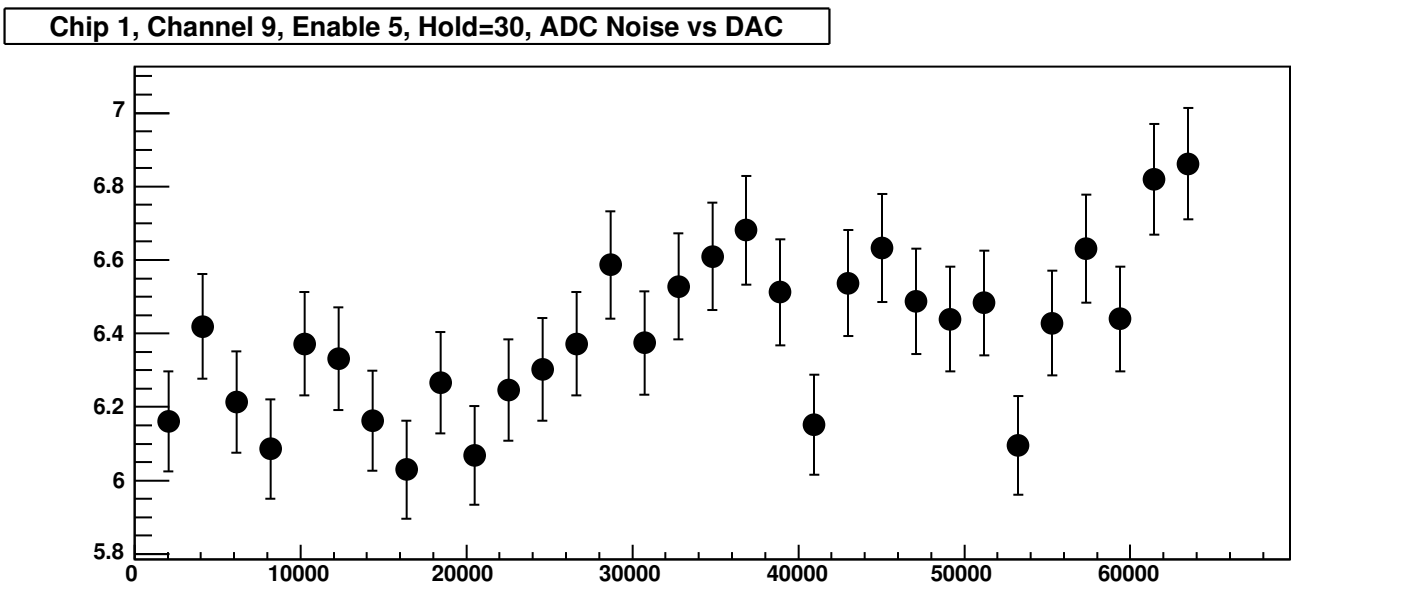

Chip 1, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

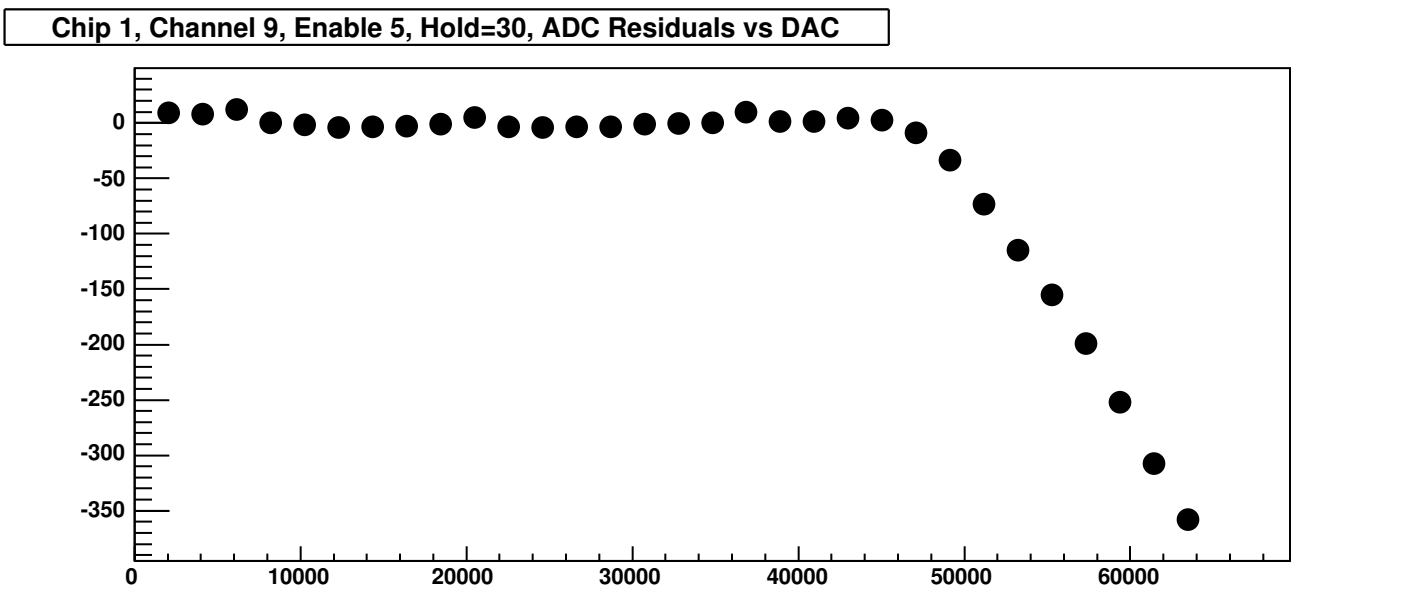

Chip 1, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7163 / 15
p0 945.3 ± 0.1152
p1 -0.0001746 ± 4.694e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

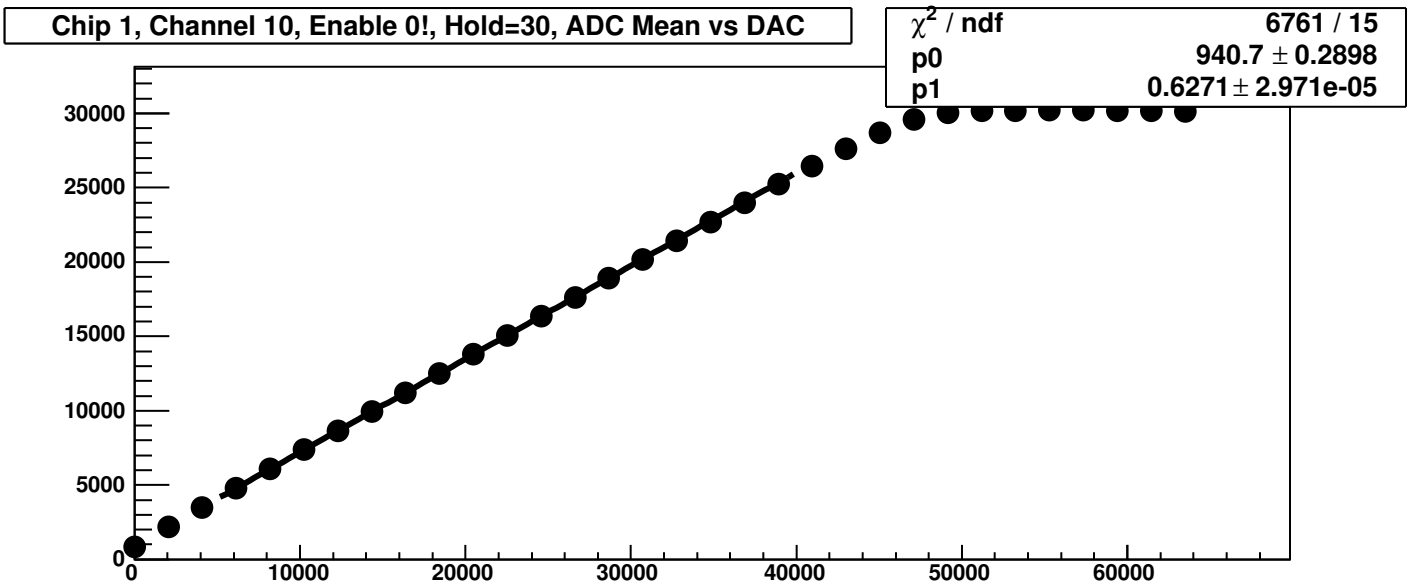

Chip 1, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

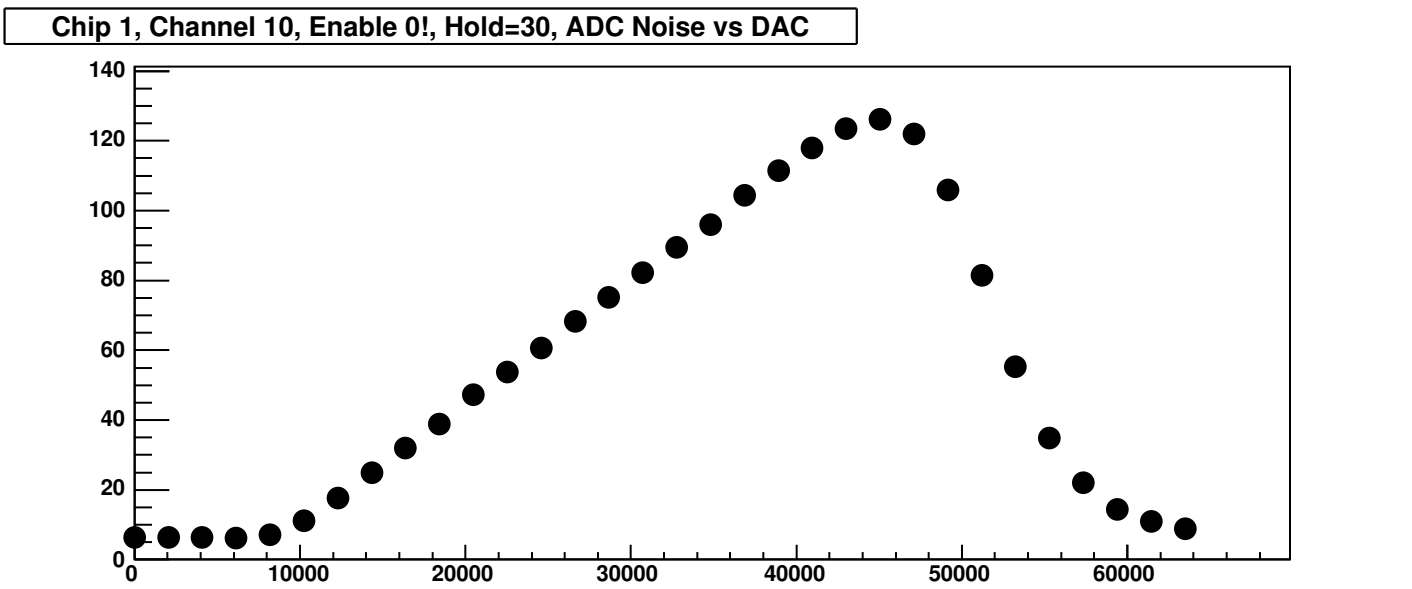

Chip 1, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

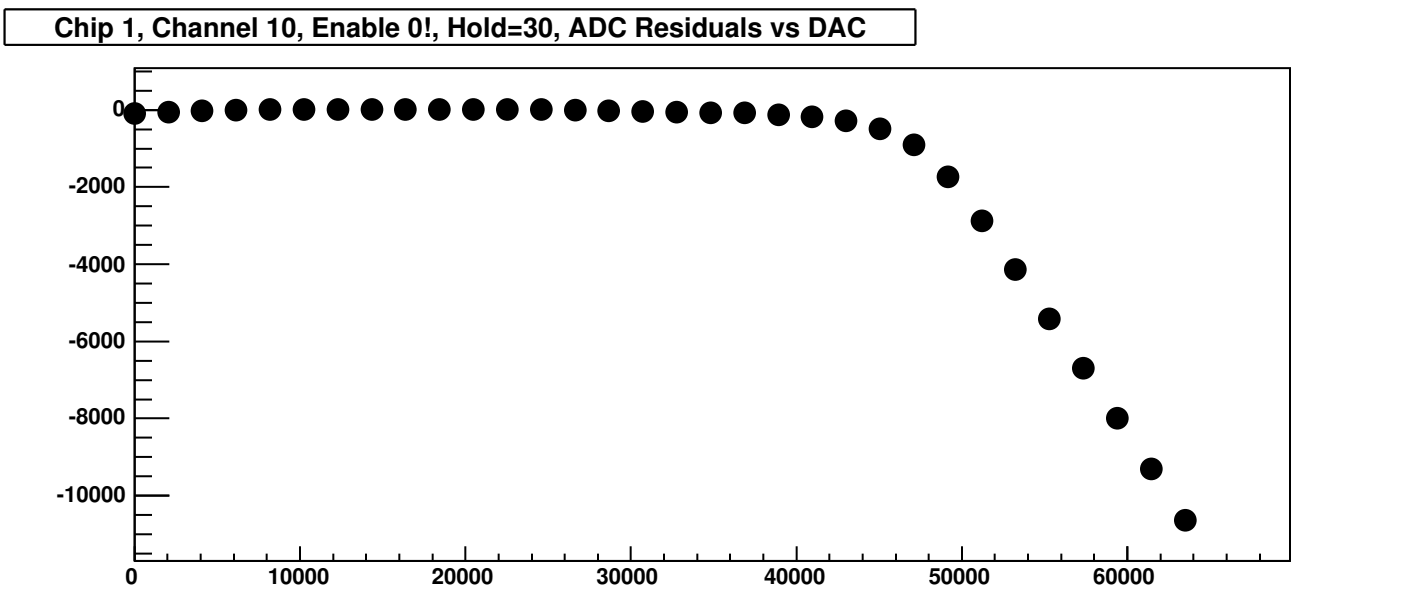

Chip 1, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9428 / 15
p0 945.9 ± 0.1168
p1 -0.0007541 ± 4.822e-06
950
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

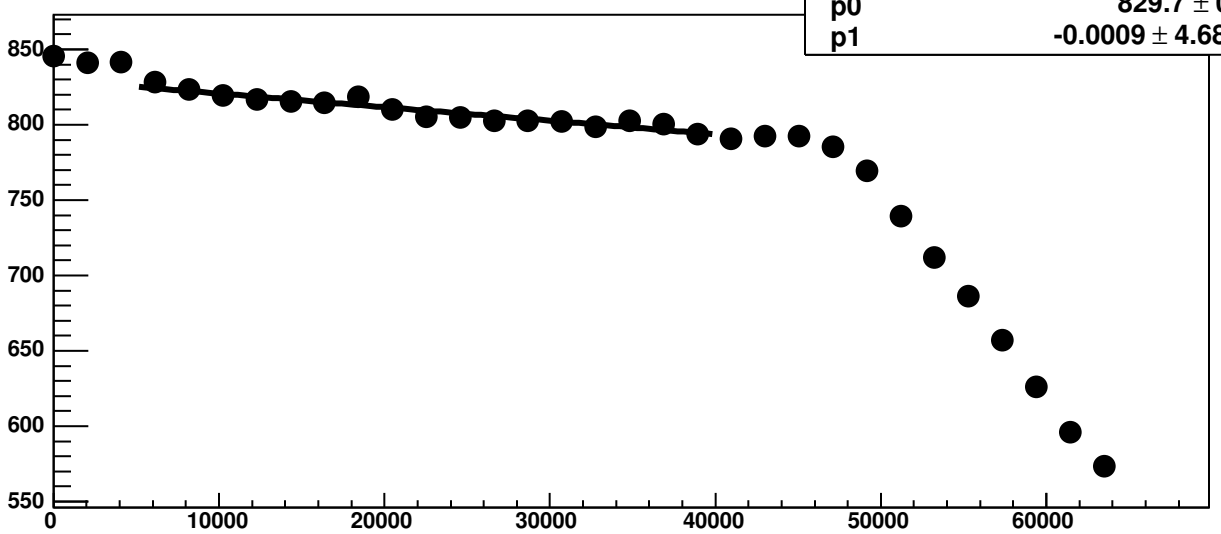

Chip 1, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 6761 / 15
p0 940.7 ± 0.2898
p1 0.6271 ± 2.971e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

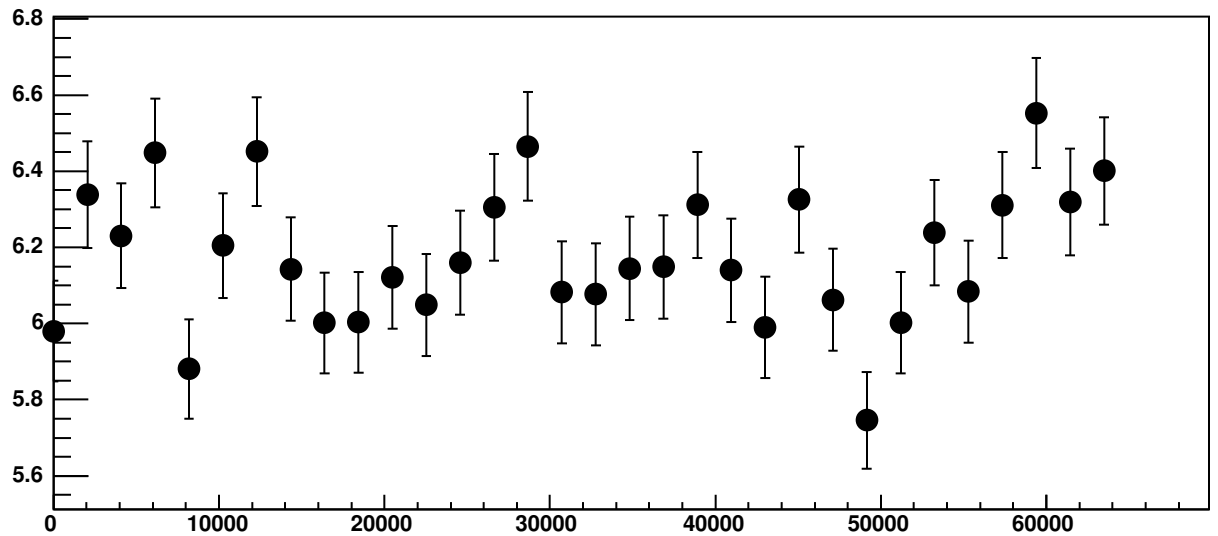

Chip 1, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

Chip 1, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

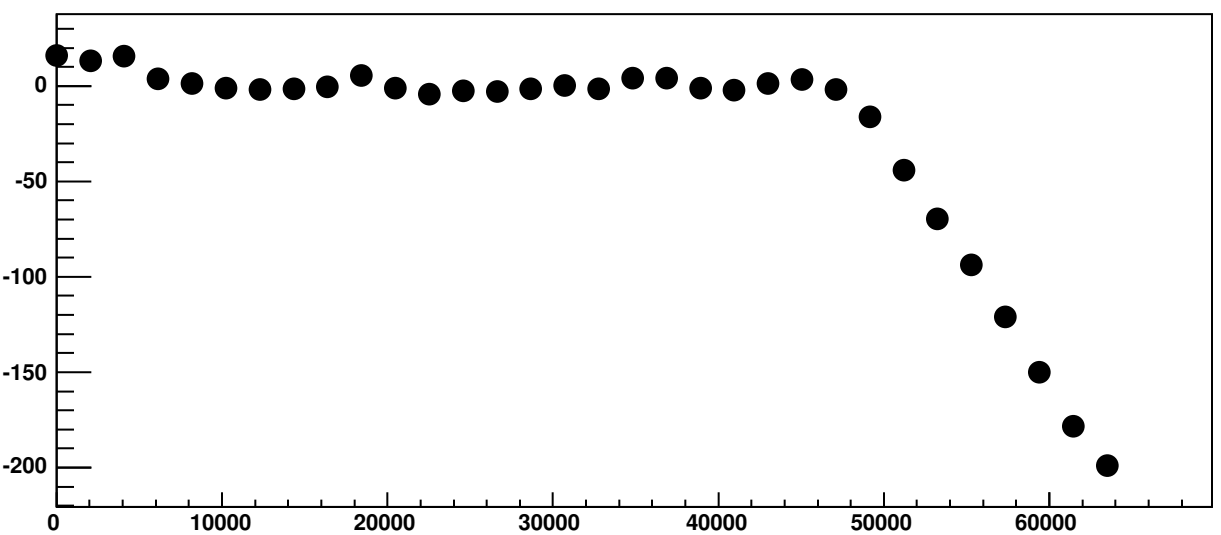
χ^2 / ndf	3467 / 15
p0	829.7 ± 0.1153
p1	-0.0009 ± 4.684e-06

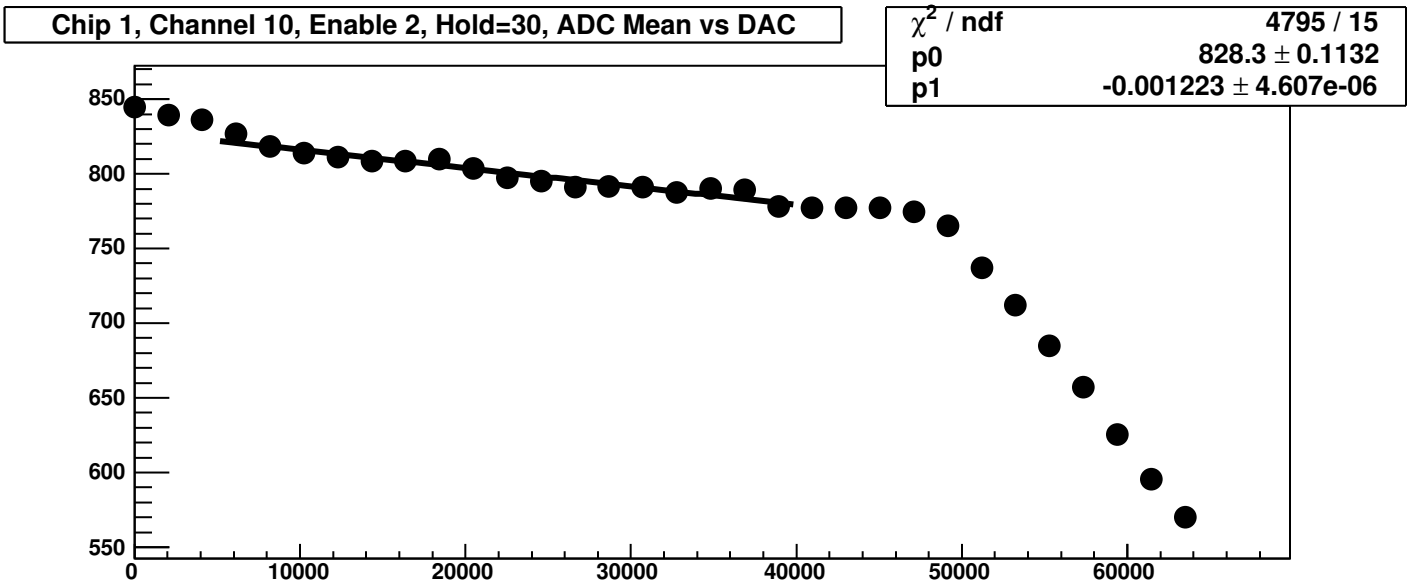


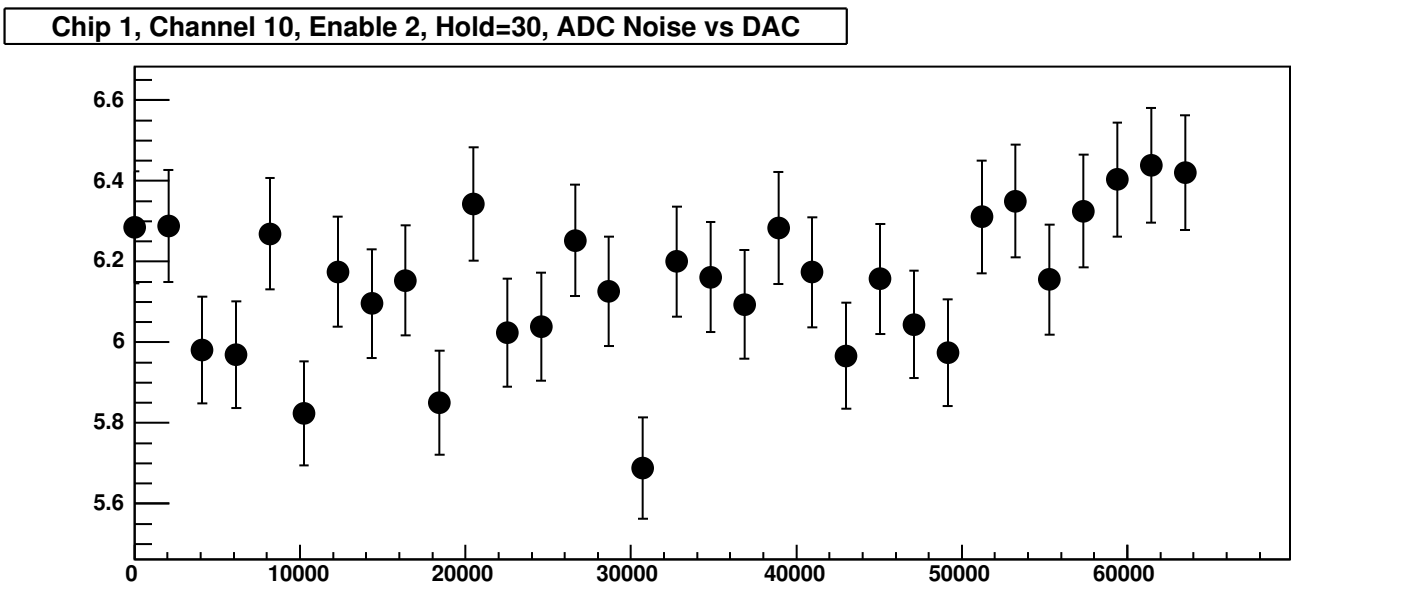
Chip 1, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC

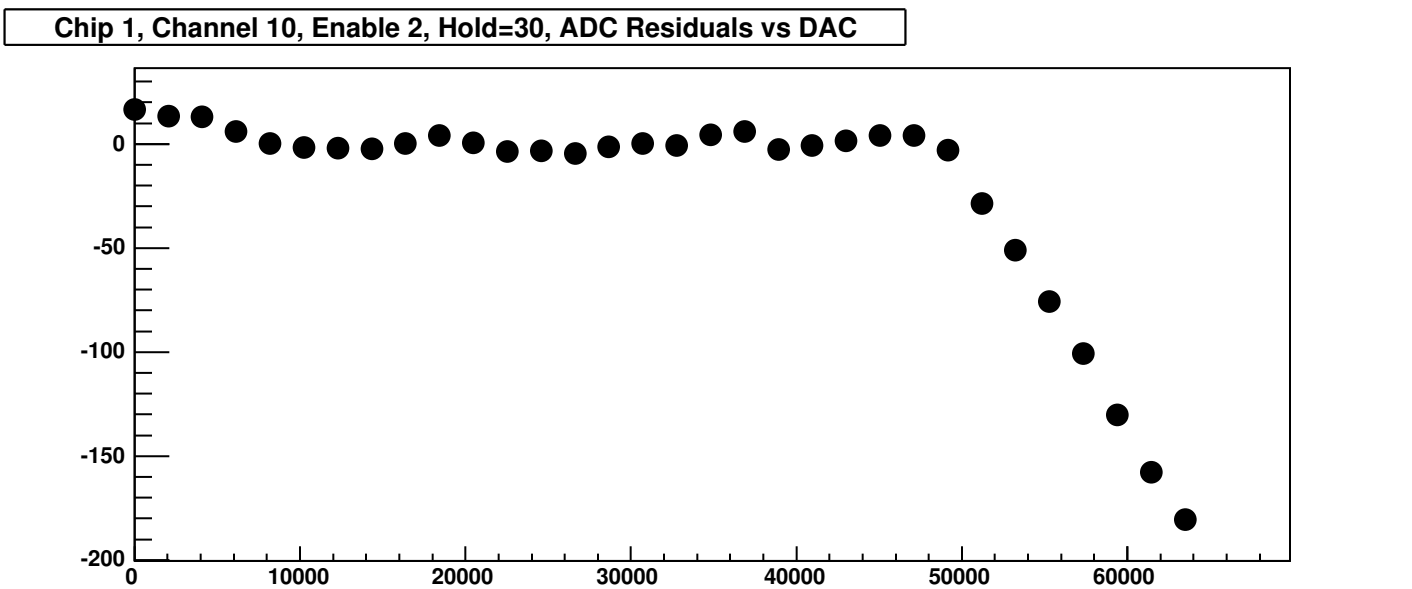


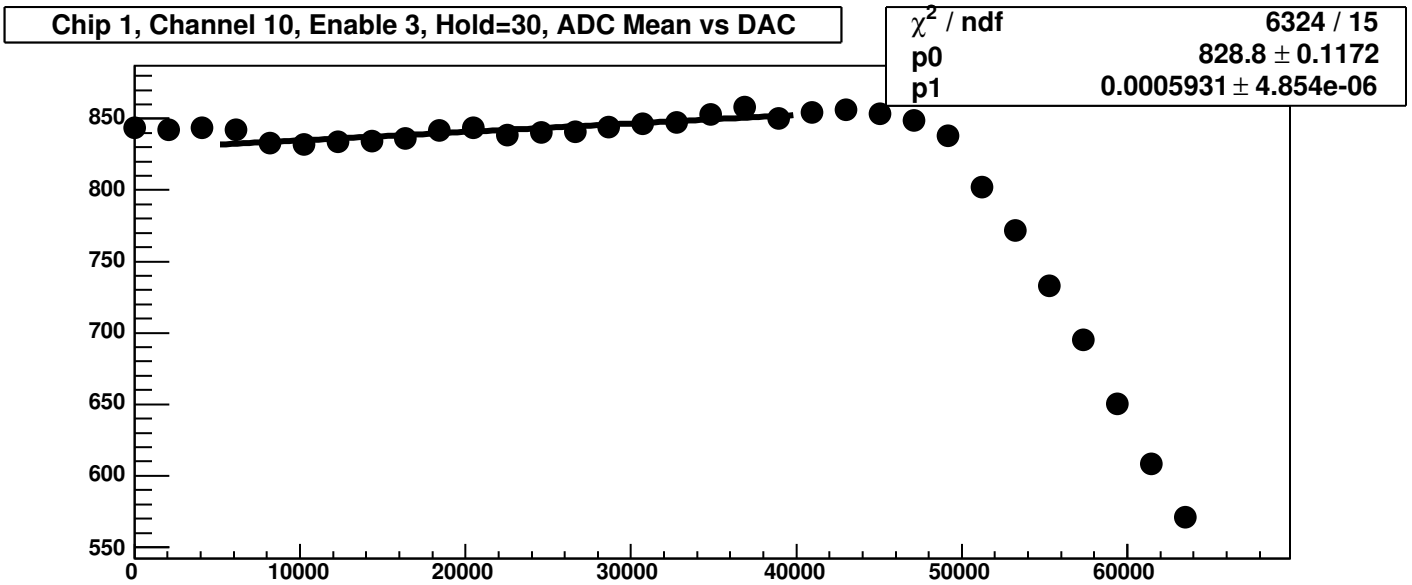
Chip 1, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC

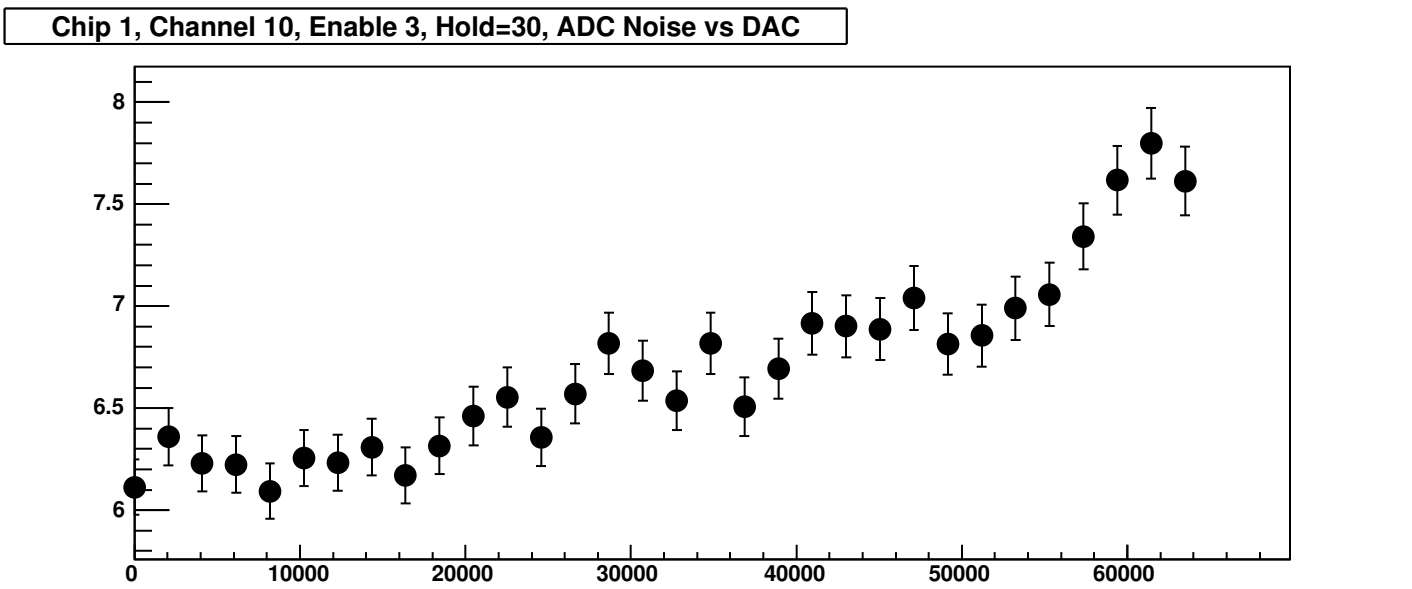


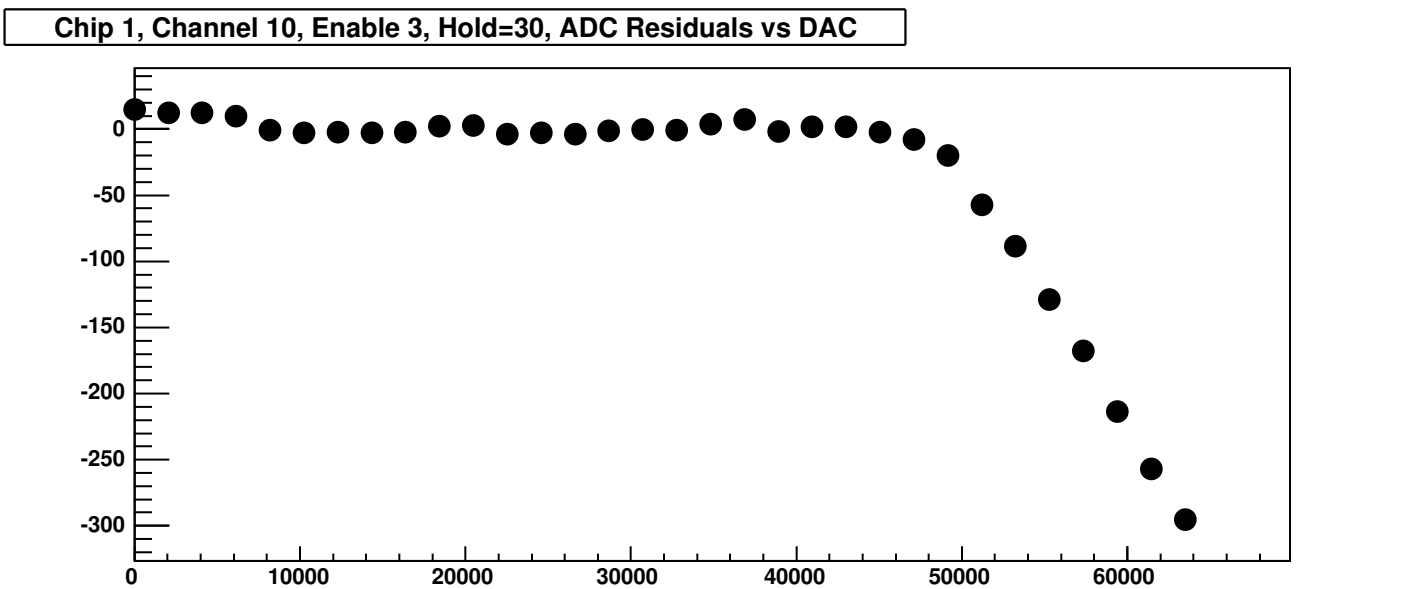

Chip 1, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4795 / 15
p0 828.3 ± 0.1132
p1 -0.001223 ± 4.607e-06
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

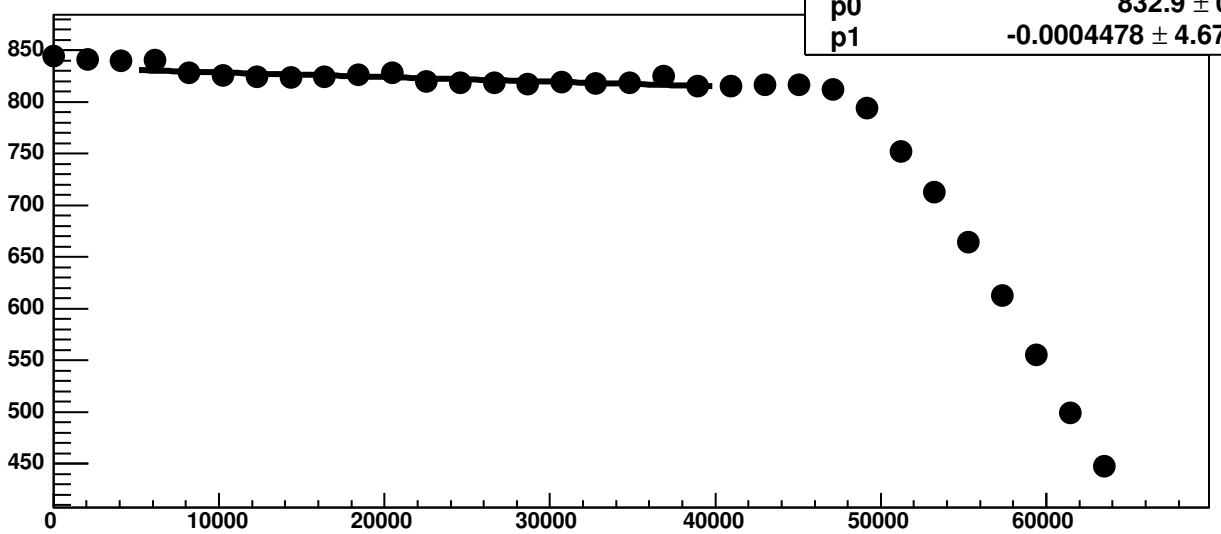

Chip 1, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 6324 / 15
p0 828.8 ± 0.1172
p1 0.0005931 ± 4.854e-06
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC
8
7.5
7
6.5
6
0
10000
20000
30000
40000
50000
60000

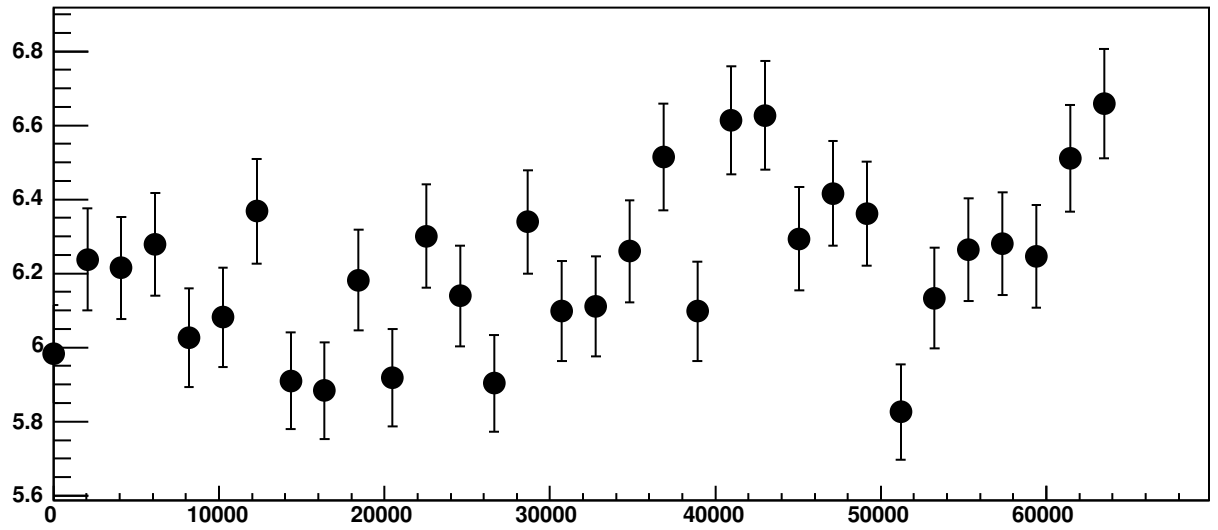

Chip 1, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

Chip 1, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

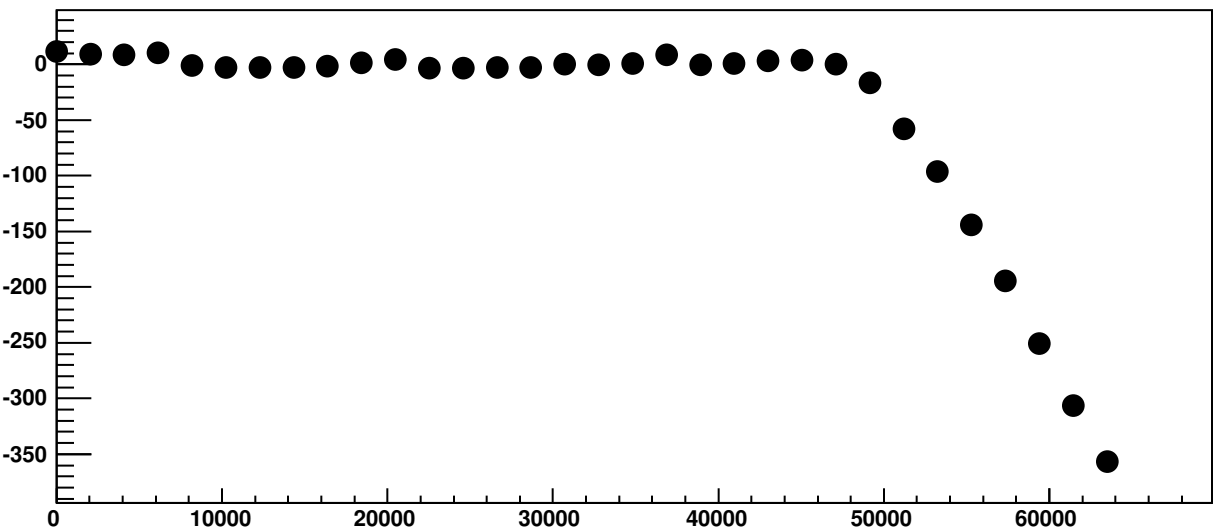
χ^2 / ndf	7011 / 15
p0	832.9 ± 0.1145
p1	-0.0004478 ± 4.671e-06

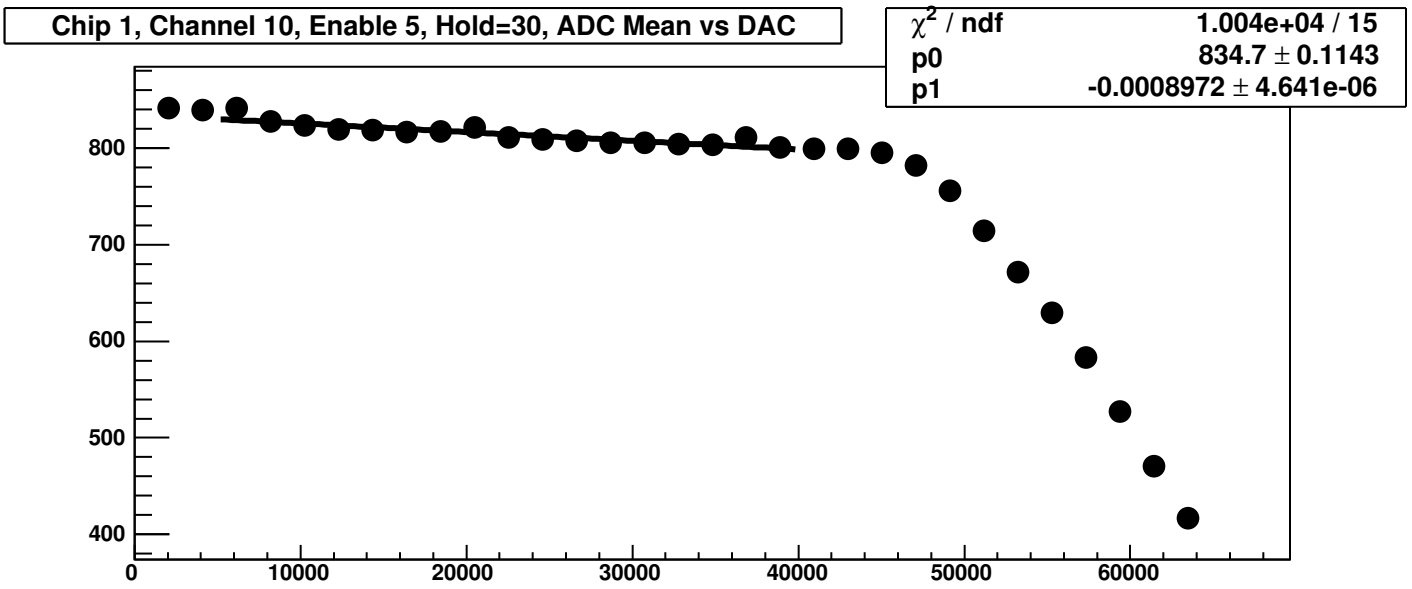


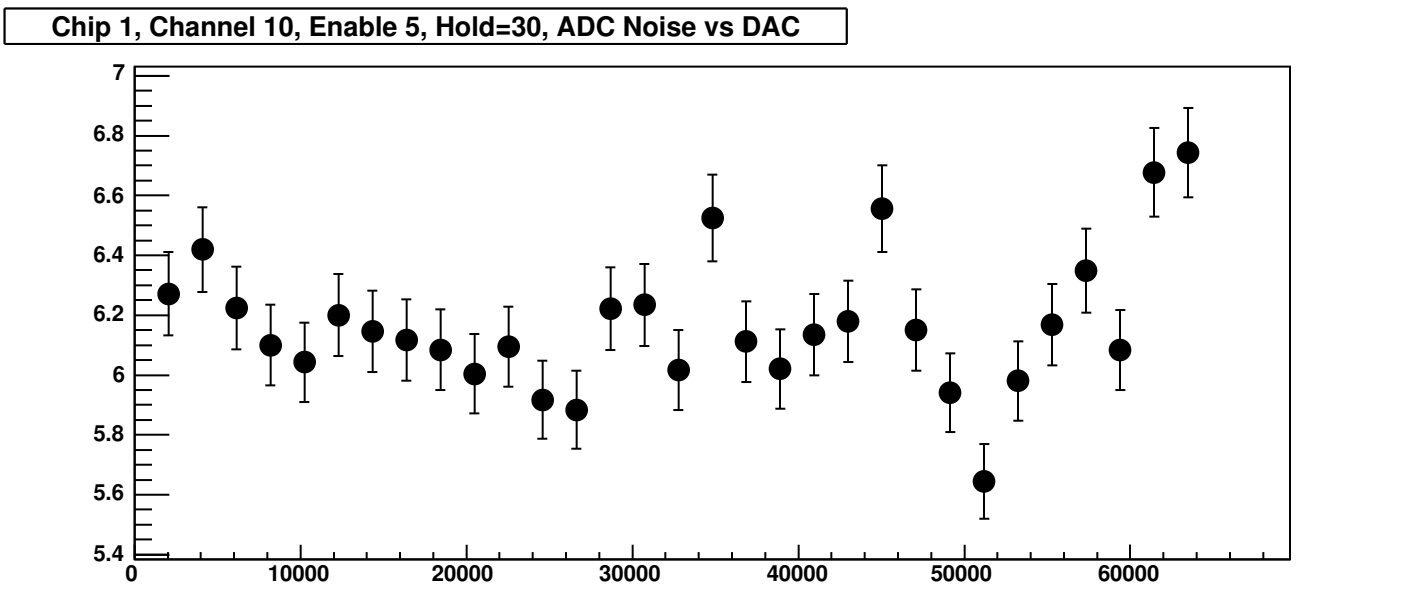
Chip 1, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC

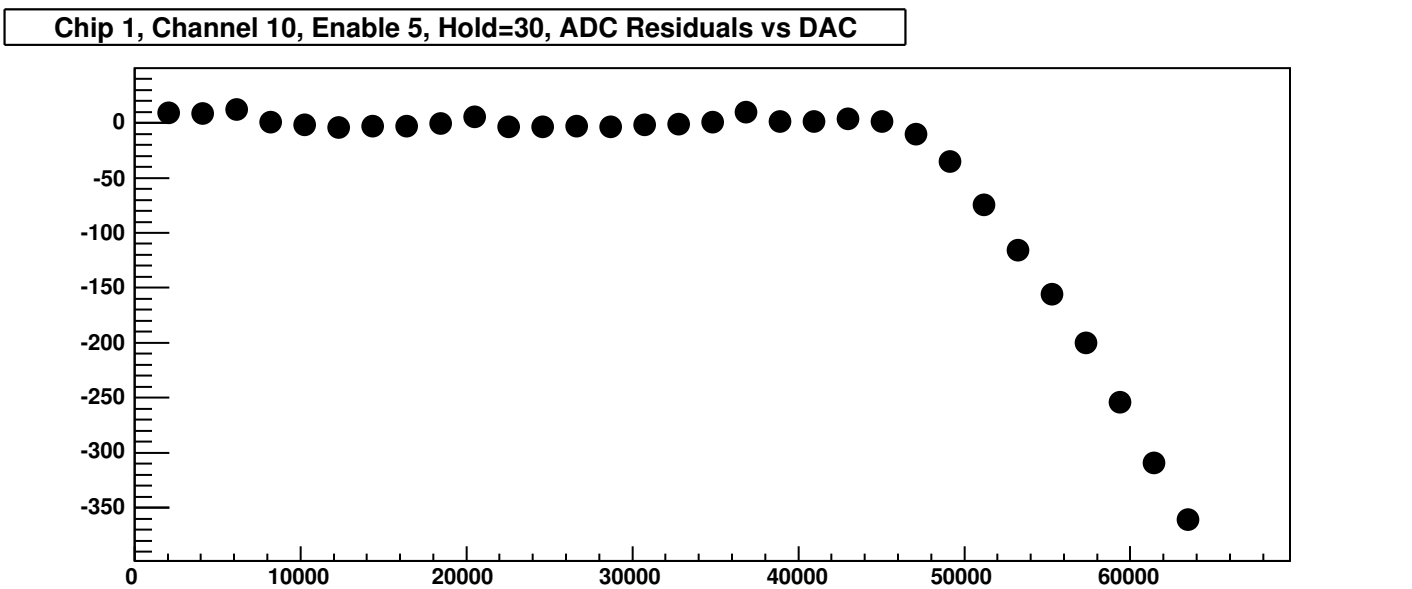


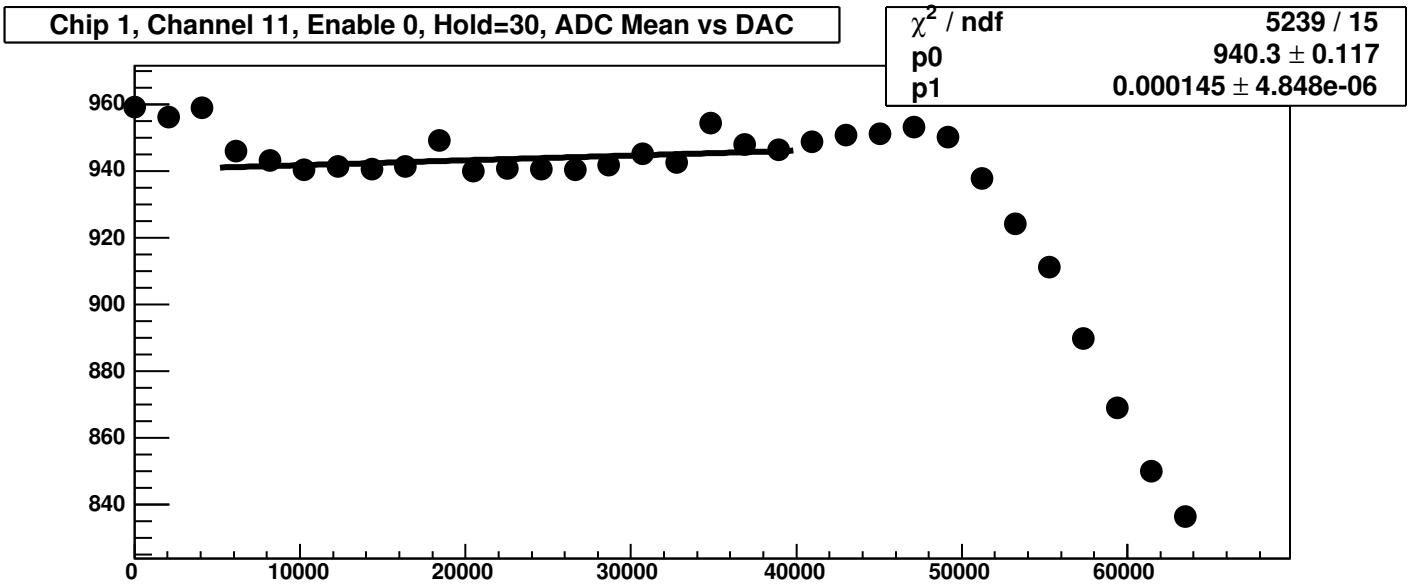
Chip 1, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC

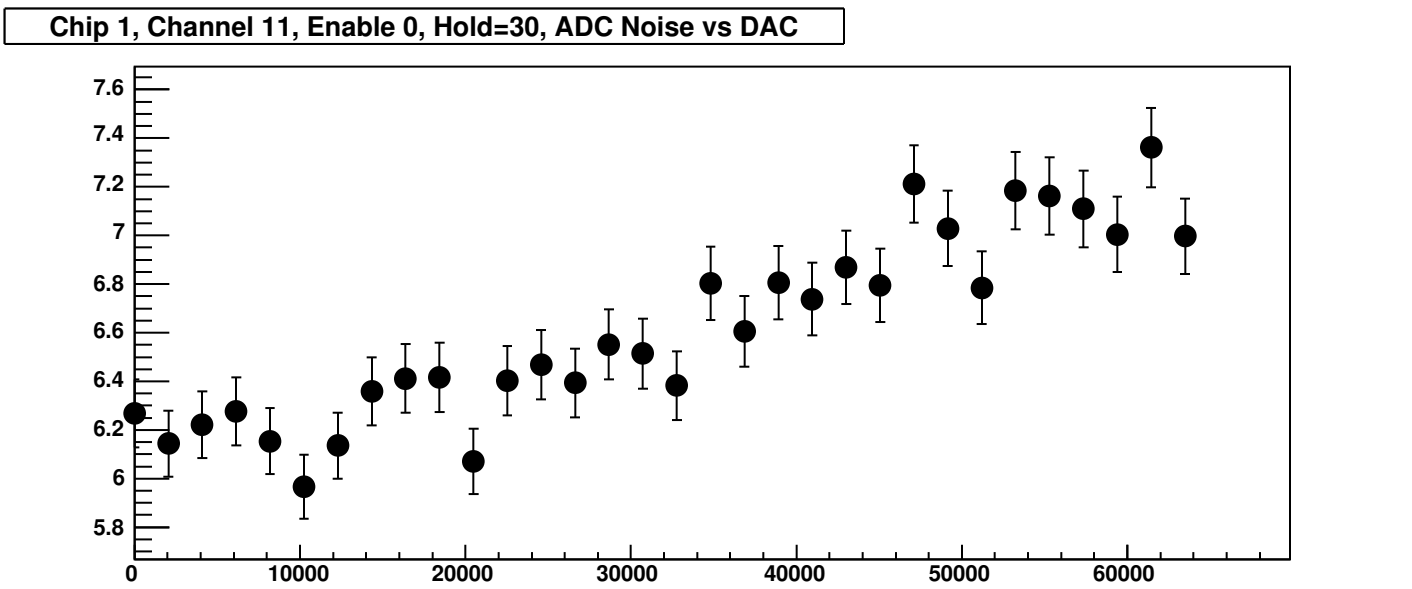


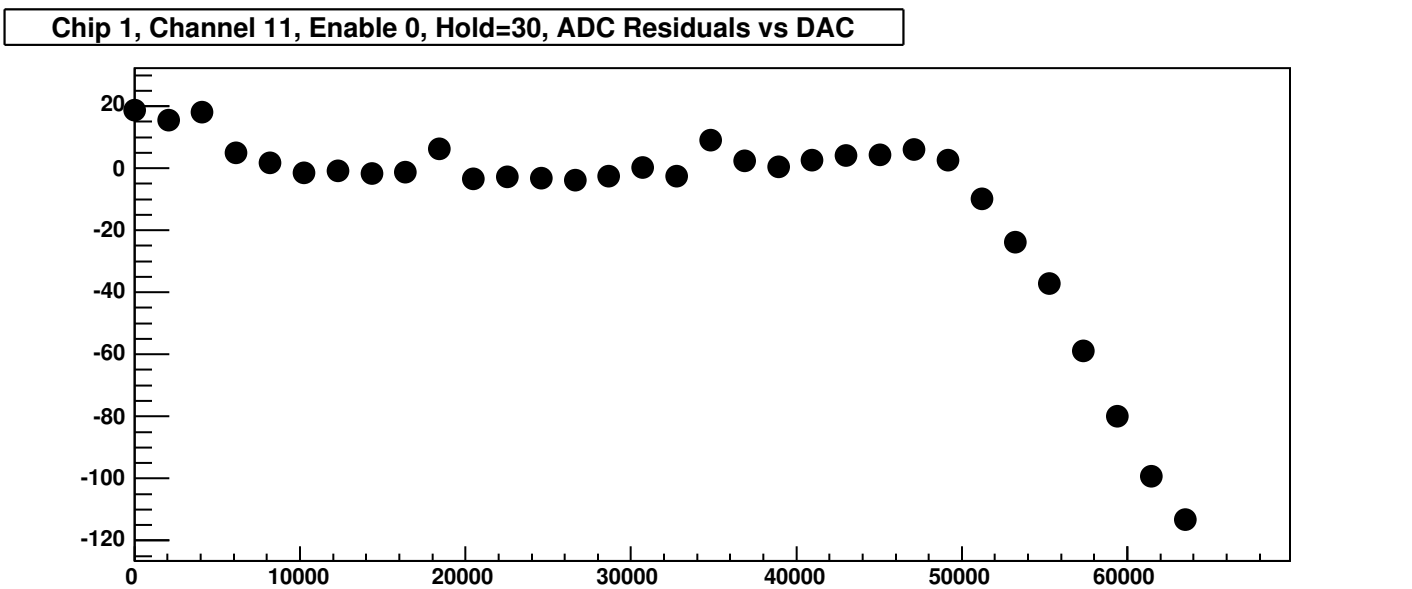

Chip 1, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.004e+04 / 15
p0 834.7 ± 0.1143
p1 -0.0008972 ± 4.641e-06
800
700
600
500
400
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

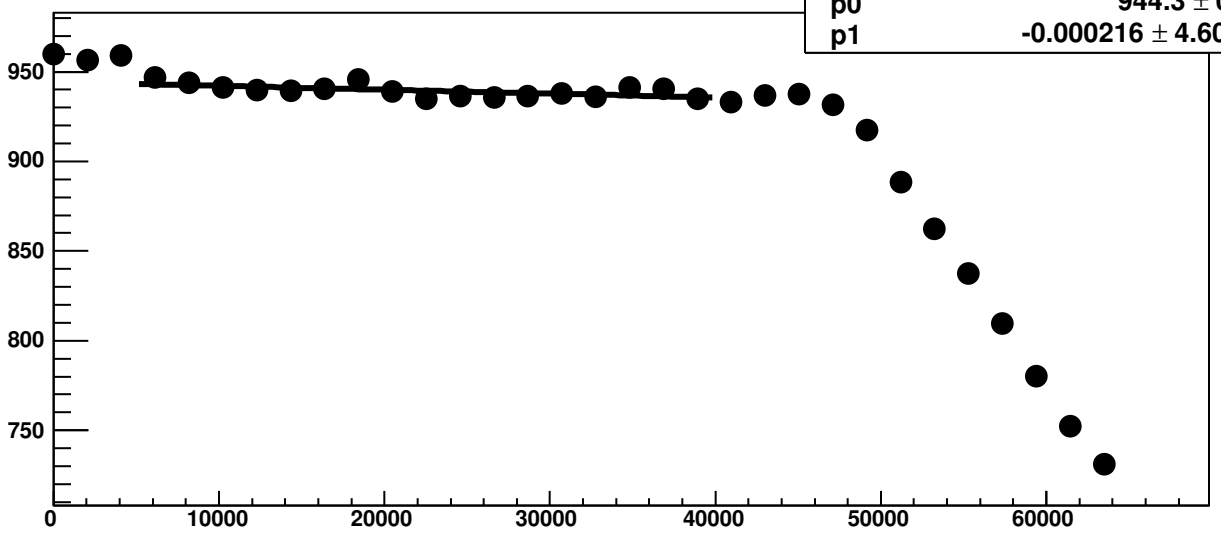

Chip 1, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5239 / 15
p0 940.3 ± 0.117
p1 0.000145 ± 4.848e-06
960
940
920
900
880
860
840
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

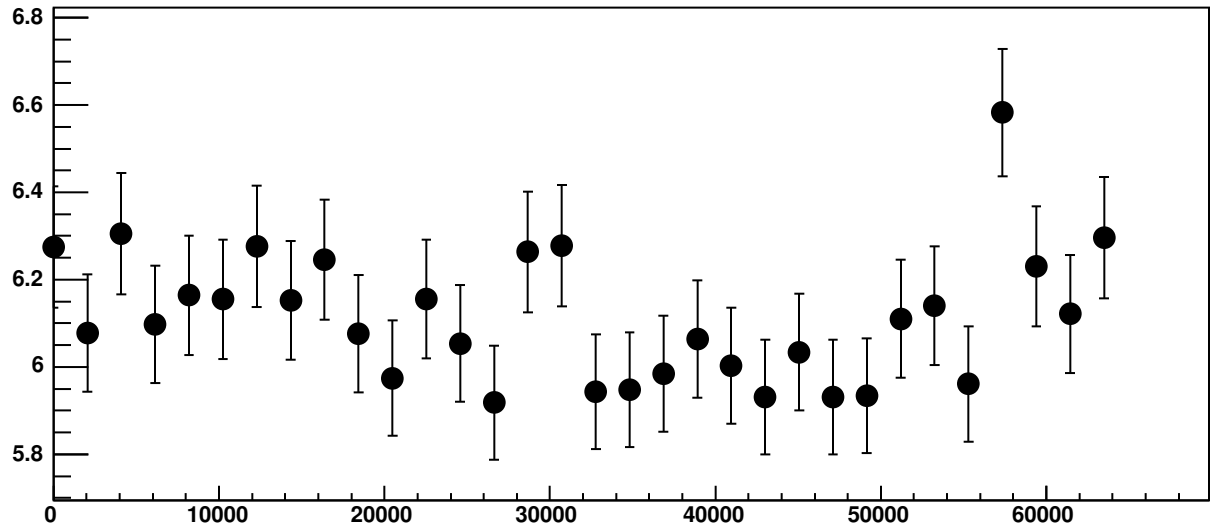

Chip 1, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

Chip 1, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

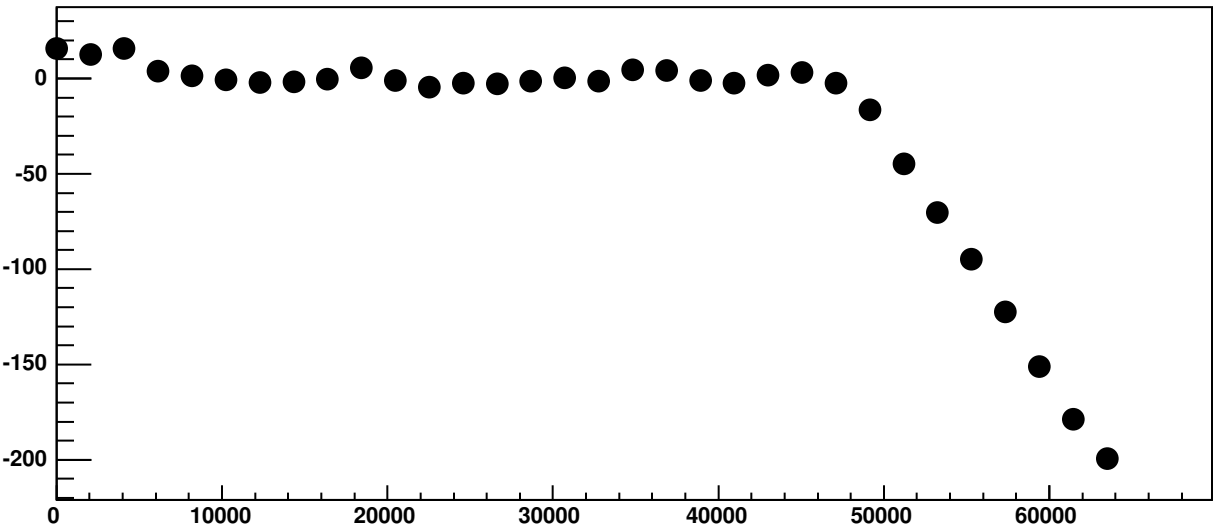
χ^2 / ndf	3781 / 15
p0	944.3 ± 0.1142
p1	-0.000216 ± 4.602e-06



Chip 1, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC

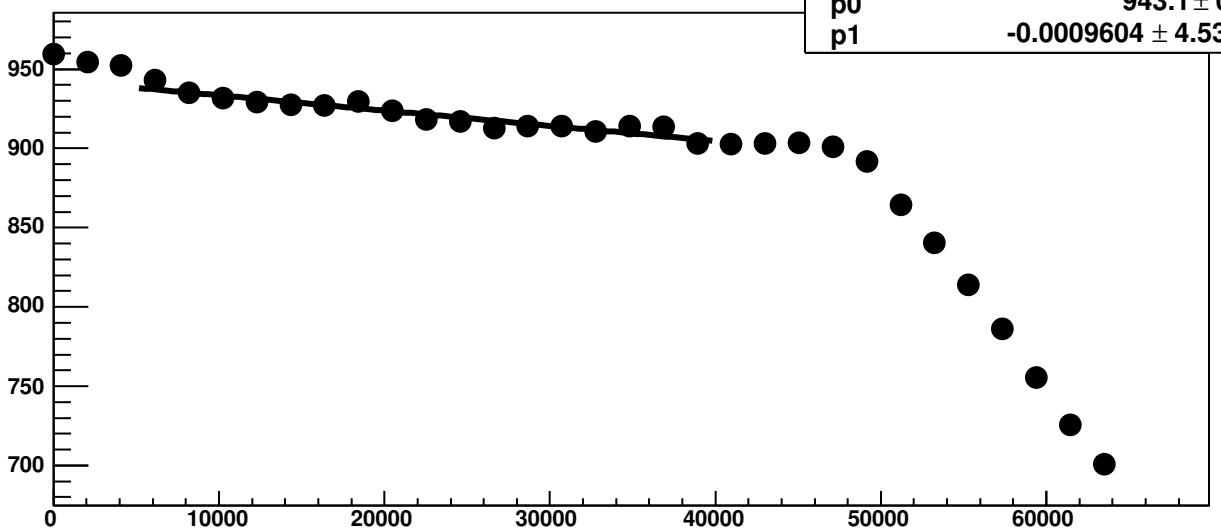


Chip 1, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

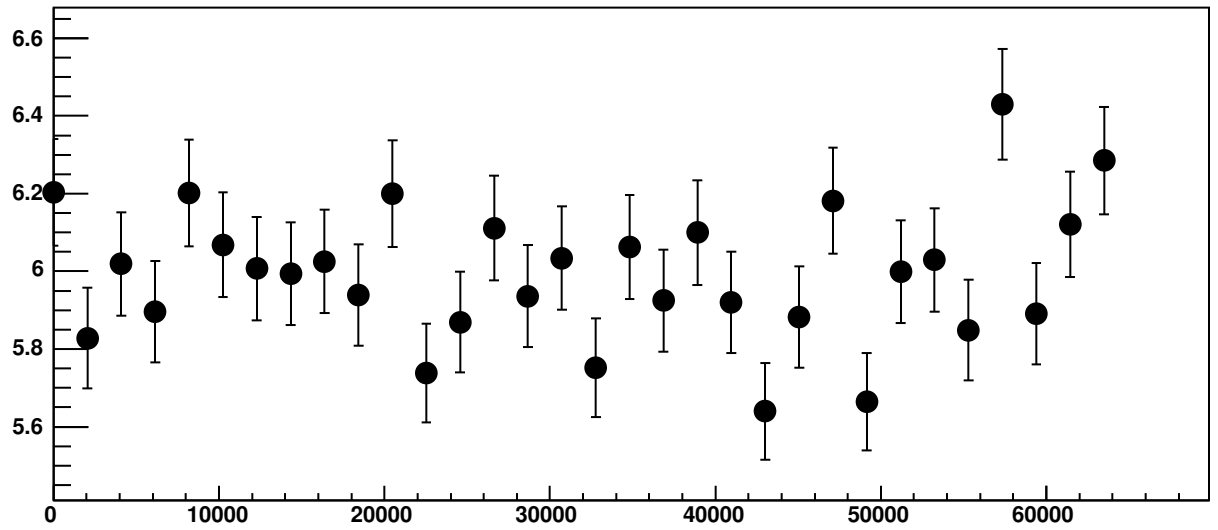


Chip 1, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

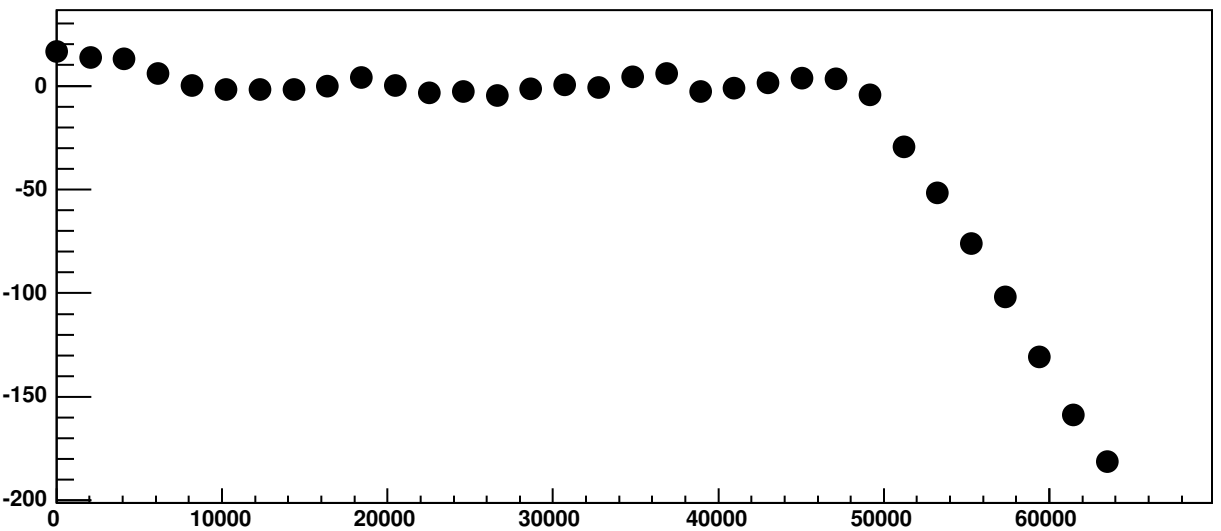
χ^2 / ndf	4774 / 15
p0	943.1 ± 0.1122
p1	-0.0009604 ± 4.539e-06

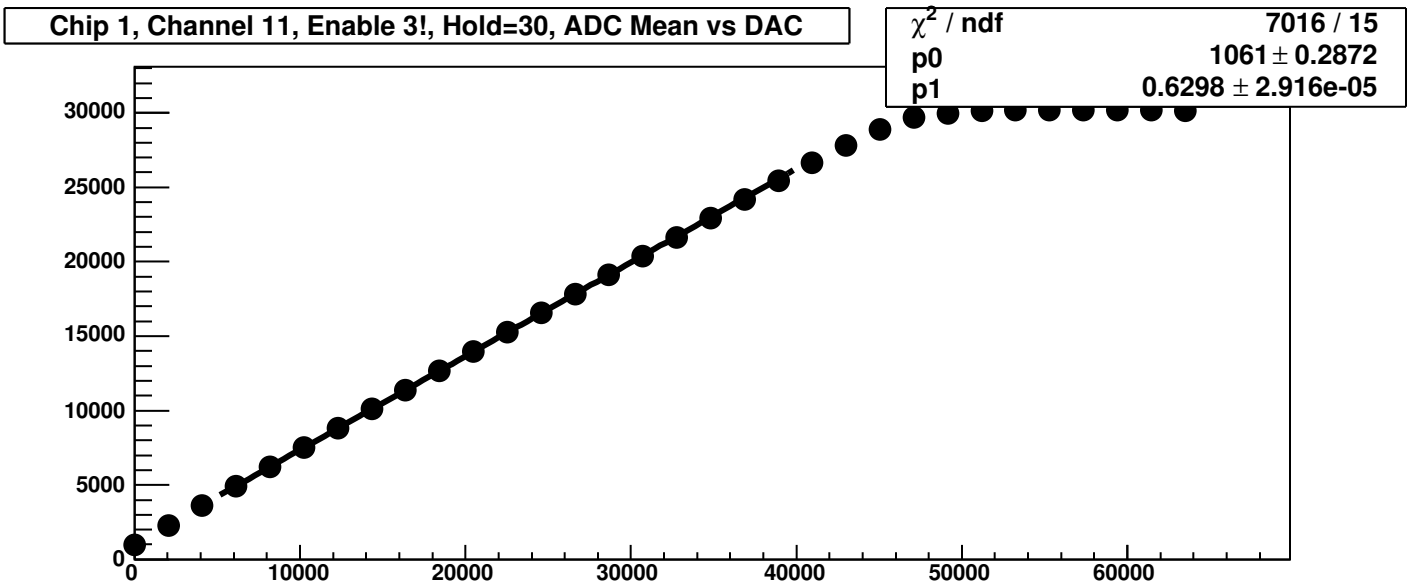


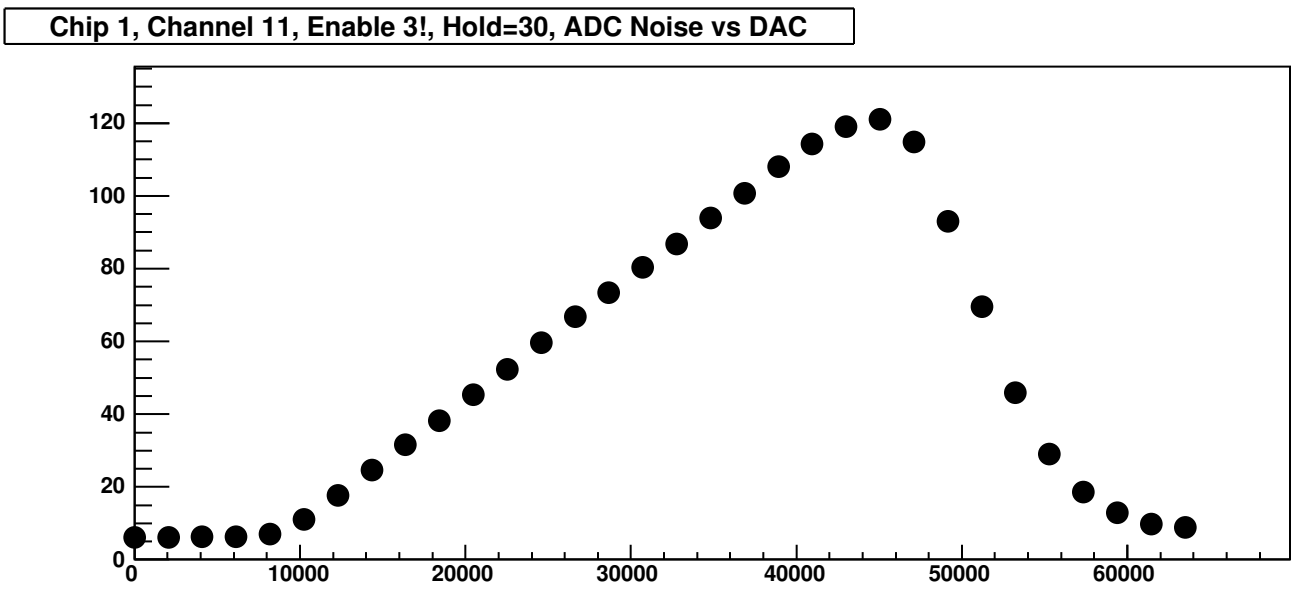
Chip 1, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC

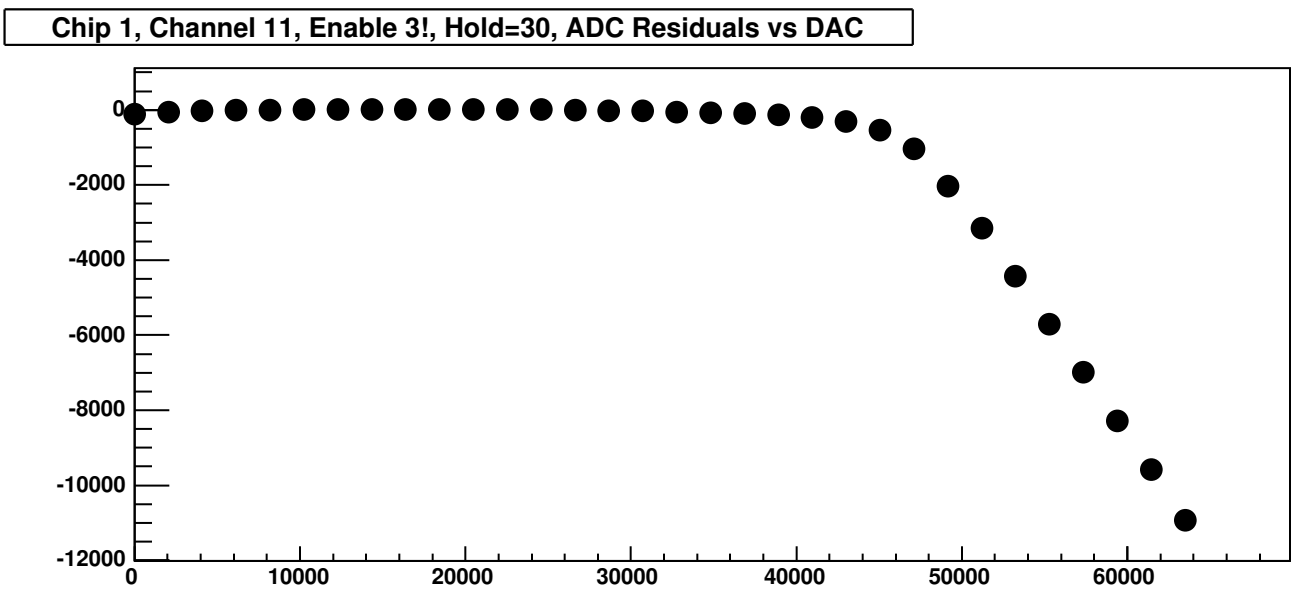


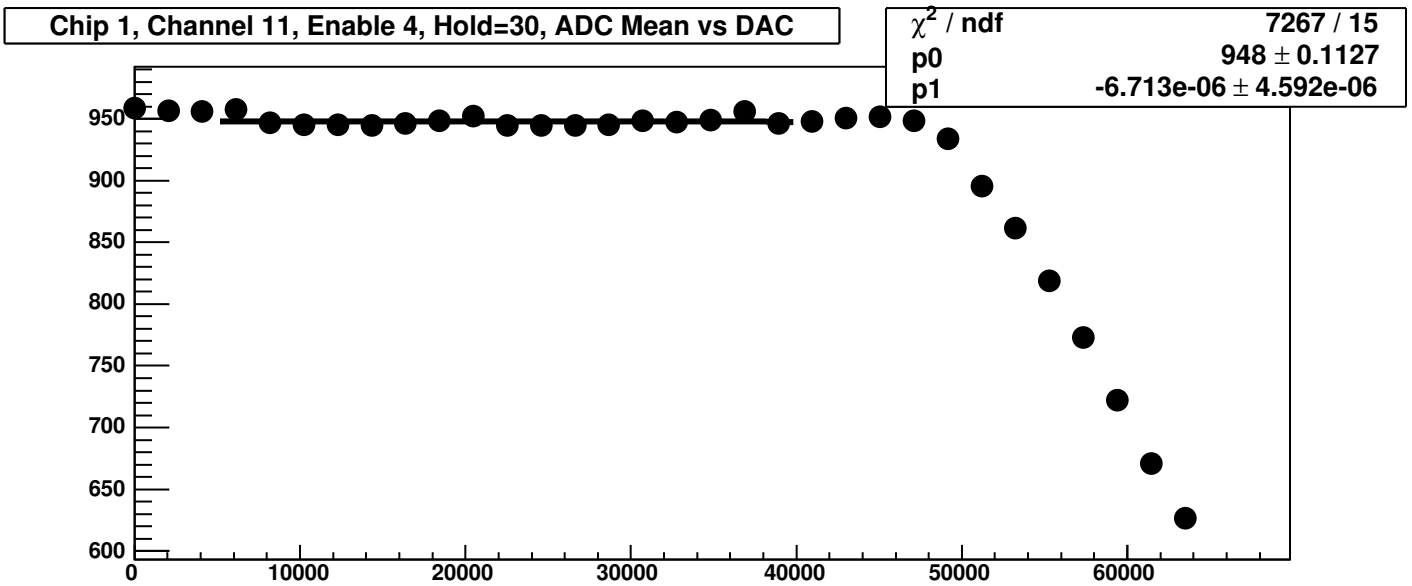
Chip 1, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC

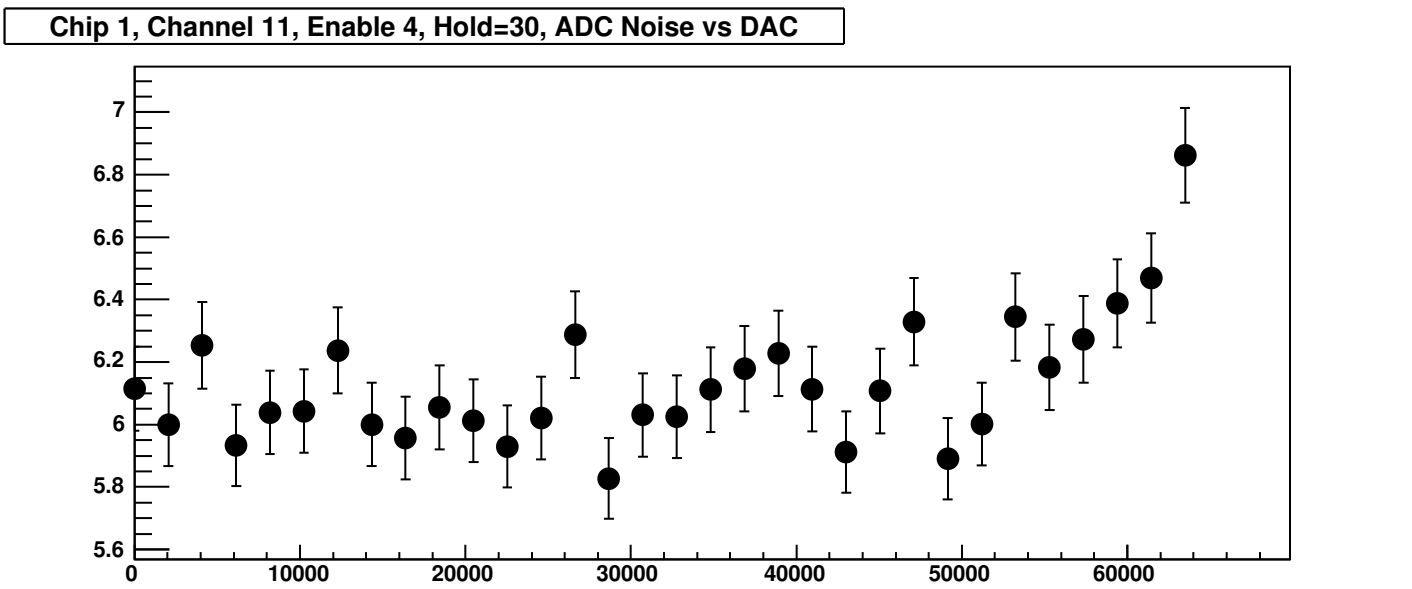


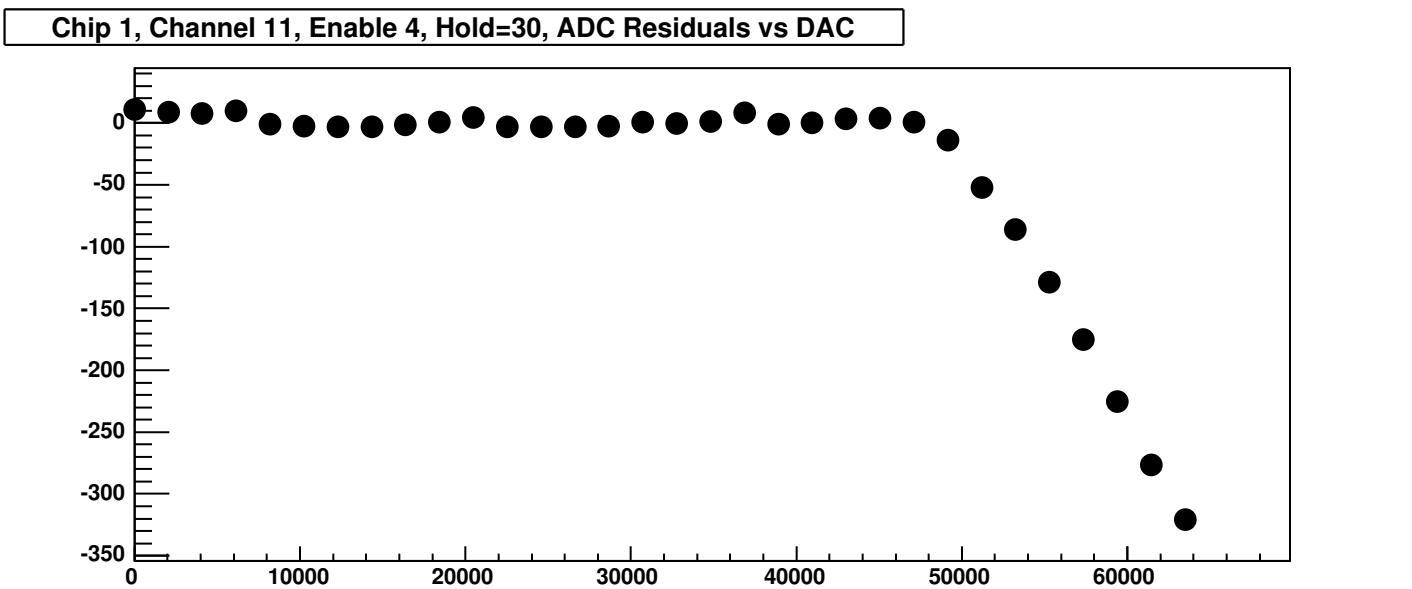

Chip 1, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 7016 / 15
p0 1061 ± 0.2872
p1 0.6298 ± 2.916e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

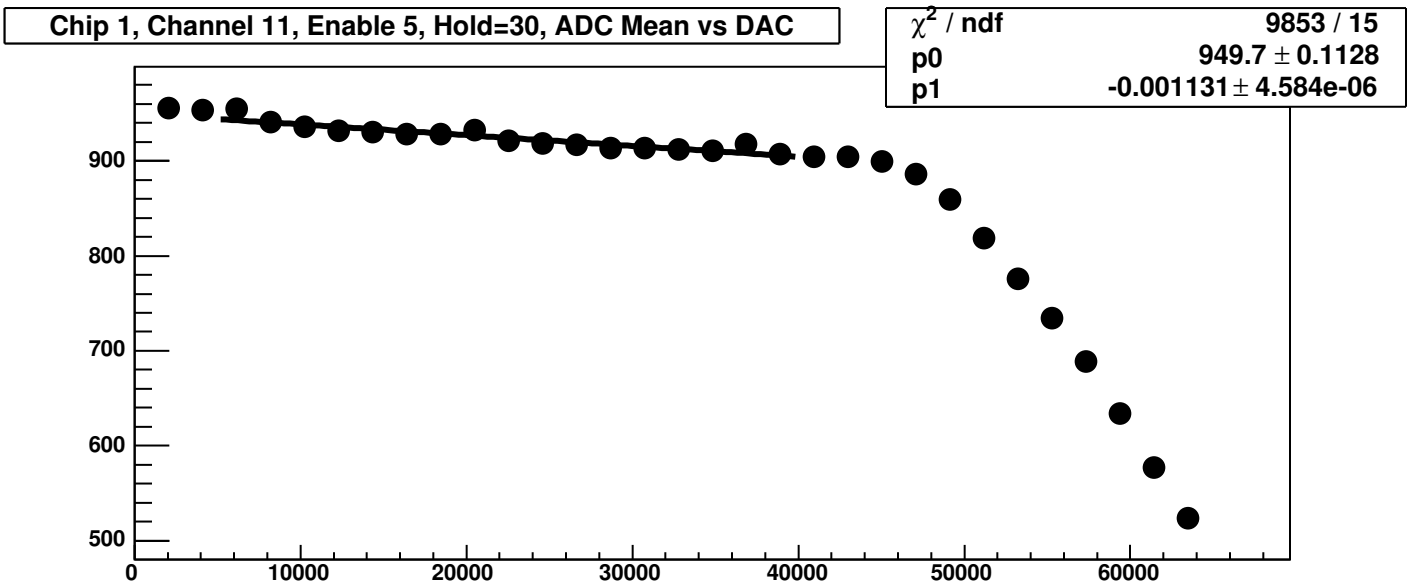

Chip 1, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

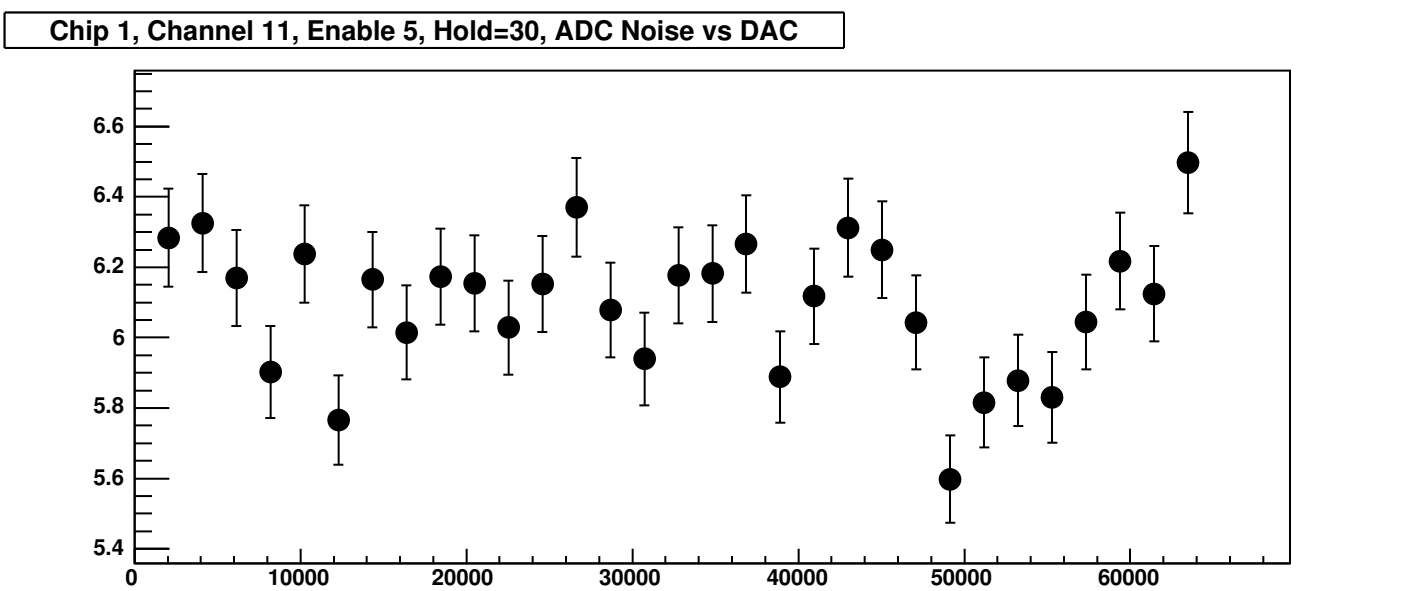

Chip 1, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

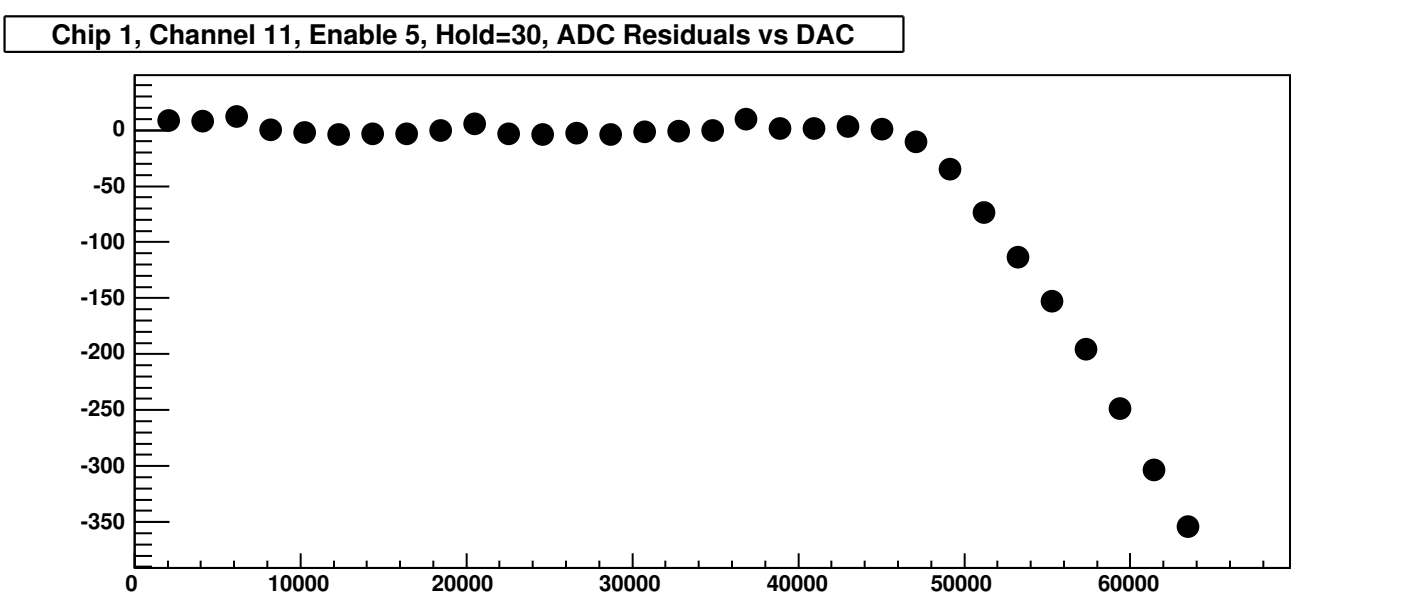

Chip 1, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7267 / 15
p0 948 ± 0.1127
p1 -6.713e-06 ± 4.592e-06
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

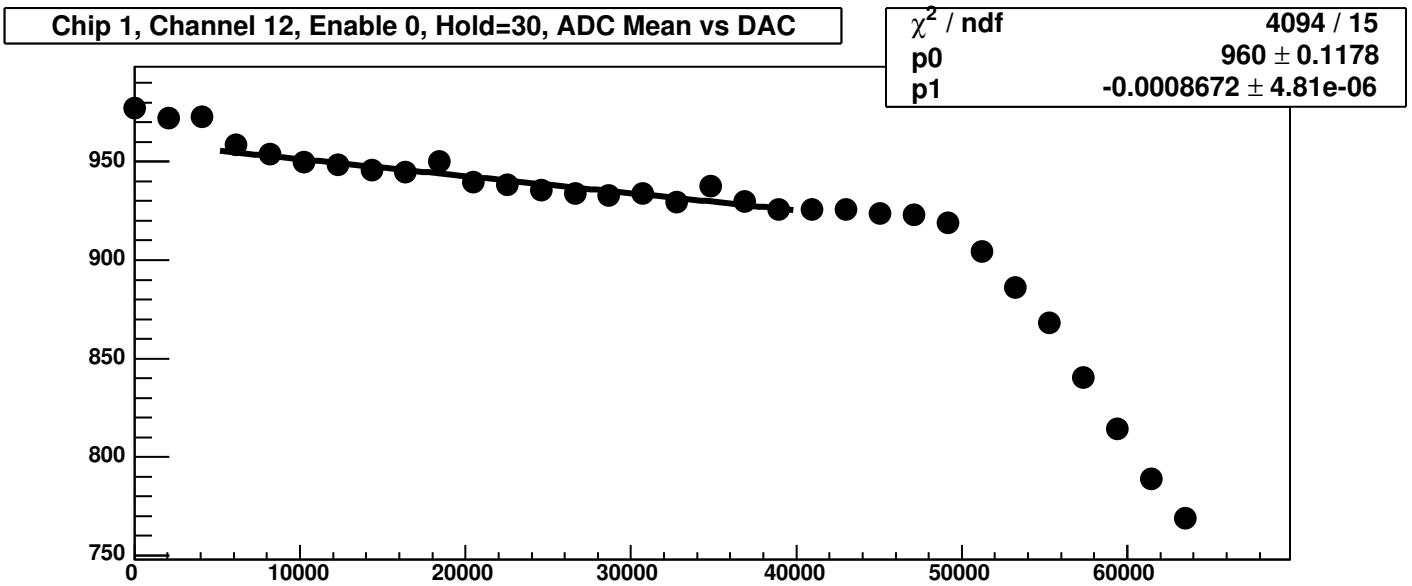

Chip 1, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

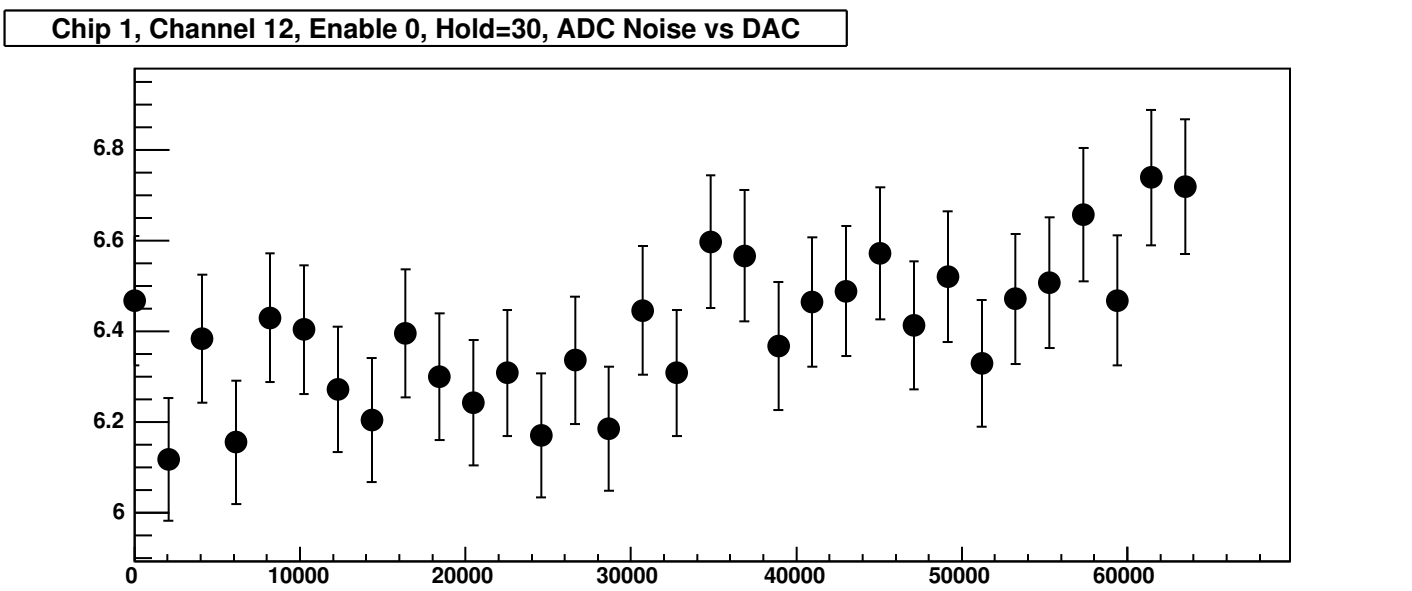

Chip 1, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

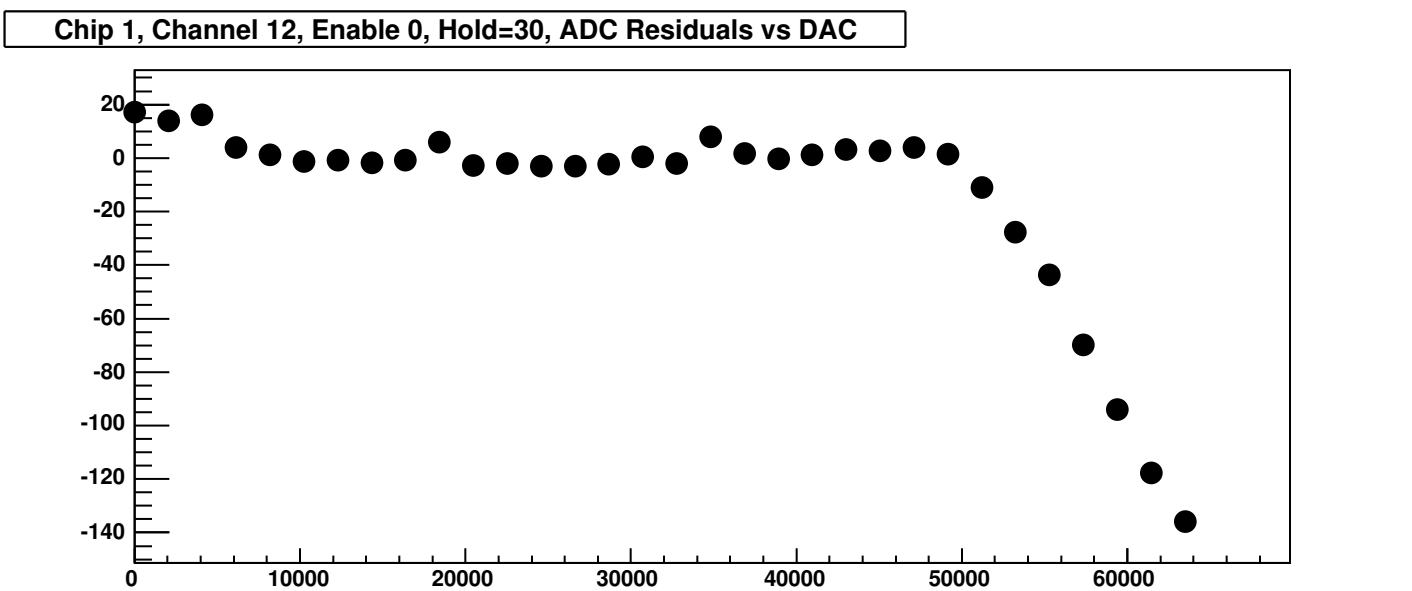

Chip 1, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9853 / 15
p0 949.7 ± 0.1128
p1 -0.001131 ± 4.584e-06
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

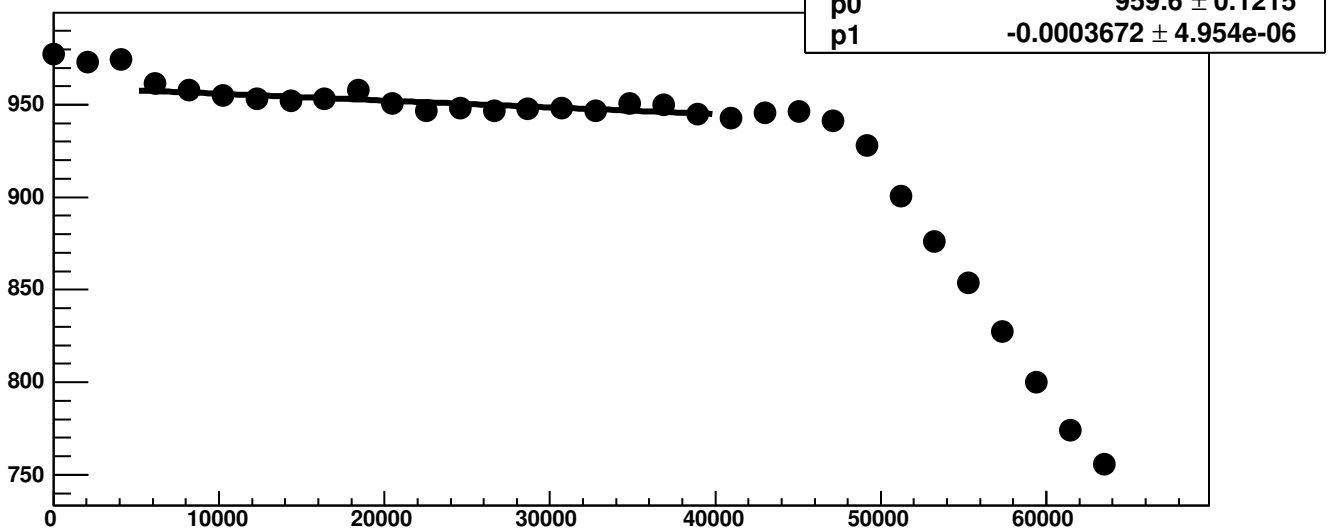

Chip 1, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4094 / 15
p0 960 ± 0.1178
p1 -0.0008672 ± 4.81e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

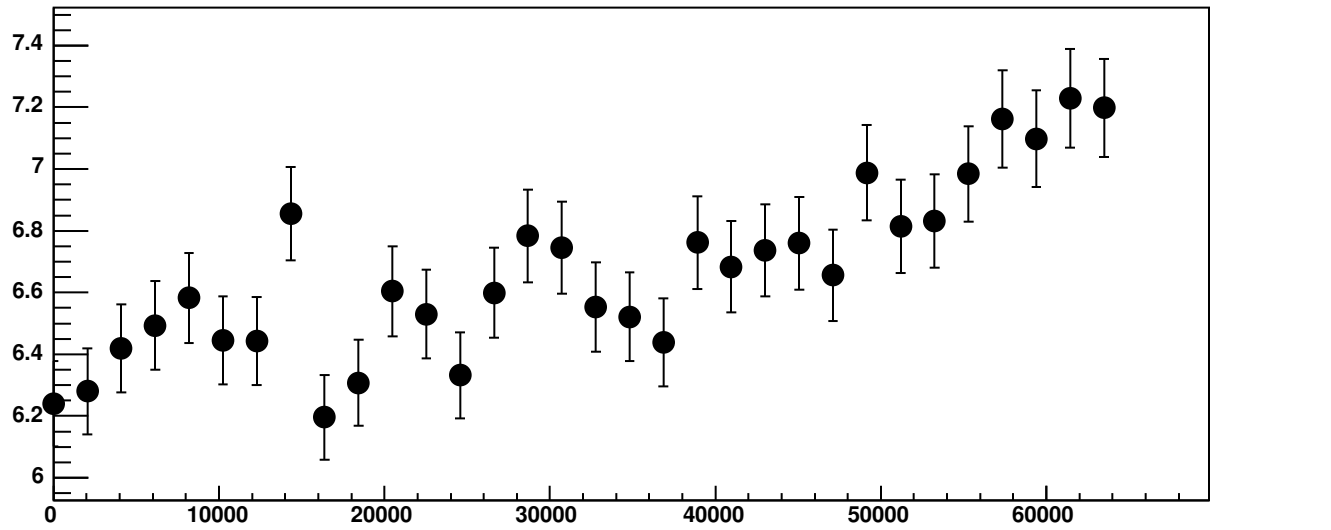

Chip 1, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

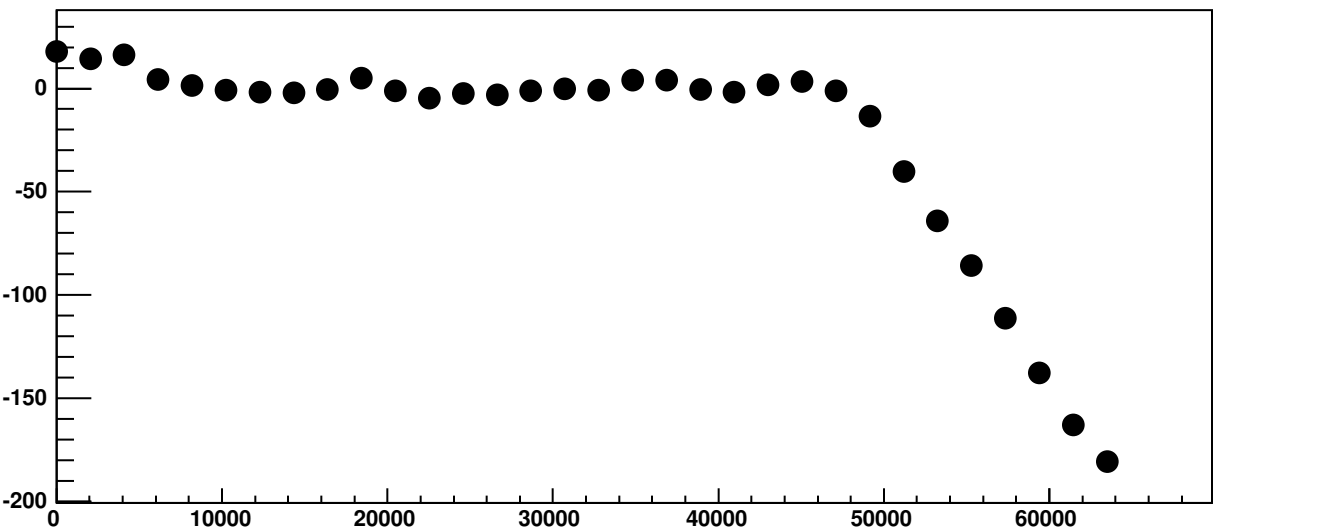
Chip 1, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC

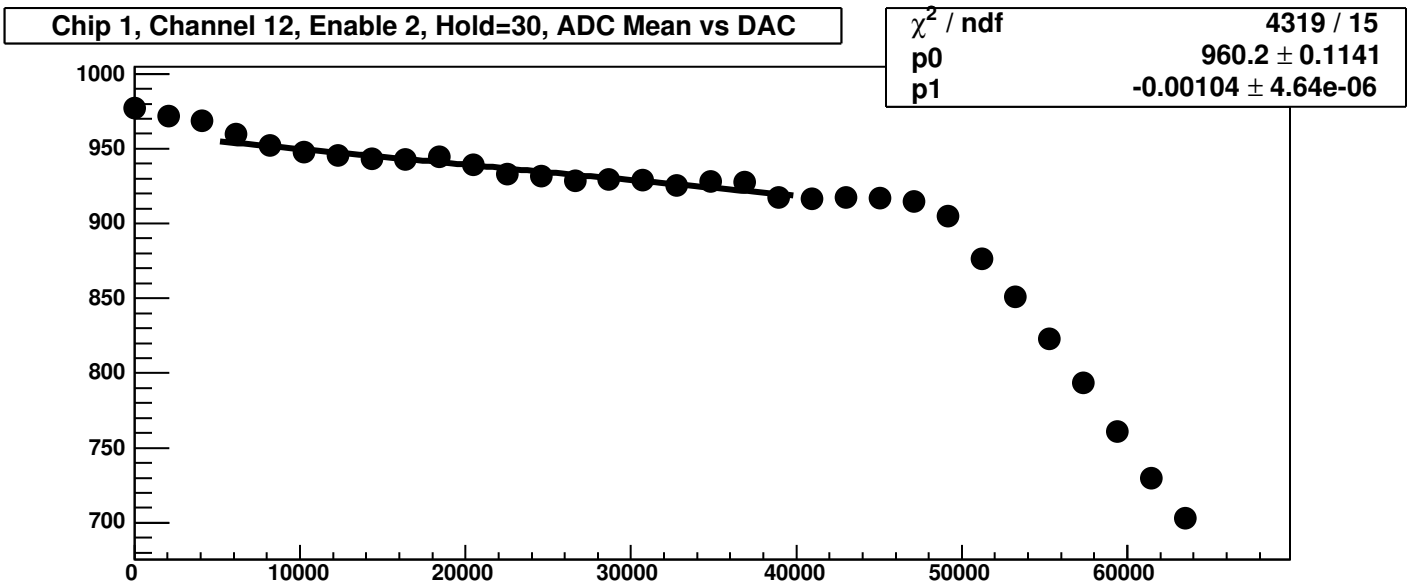


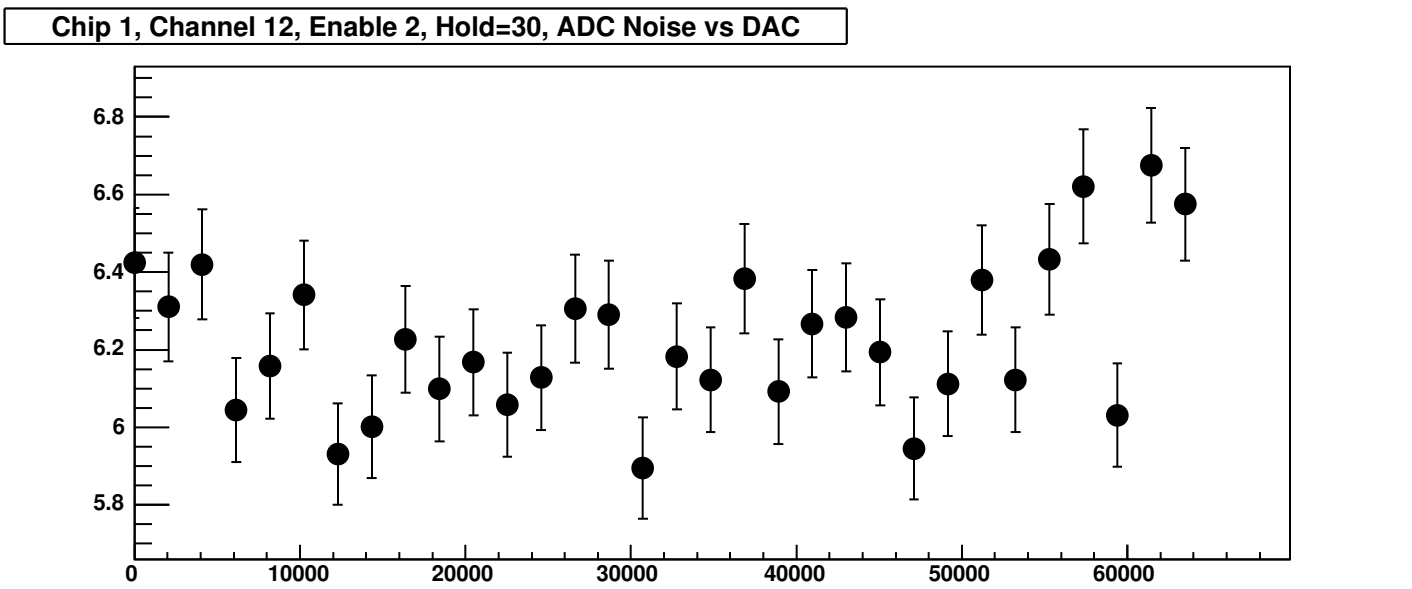
Chip 1, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC

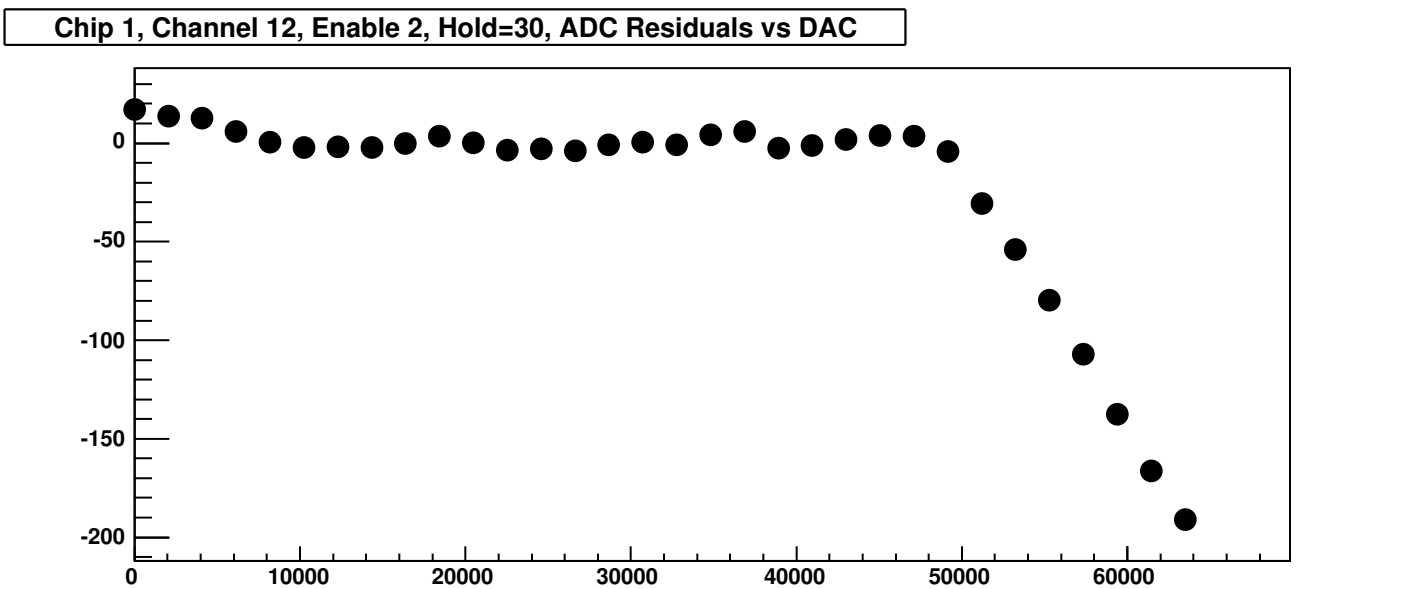


Chip 1, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC



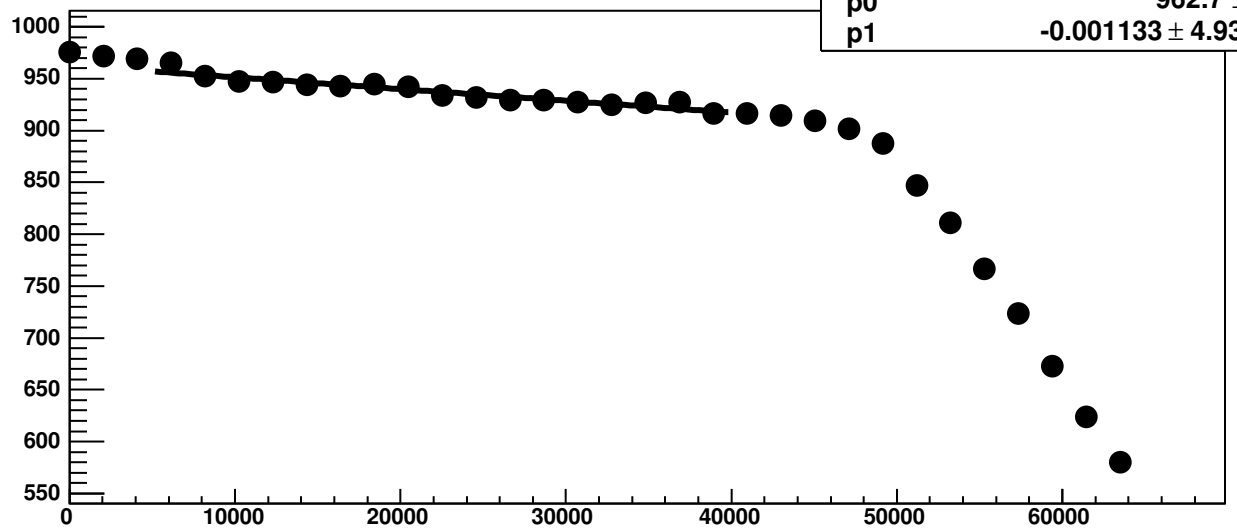

Chip 1, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4319 / 15
p0 960.2 ± 0.1141
p1 -0.00104 ± 4.64e-06
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

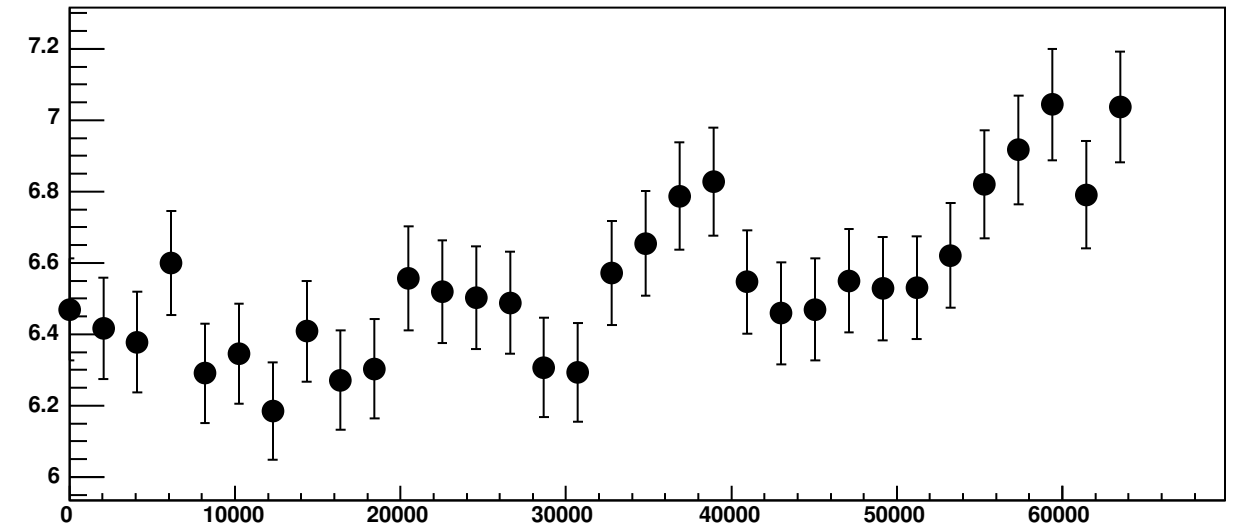

Chip 1, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

Chip 1, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

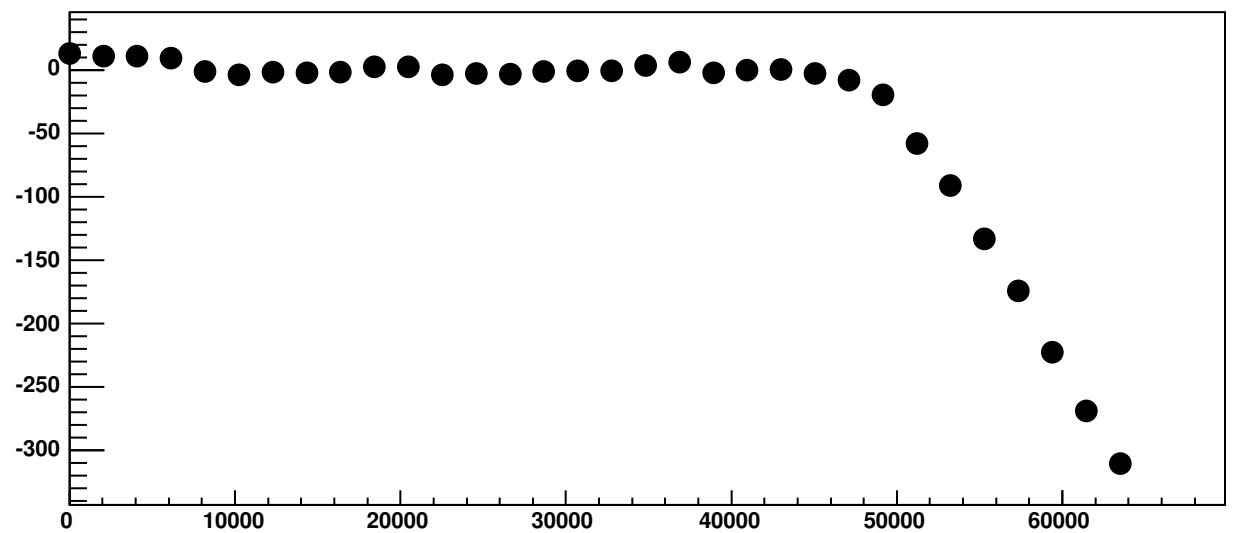
χ^2 / ndf	5115 / 15
p0	962.7 ± 0.12
p1	-0.001133 ± 4.934e-06

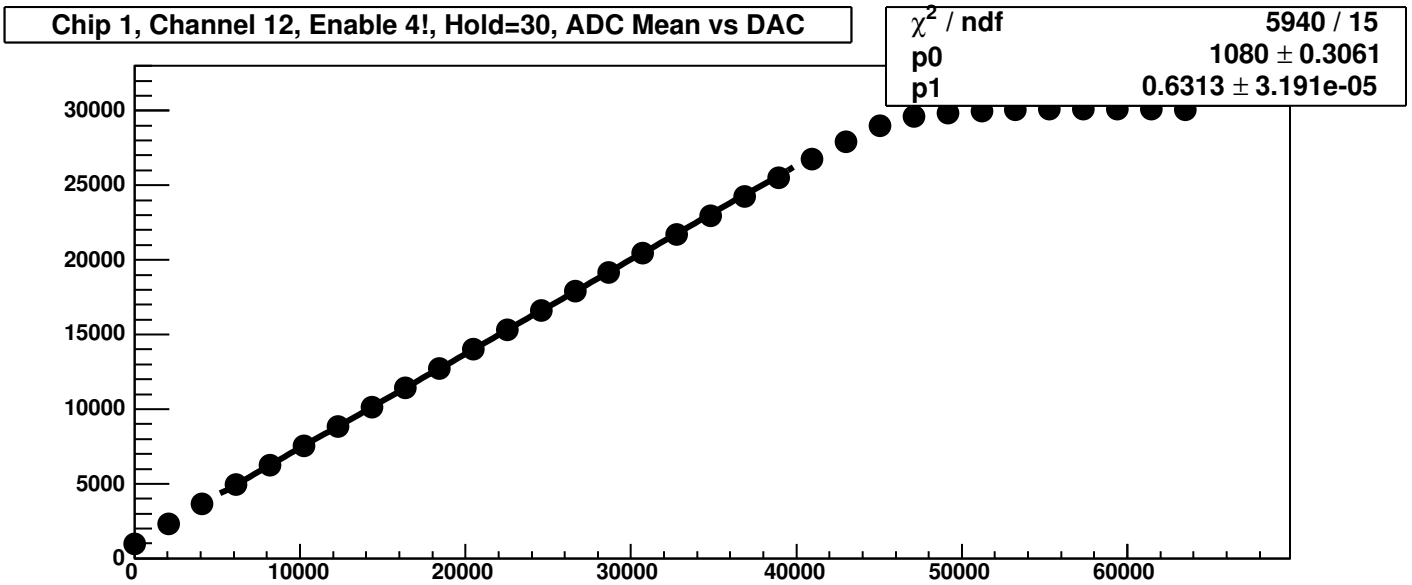


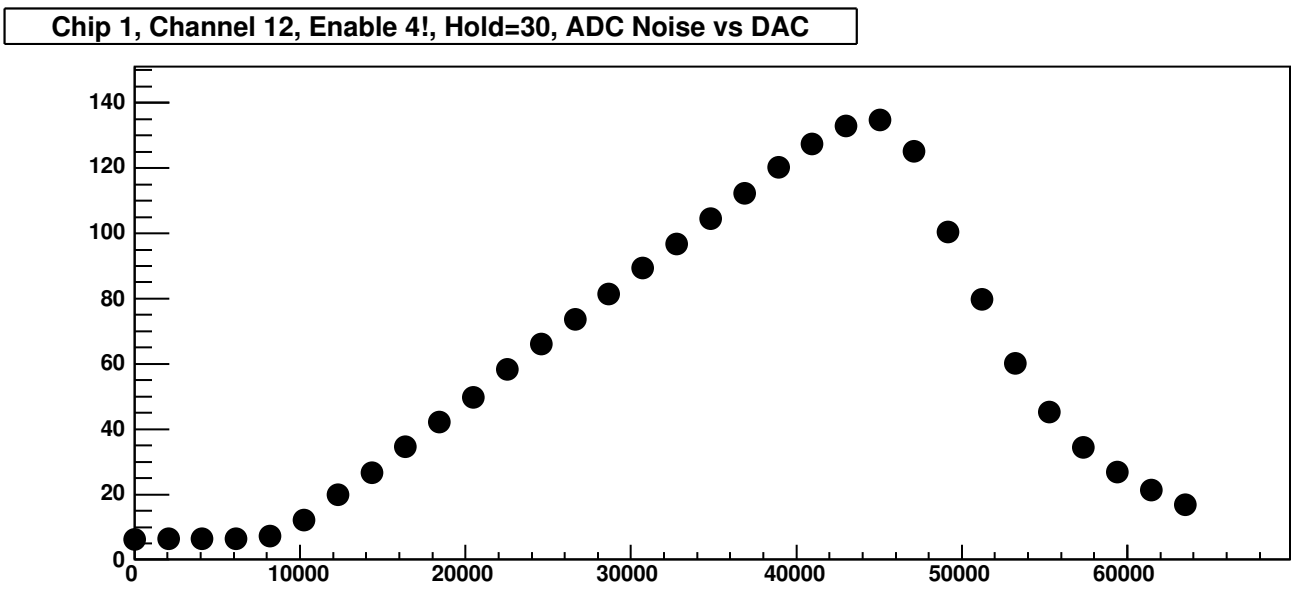
Chip 1, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC

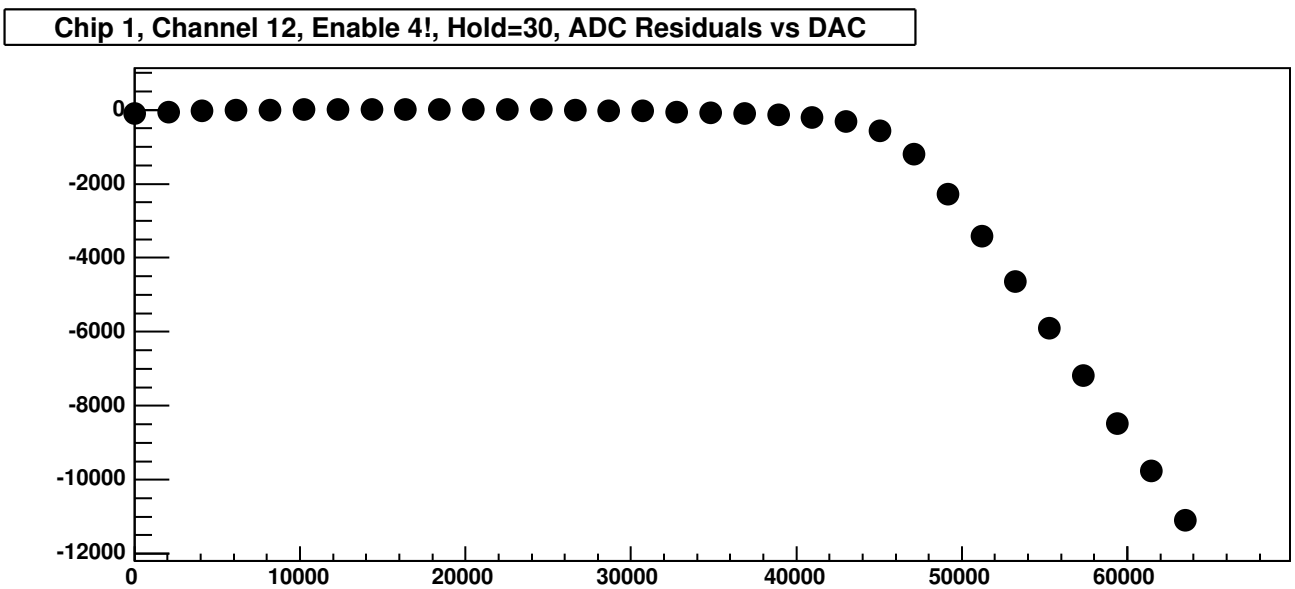


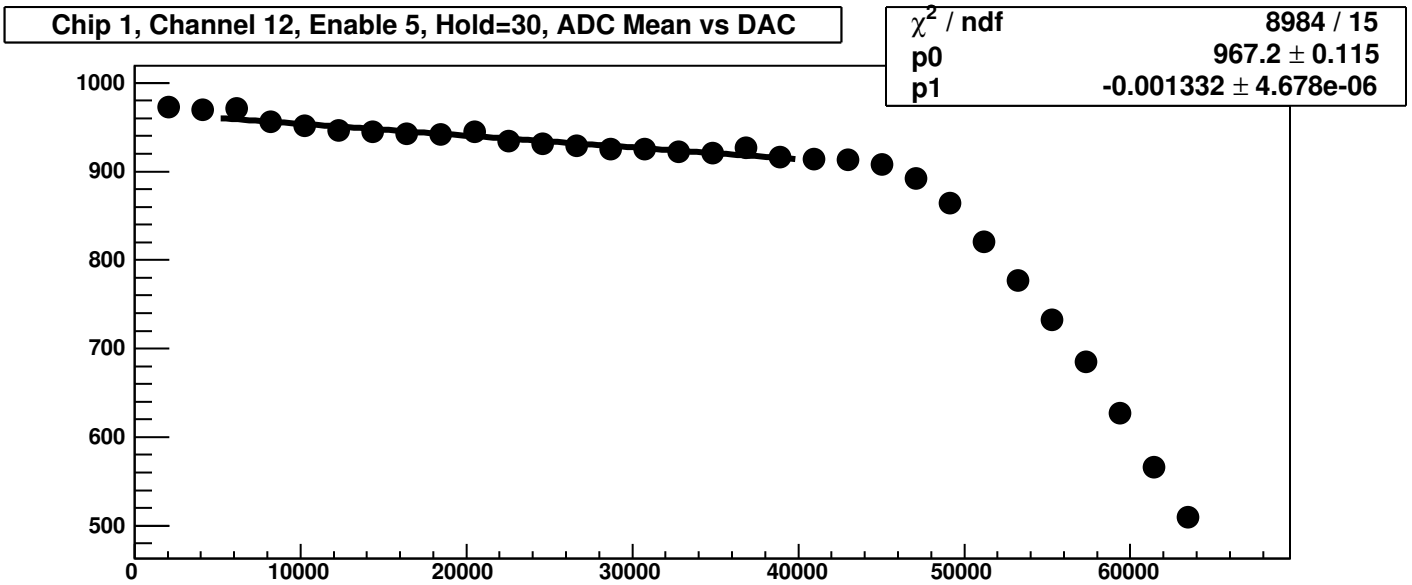
Chip 1, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

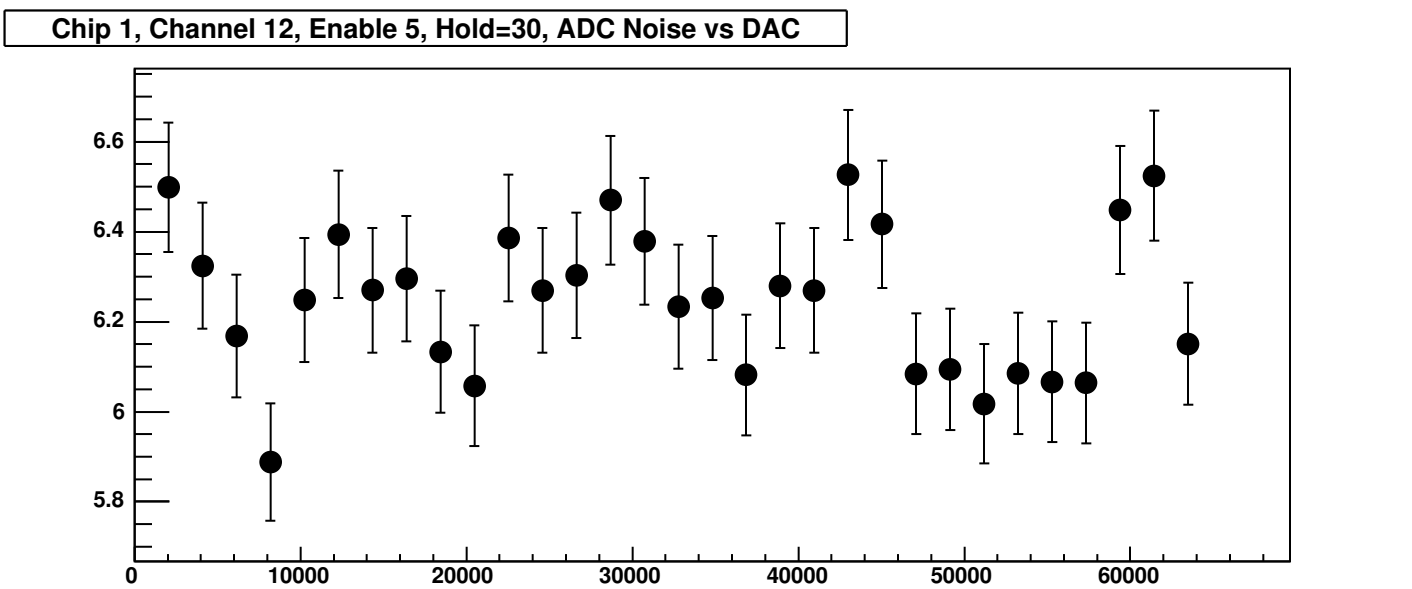


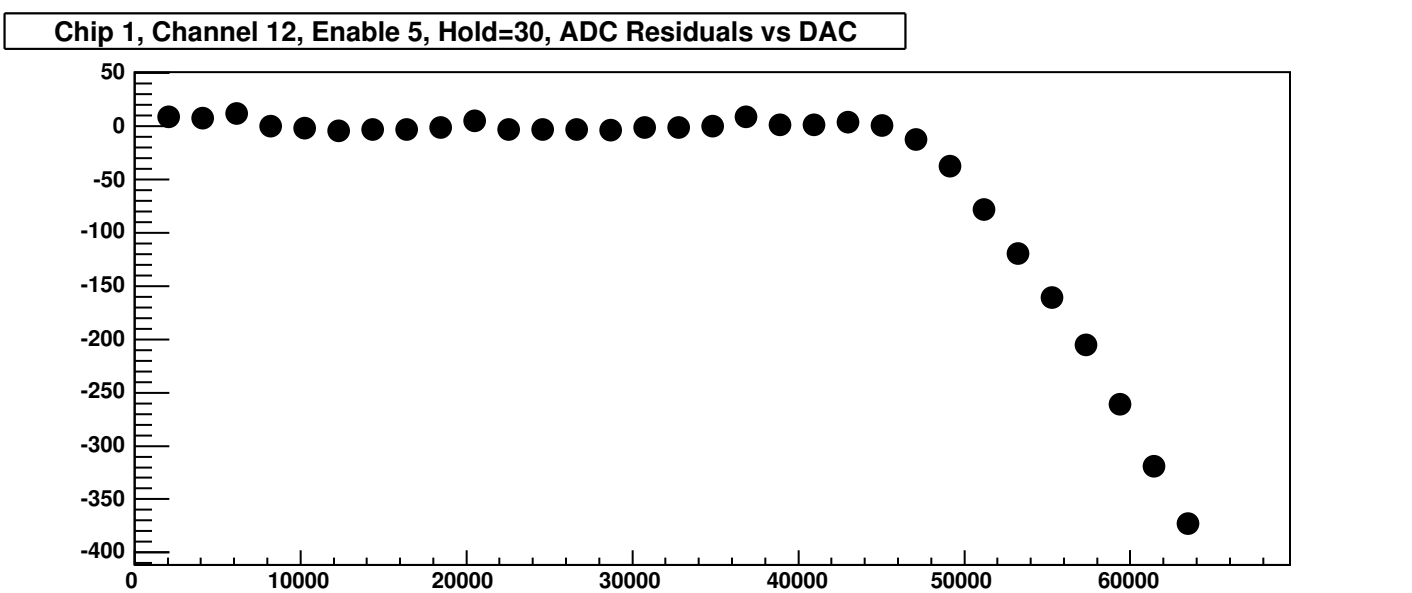

Chip 1, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 5940 / 15
p0 1080 ± 0.3061
p1 0.6313 ± 3.191e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

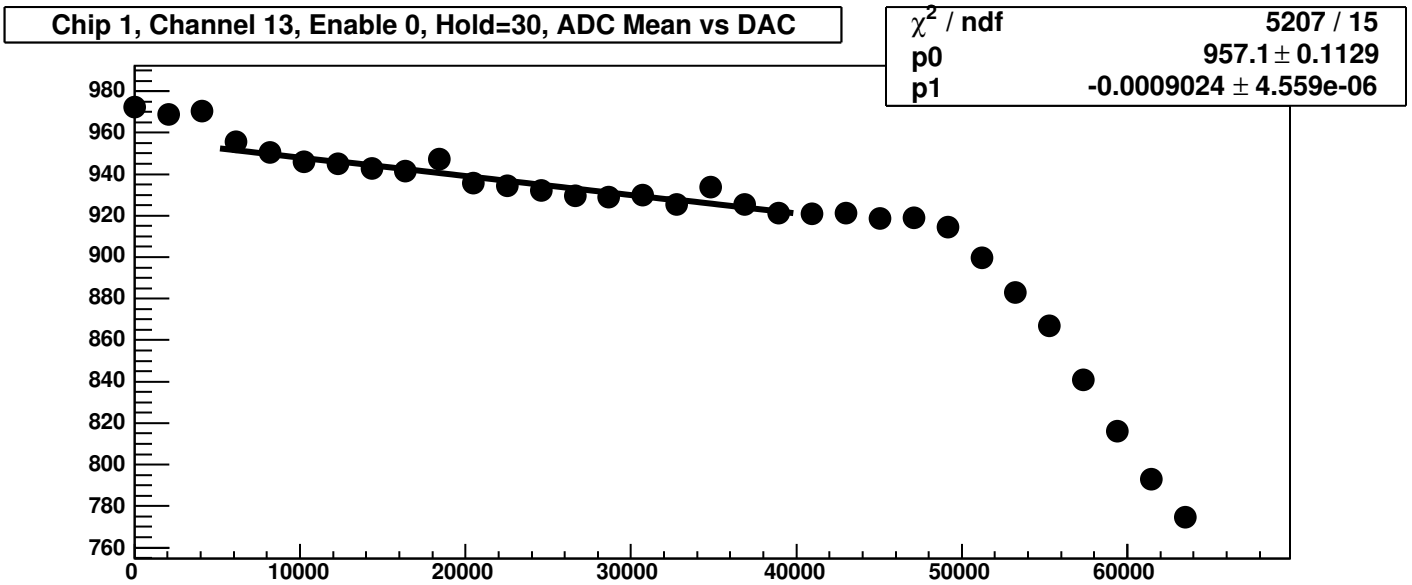

Chip 1, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

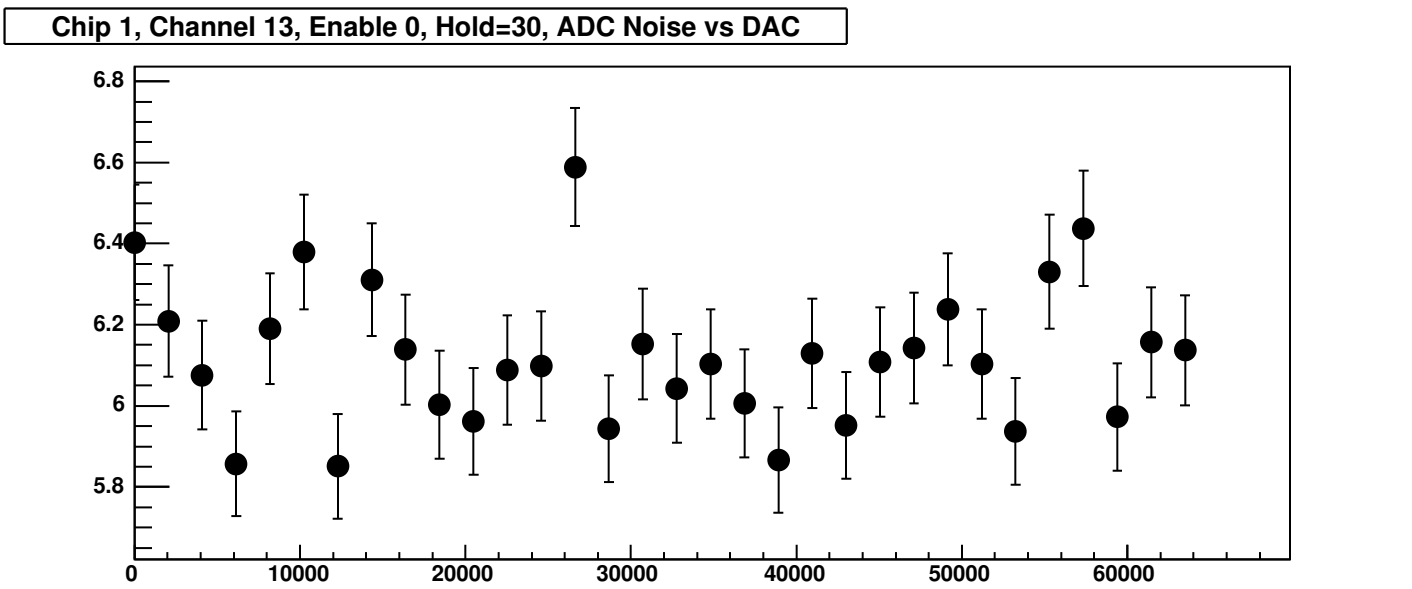

Chip 1, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

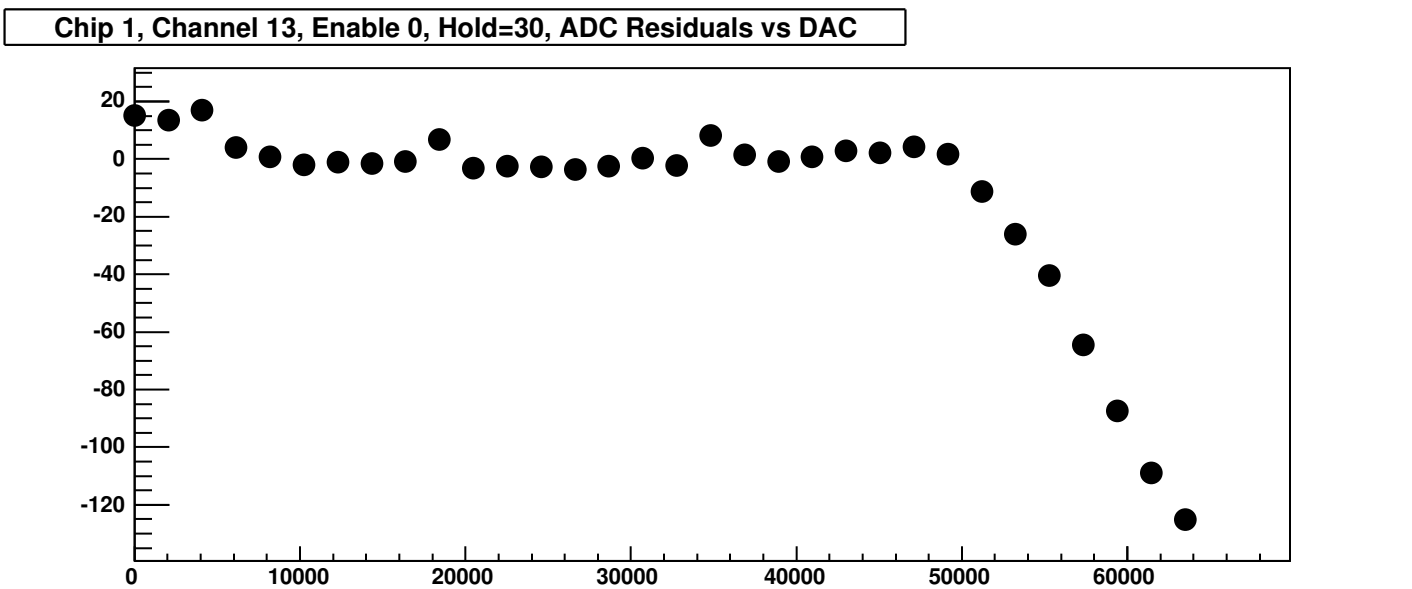

Chip 1, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8984 / 15
p0 967.2 ± 0.115
p1 -0.001332 ± 4.678e-06
1000
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000

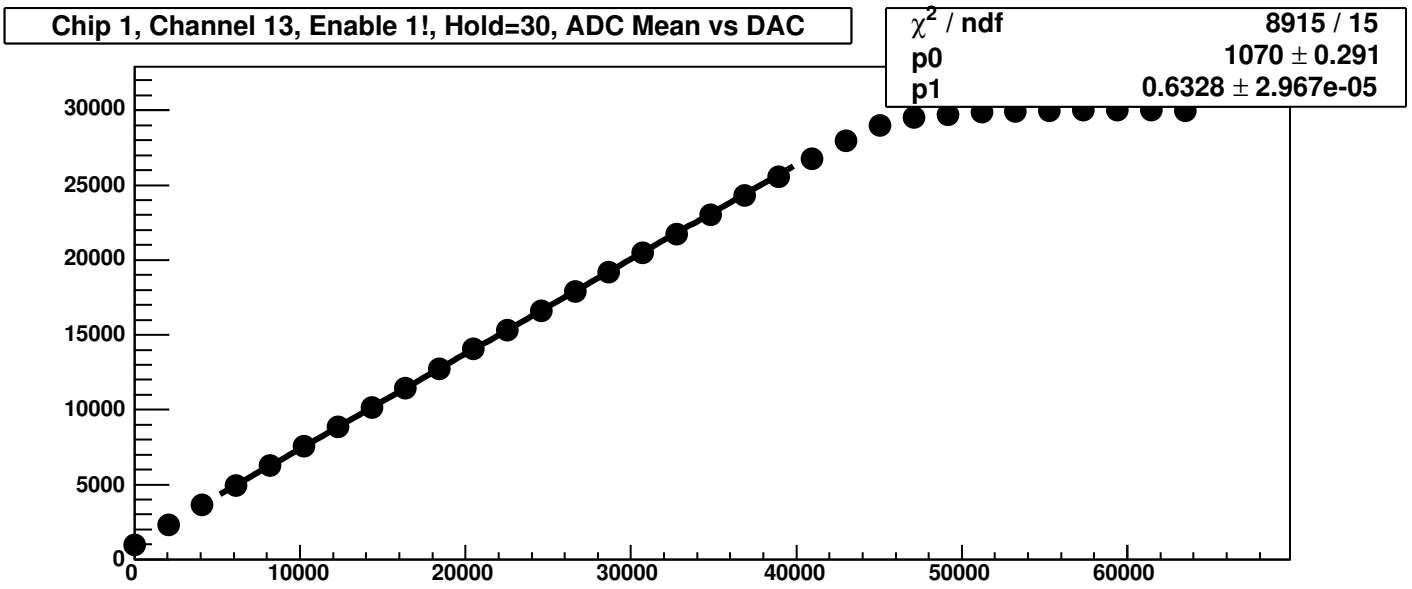

Chip 1, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

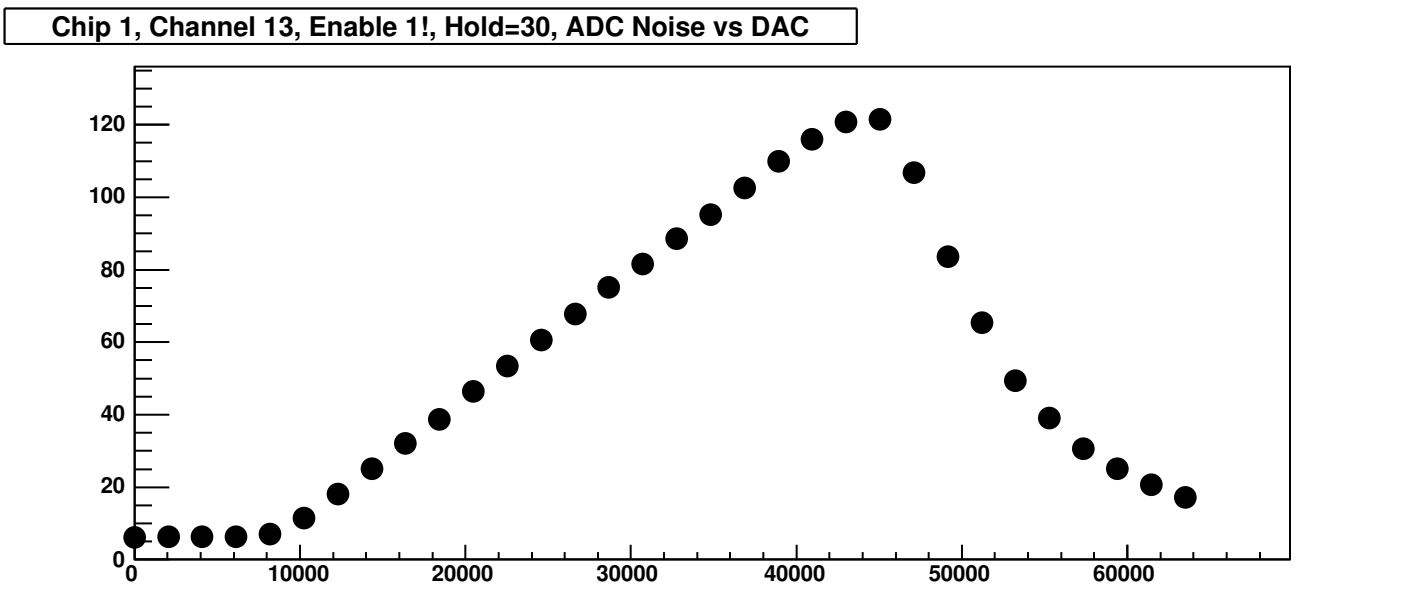

Chip 1, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

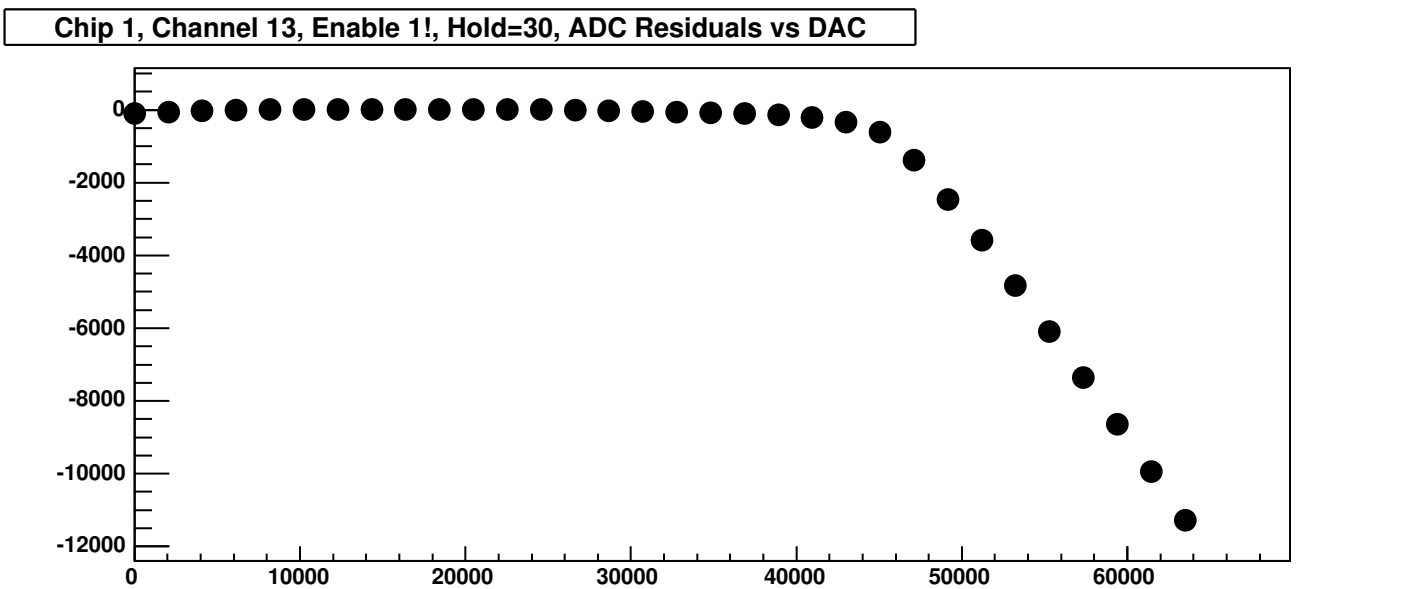

Chip 1, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5207 / 15
p0 957.1 ± 0.1129
p1 -0.0009024 ± 4.559e-06
980
960
940
920
900
880
860
840
820
800
780
760
0
10000
20000
30000
40000
50000
60000

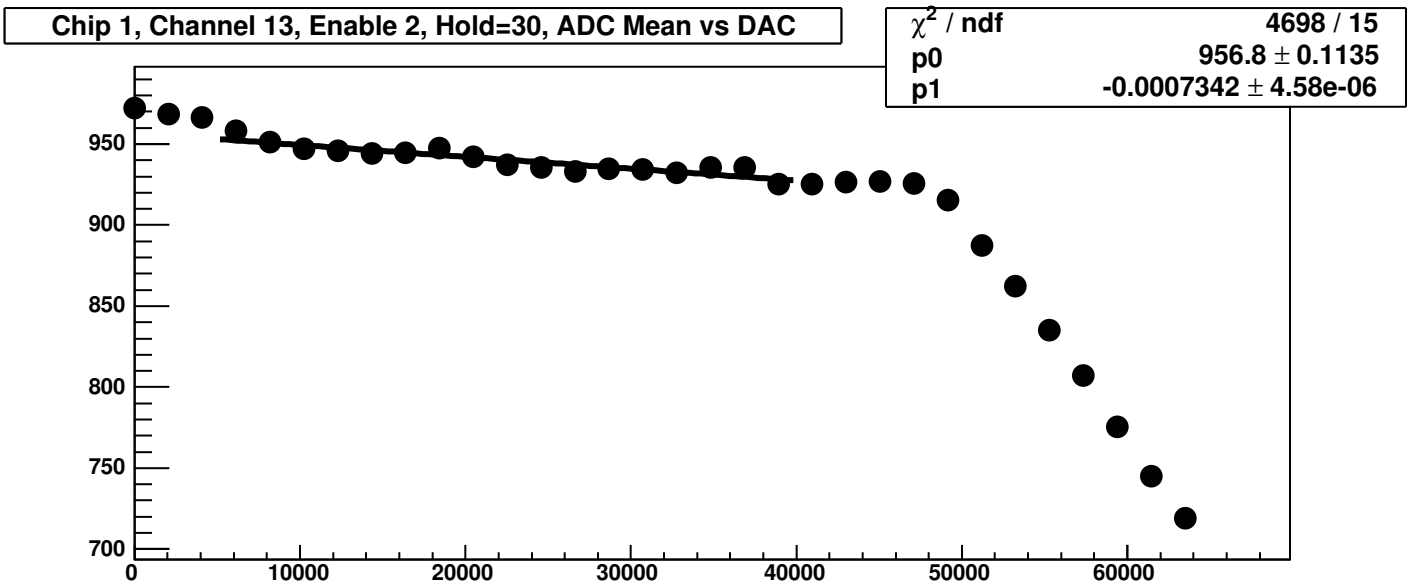

Chip 1, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

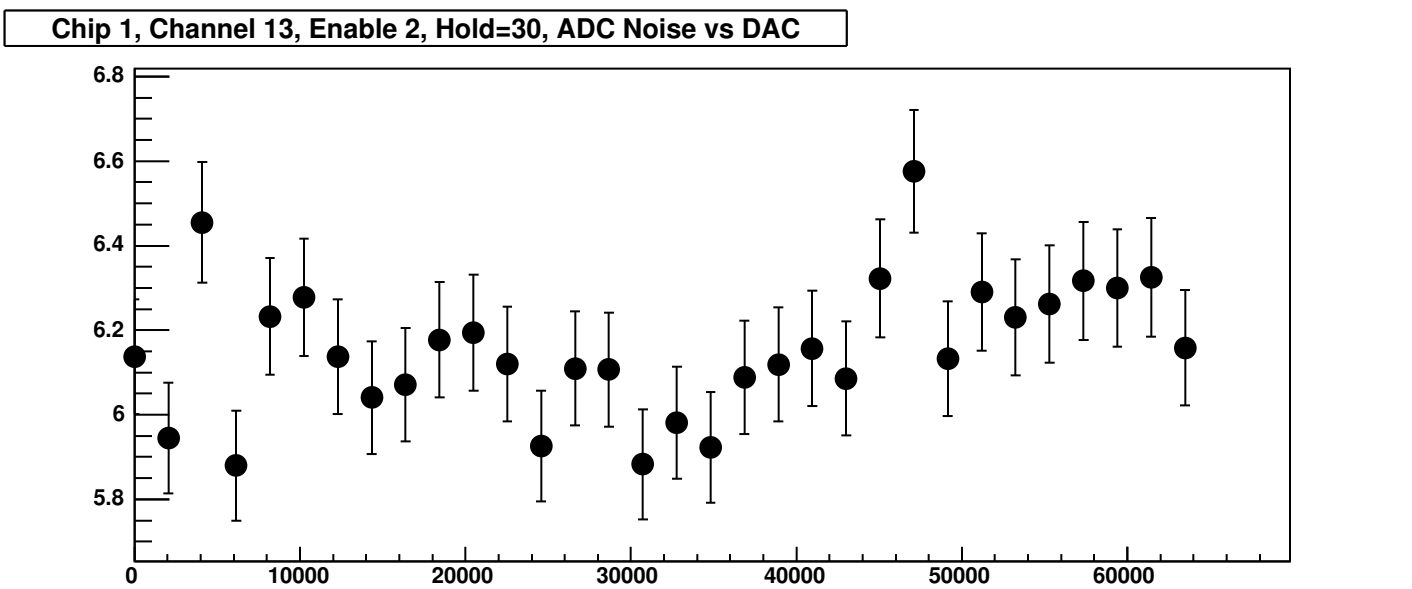

Chip 1, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

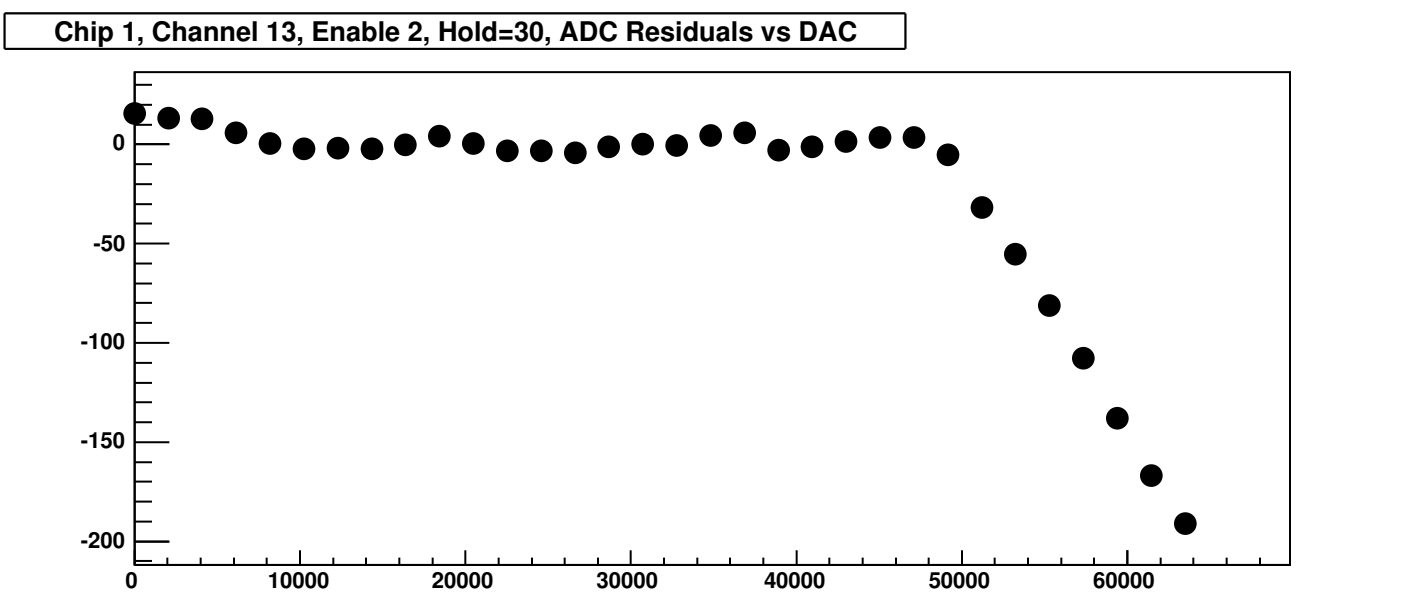

Chip 1, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 8915 / 15
p0 1070 ± 0.291
p1 0.6328 ± 2.967e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

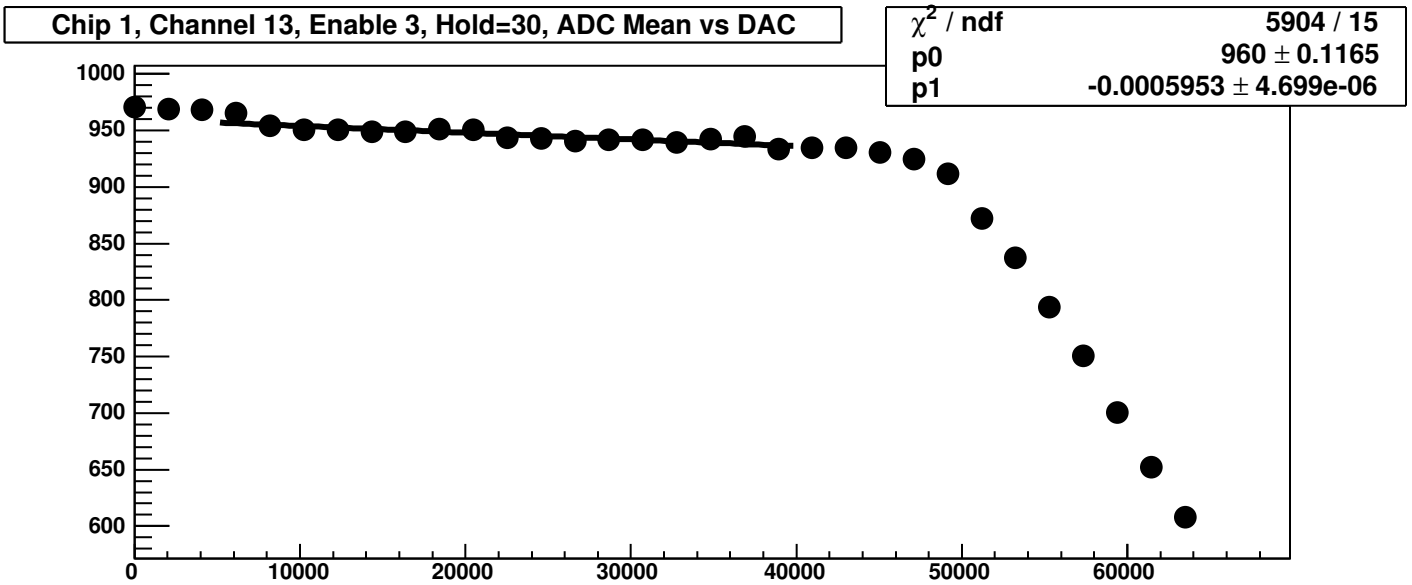

Chip 1, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

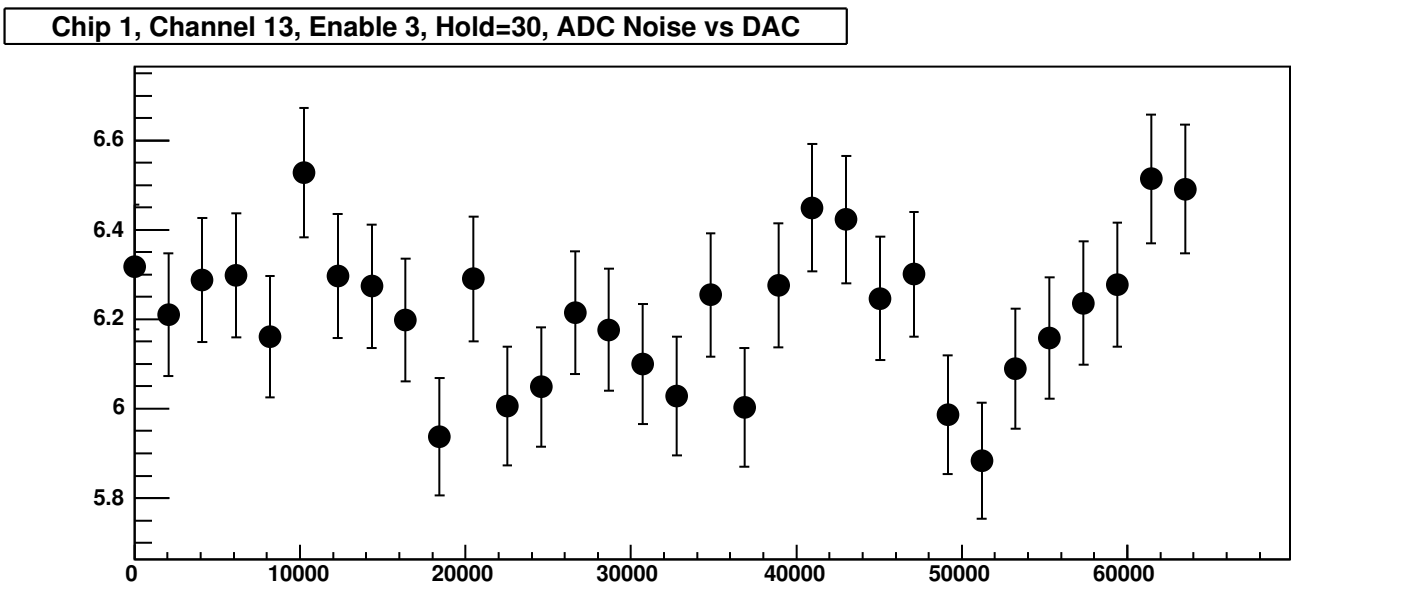

Chip 1, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

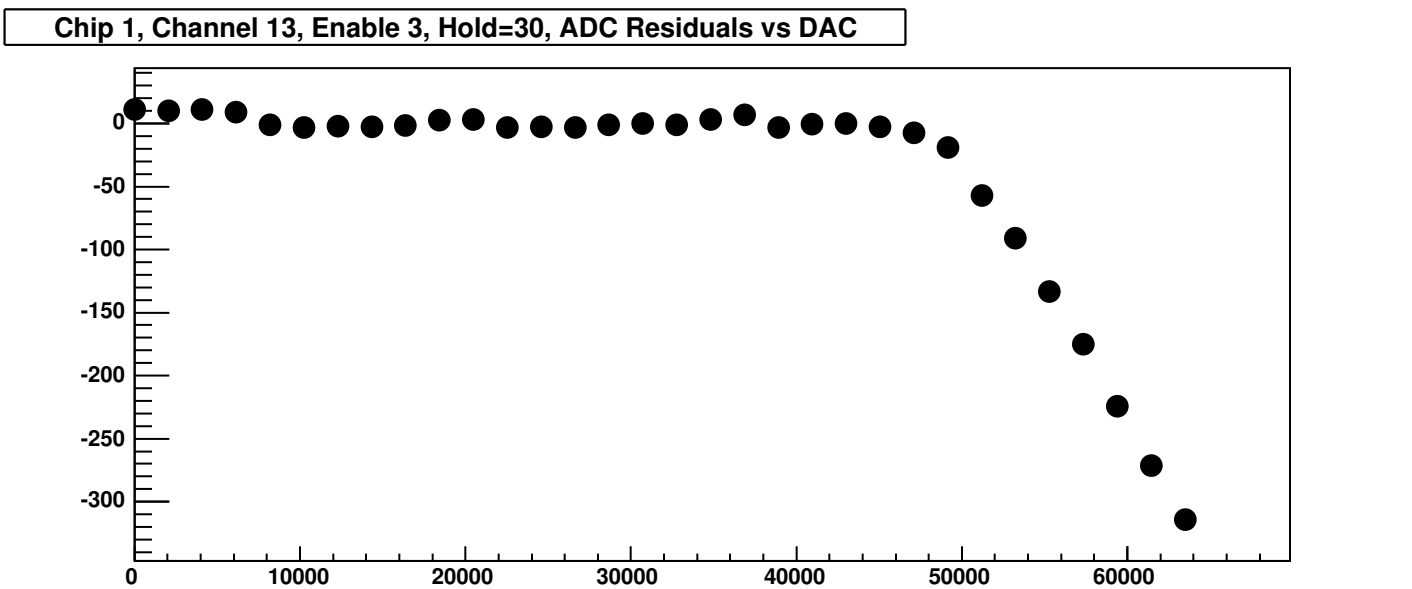

Chip 1, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4698 / 15
p0 956.8 ± 0.1135
p1 -0.0007342 ± 4.58e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

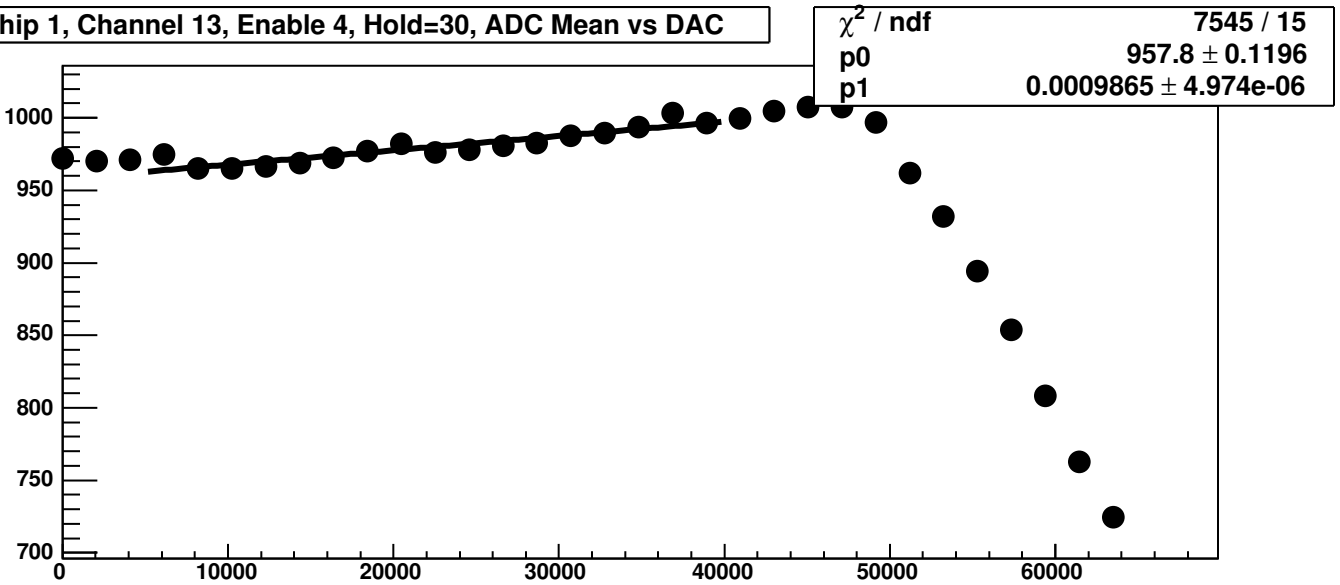

Chip 1, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5904 / 15
p0 960 ± 0.1165
p1 -0.0005953 ± 4.699e-06
1000
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

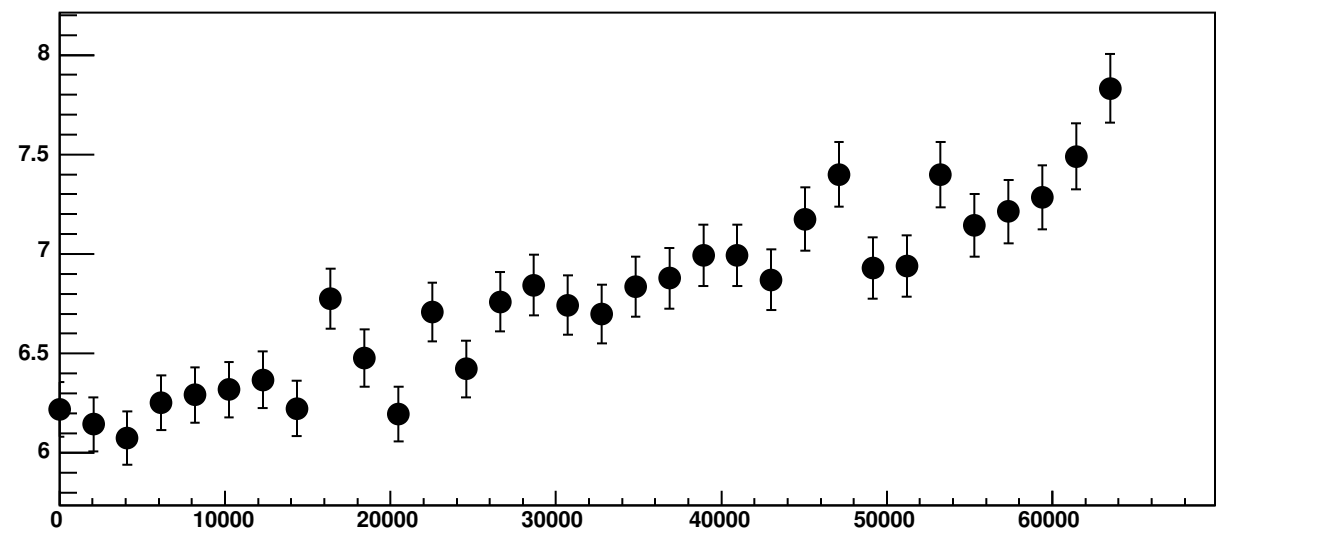

Chip 1, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

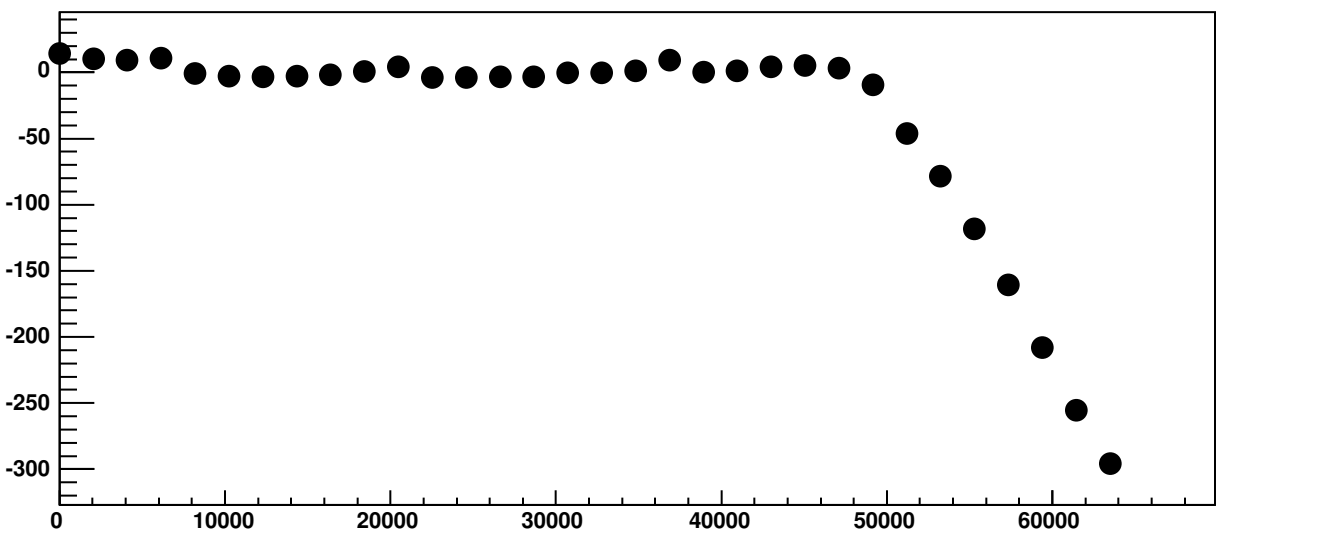
Chip 1, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC

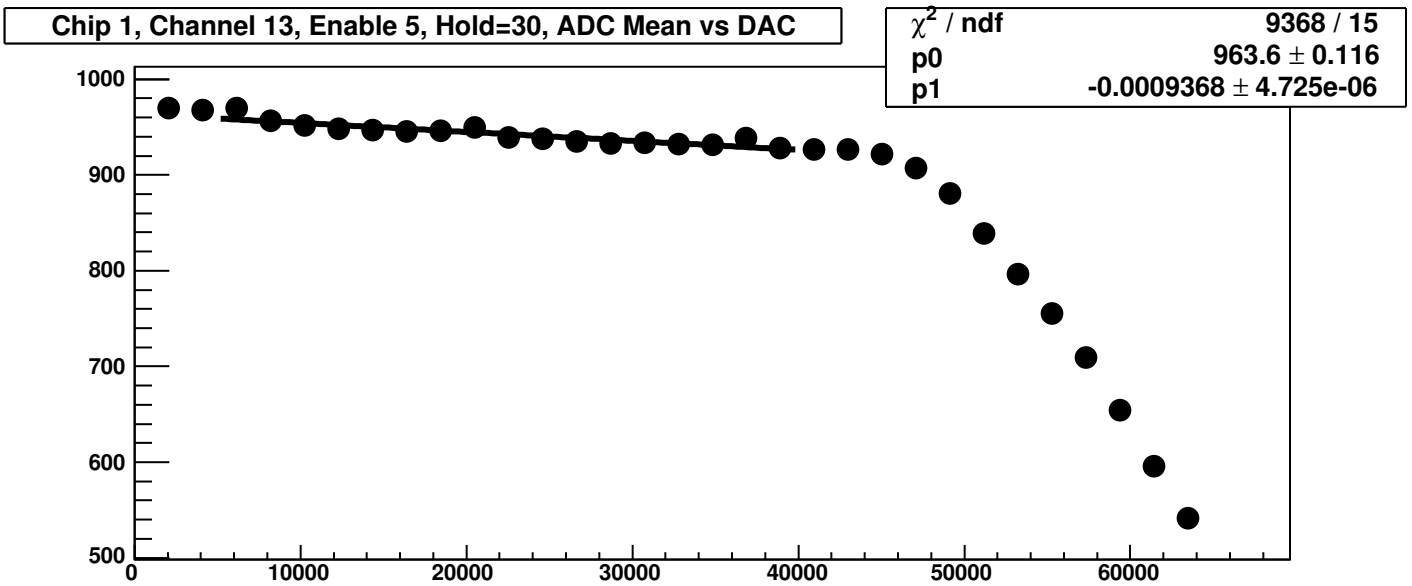


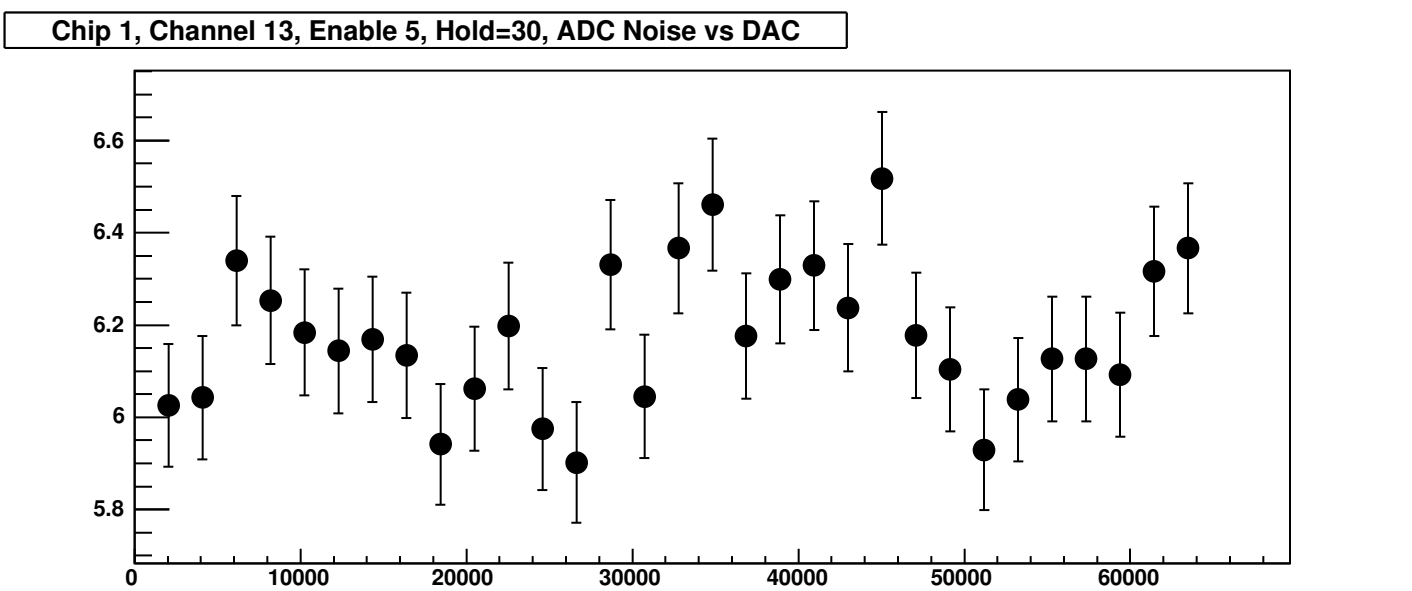
Chip 1, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC

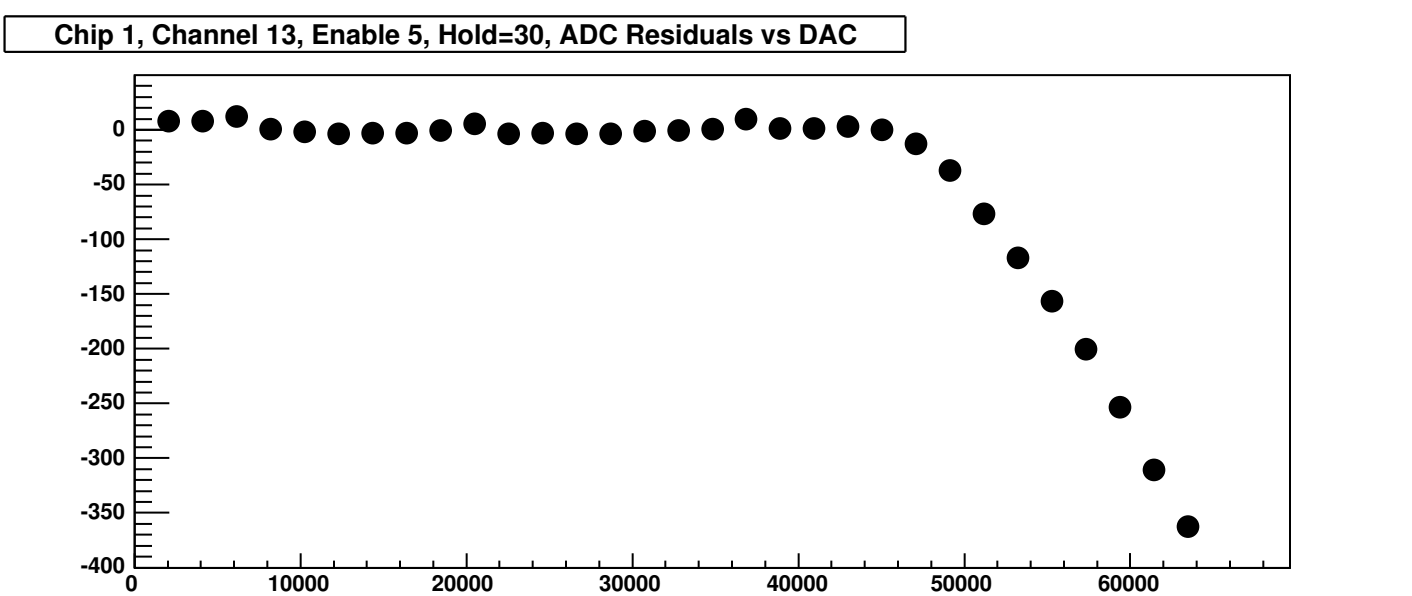


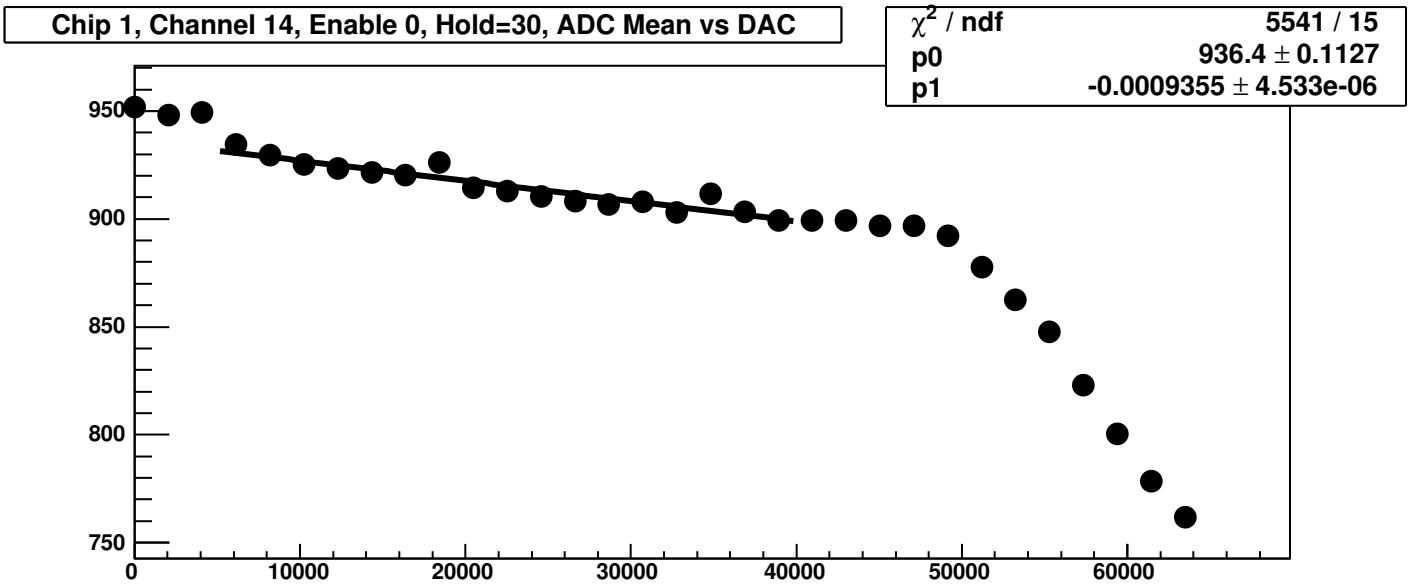
Chip 1, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC

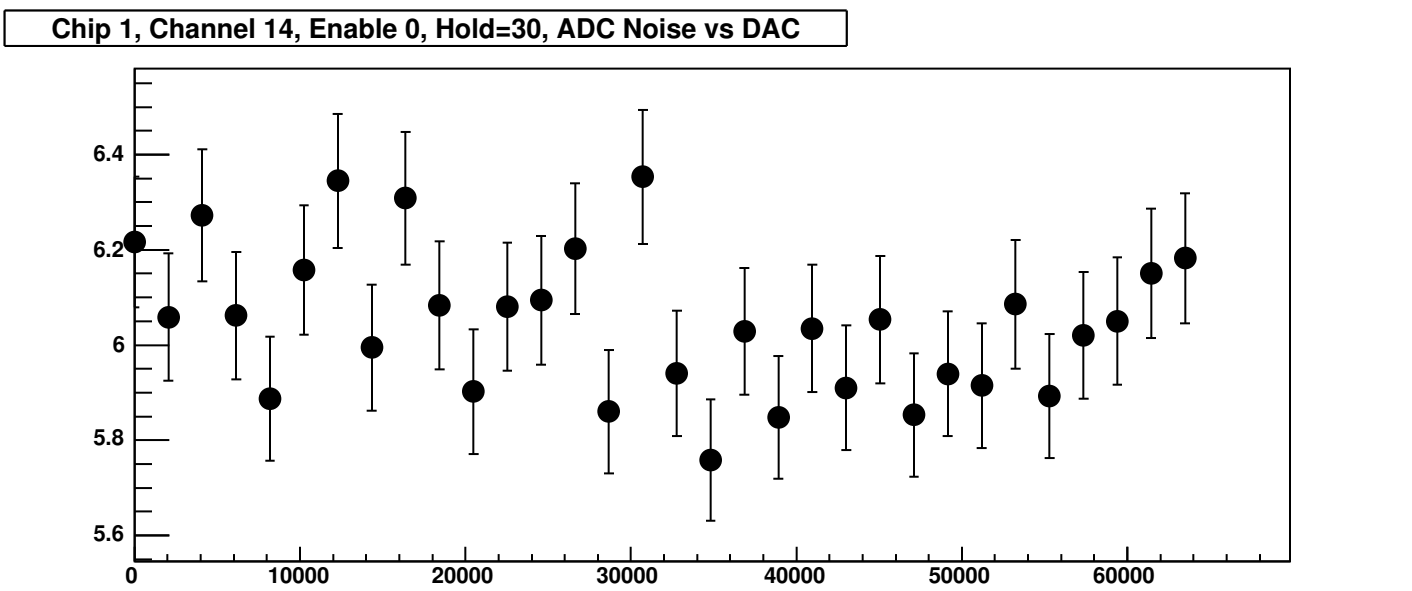


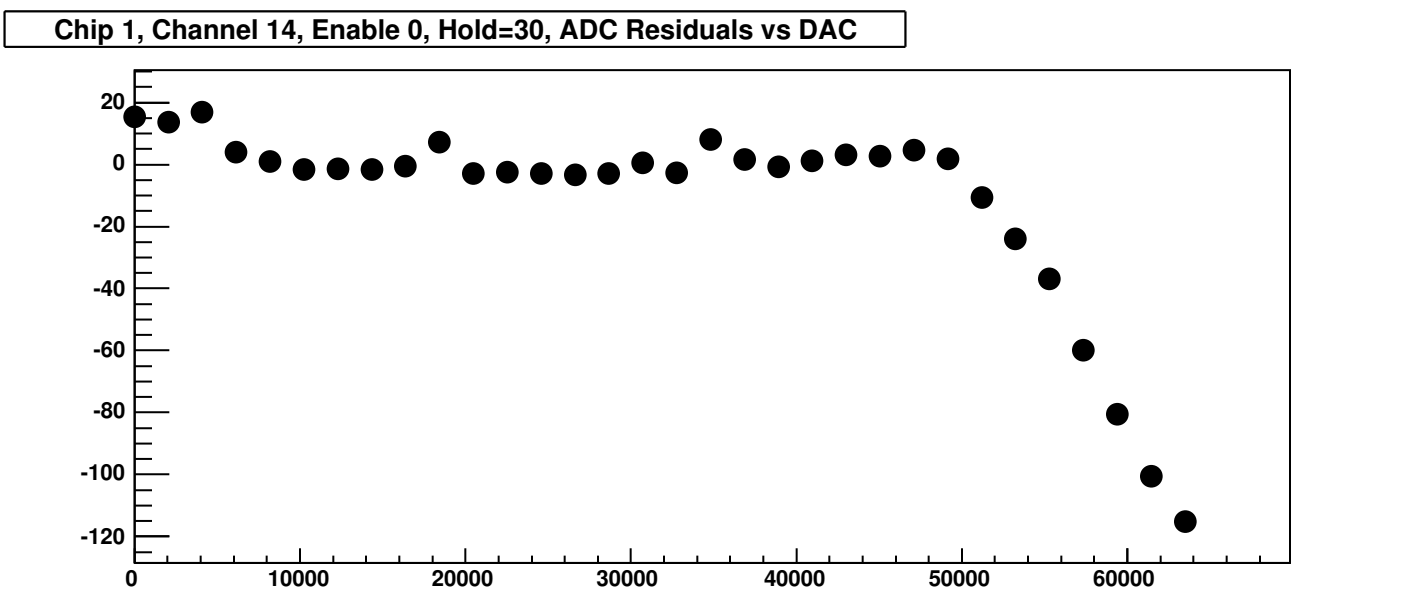

Chip 1, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9368 / 15
p0 963.6 ± 0.116
p1 -0.0009368 ± 4.725e-06
1000
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

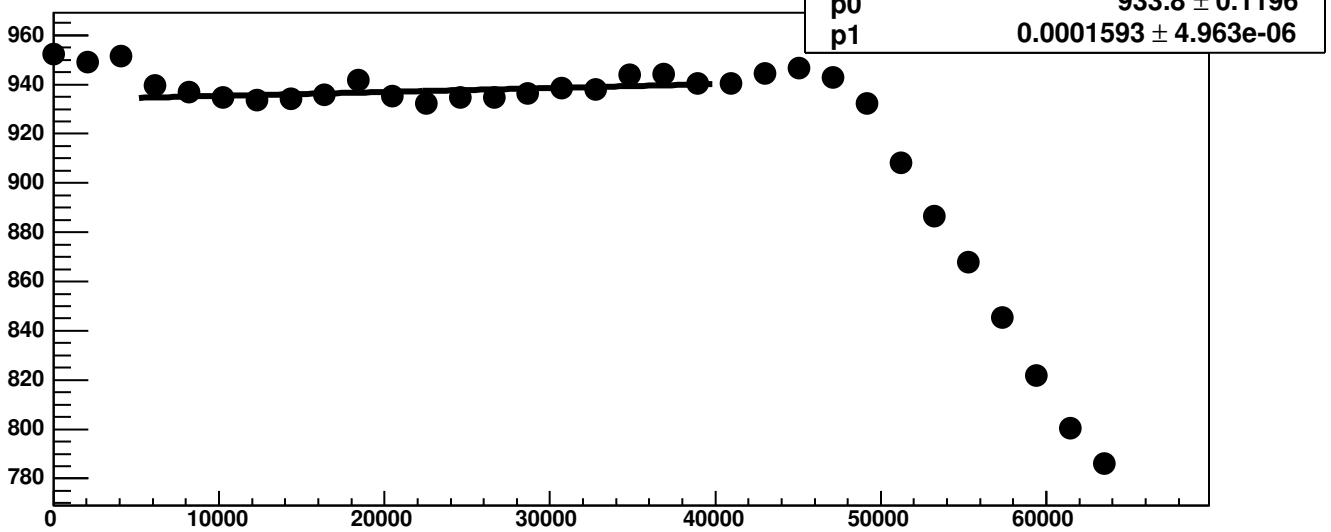

Chip 1, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5541 / 15
p0 936.4 ± 0.1127
p1 -0.0009355 ± 4.533e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

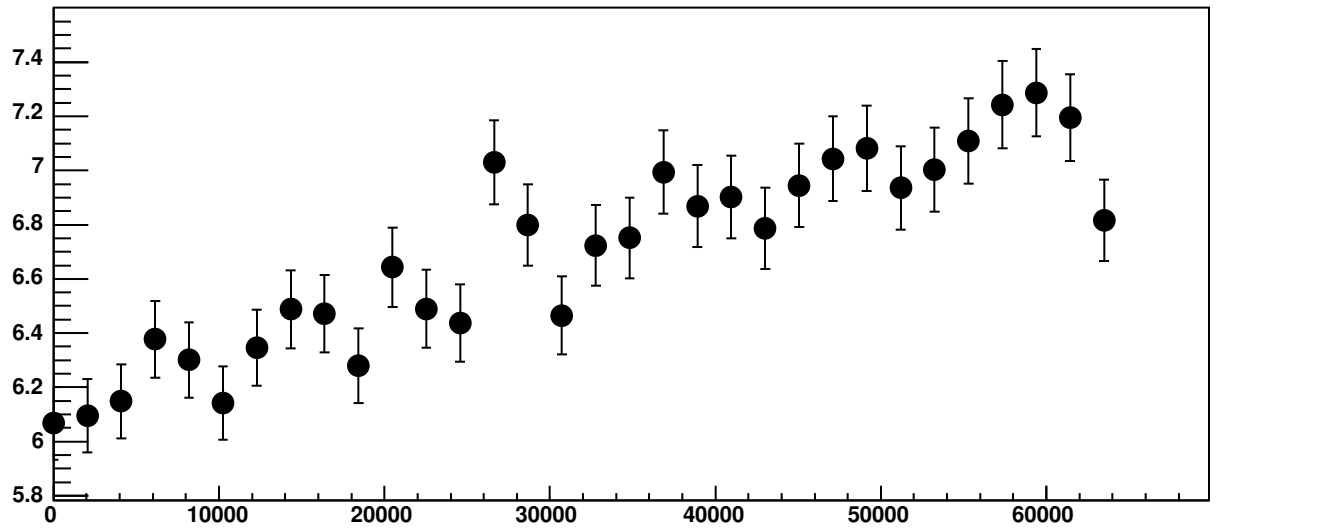

Chip 1, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

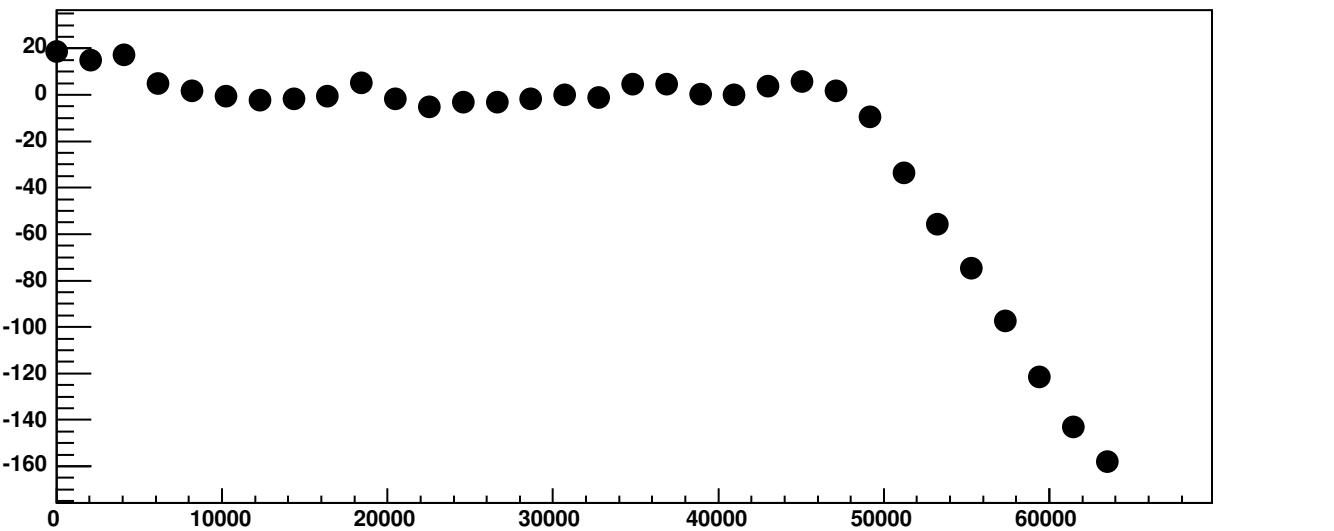
Chip 1, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC

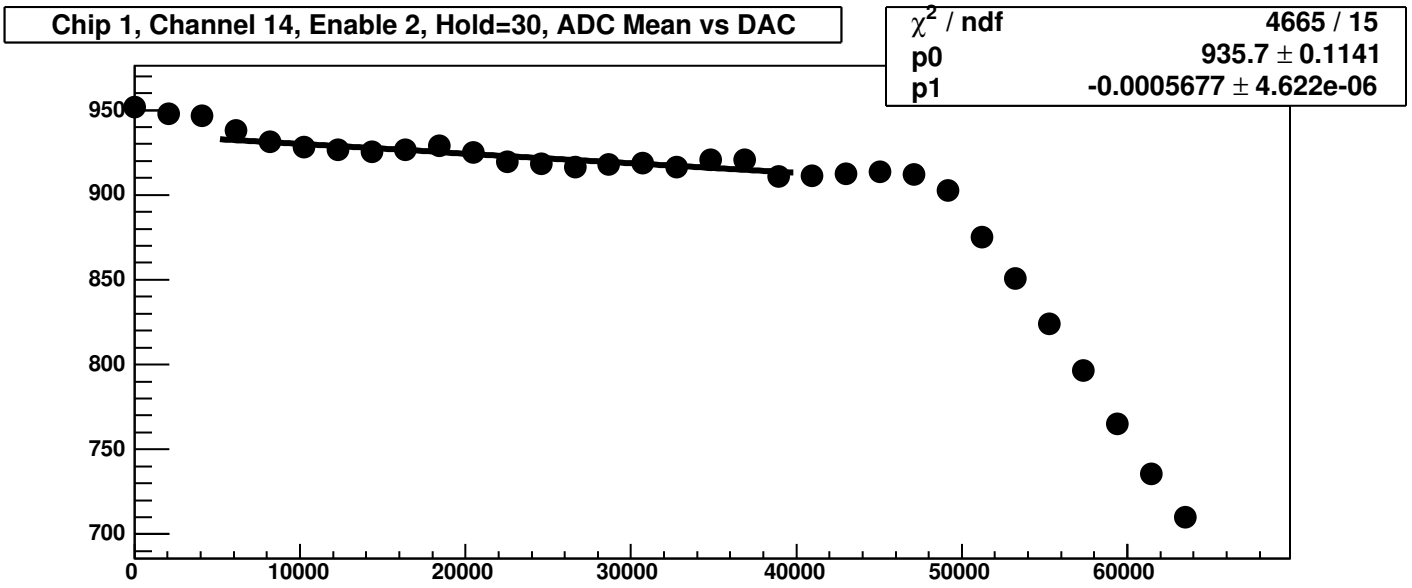


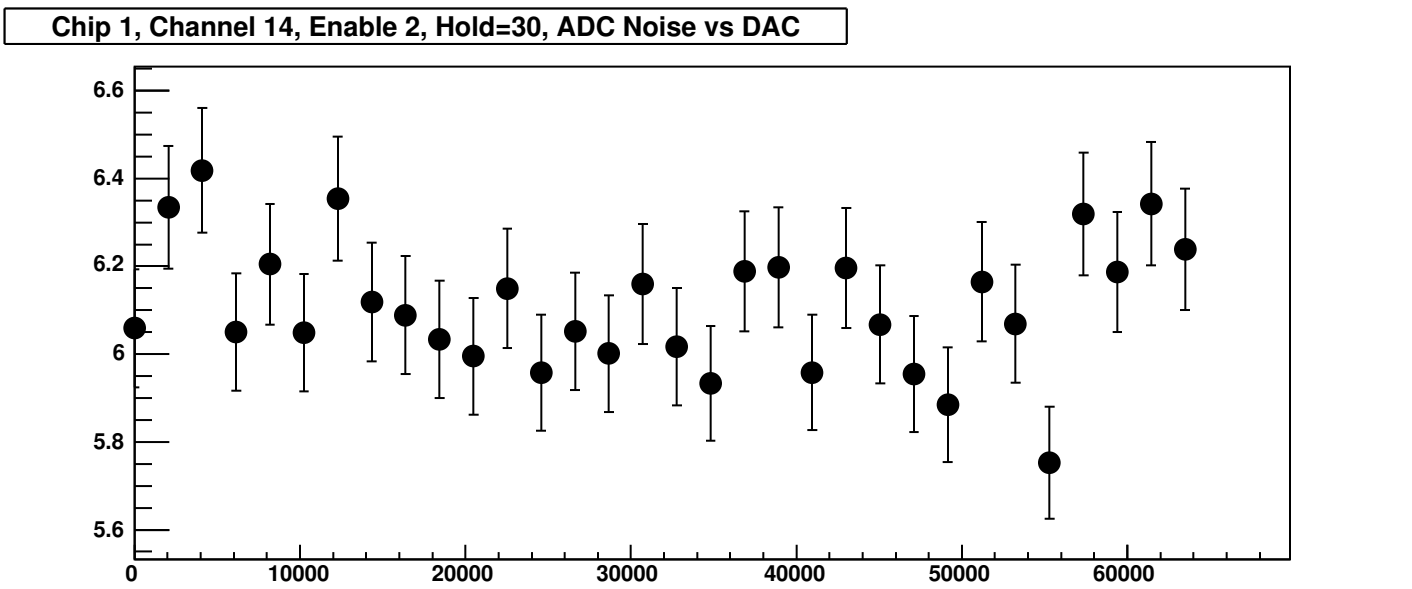
Chip 1, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC

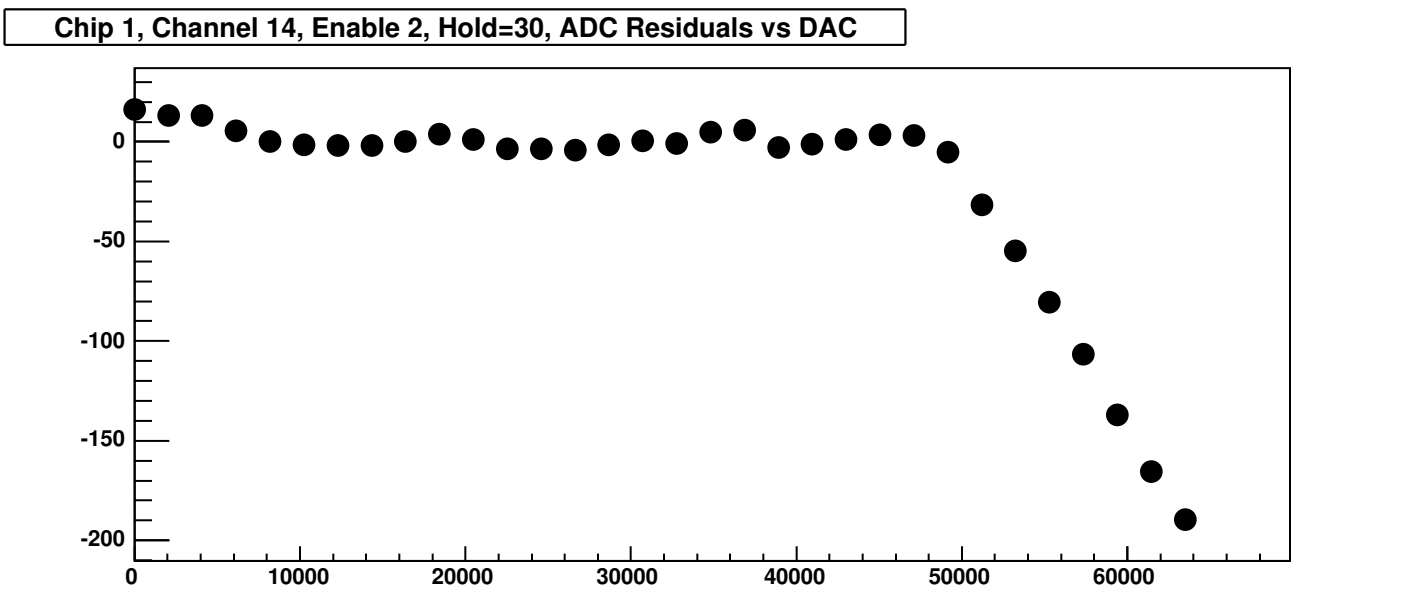


Chip 1, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC



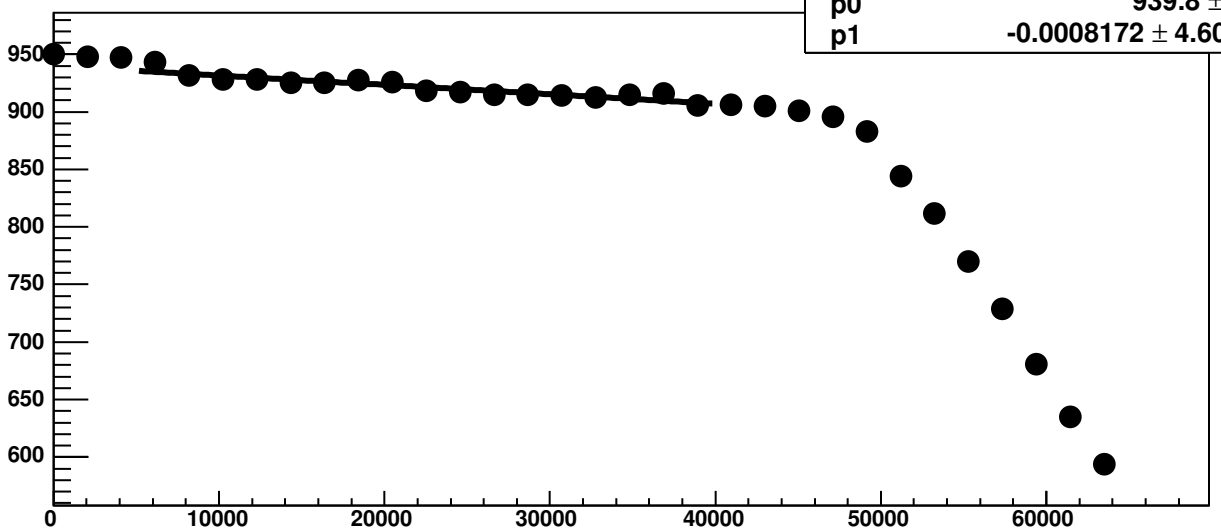

Chip 1, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4665 / 15
p0 935.7 ± 0.1141
p1 -0.0005677 ± 4.622e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

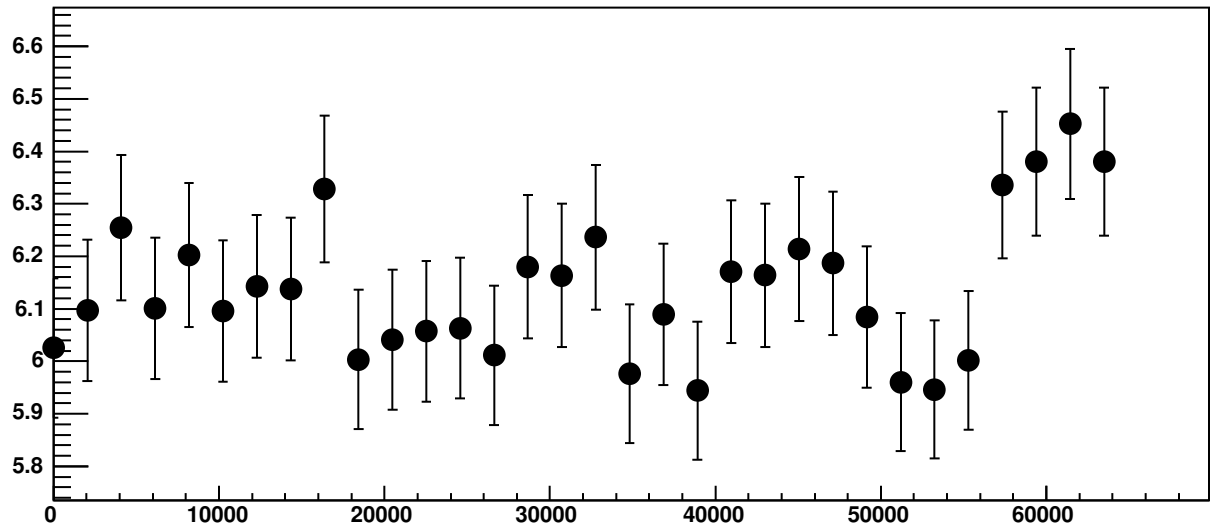

Chip 1, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

Chip 1, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

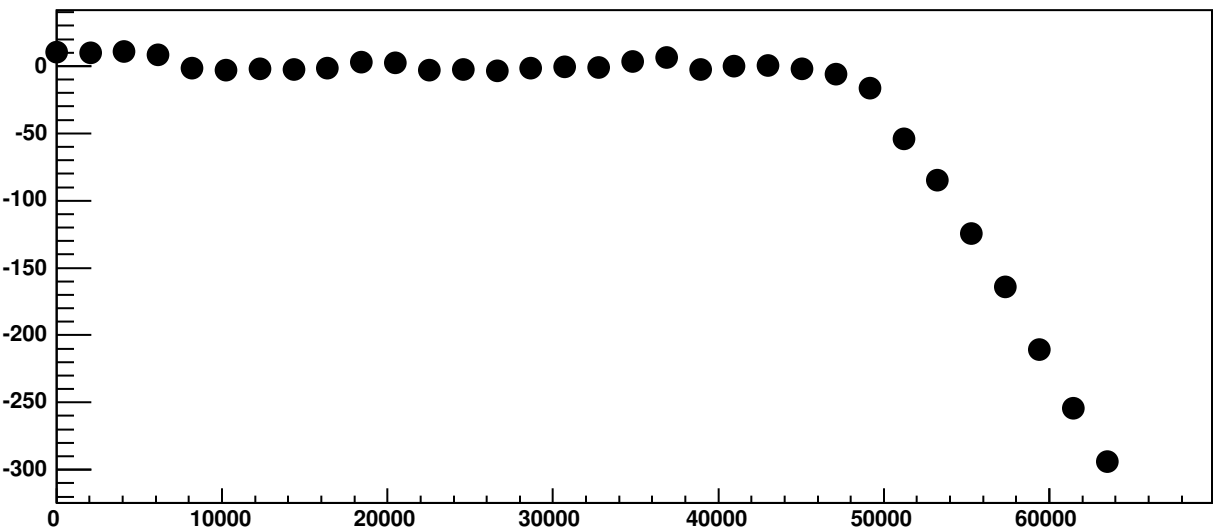
χ^2 / ndf	5647 / 15
p0	939.8 ± 0.114
p1	-0.0008172 ± 4.603e-06

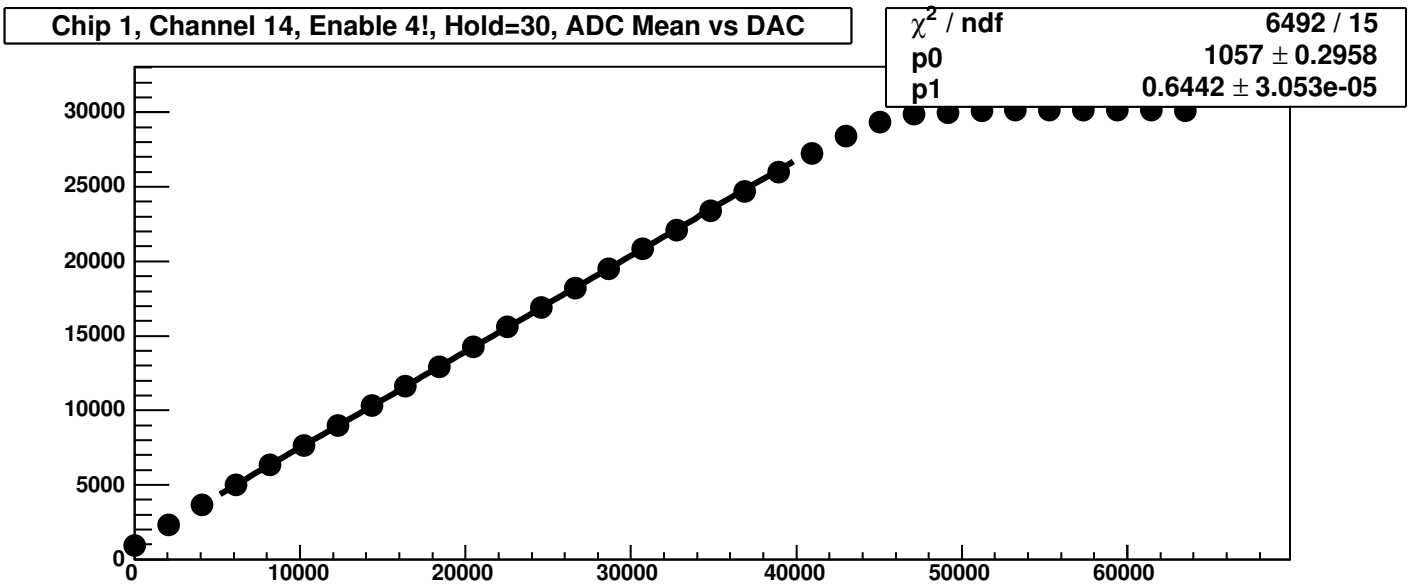


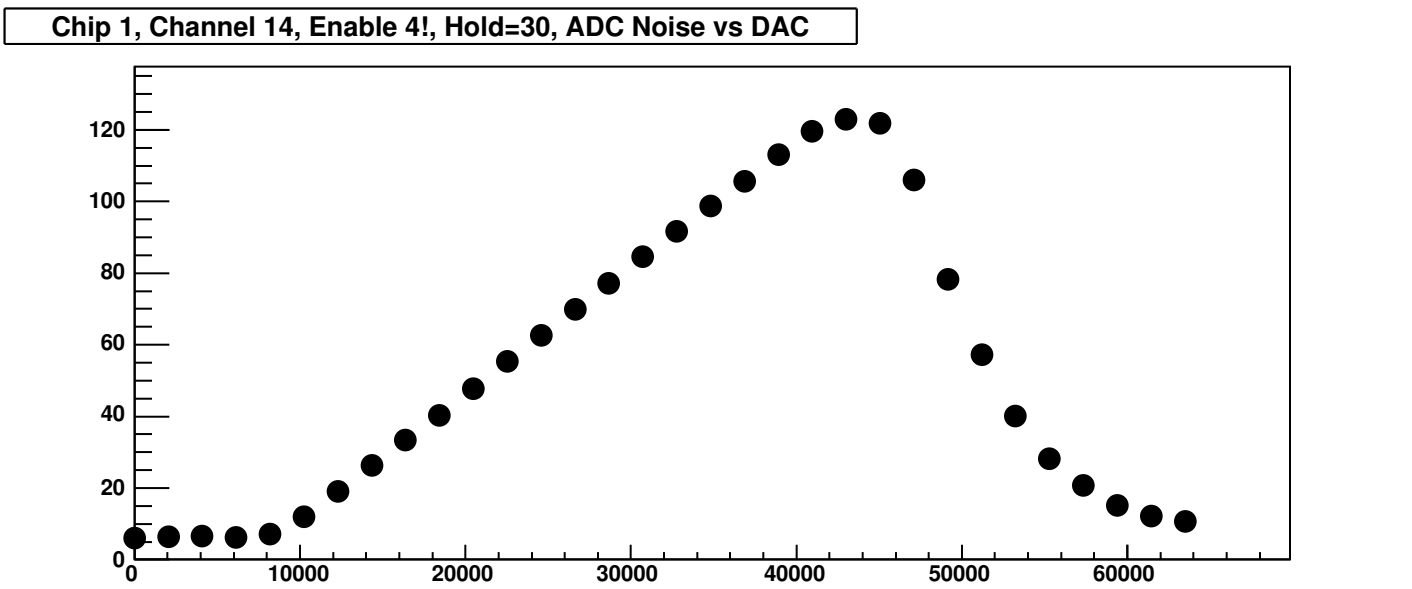
Chip 1, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC

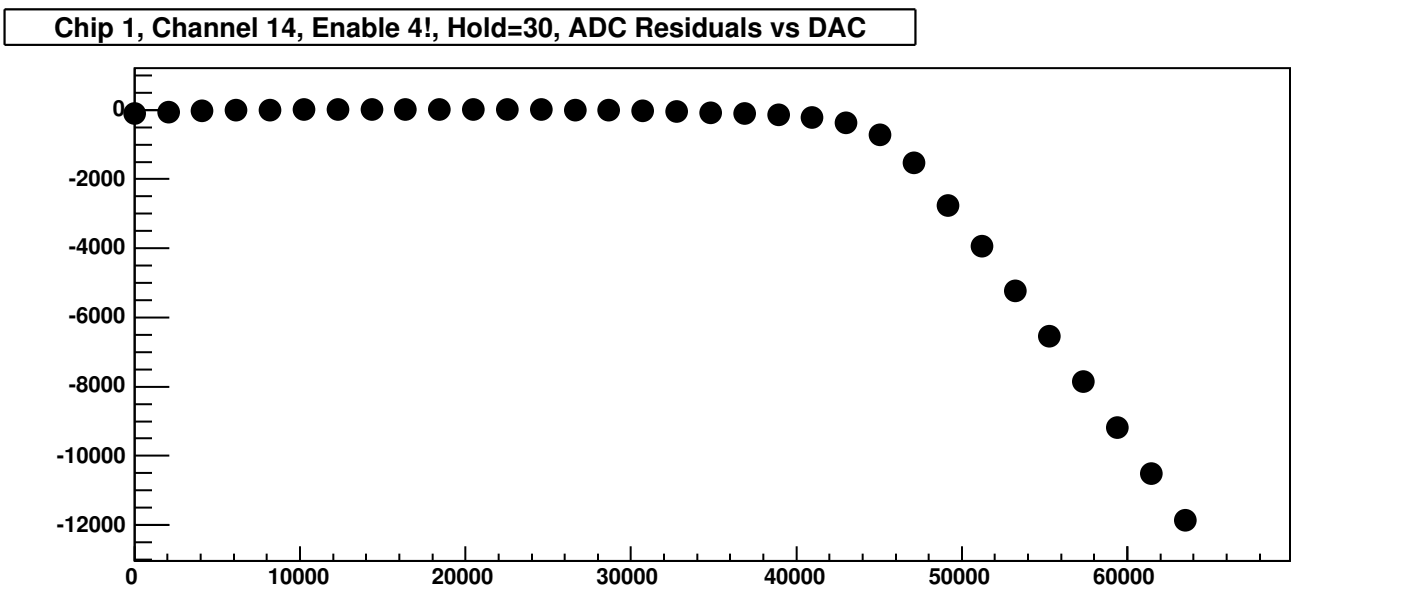


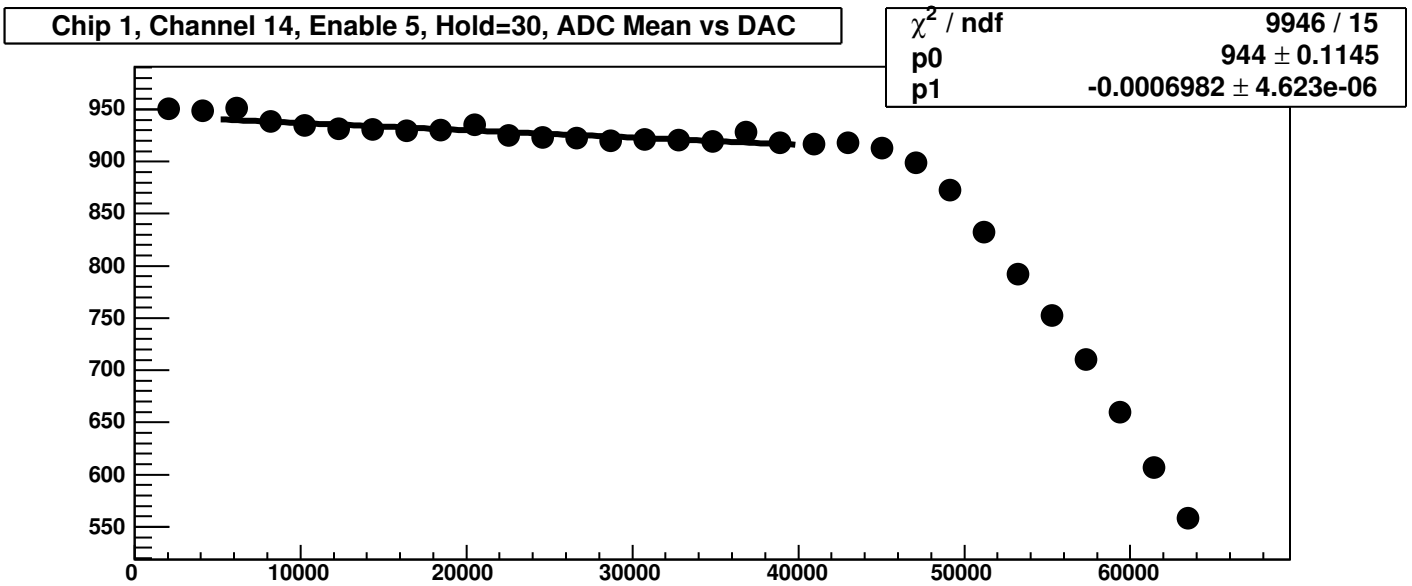
Chip 1, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC

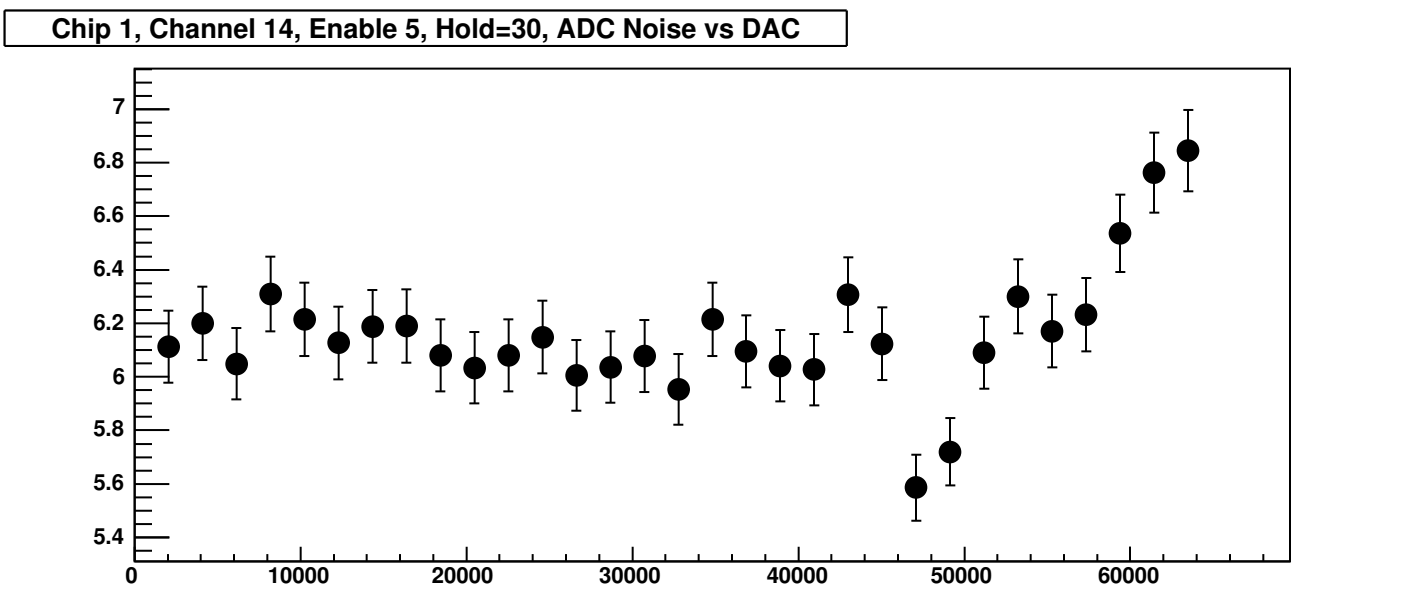


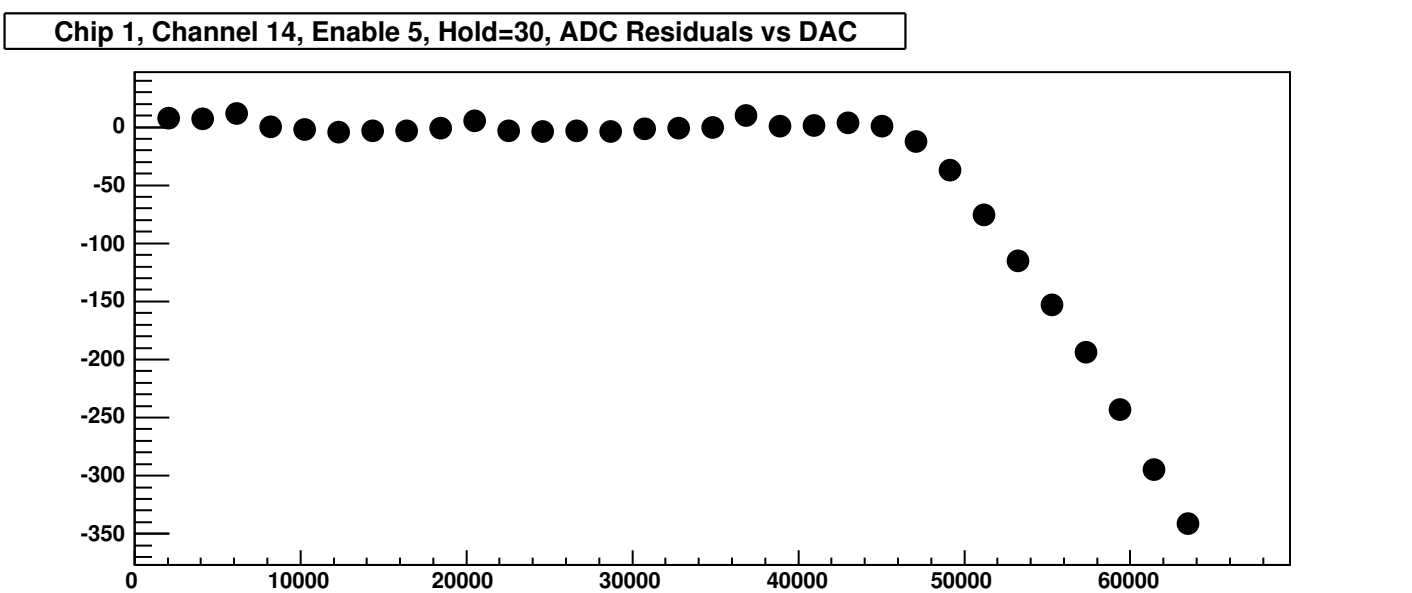

Chip 1, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 6492 / 15
p0 1057 ± 0.2958
p1 0.6442 ± 3.053e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

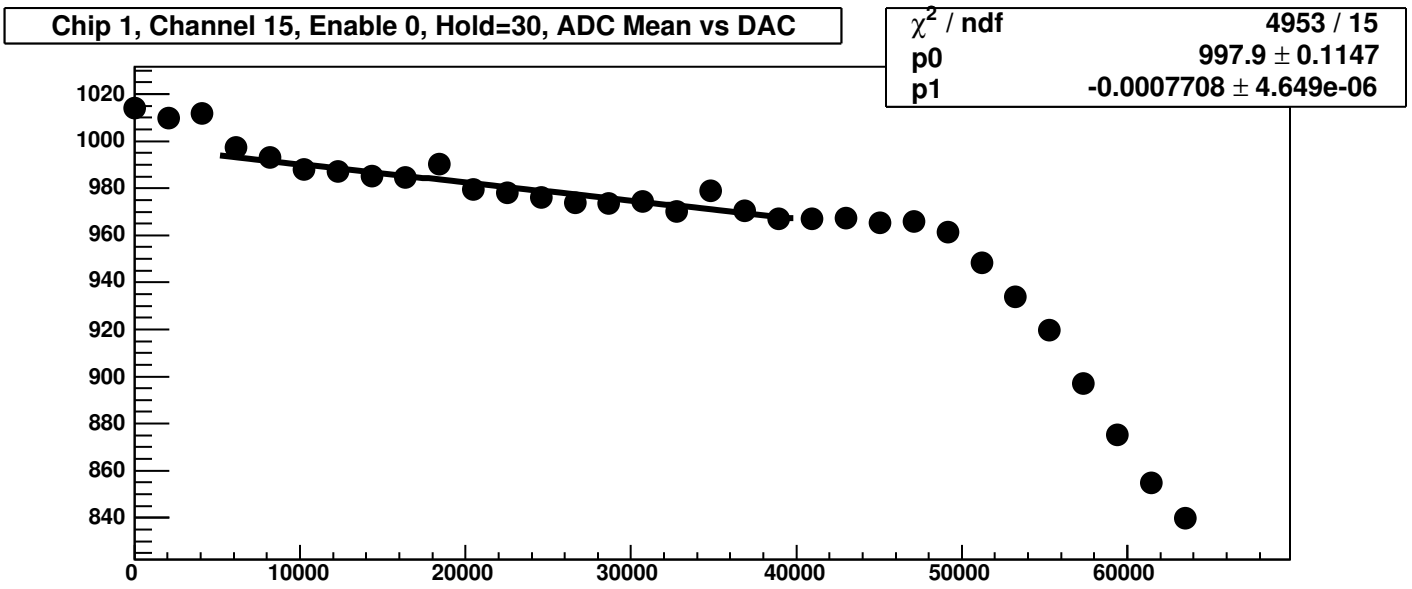

Chip 1, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

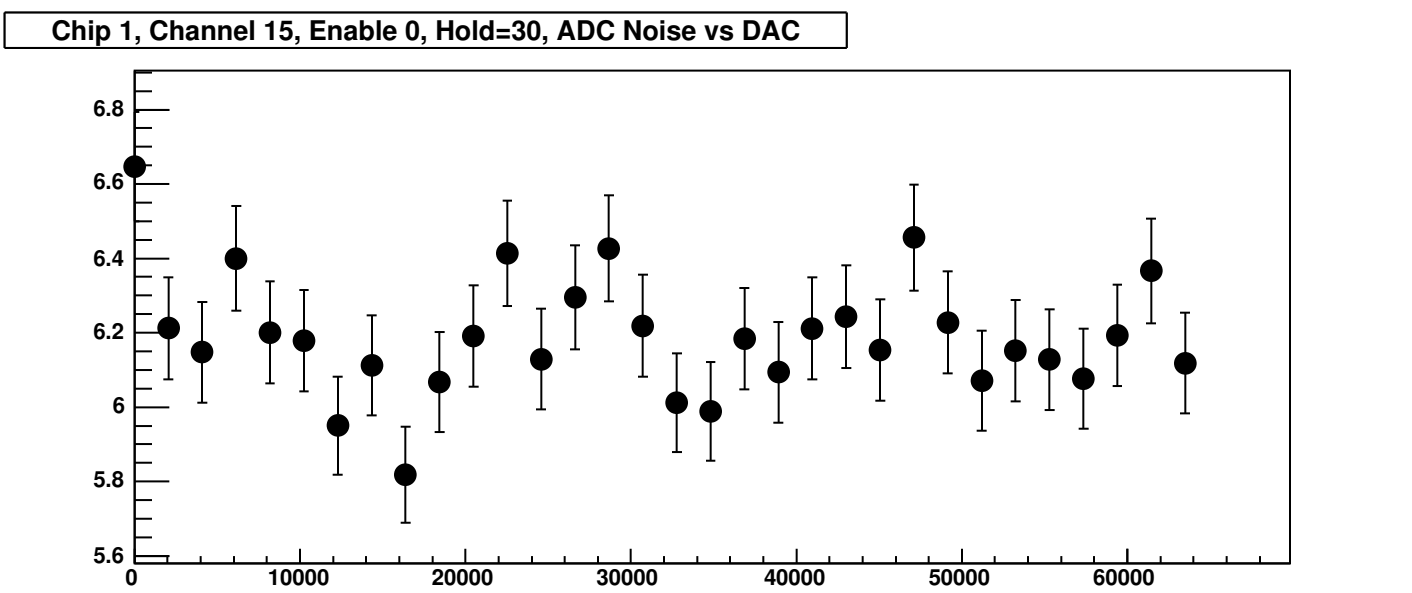

Chip 1, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

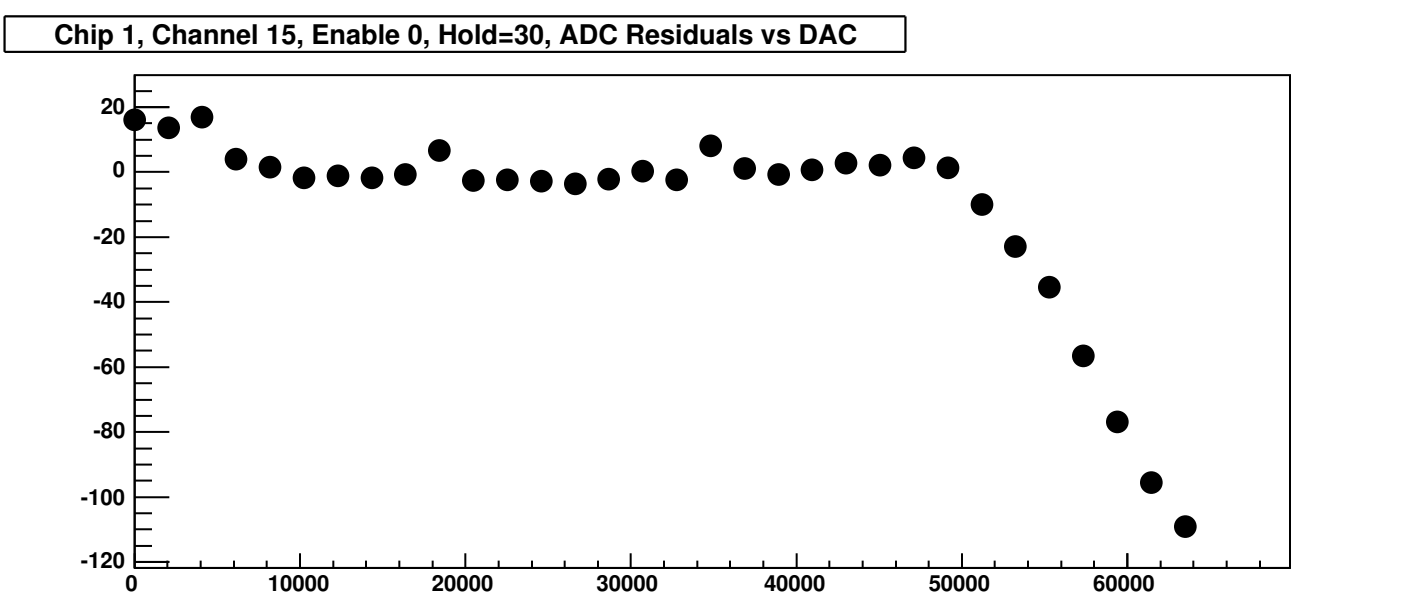

Chip 1, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9946 / 15
p0 944 ± 0.1145
p1 -0.0006982 ± 4.623e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

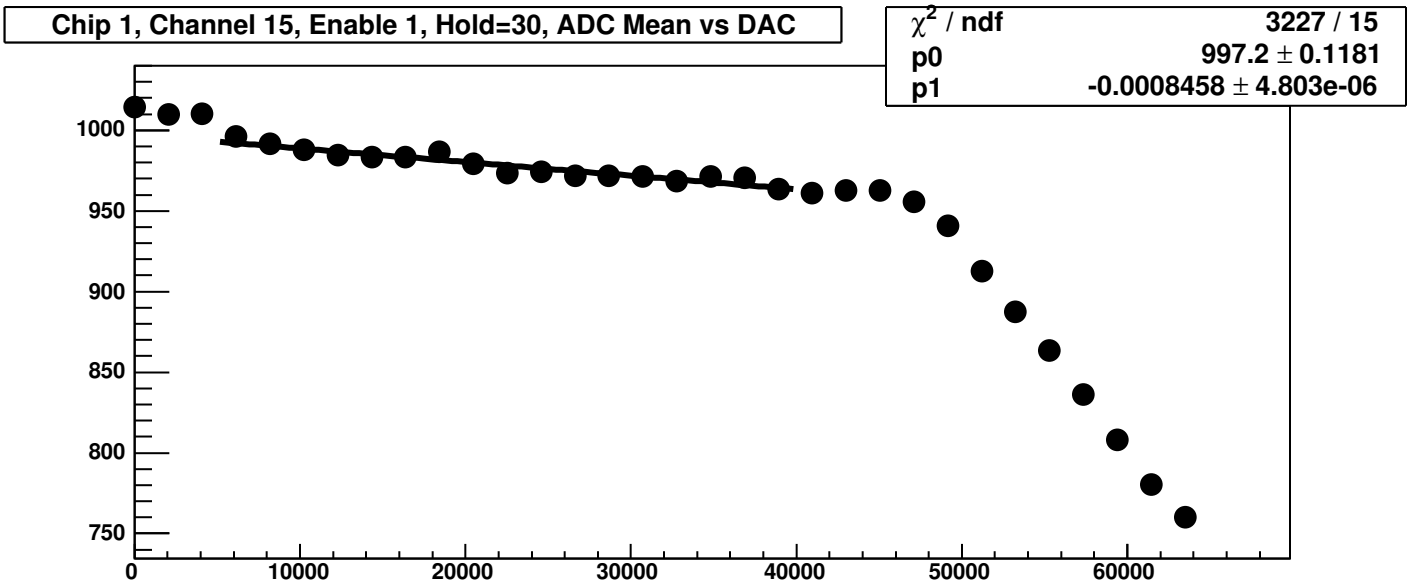

Chip 1, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

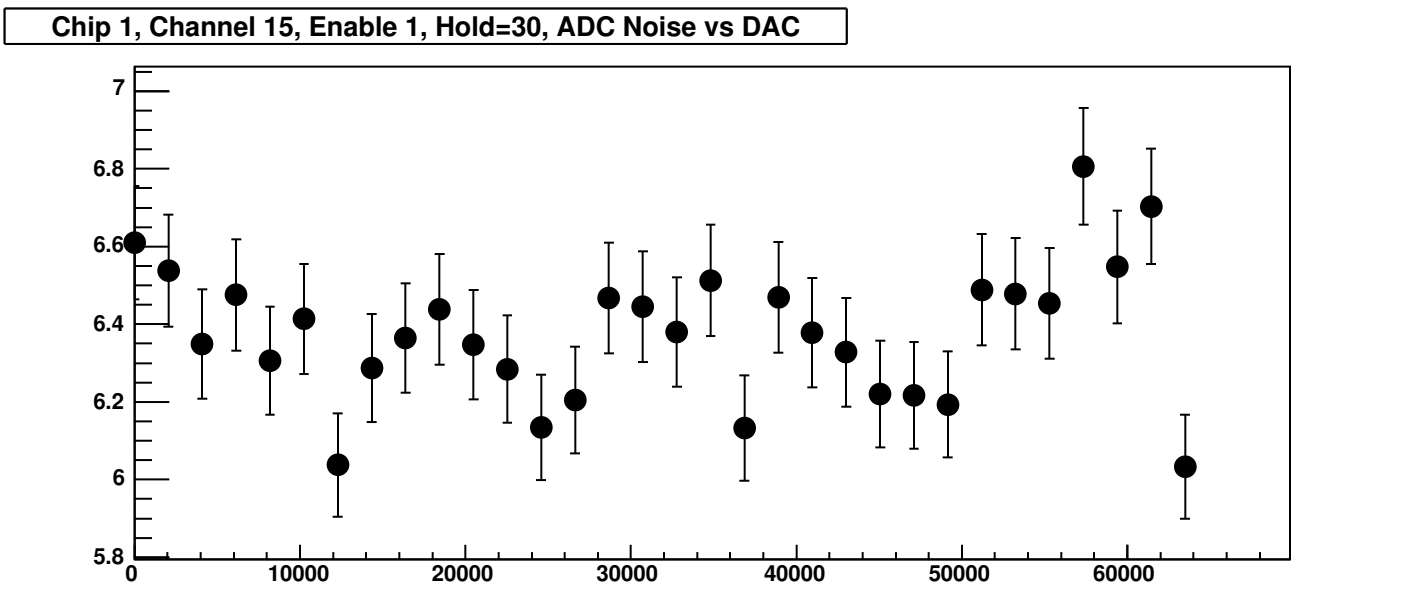

Chip 1, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

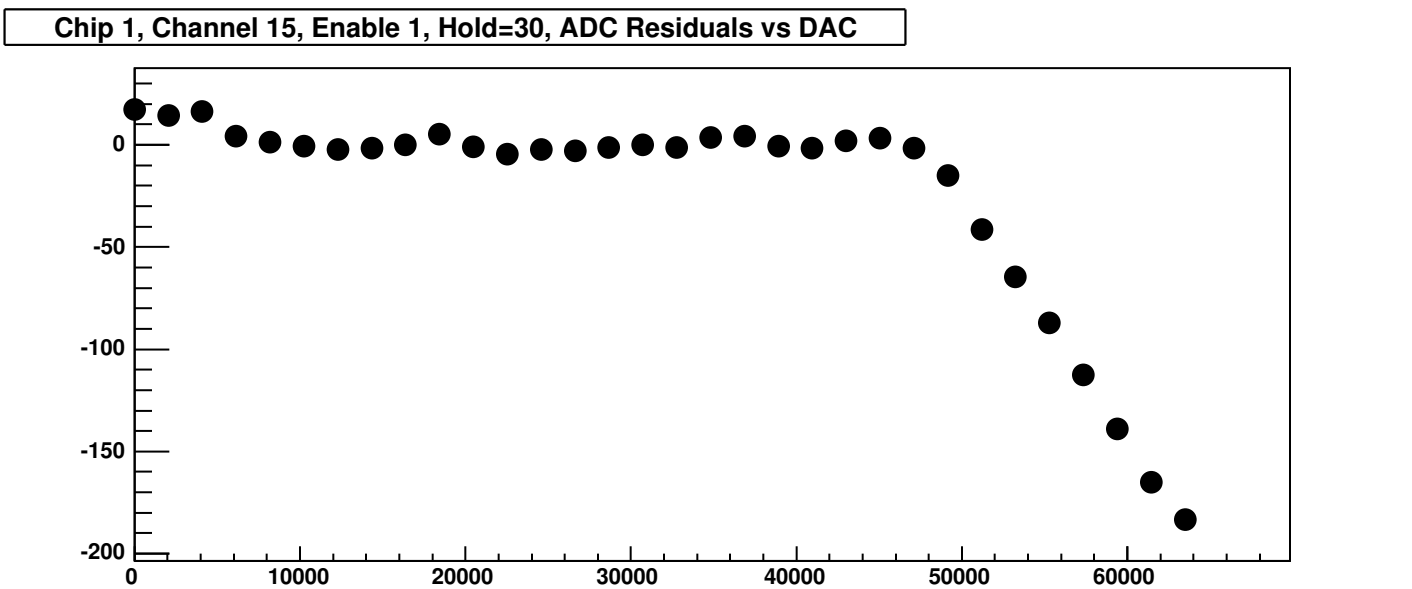

Chip 1, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4953 / 15
p0 997.9 ± 0.1147
p1 -0.0007708 ± 4.649e-06
1020
1000
980
960
940
920
900
880
860
840
0
10000
20000
30000
40000
50000
60000

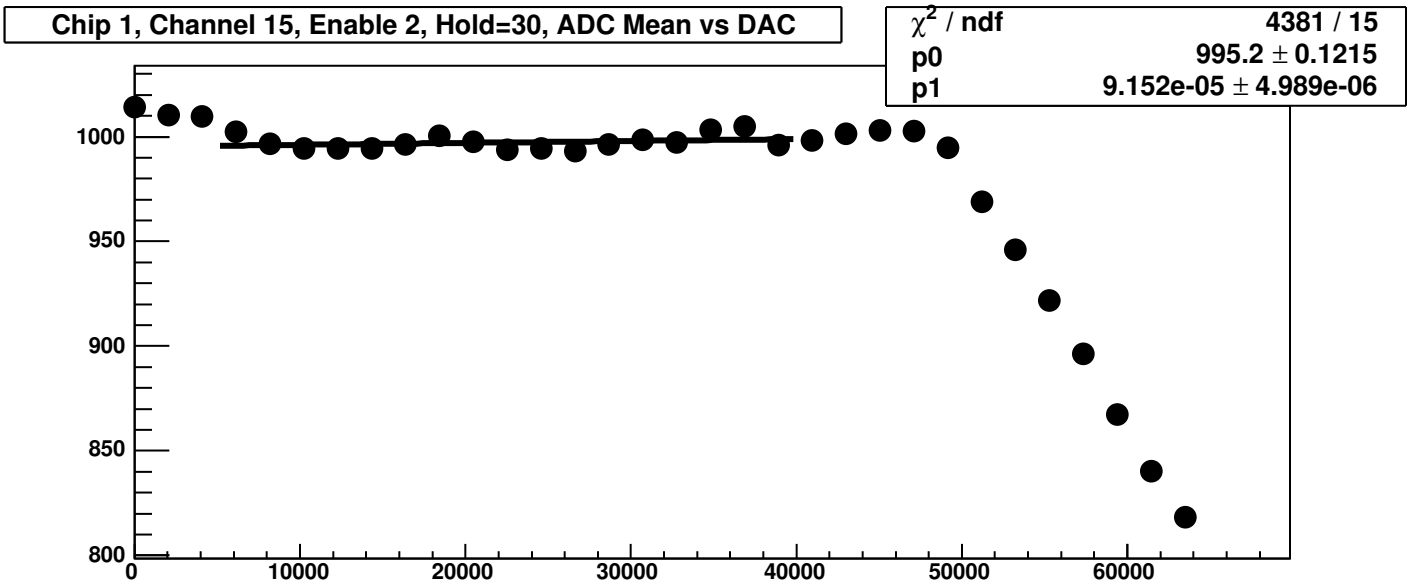

Chip 1, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

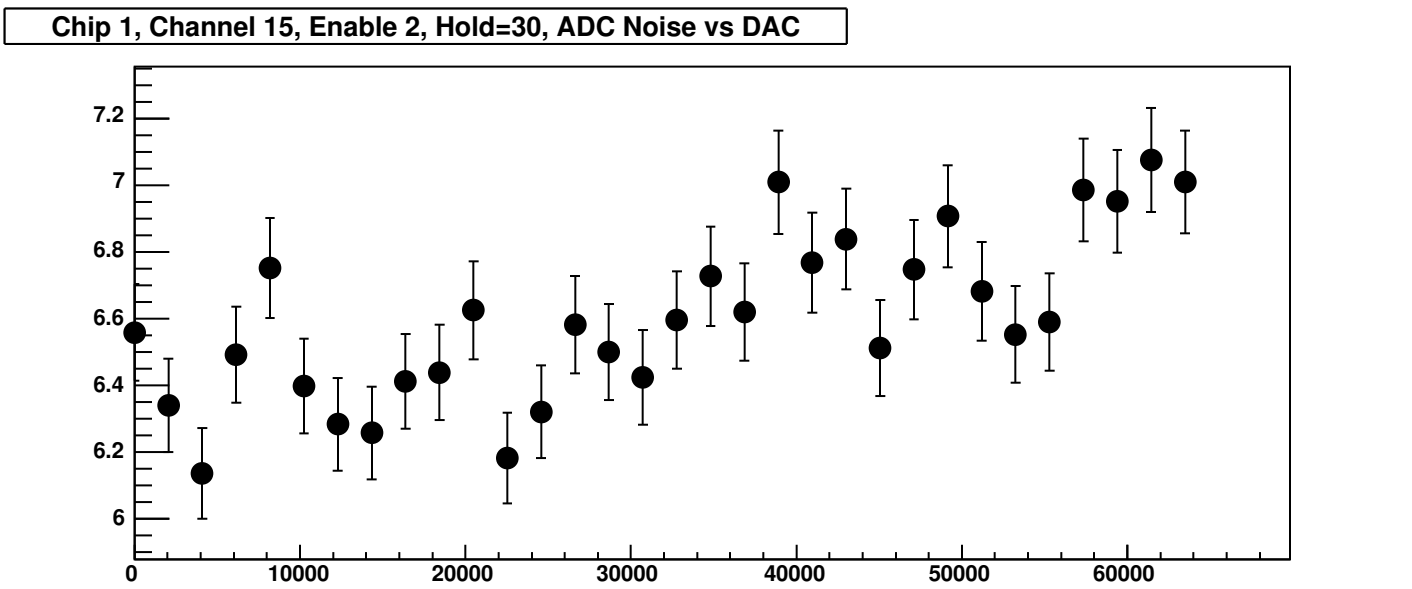

Chip 1, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

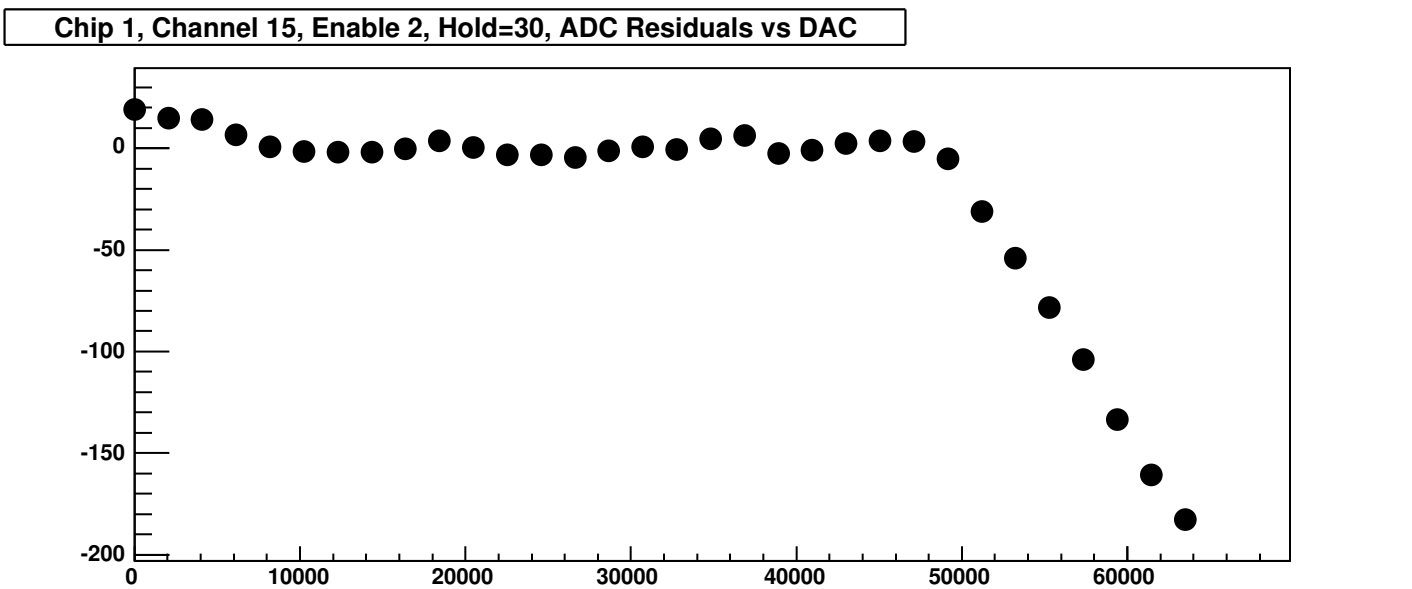

Chip 1, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3227 / 15
p0 997.2 ± 0.1181
p1 -0.0008458 ± 4.803e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

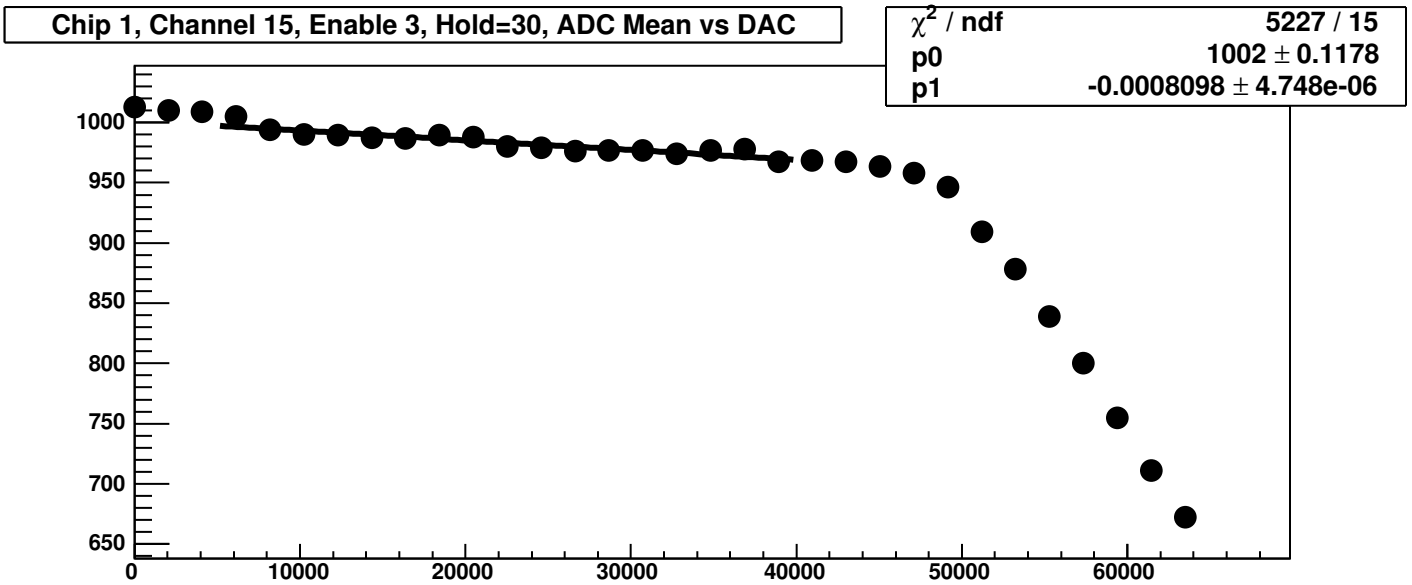

Chip 1, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

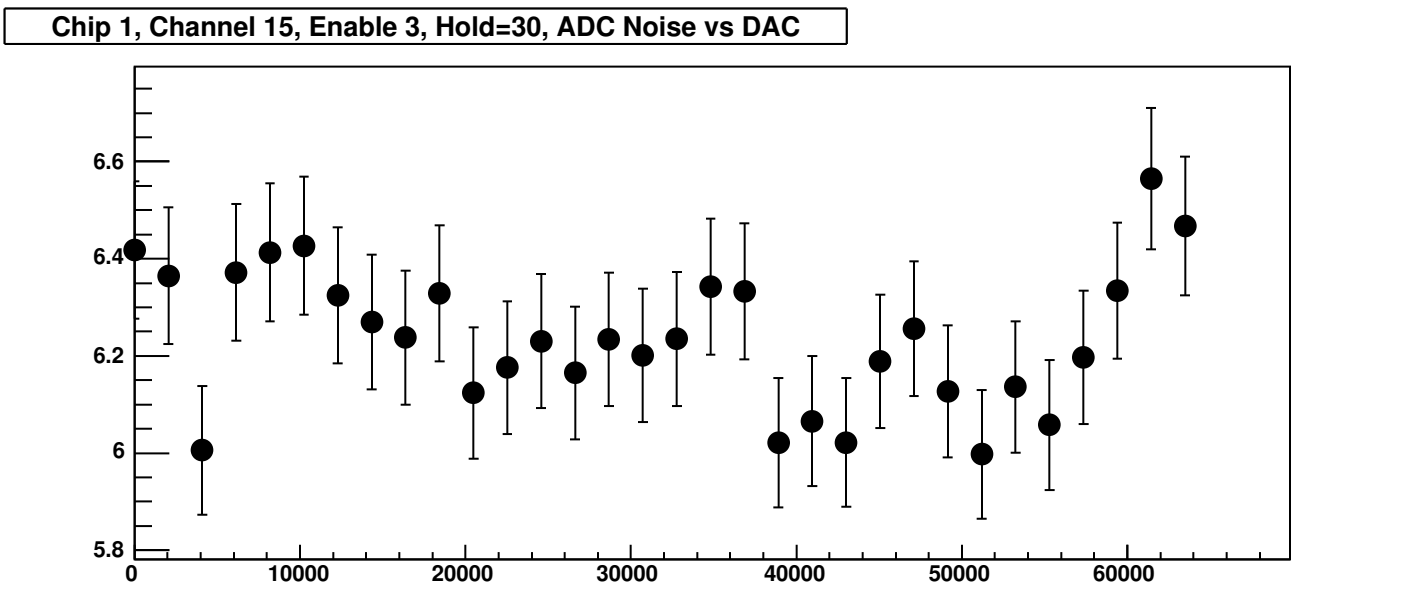

Chip 1, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

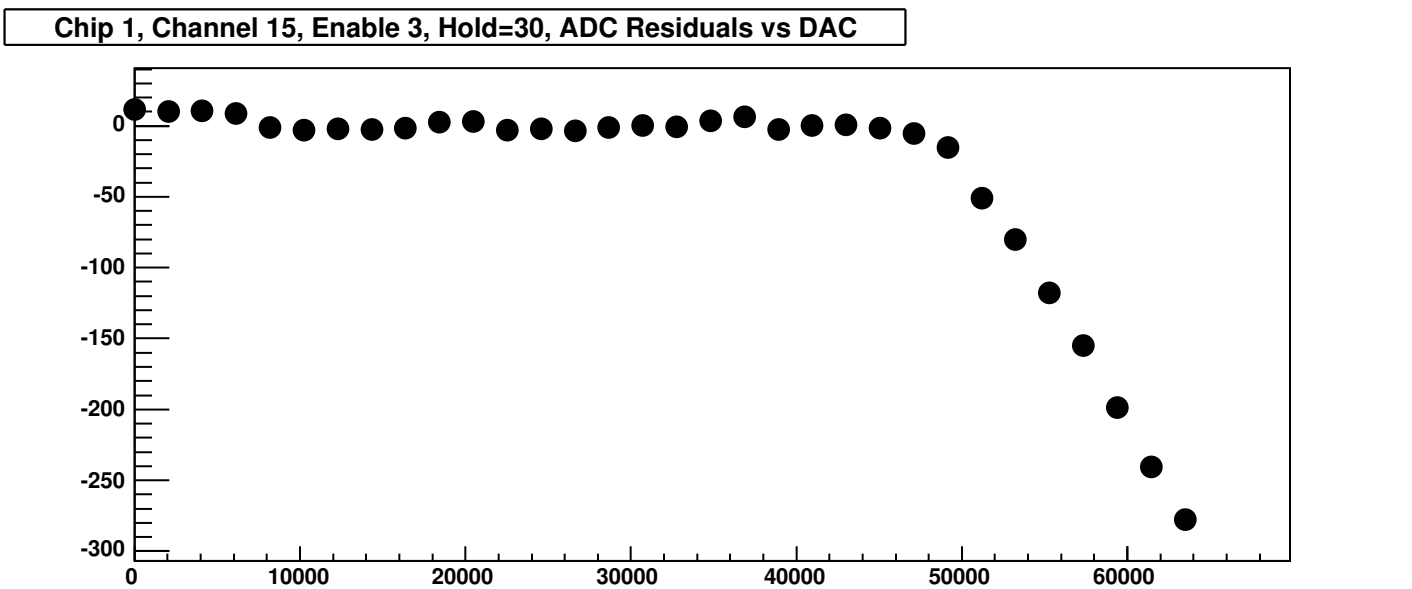

Chip 1, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4381 / 15
p0 995.2 ± 0.1215
p1 9.152e-05 ± 4.989e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

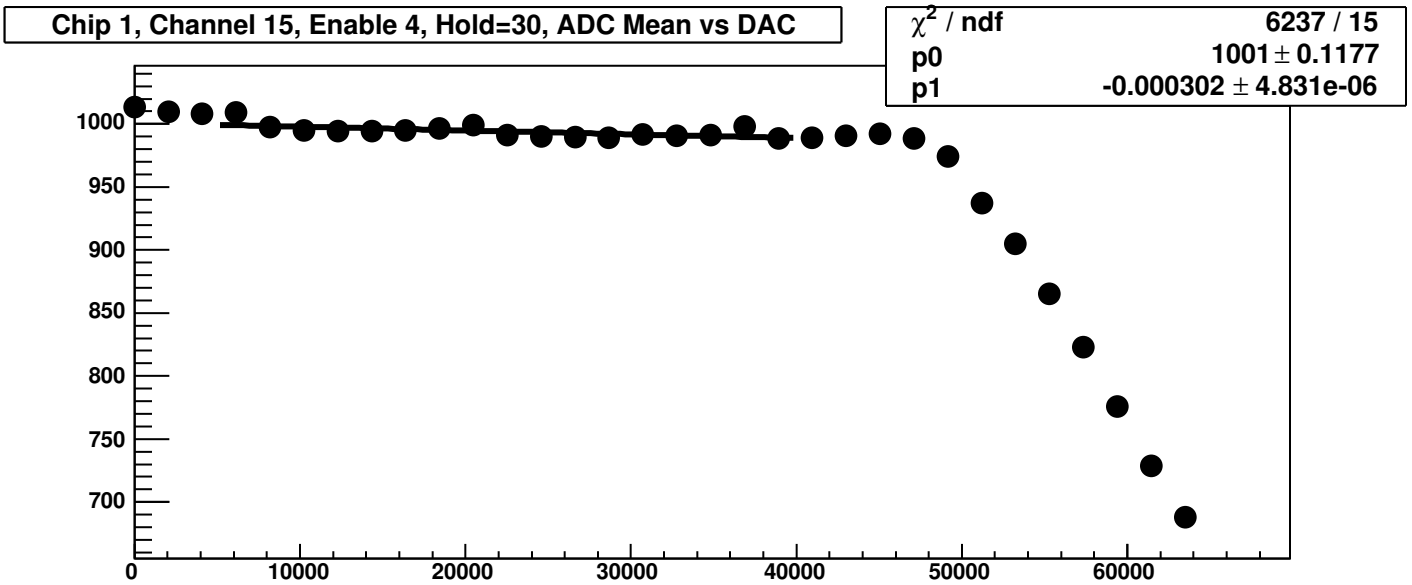

Chip 1, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

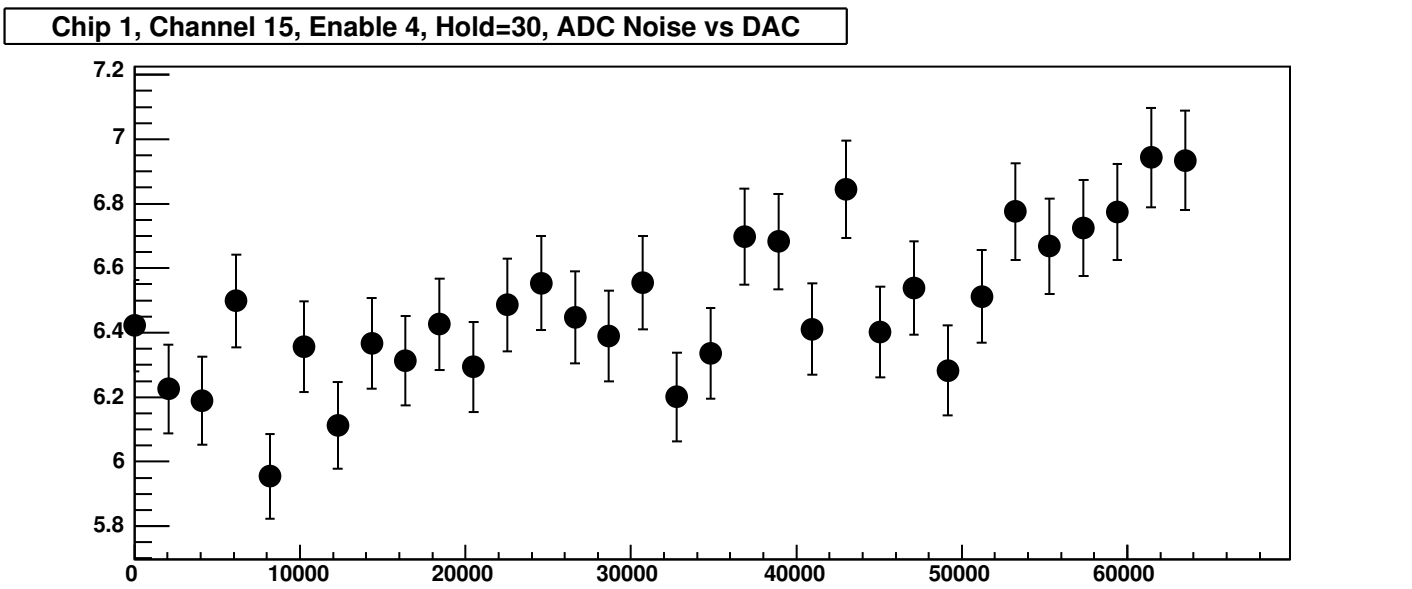

Chip 1, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

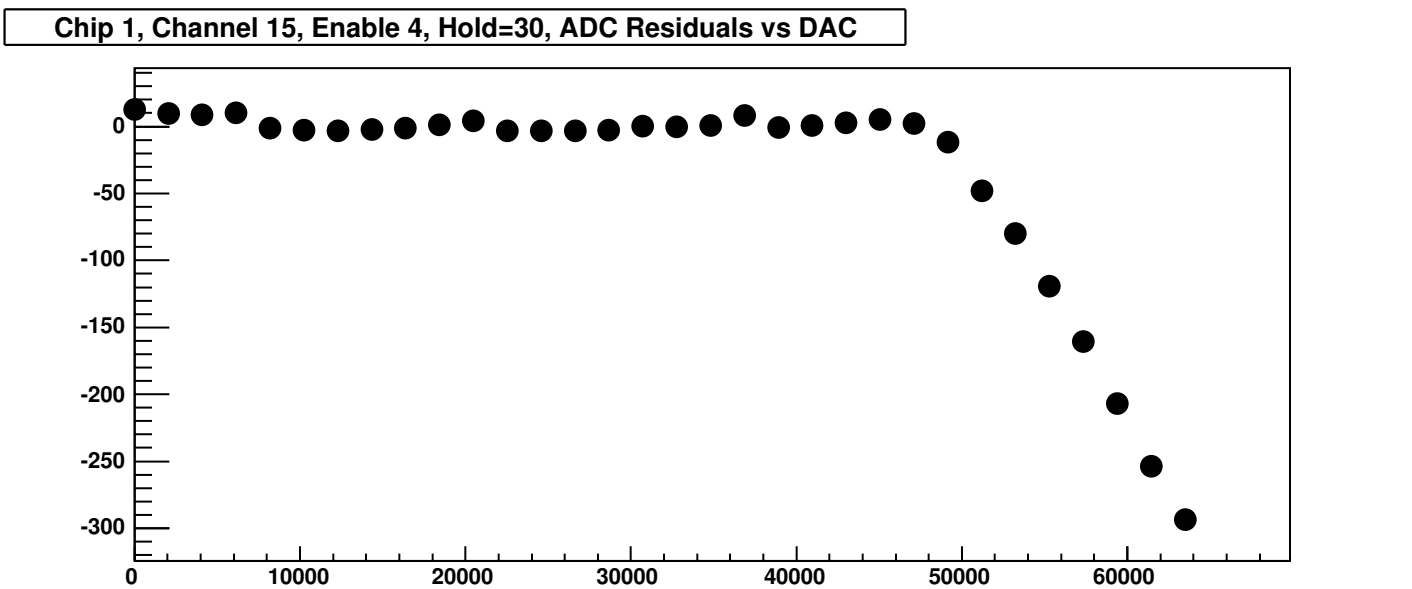

Chip 1, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5227 / 15
p0 1002 ± 0.1178
p1 -0.0008098 ± 4.748e-06
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

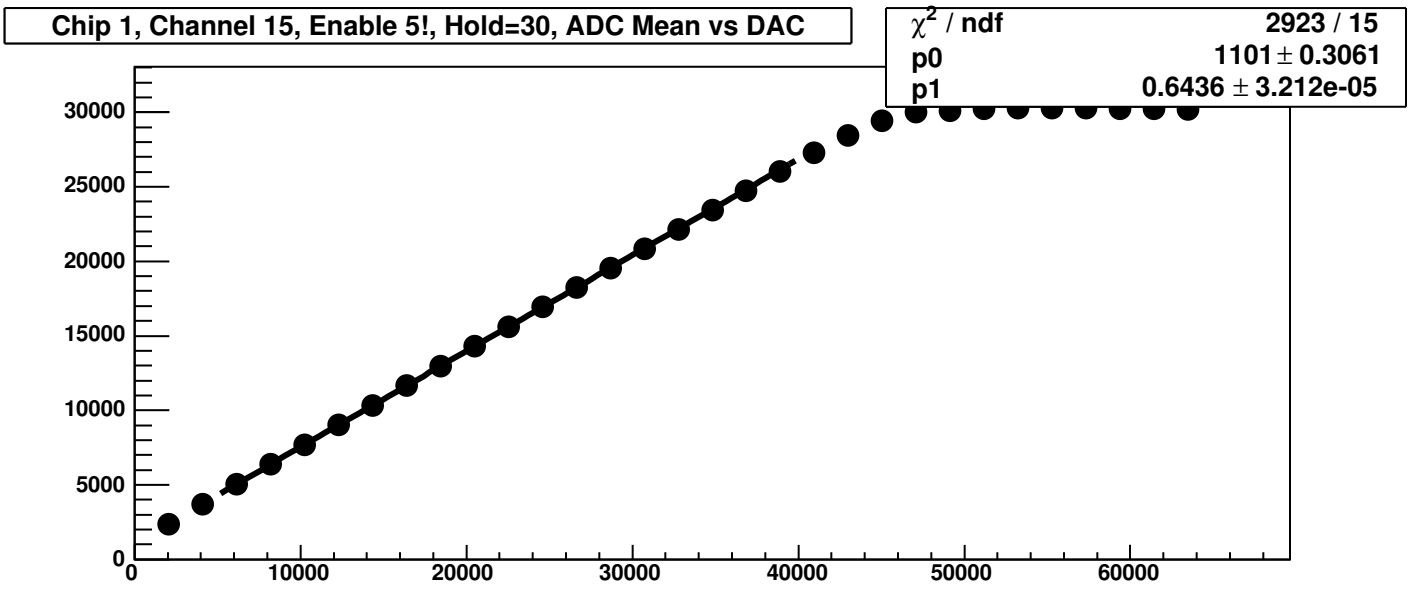

Chip 1, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

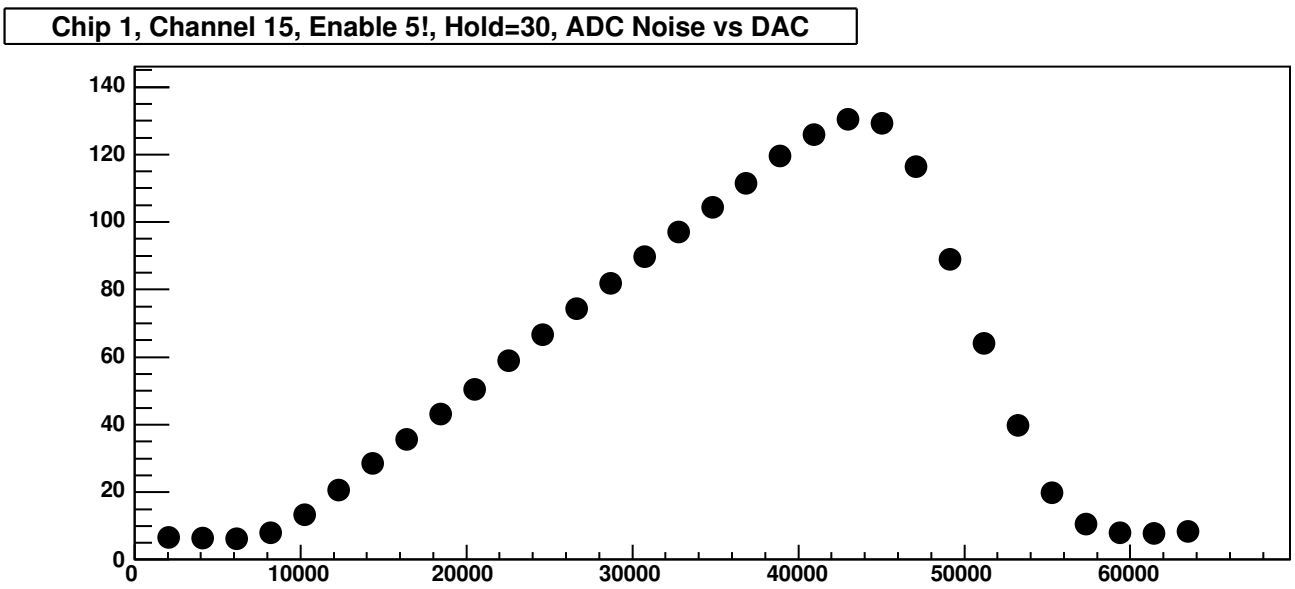

Chip 1, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

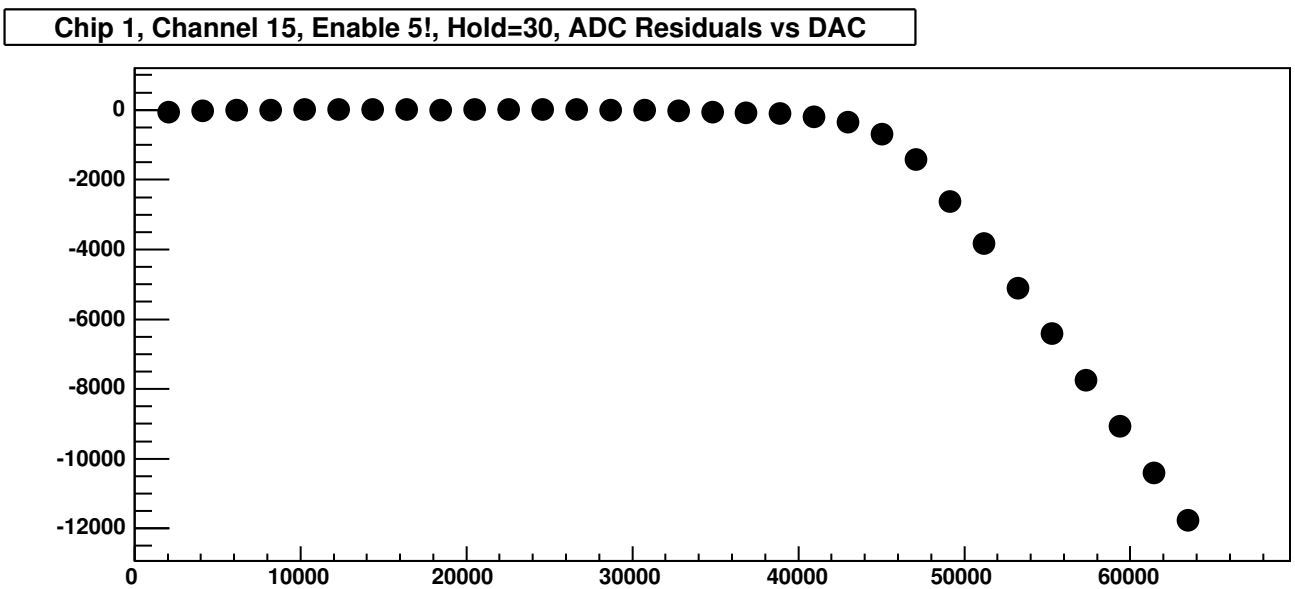

Chip 1, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6237 / 15
p0 1001 ± 0.1177
p1 -0.000302 ± 4.831e-06
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

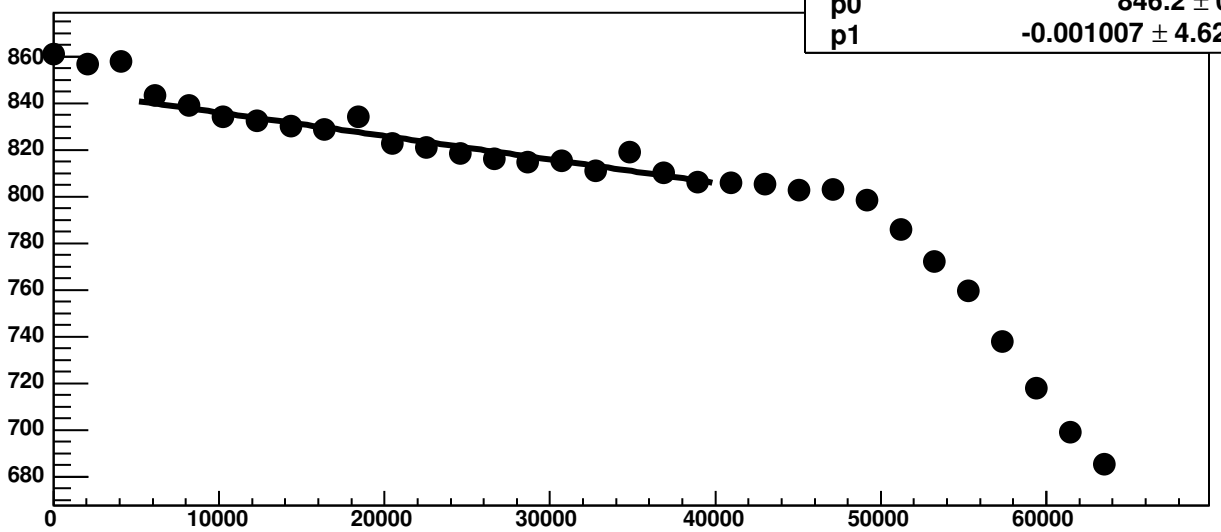

Chip 1, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 2923 / 15
p0 1101 ± 0.3061
p1 0.6436 ± 3.212e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

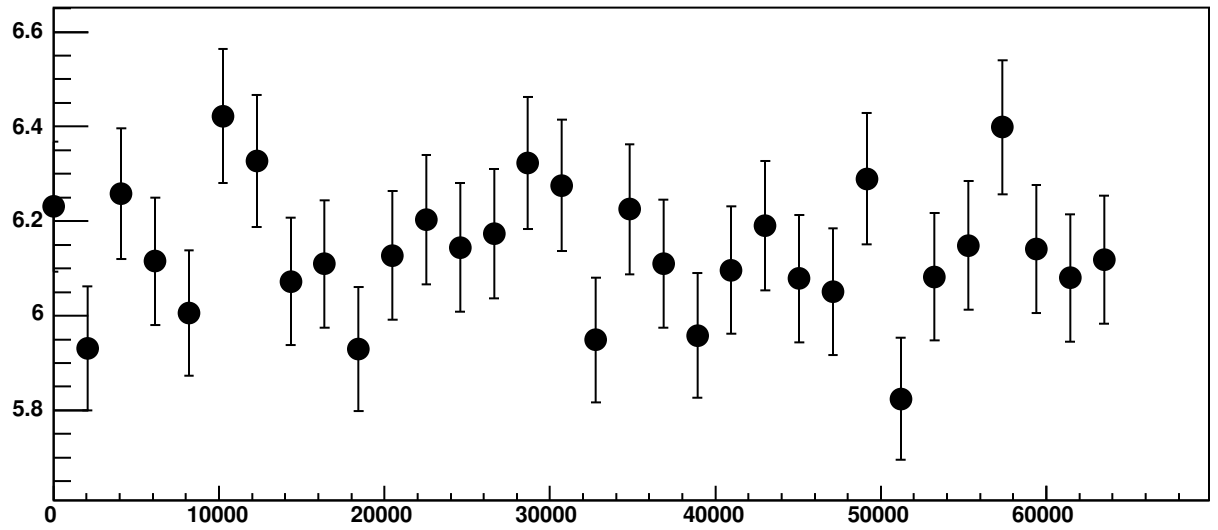

Chip 1, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

Chip 1, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC

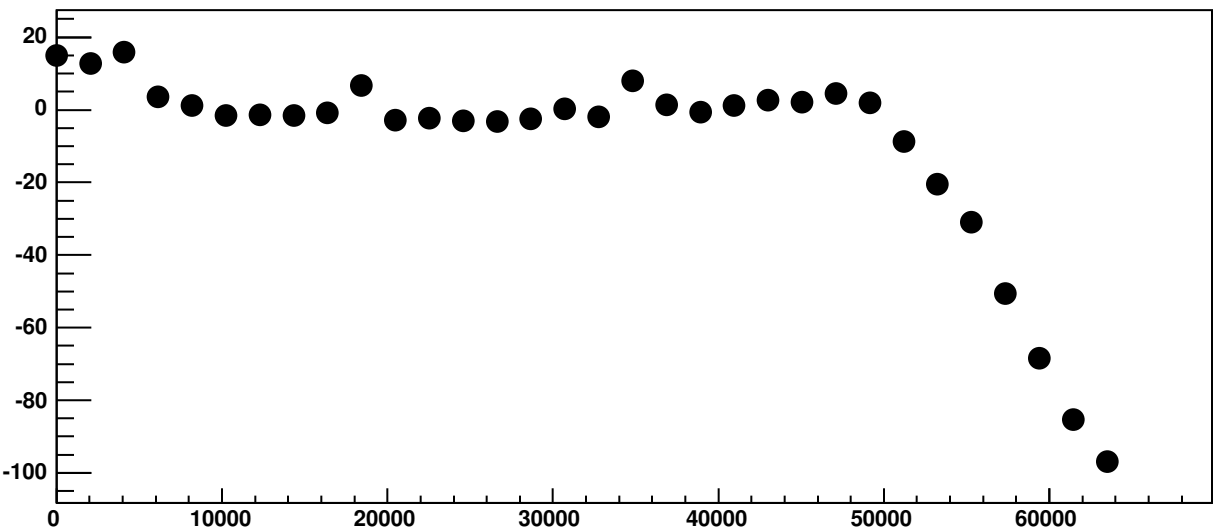
χ^2 / ndf	4754 / 15
p0	846.2 ± 0.1143
p1	-0.001007 ± 4.623e-06

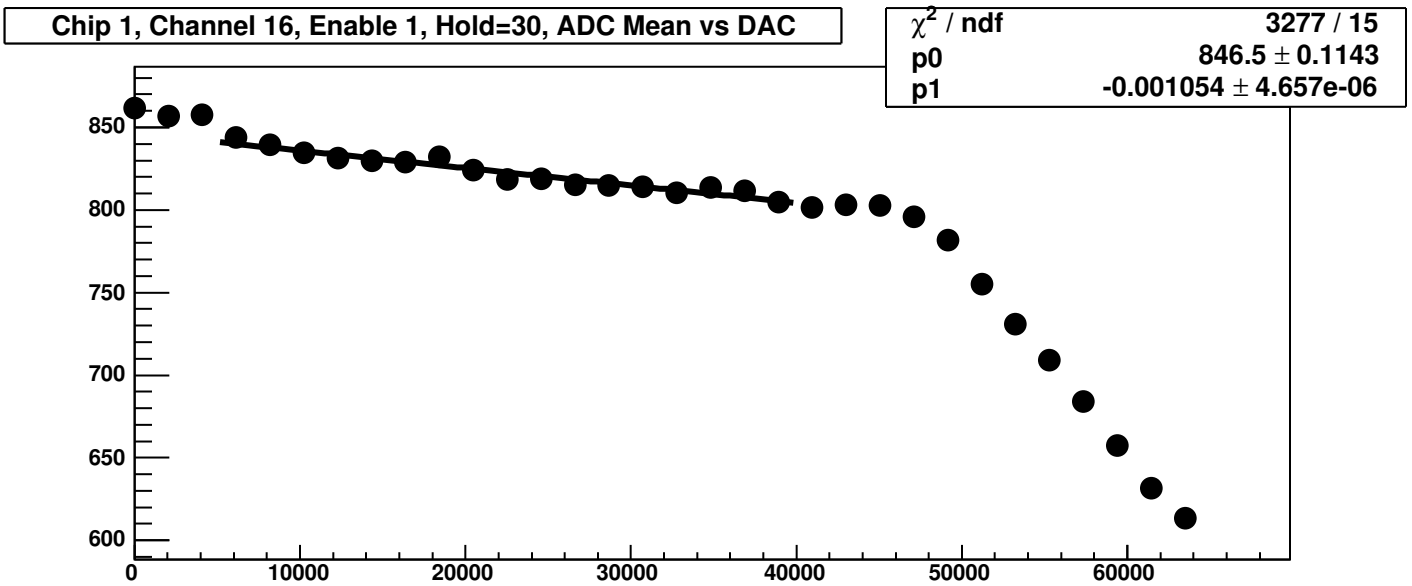


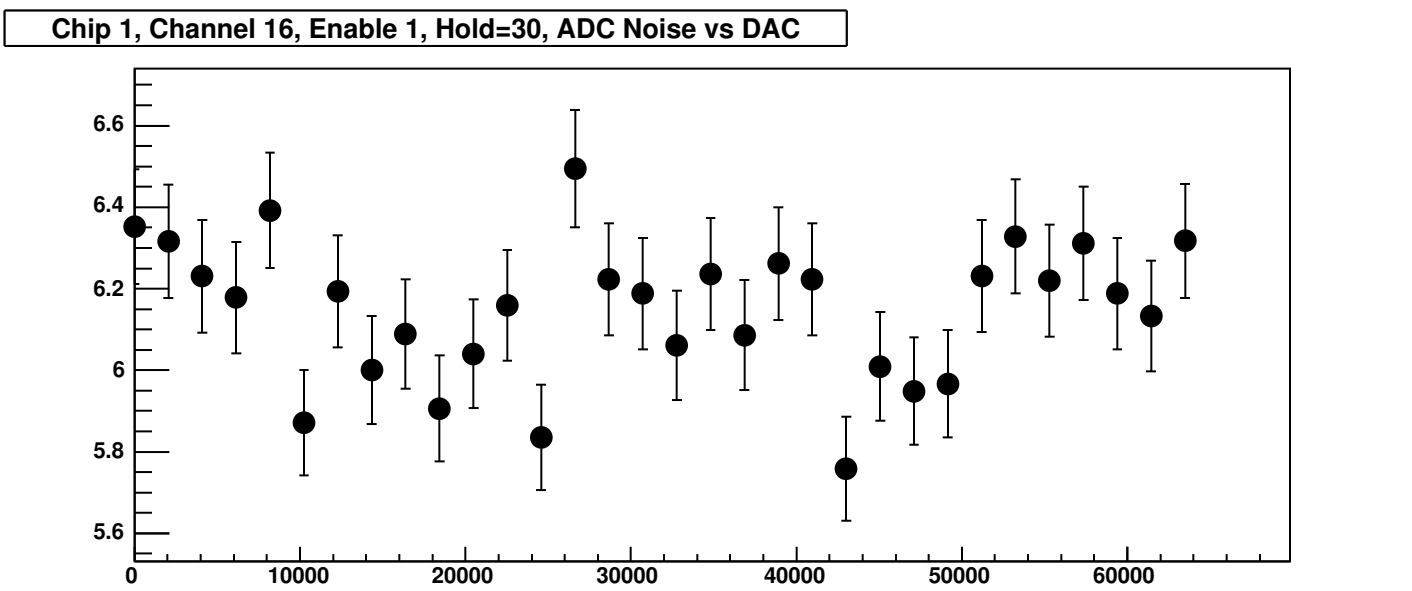
Chip 1, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC

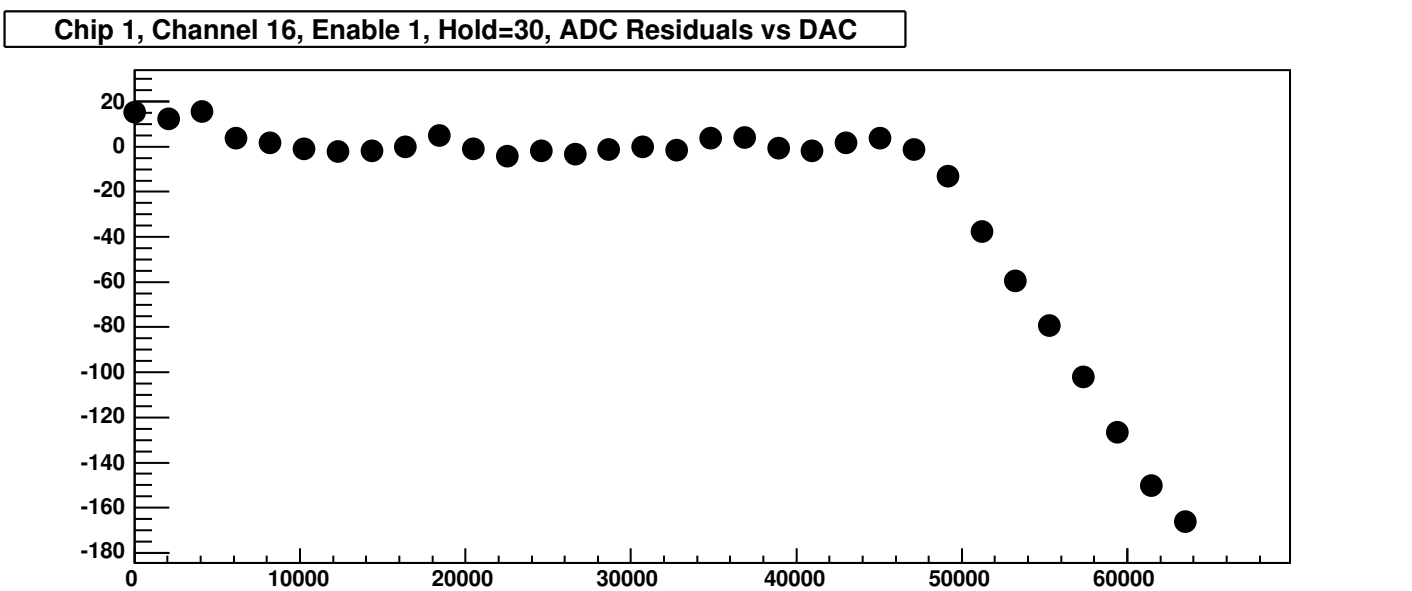


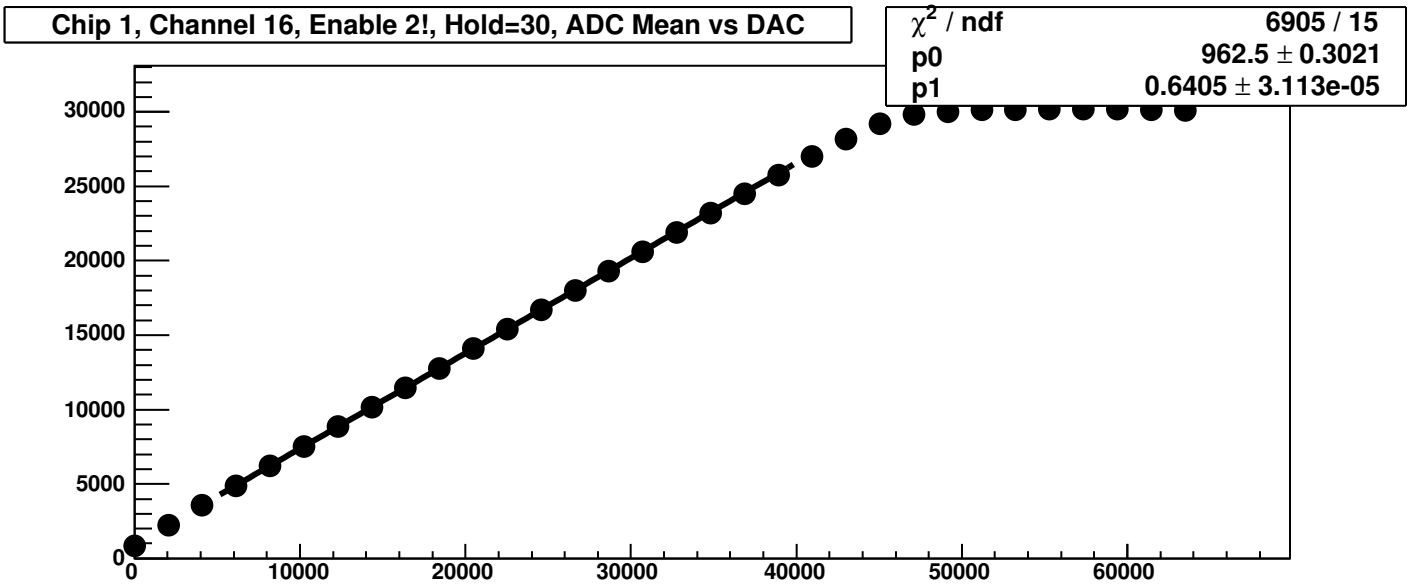
Chip 1, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC

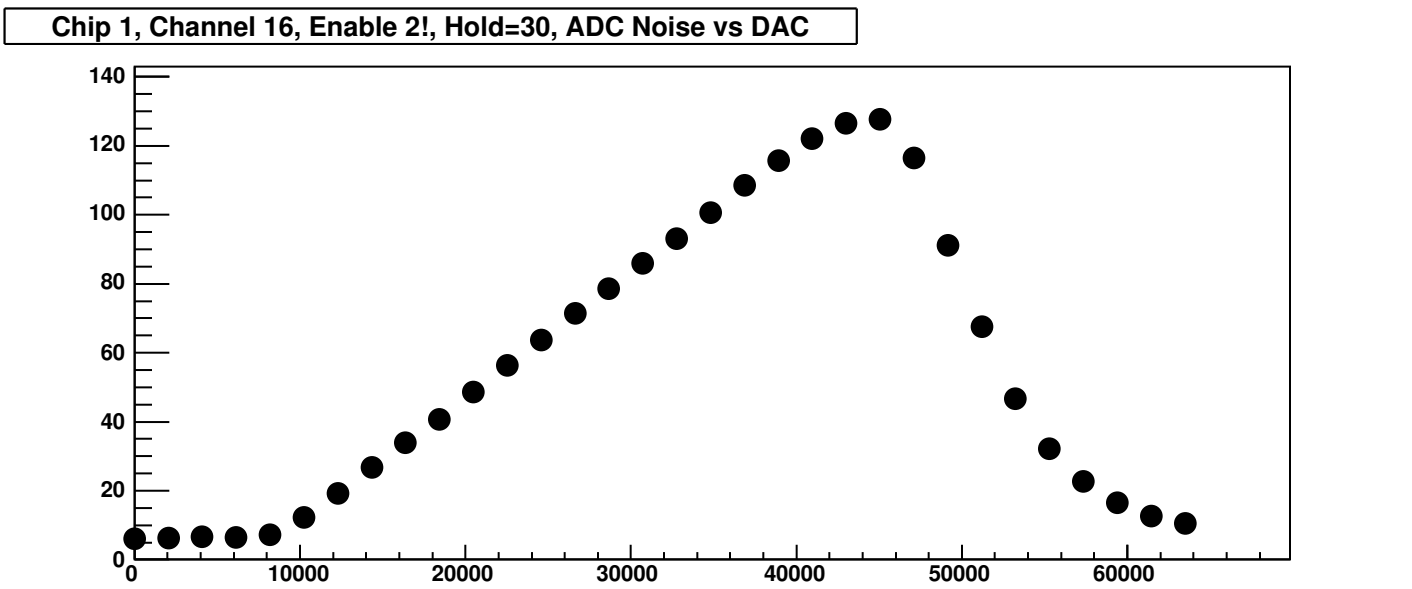


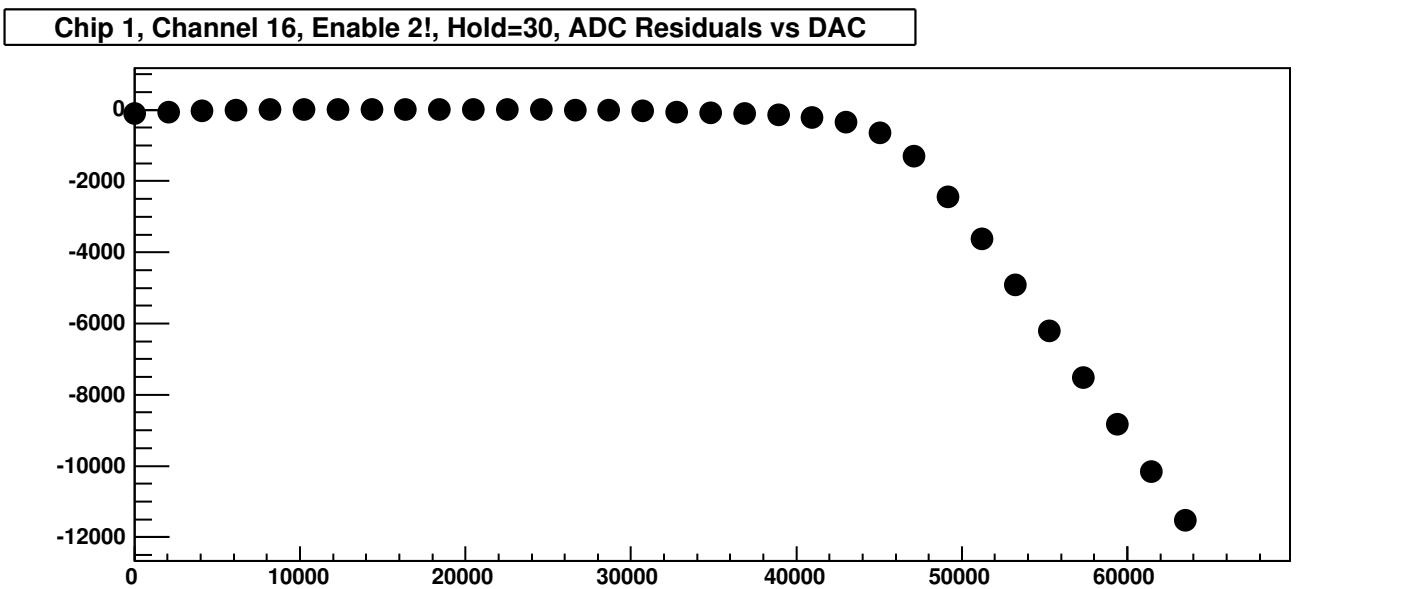

Chip 1, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3277 / 15
p0 846.5 ± 0.1143
p1 -0.001054 ± 4.657e-06
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

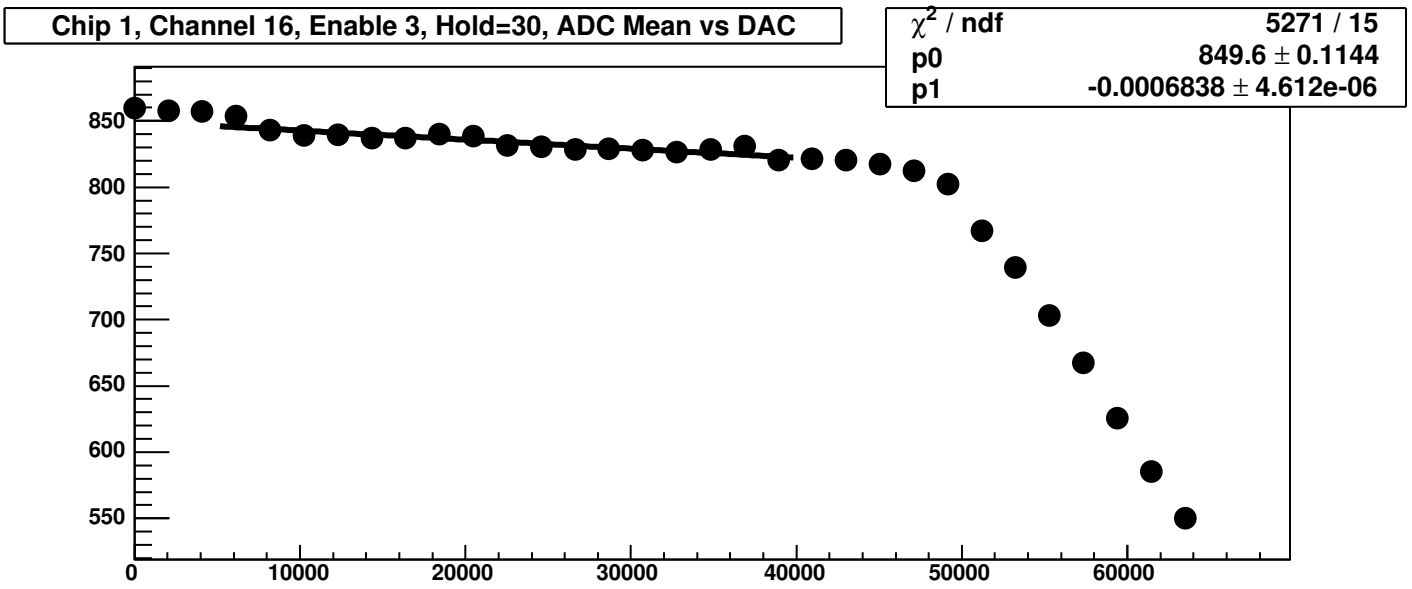

Chip 1, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

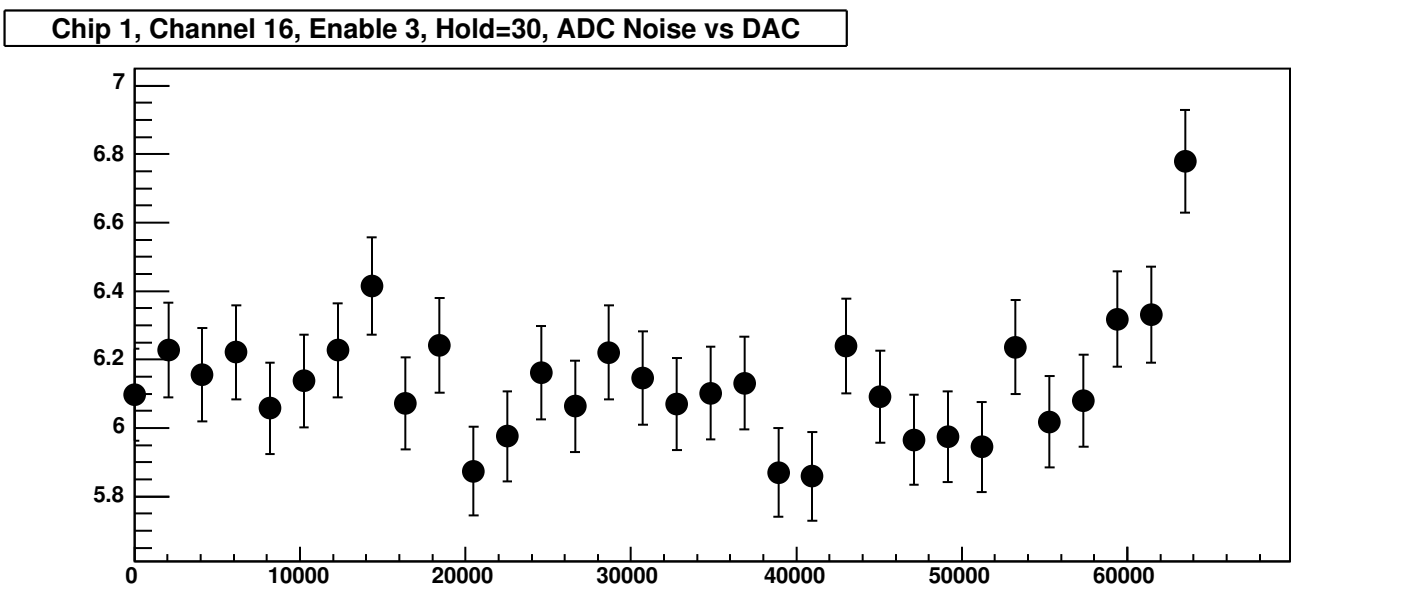

Chip 1, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

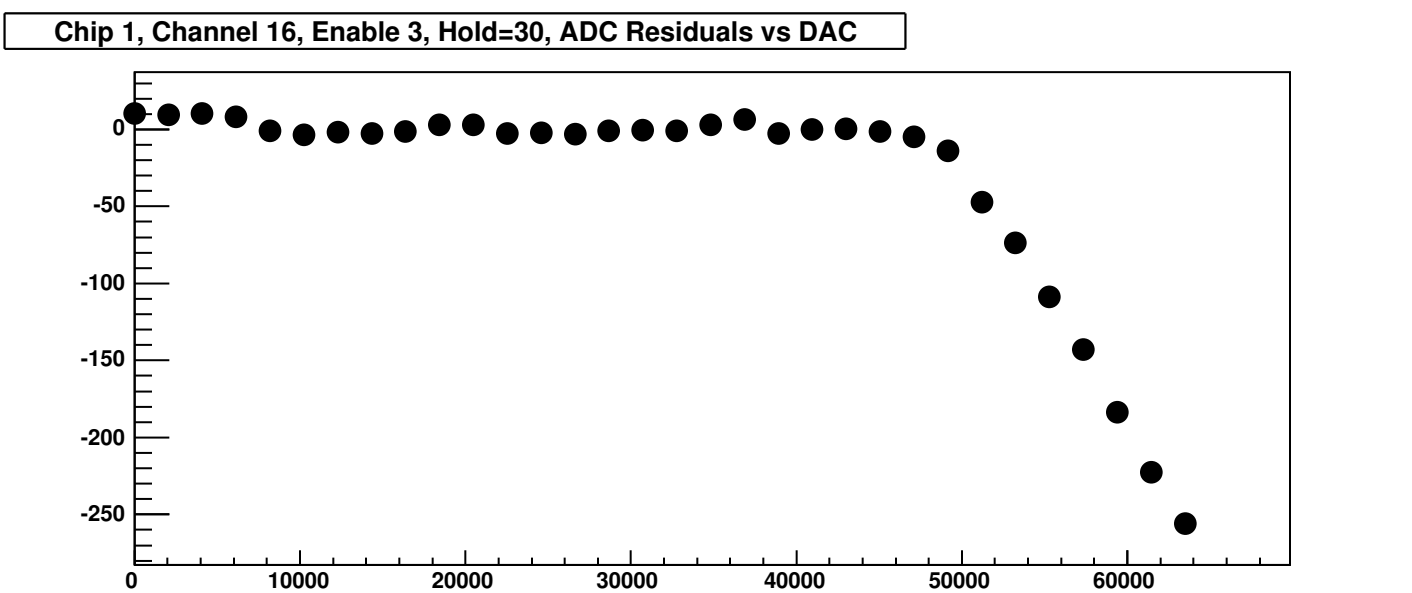

Chip 1, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 6905 / 15
p0 962.5 ± 0.3021
p1 0.6405 ± 3.113e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

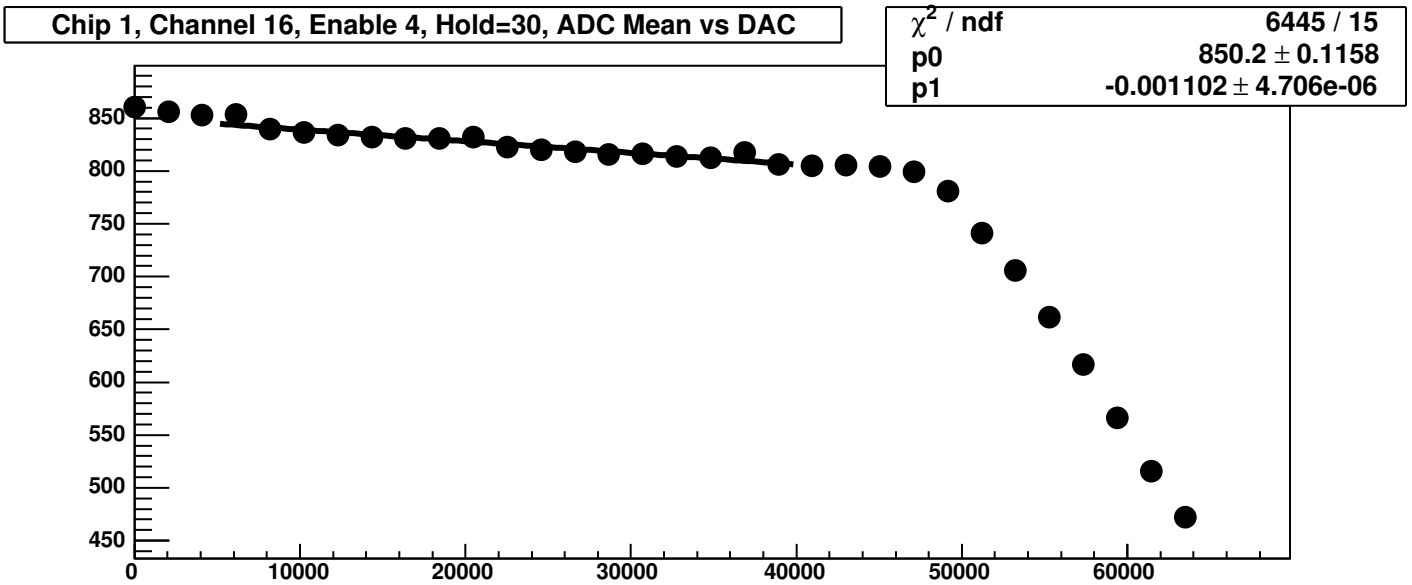

Chip 1, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

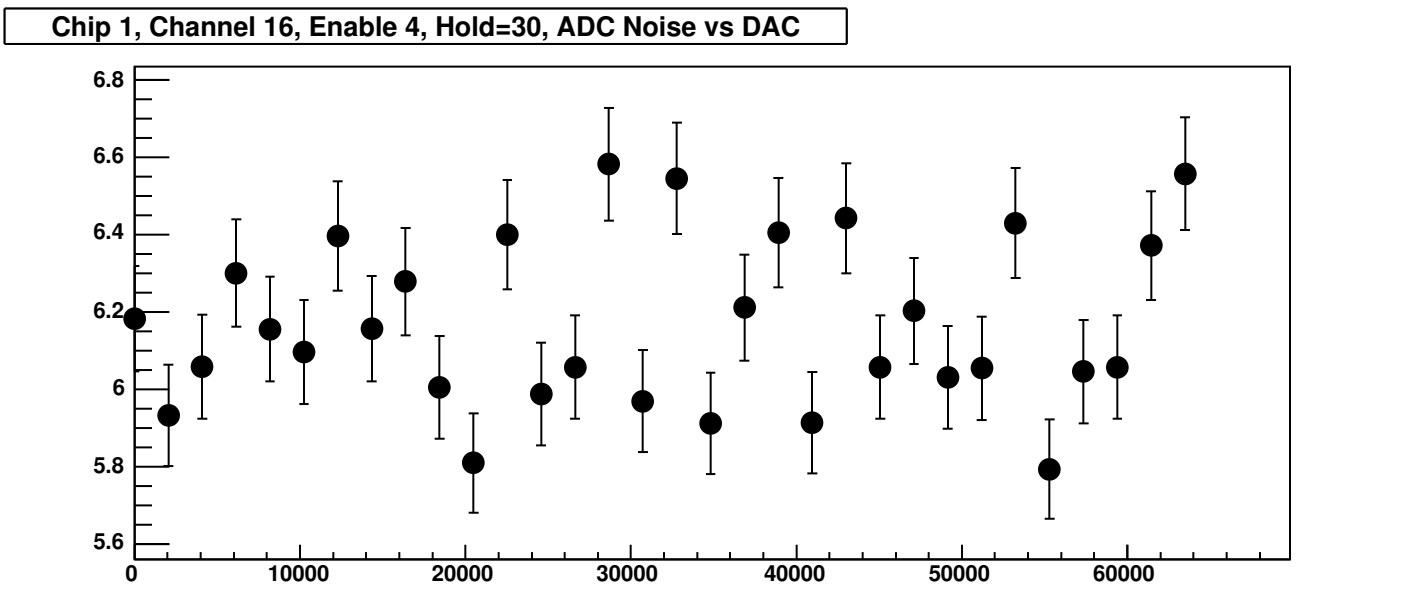

Chip 1, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

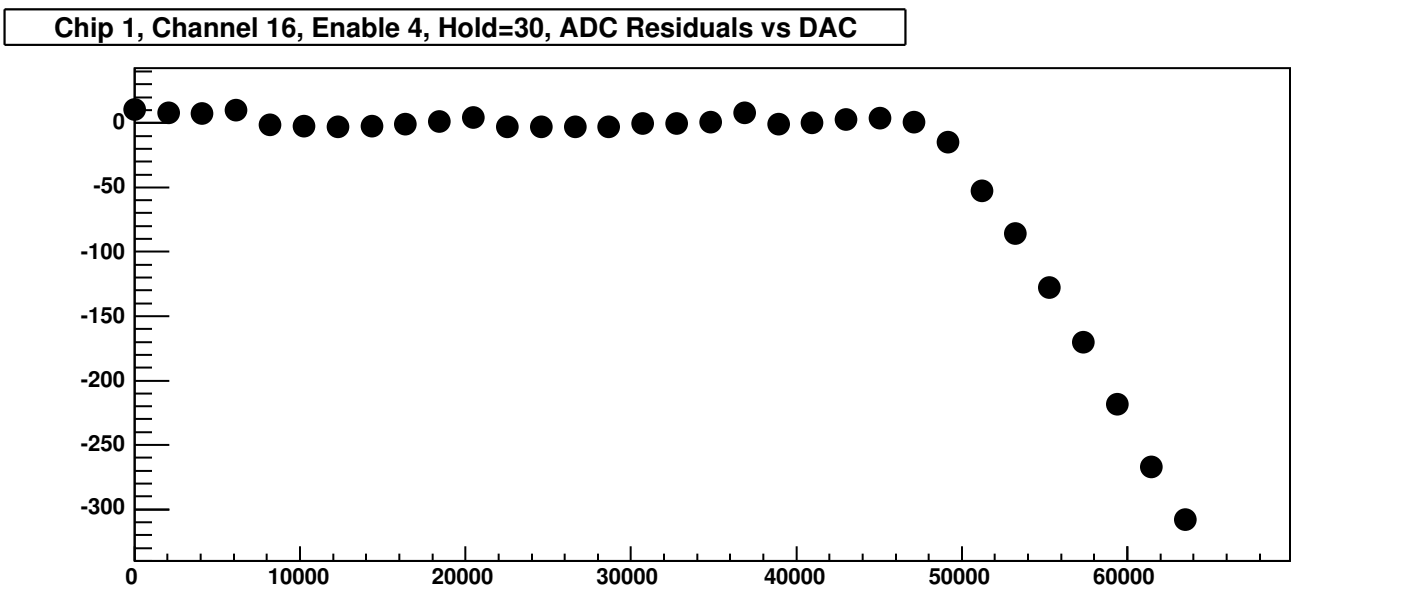

Chip 1, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5271 / 15
p0 849.6 ± 0.1144
p1 -0.0006838 ± 4.612e-06
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

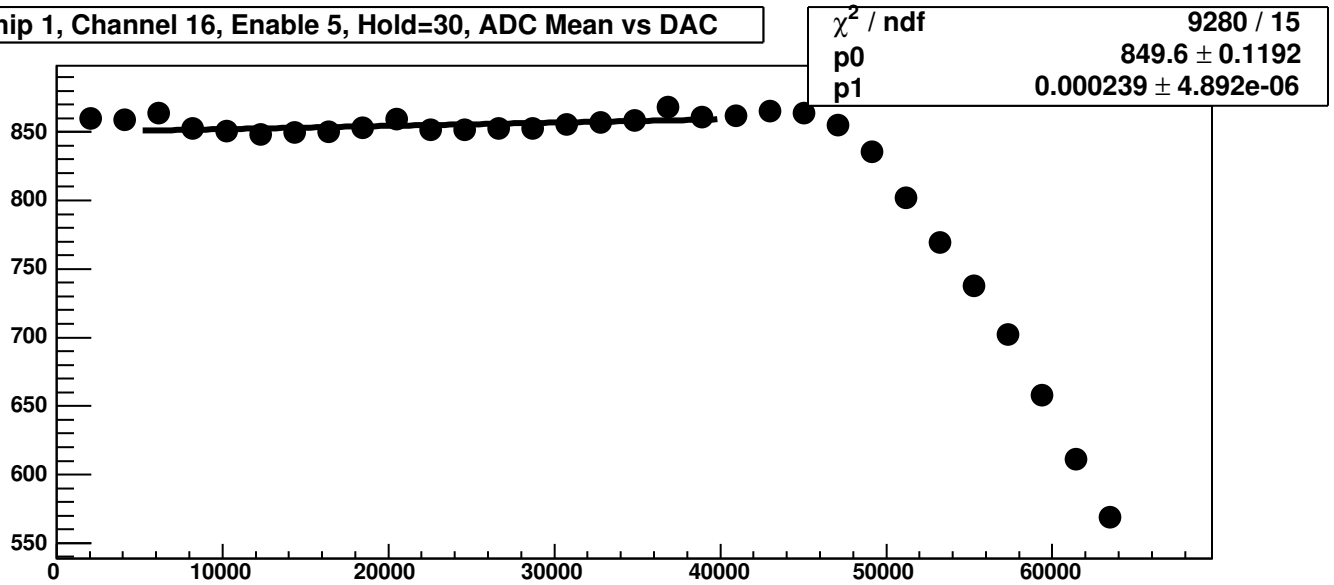

Chip 1, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6445 / 15
p0 850.2 ± 0.1158
p1 -0.001102 ± 4.706e-06
850
800
750
700
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000

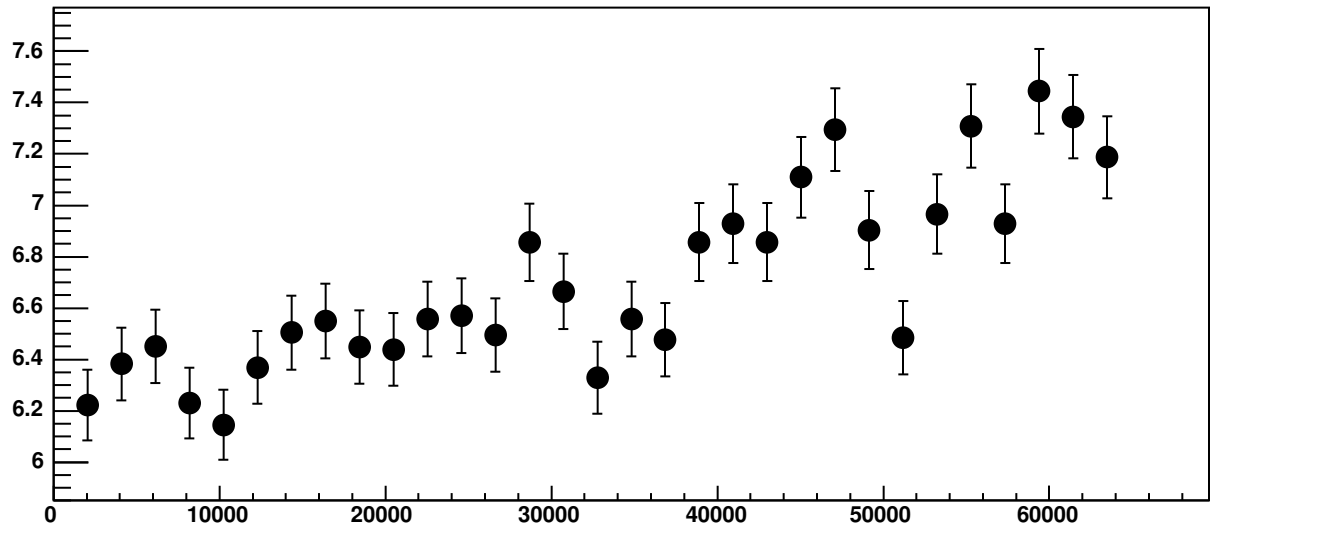

Chip 1, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

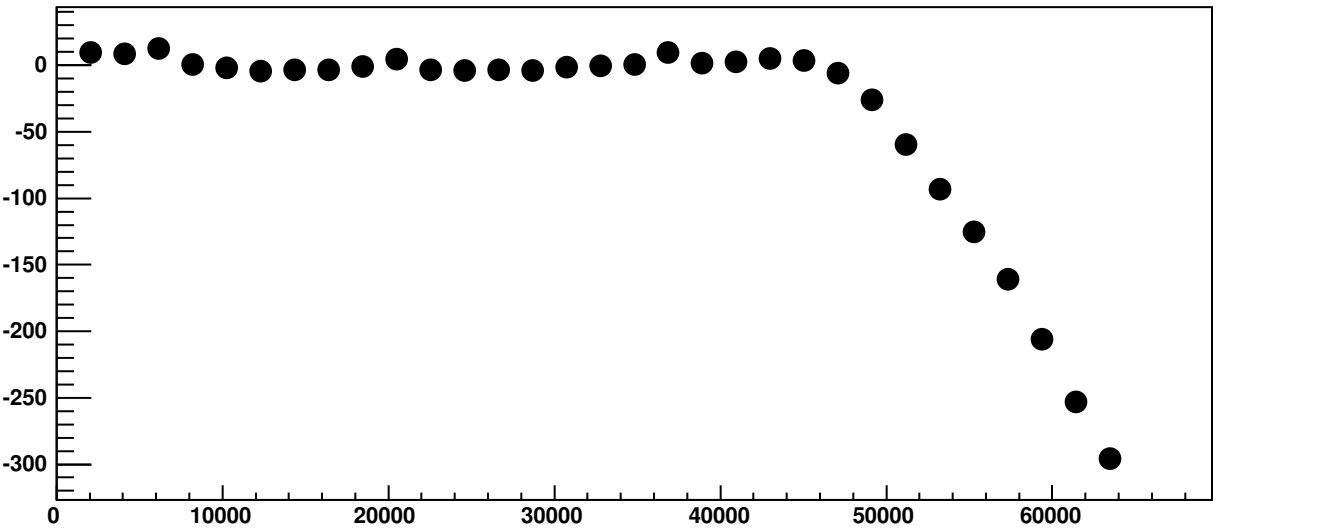
Chip 1, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC

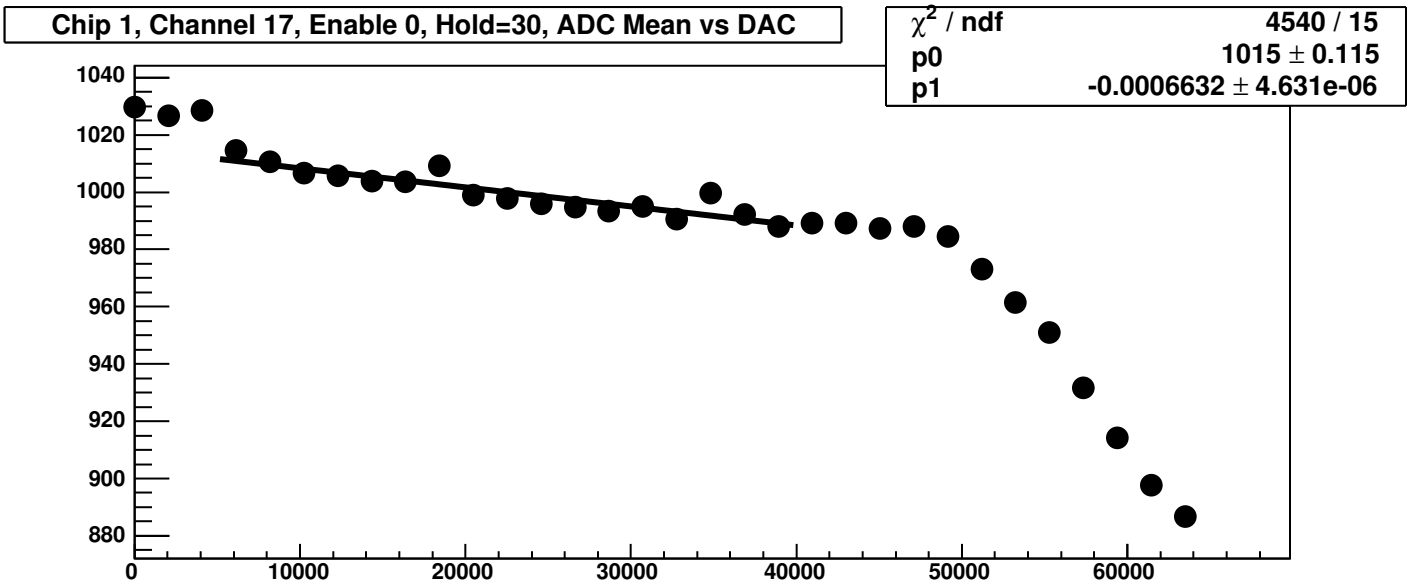


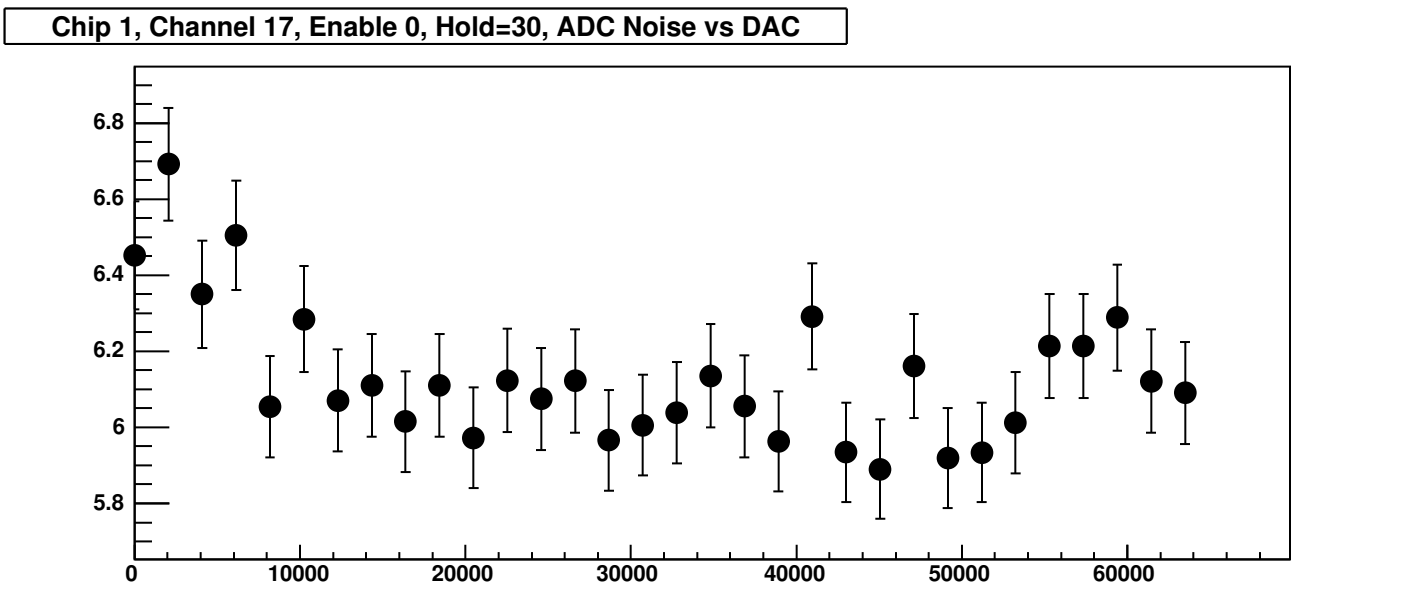
Chip 1, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC

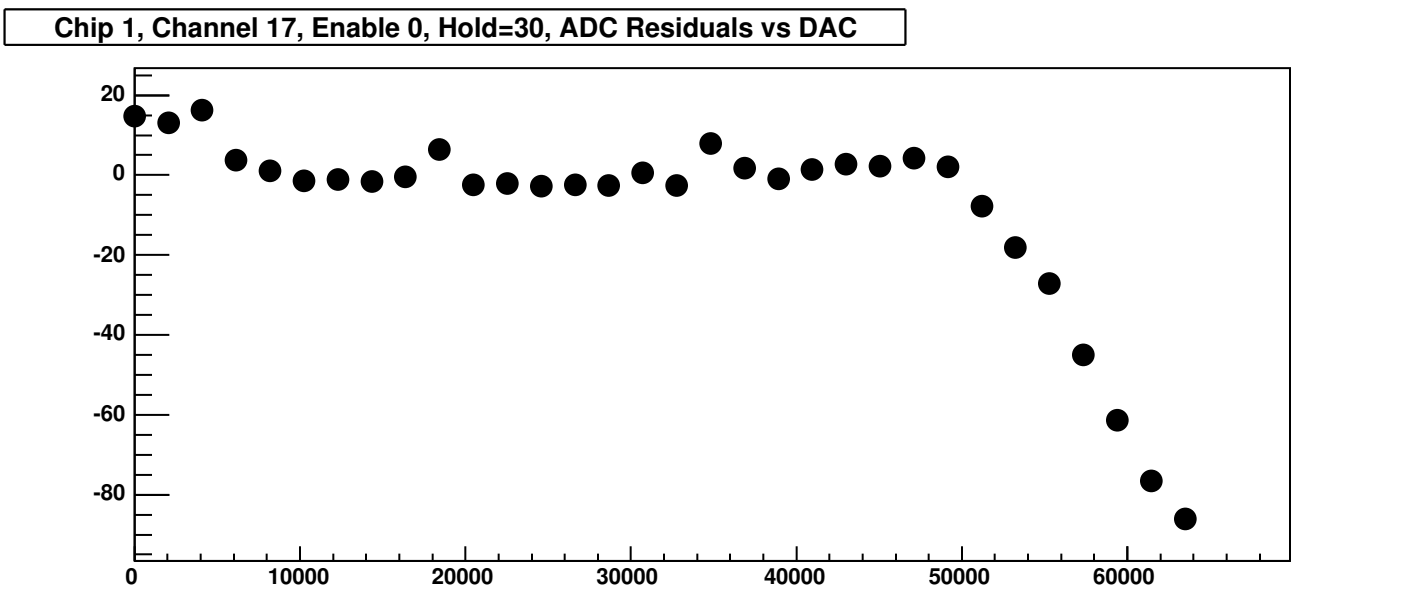


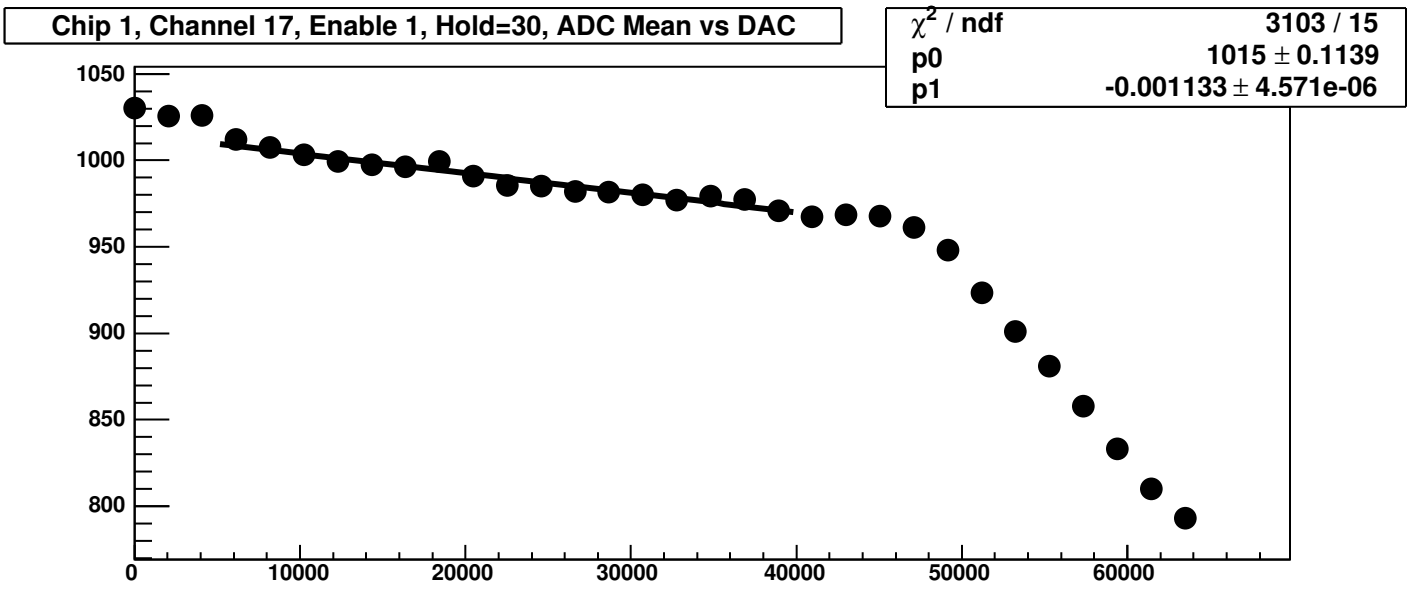
Chip 1, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC

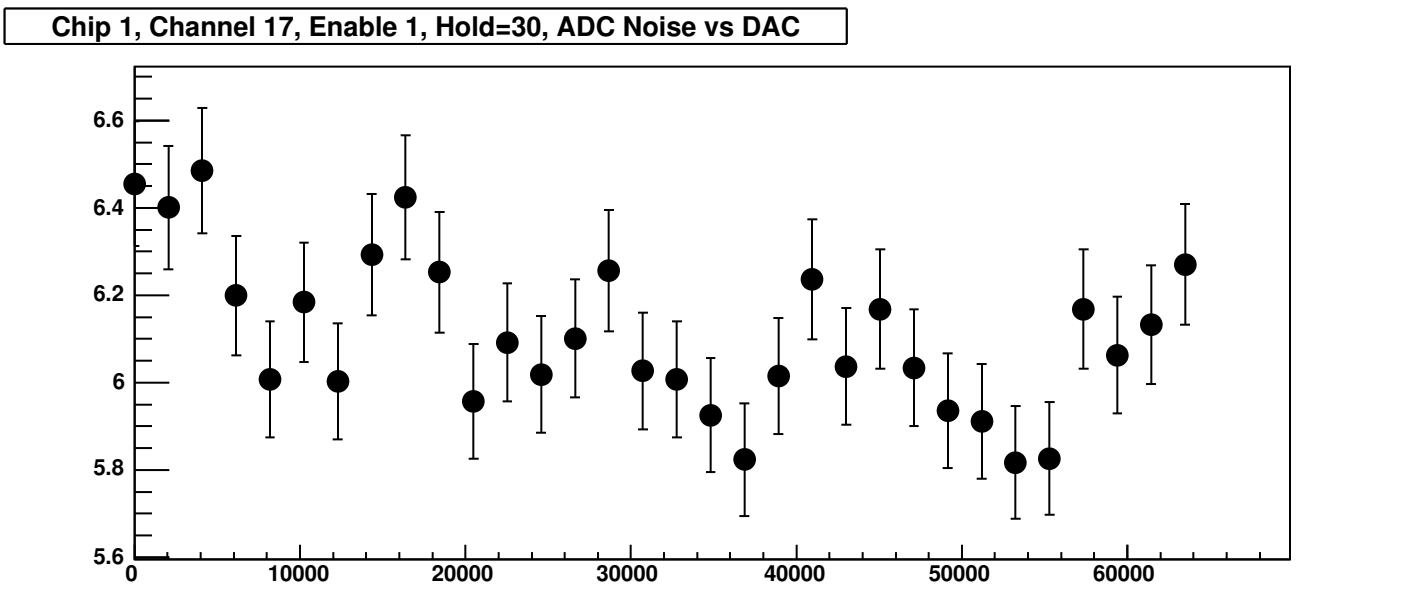


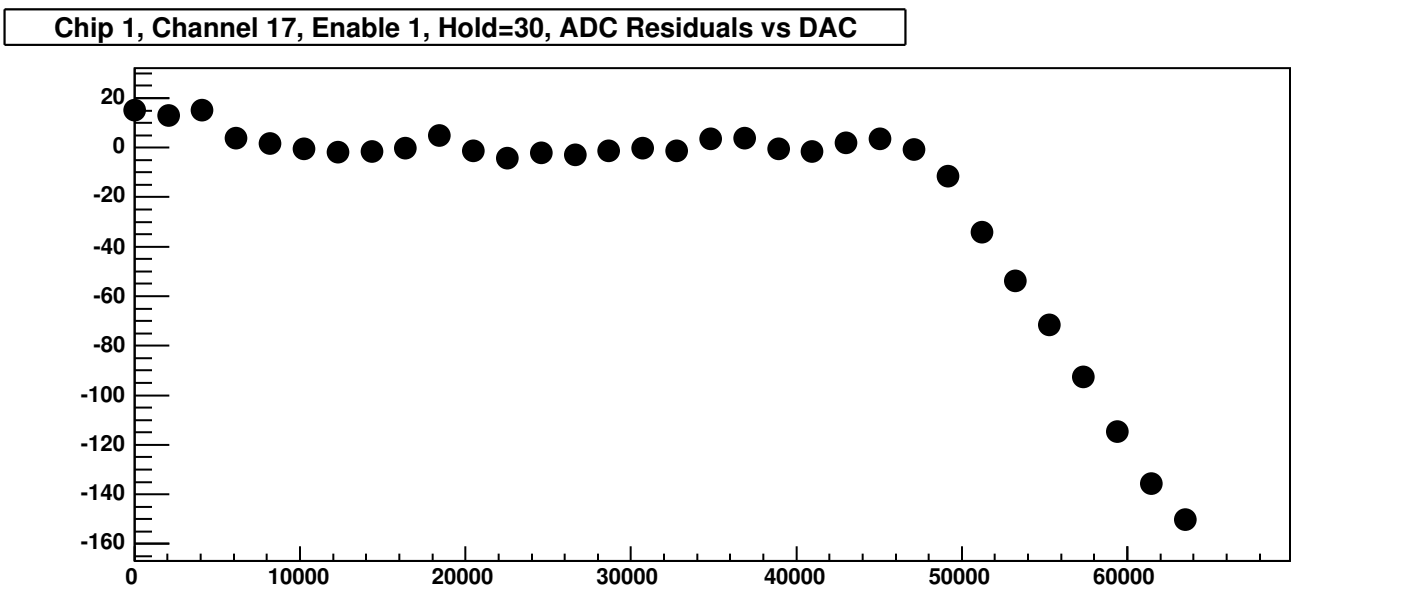

Chip 1, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4540 / 15
p0 1015 ± 0.115
p1 -0.0006632 ± 4.631e-06
1040
1020
1000
980
960
940
920
900
880
0
10000
20000
30000
40000
50000
60000

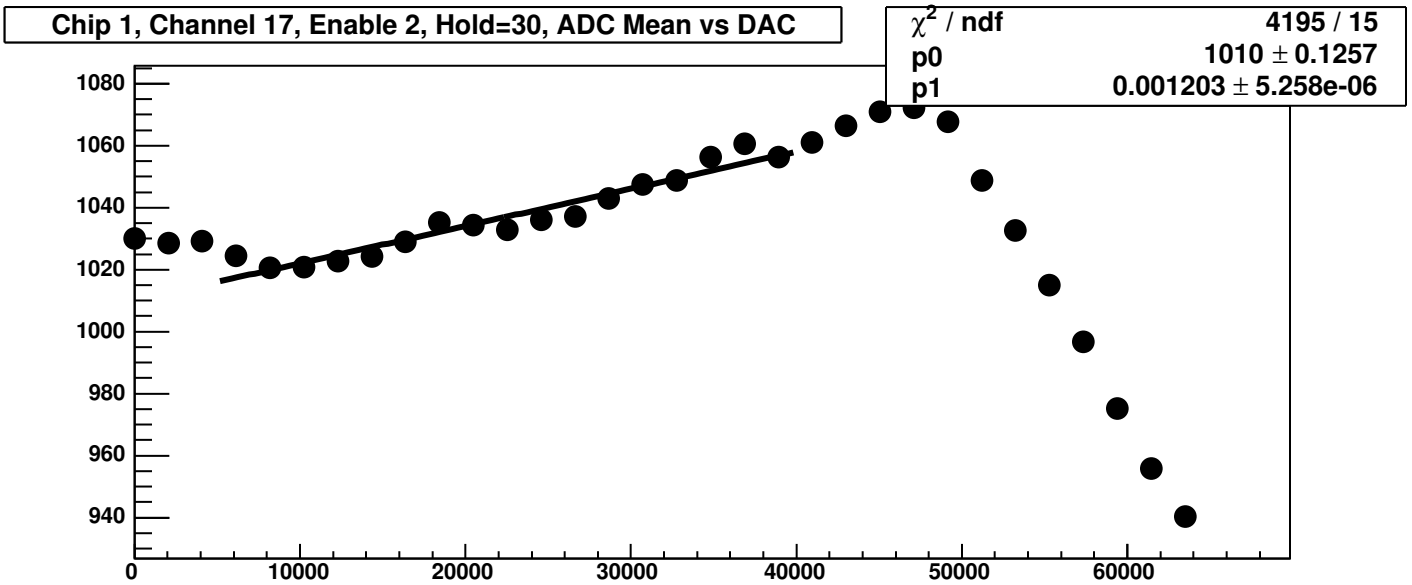

Chip 1, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

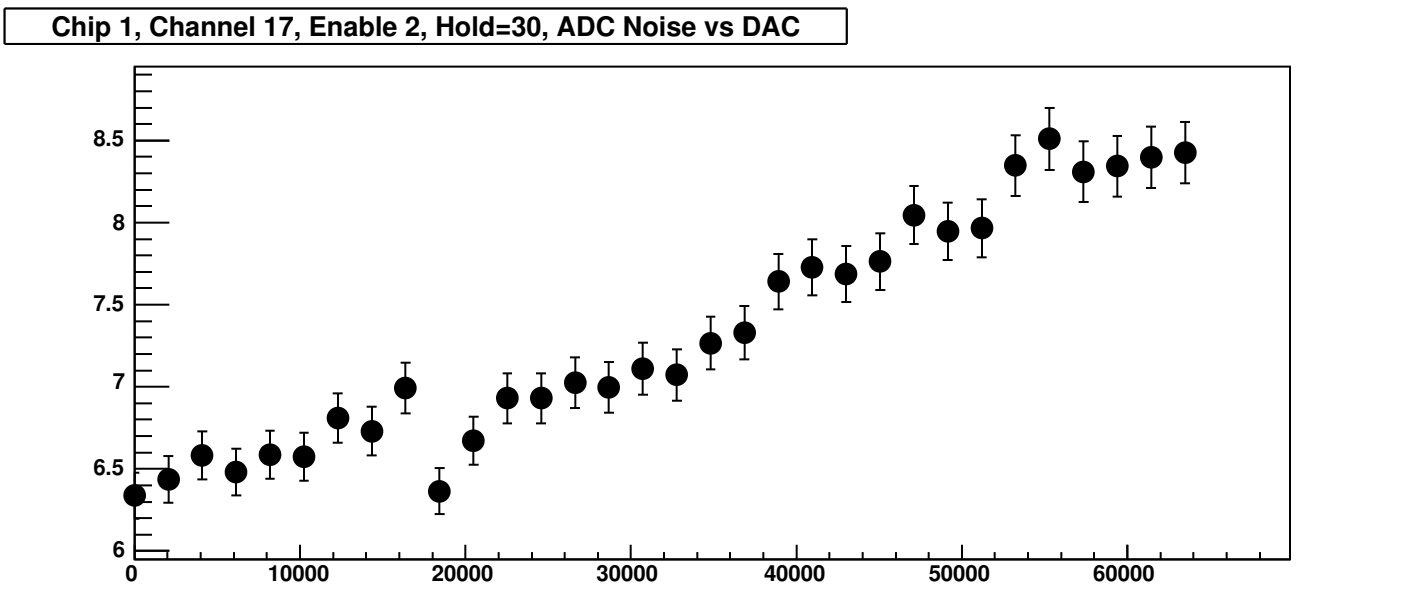

Chip 1, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

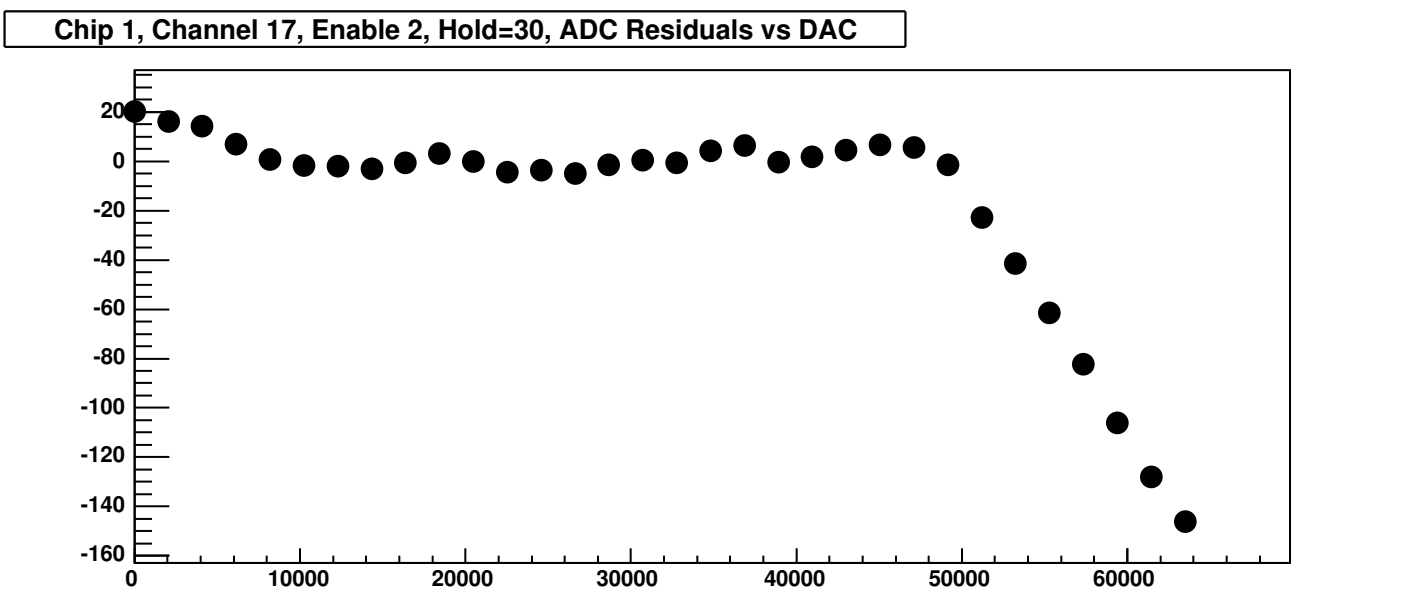

Chip 1, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3103 / 15
p0 1015 ± 0.1139
p1 -0.001133 ± 4.571e-06
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

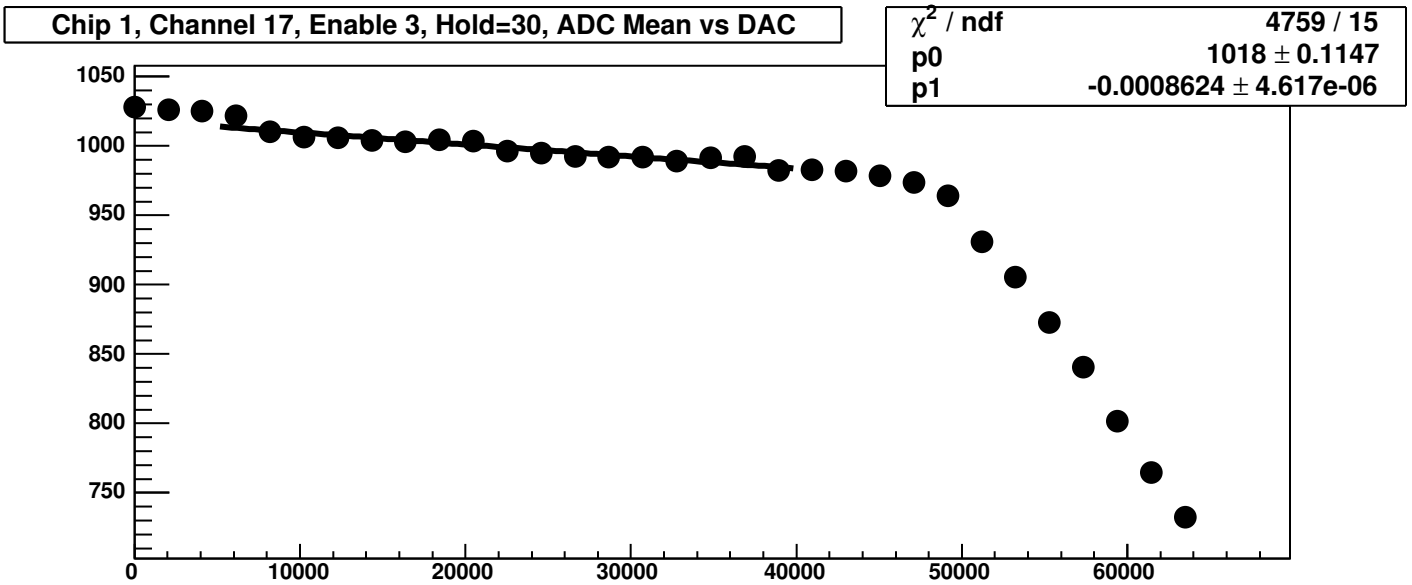

Chip 1, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

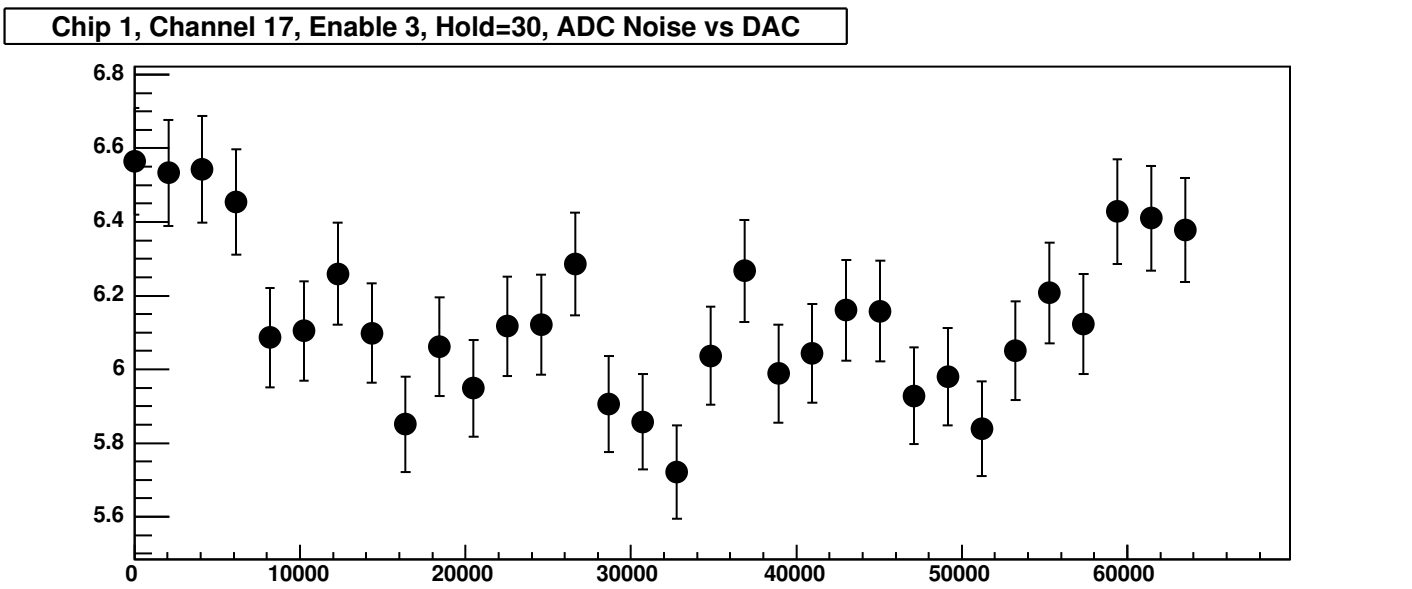

Chip 1, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

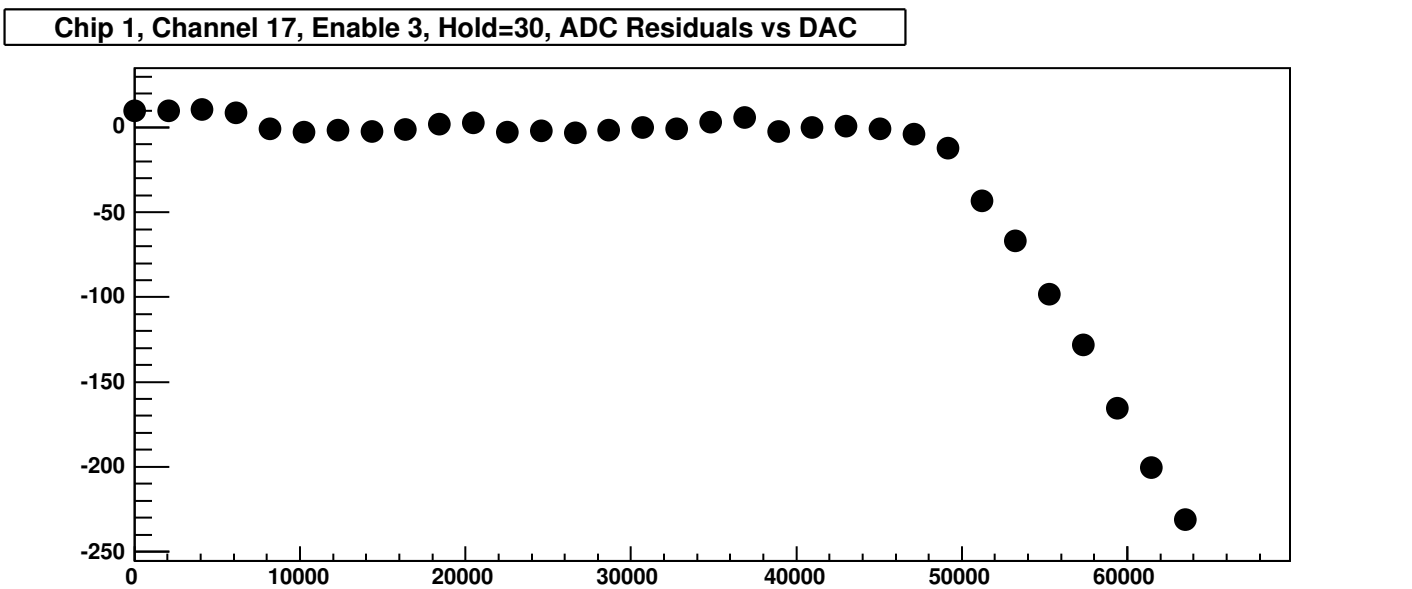

Chip 1, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4195 / 15
p0 1010 ± 0.1257
p1 0.001203 ± 5.258e-06
1080
1060
1040
1020
1000
980
960
940
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
6
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

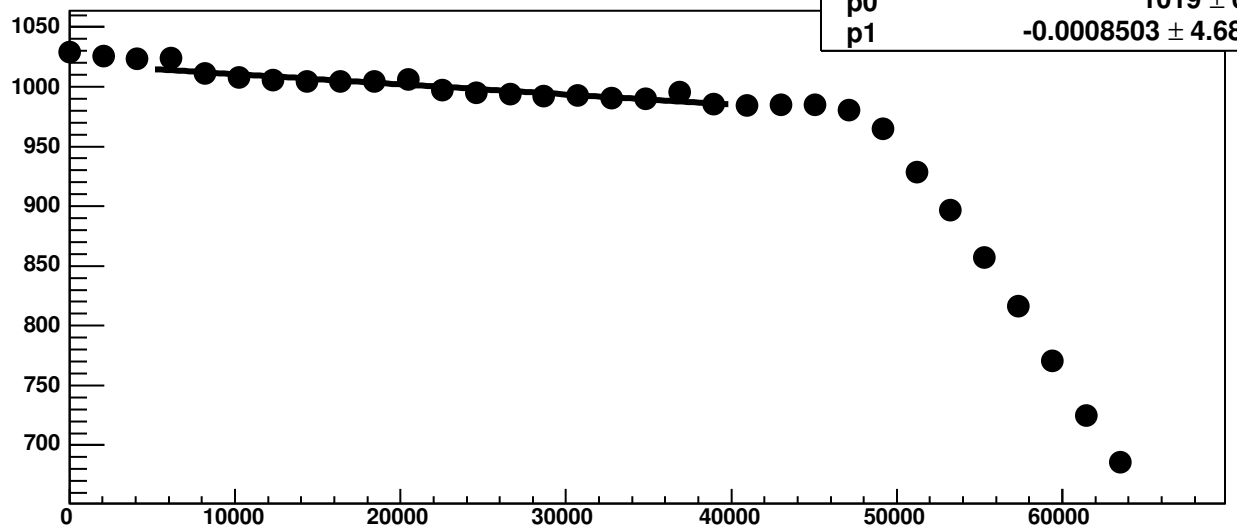

Chip 1, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4759 / 15
p0 1018 ± 0.1147
p1 -0.0008624 ± 4.617e-06
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

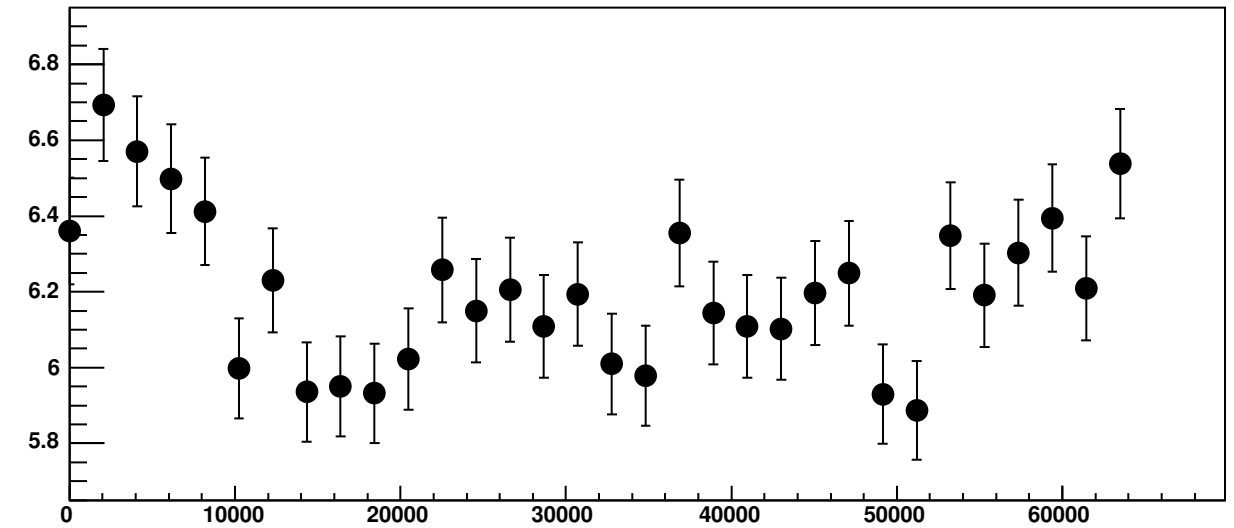

Chip 1, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

Chip 1, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

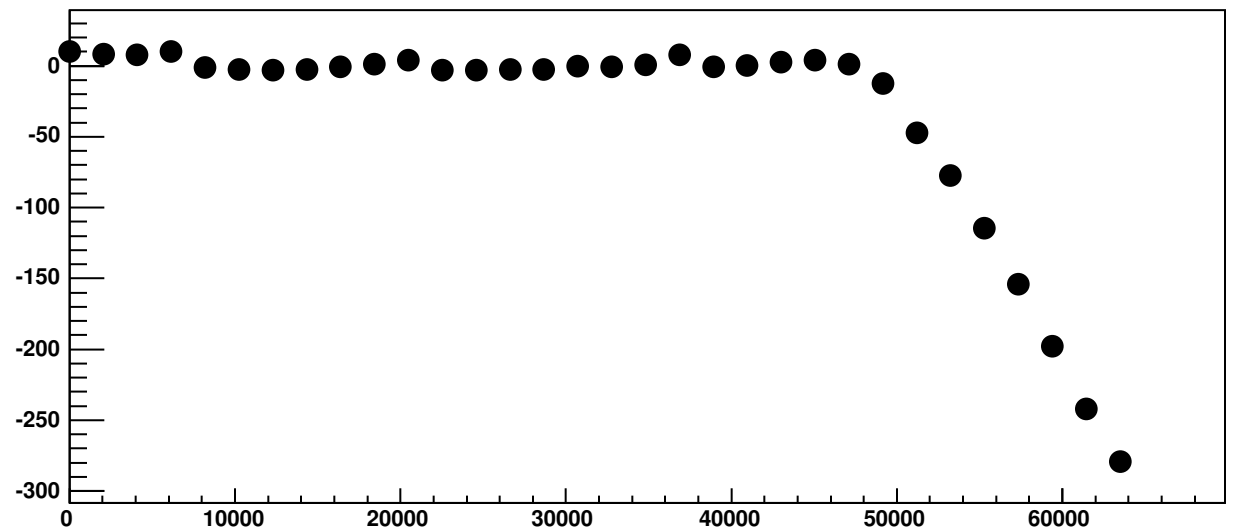
χ^2 / ndf	5922 / 15
p0	1019 ± 0.1157
p1	-0.0008503 ± 4.687e-06

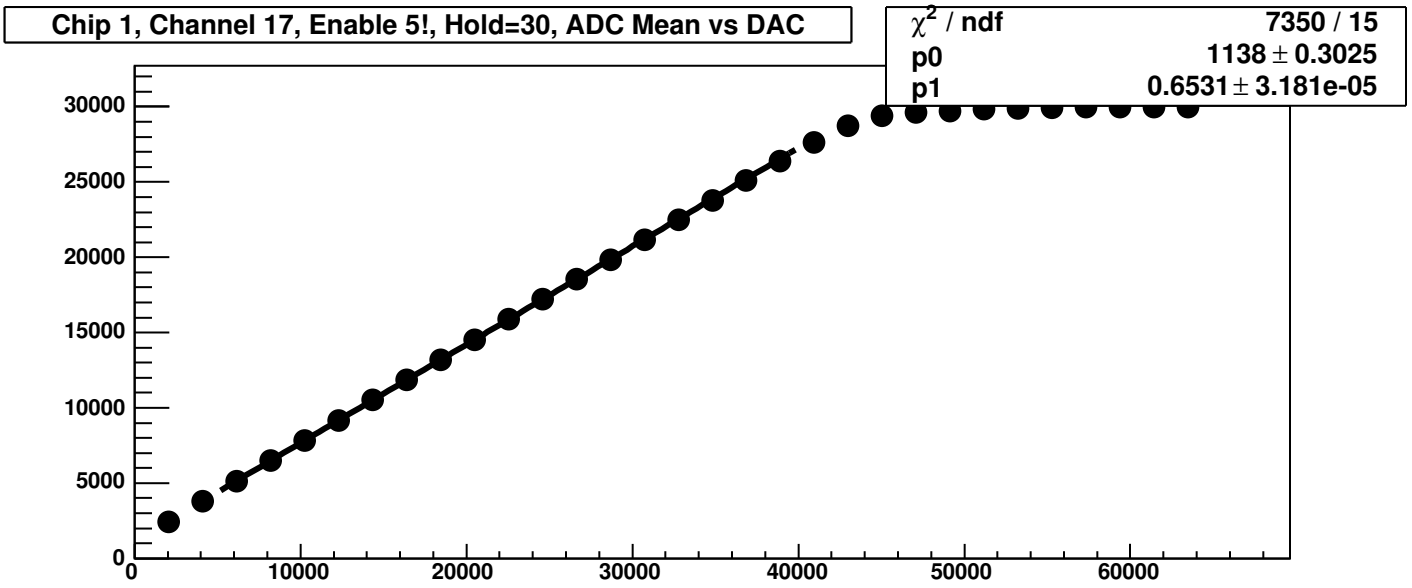


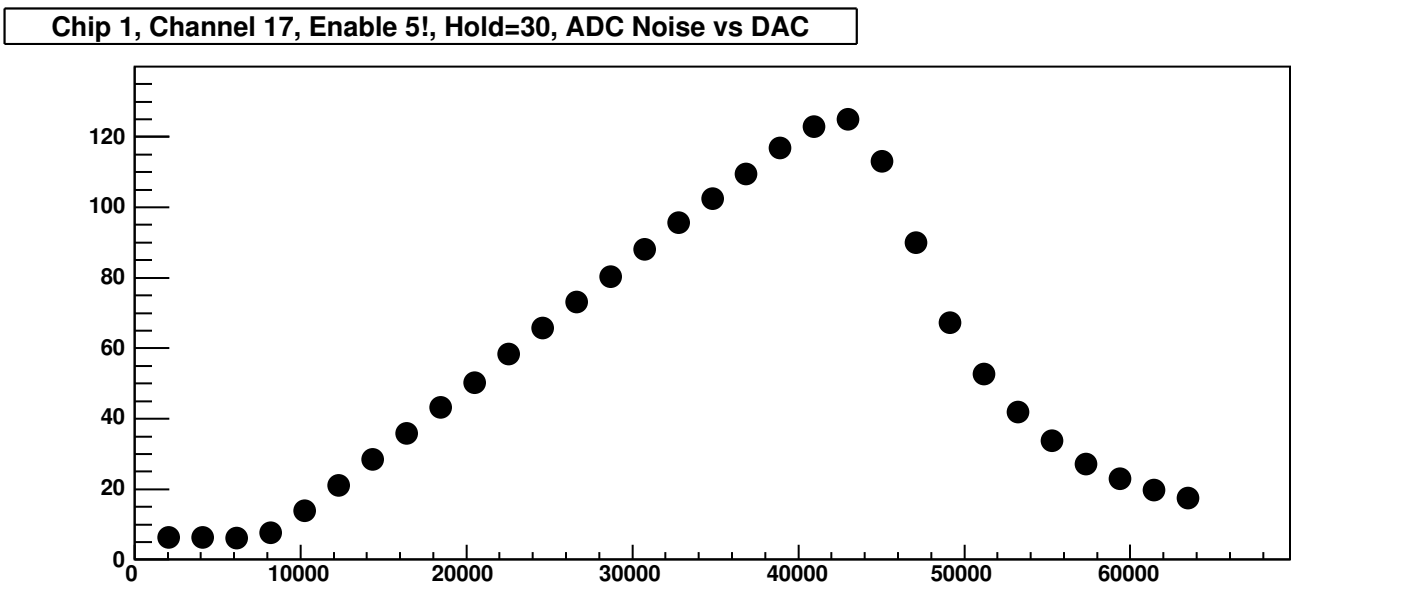
Chip 1, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC

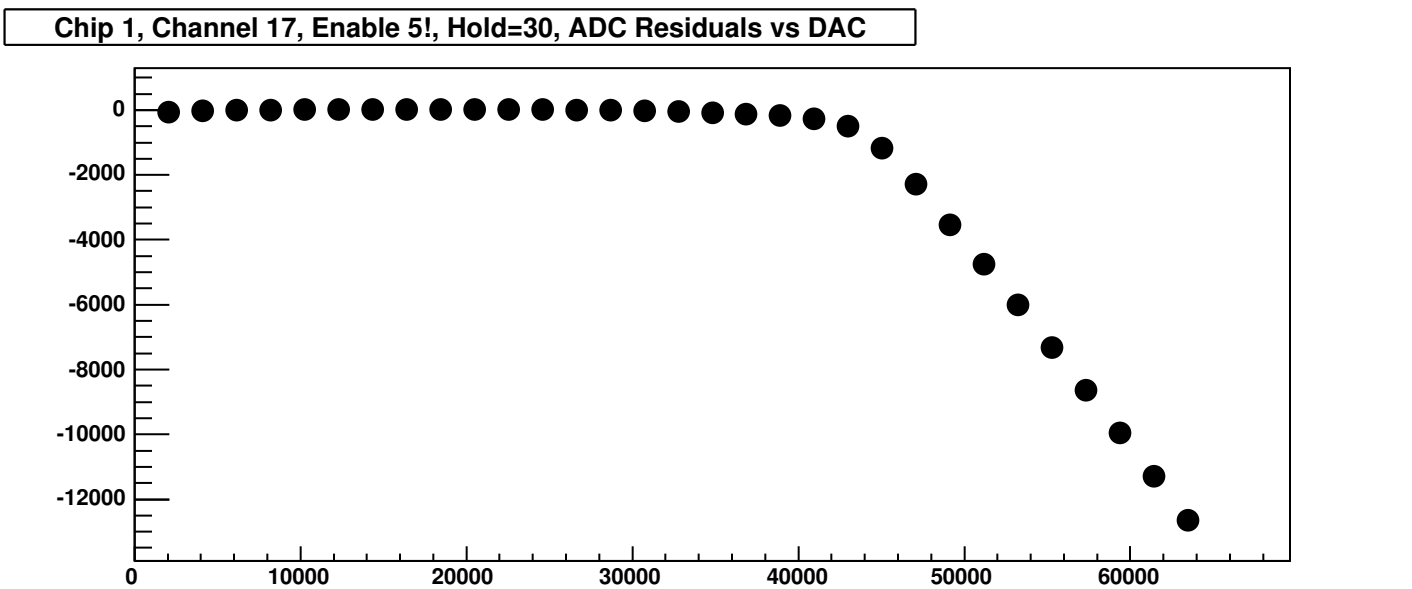


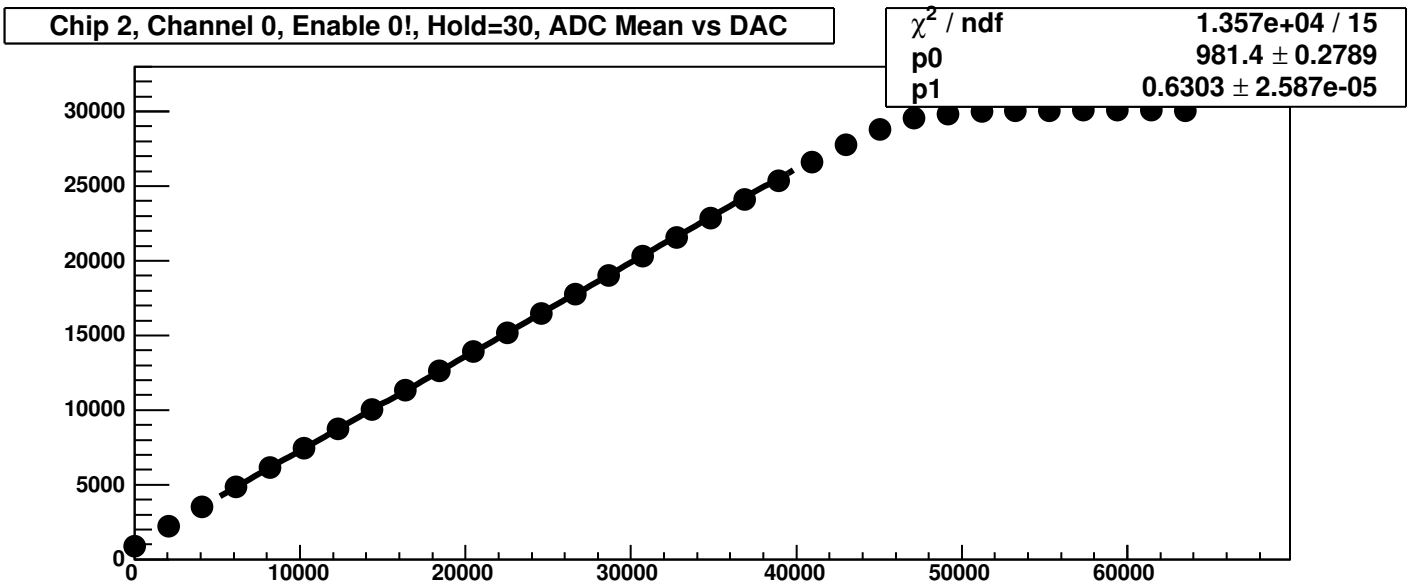
Chip 1, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC

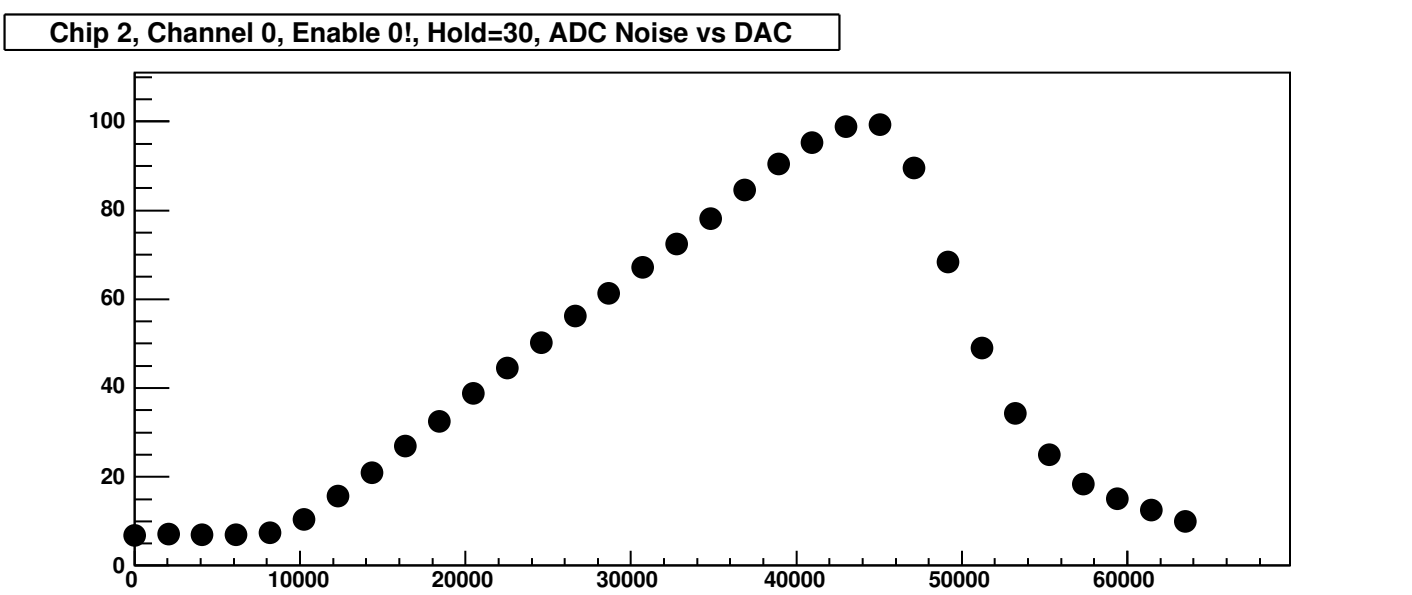


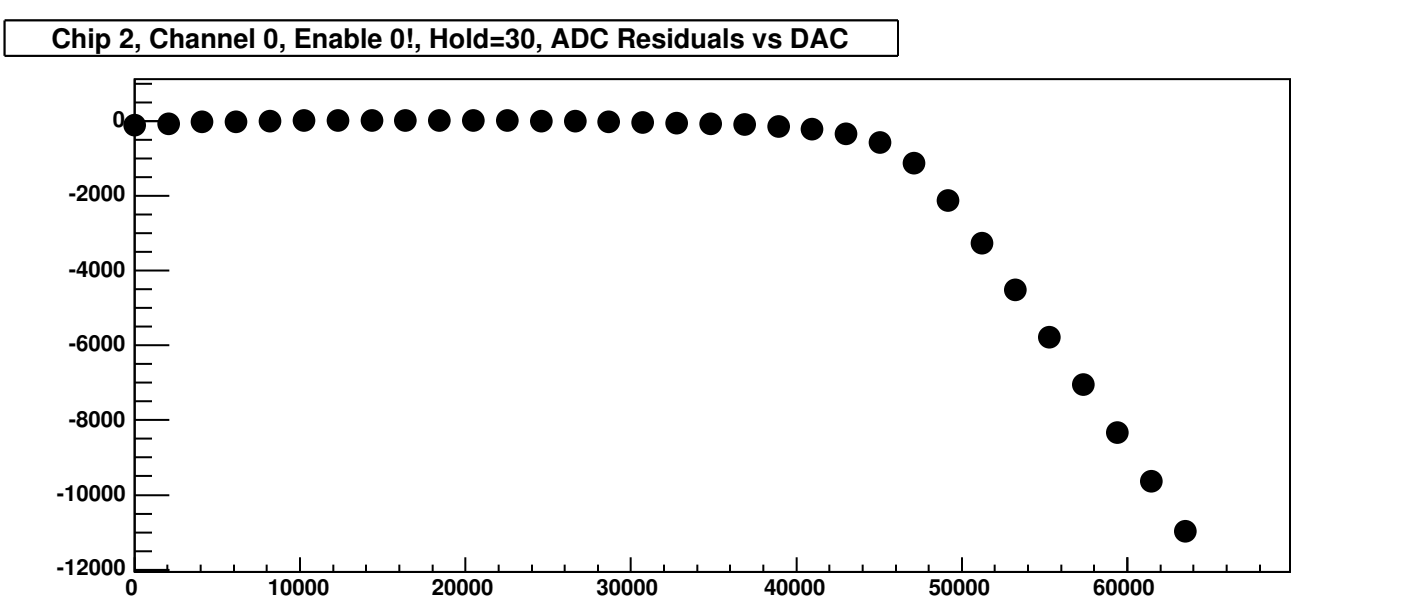

Chip 1, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7350 / 15
p0 1138 ± 0.3025
p1 0.6531 ± 3.181e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

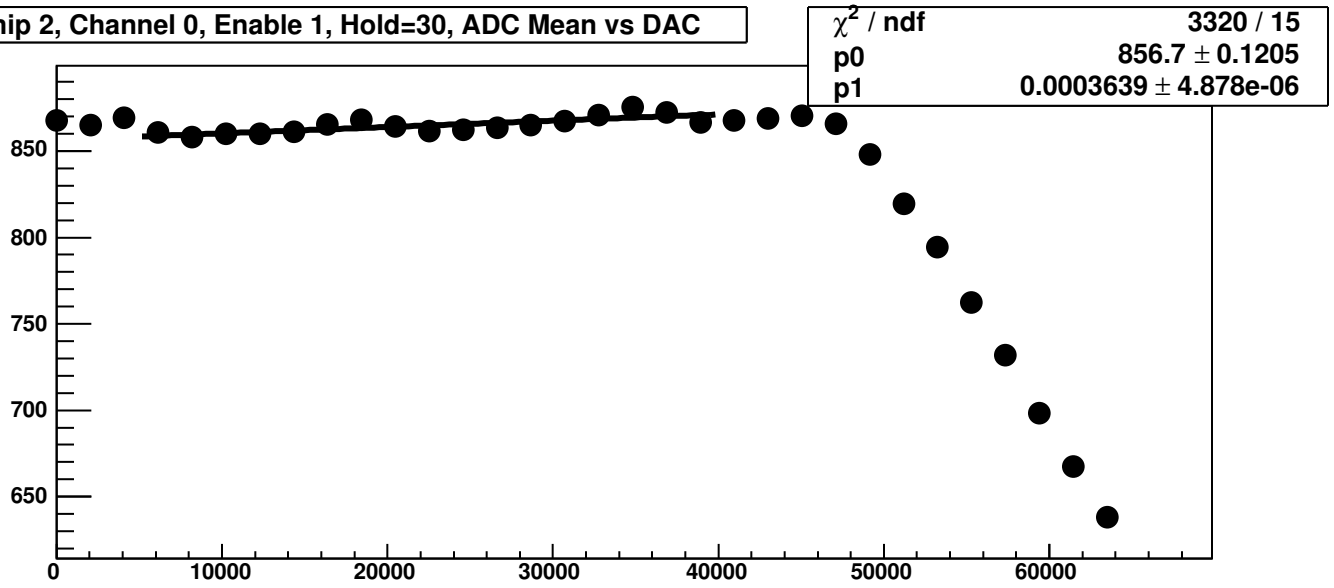

Chip 1, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.357e+04 / 15
p0 981.4 ± 0.2789
p1 0.6303 ± 2.587e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

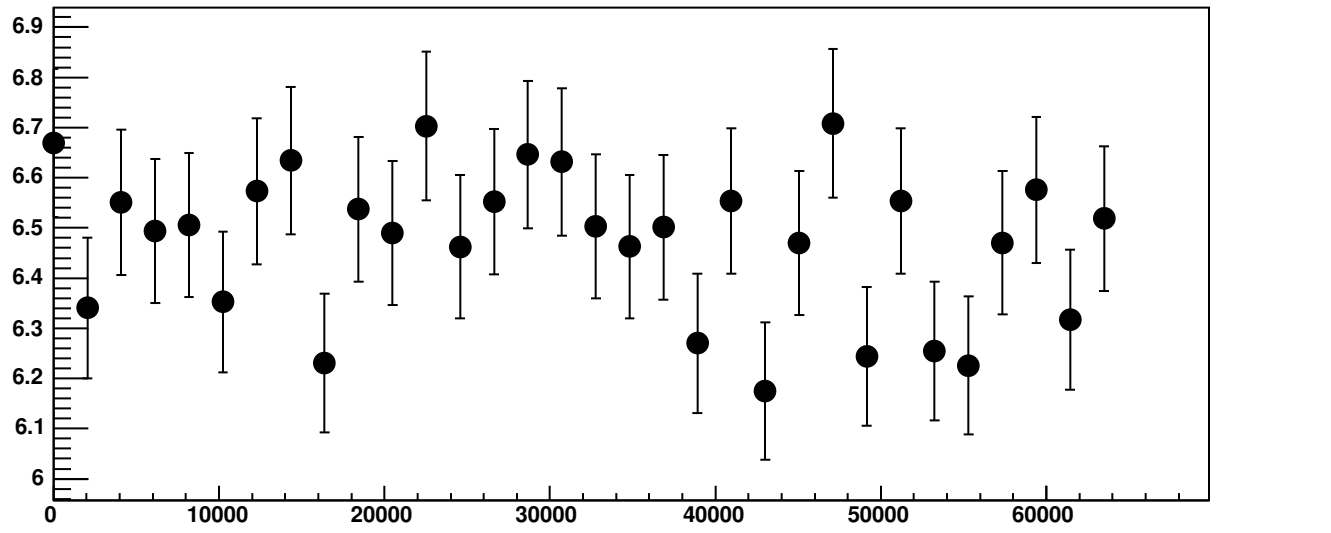

Chip 2, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

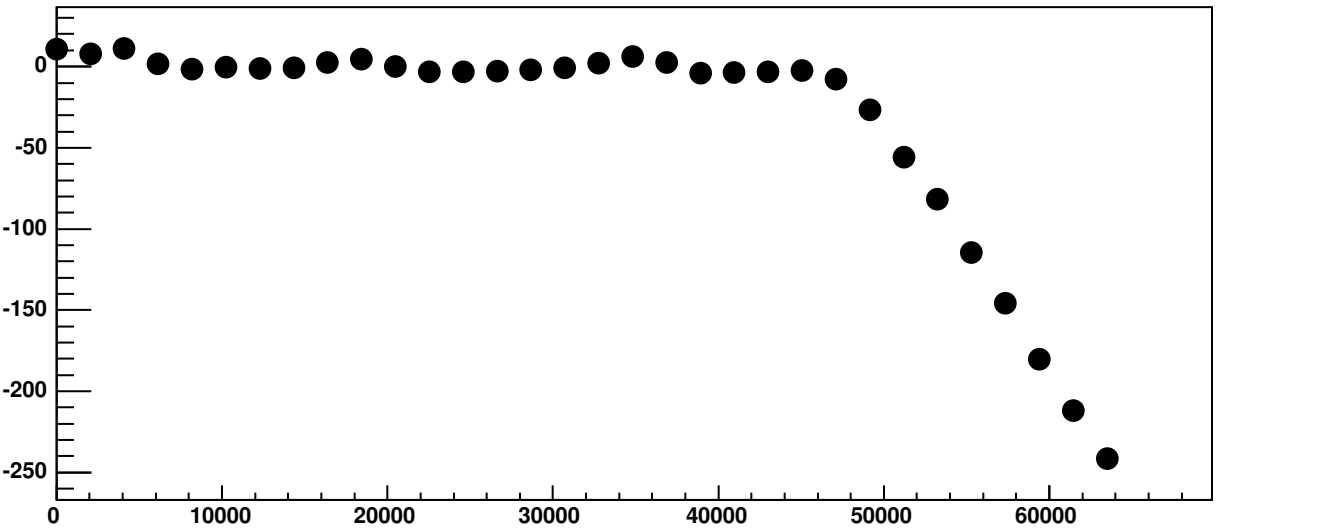
Chip 2, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC

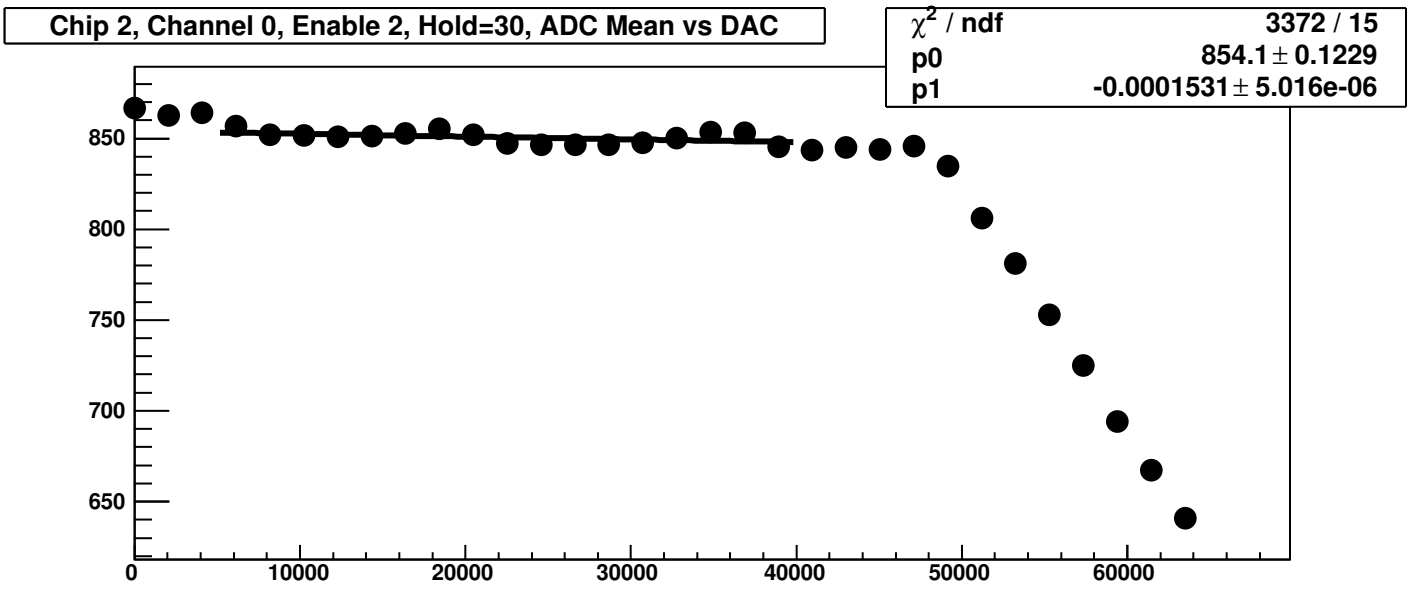


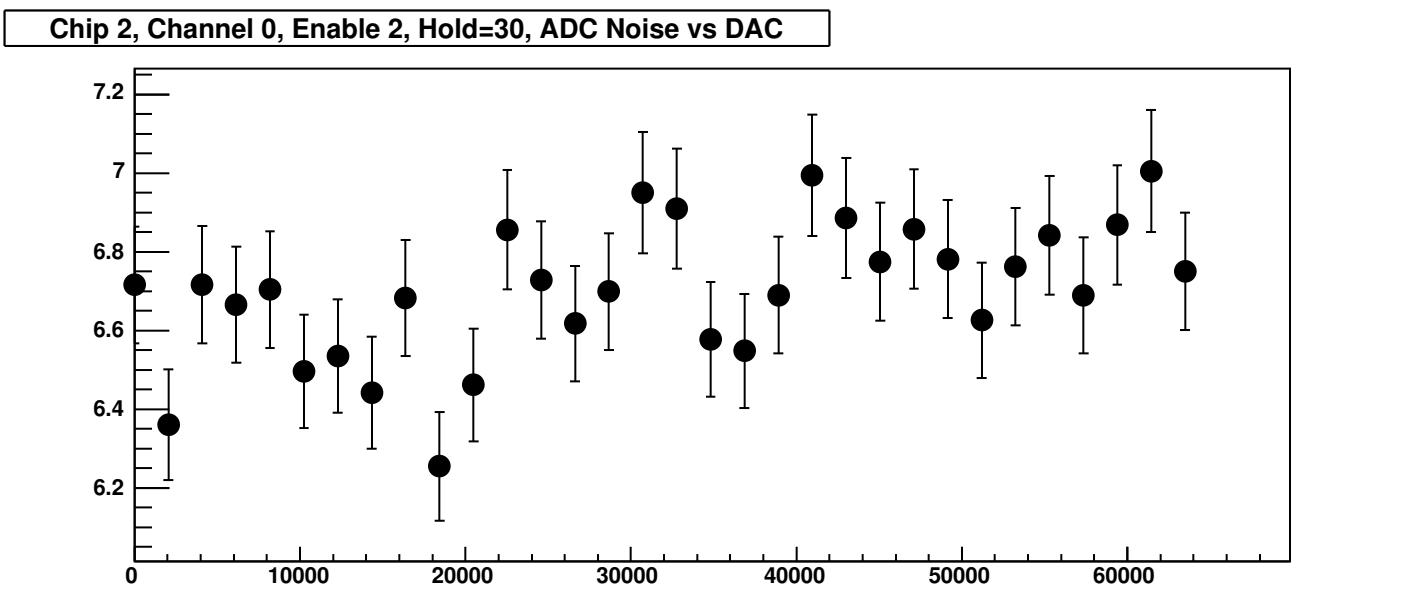
Chip 2, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC

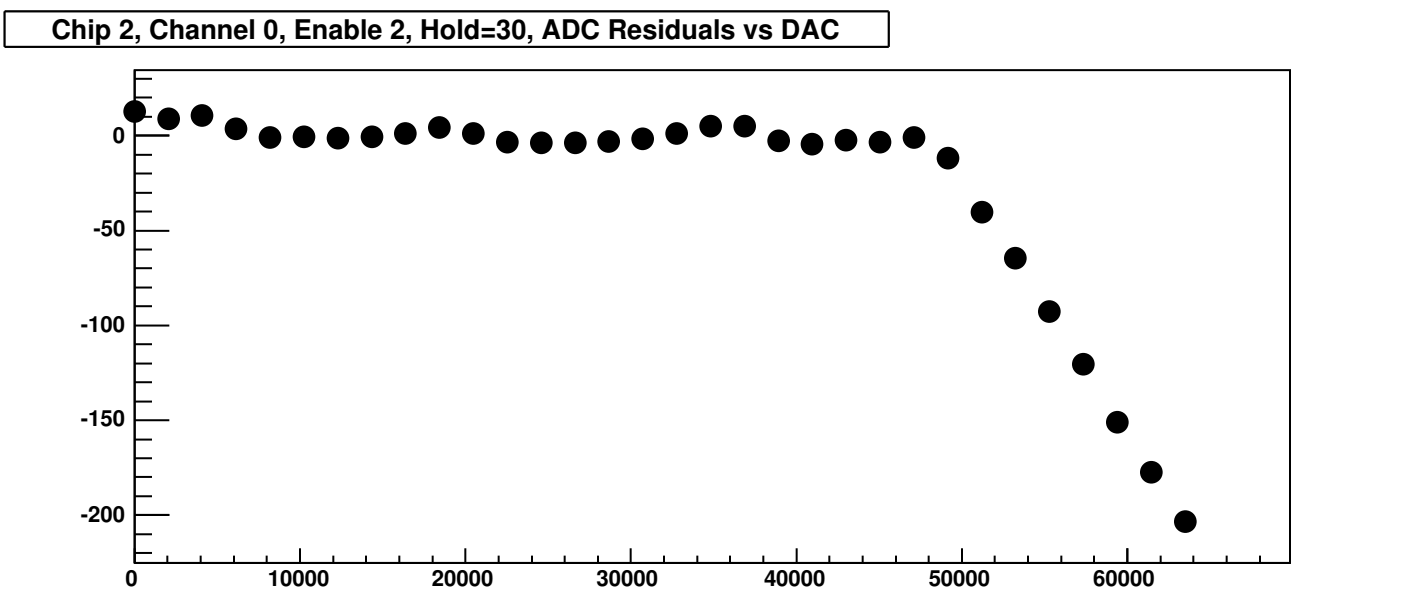


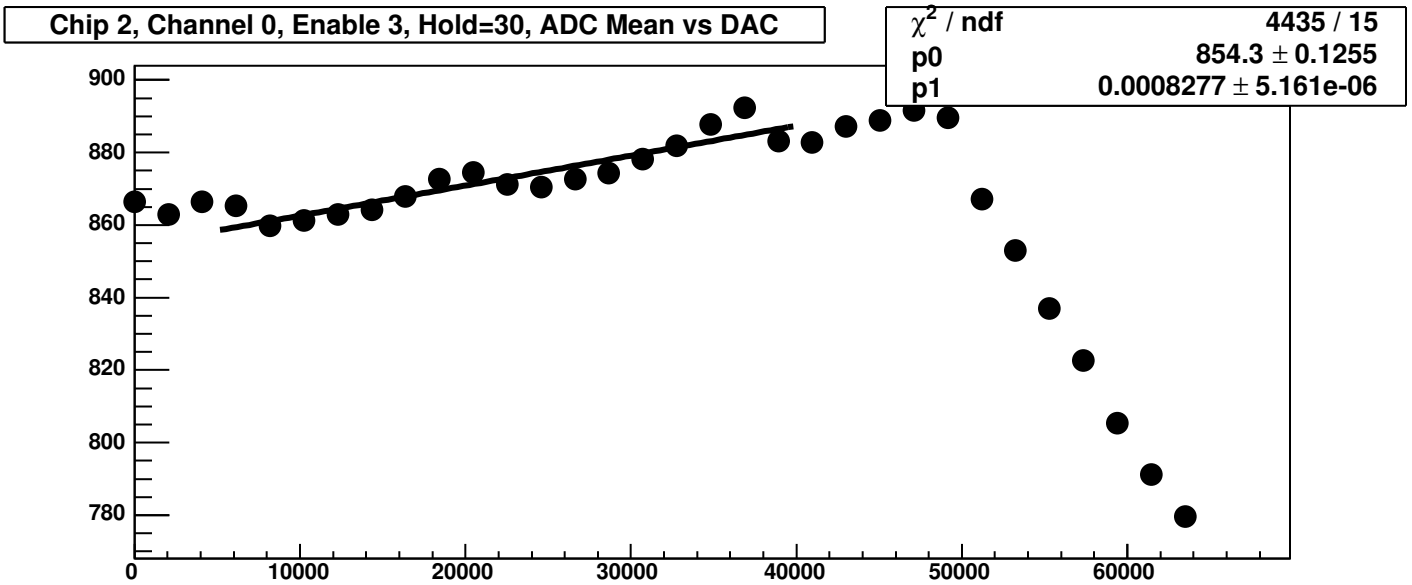
Chip 2, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC

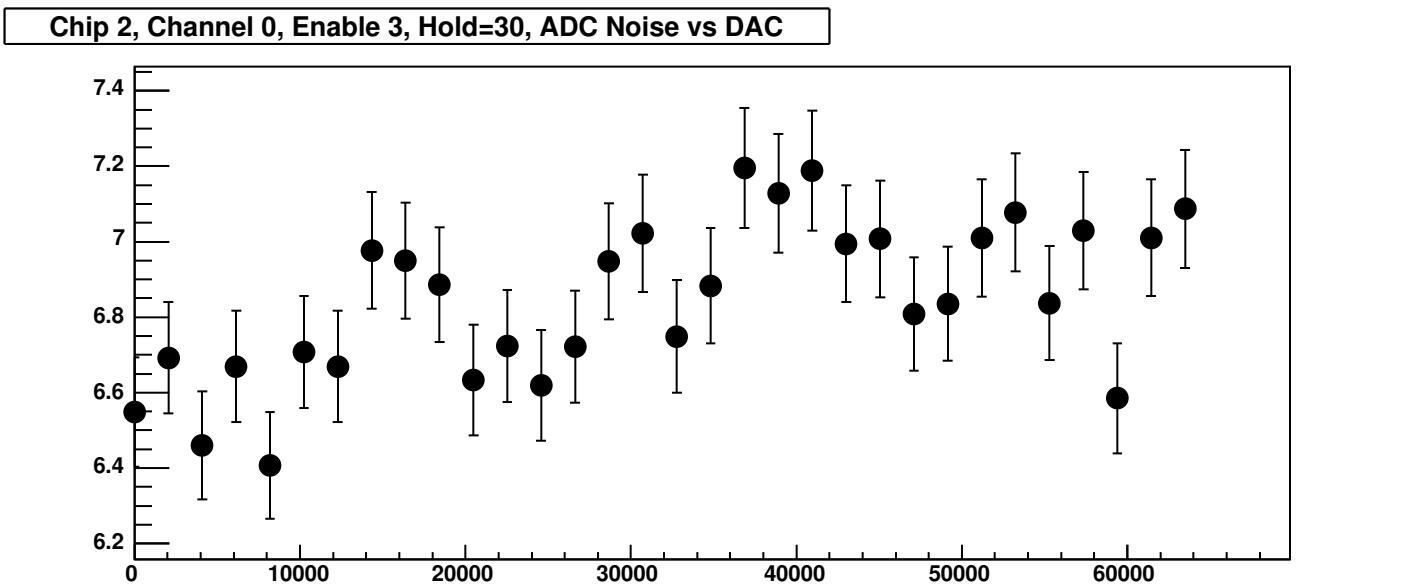


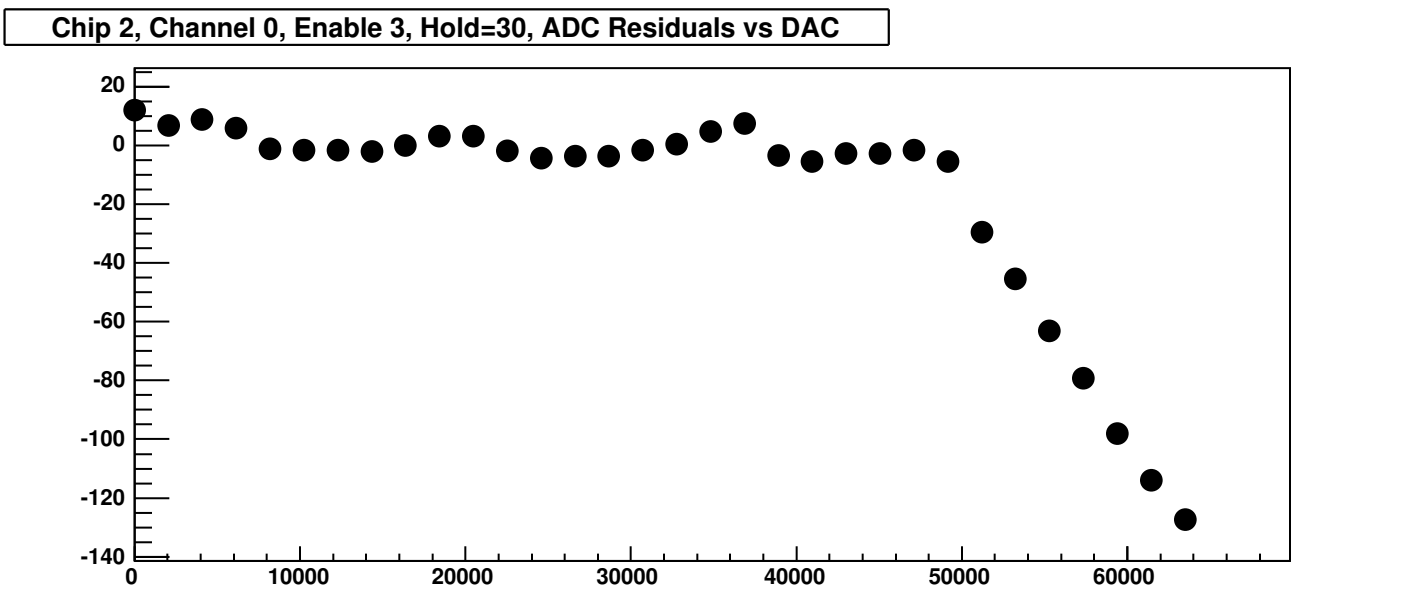

Chip 2, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3372 / 15
p0 854.1 ± 0.1229
p1 -0.0001531 ± 5.016e-06
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

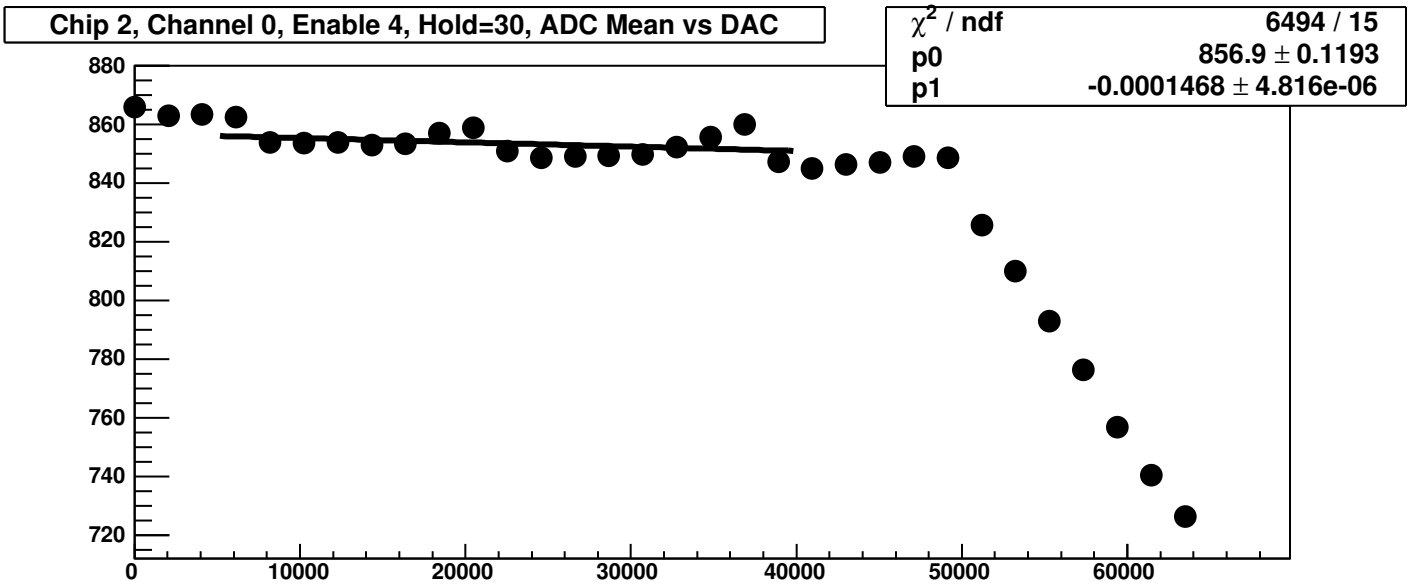

Chip 2, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

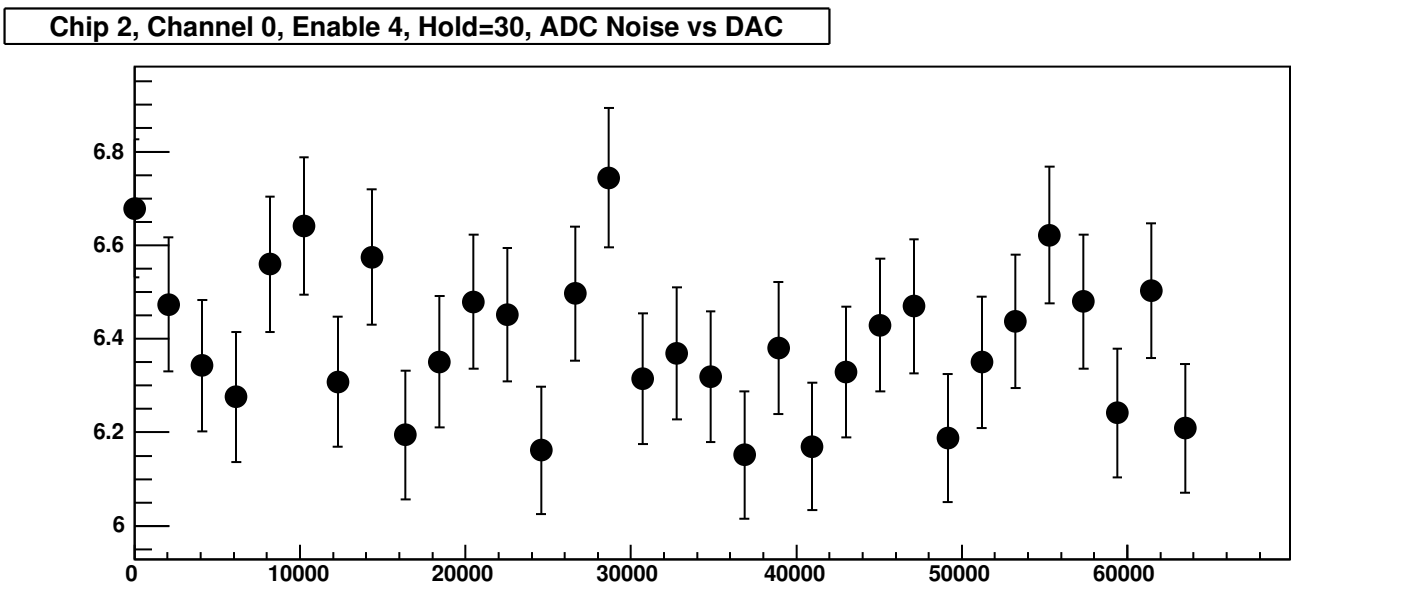

Chip 2, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

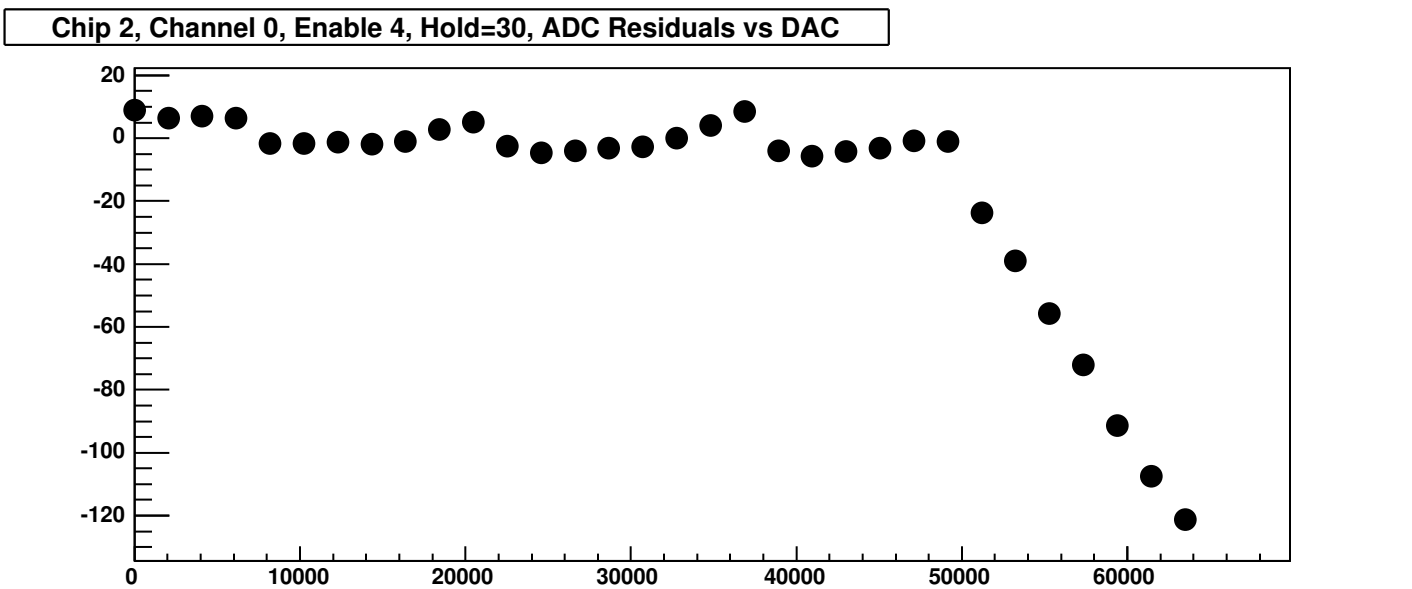

Chip 2, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4435 / 15
p0 854.3 ± 0.1255
p1 0.0008277 ± 5.161e-06
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

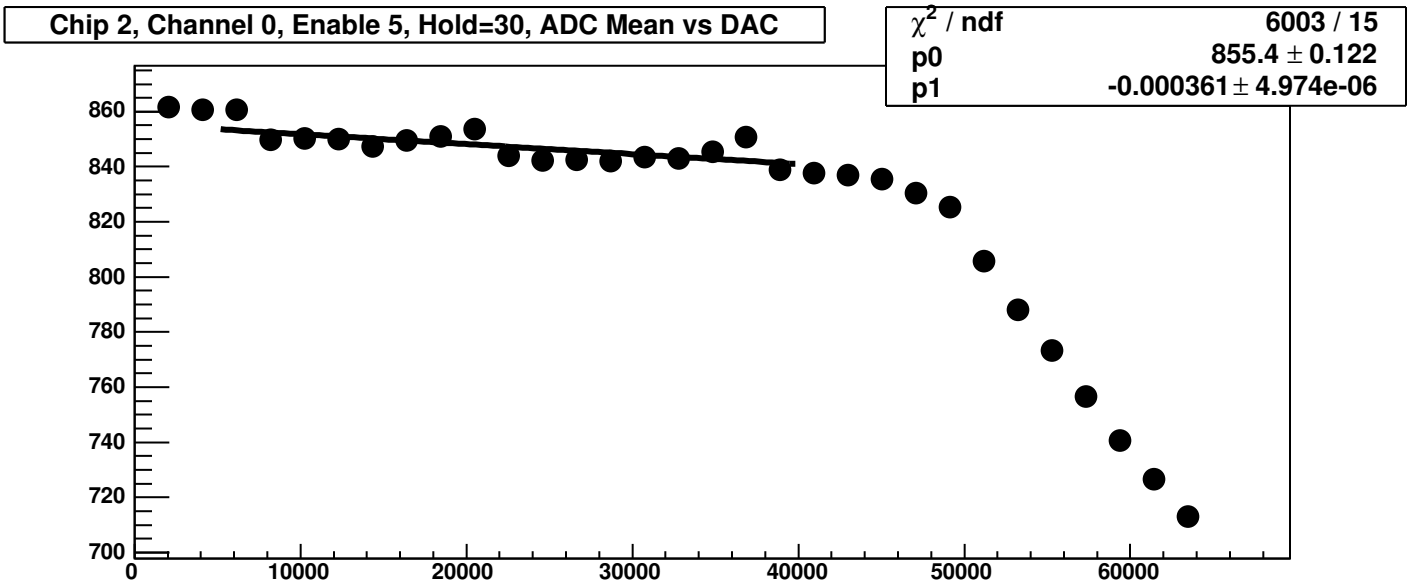

Chip 2, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

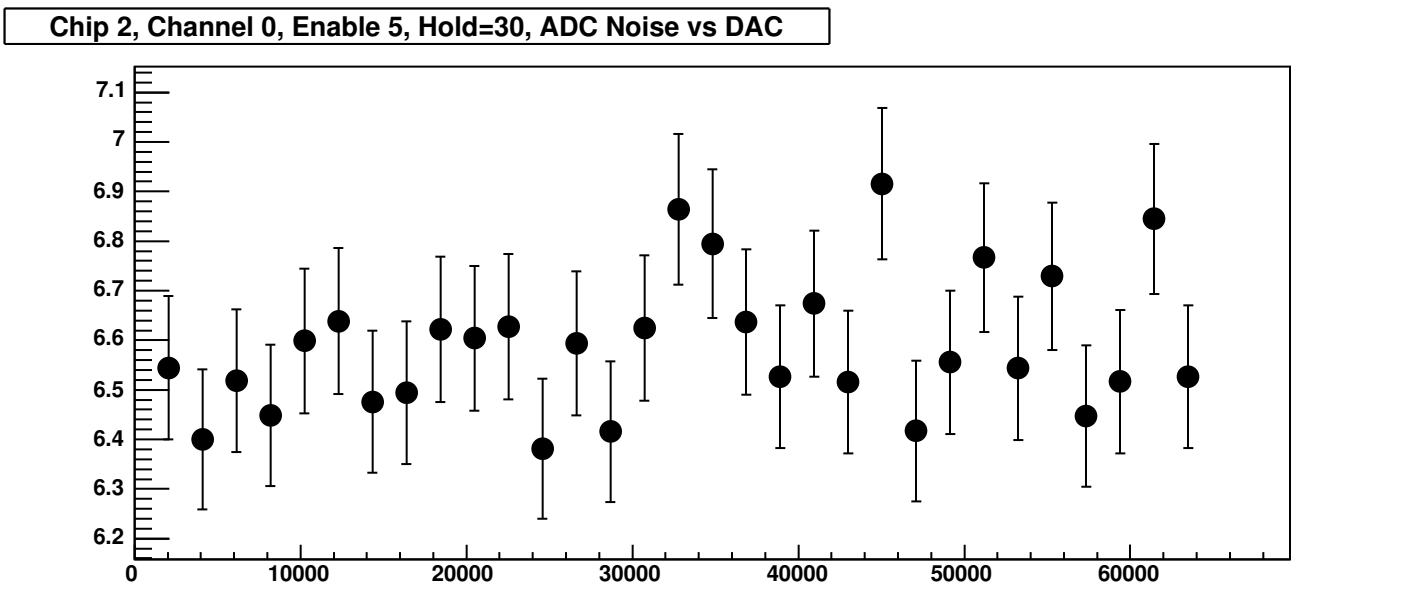

Chip 2, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

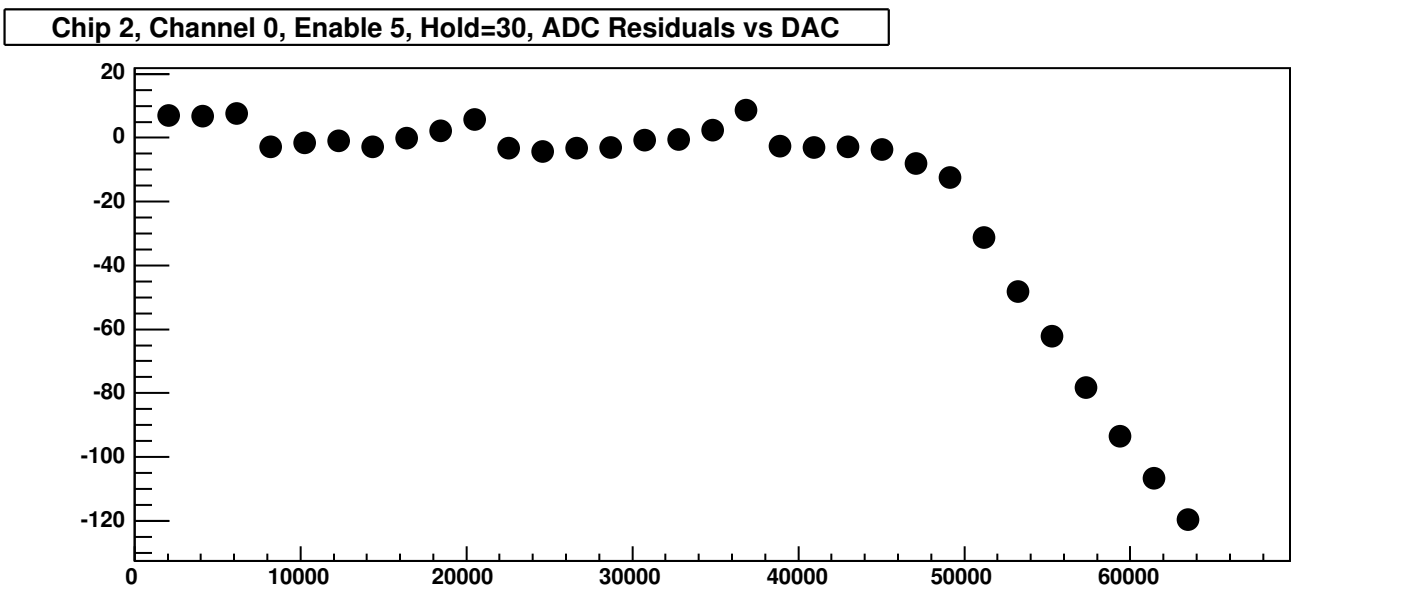

Chip 2, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6494 / 15
p0 856.9 ± 0.1193
p1 -0.0001468 ± 4.816e-06
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

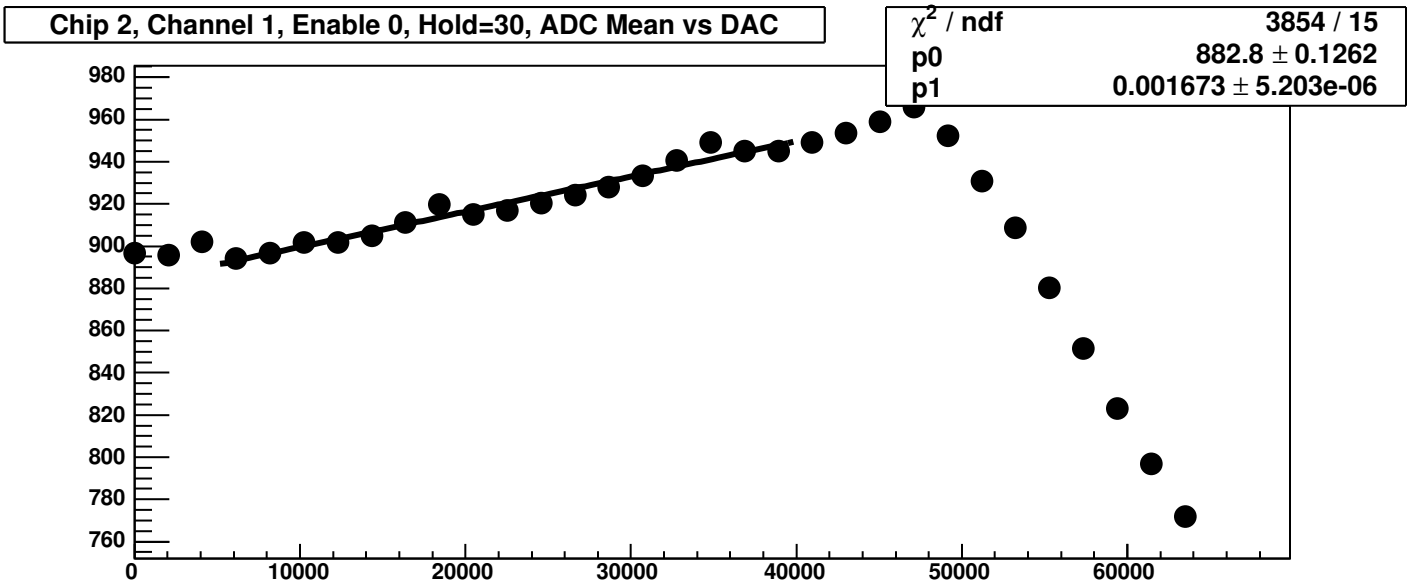

Chip 2, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

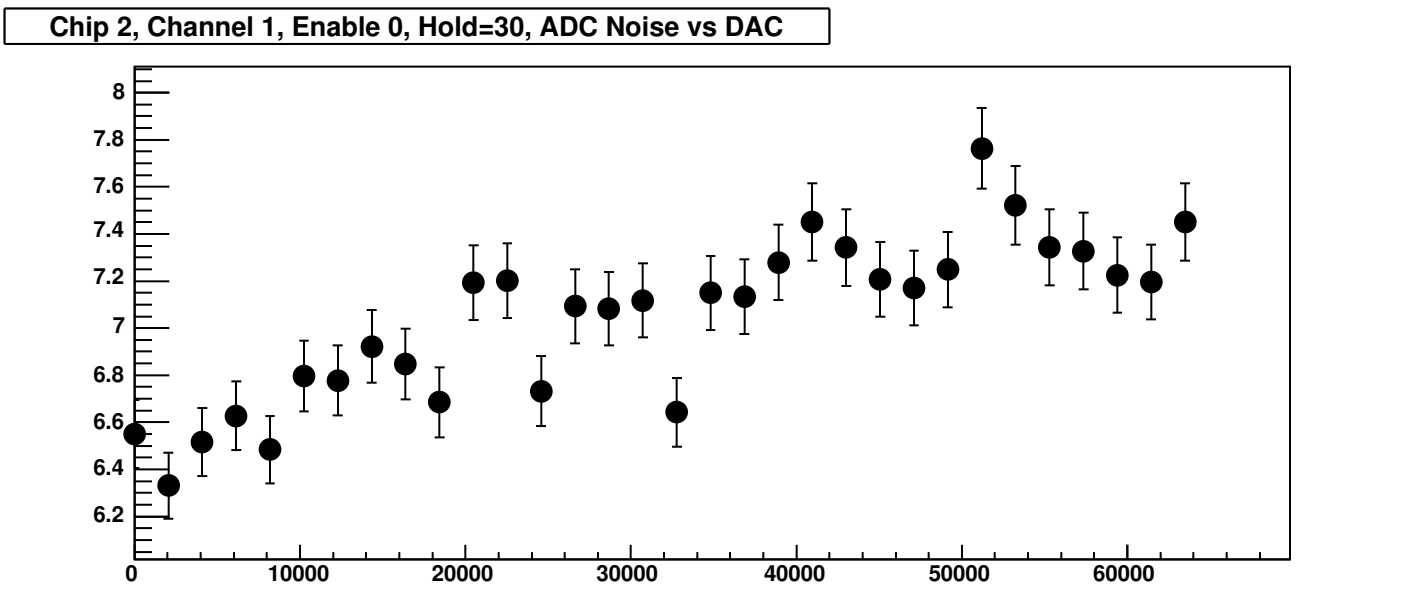

Chip 2, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

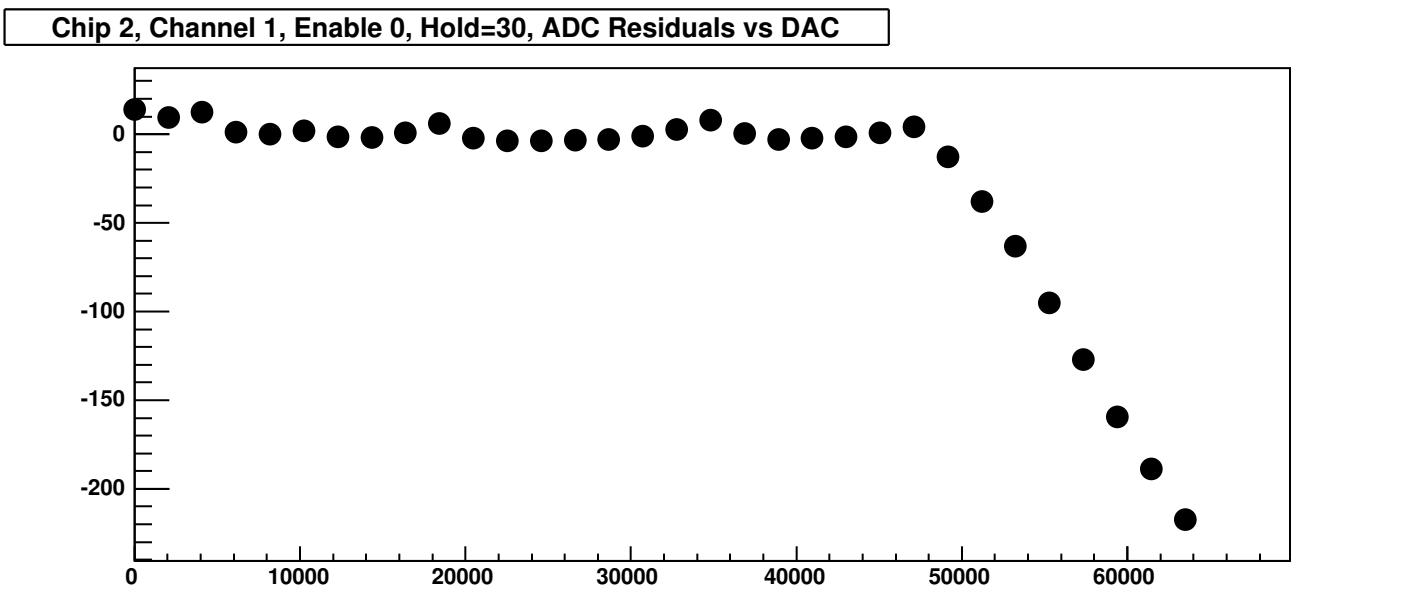

Chip 2, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6003 / 15
p0 855.4 ± 0.122
p1 -0.000361 ± 4.974e-06
860
840
820
800
780
760
740
720
700
0
10000
20000
30000
40000
50000
60000

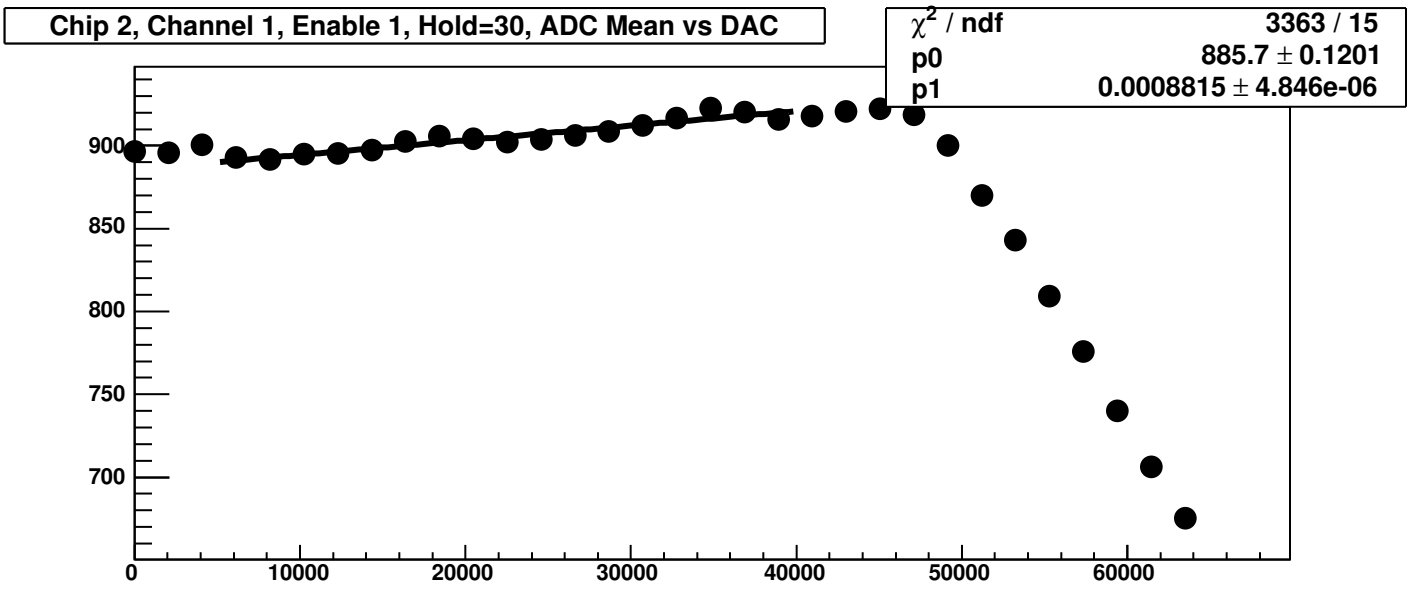

Chip 2, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
7.1
7
6.9
6.8
6.7
6.6
6.5
6.4
6.3
6.2
0
10000
20000
30000
40000
50000
60000

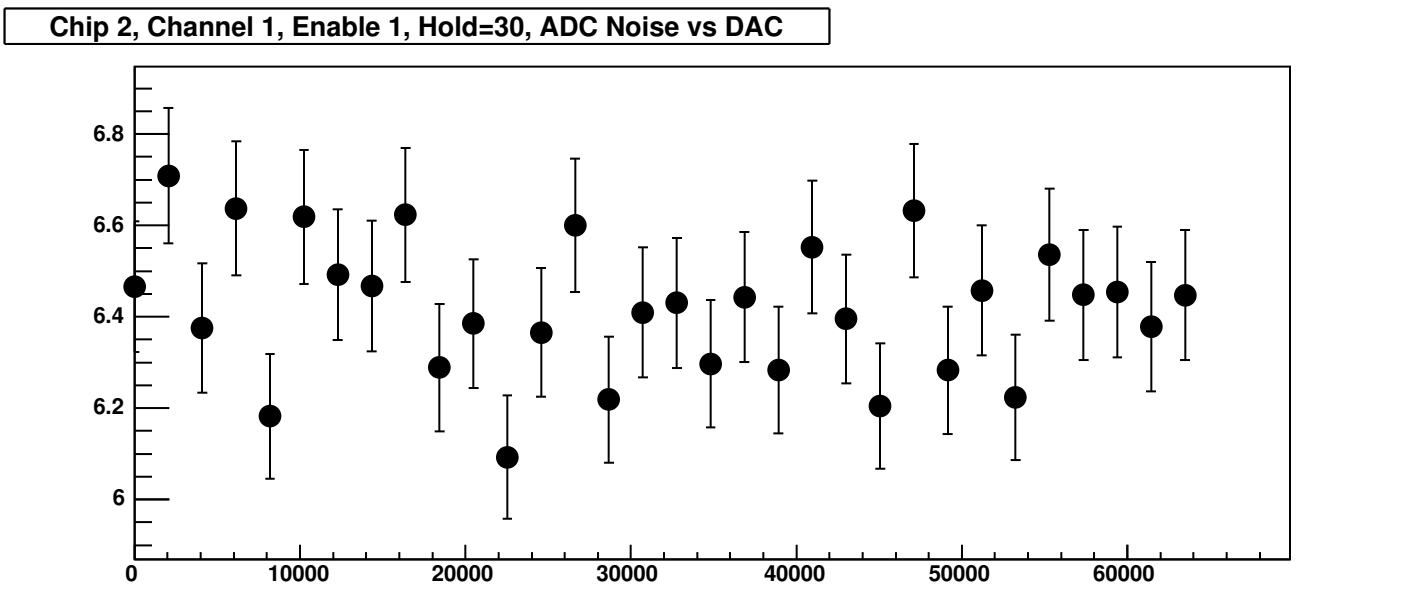

Chip 2, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

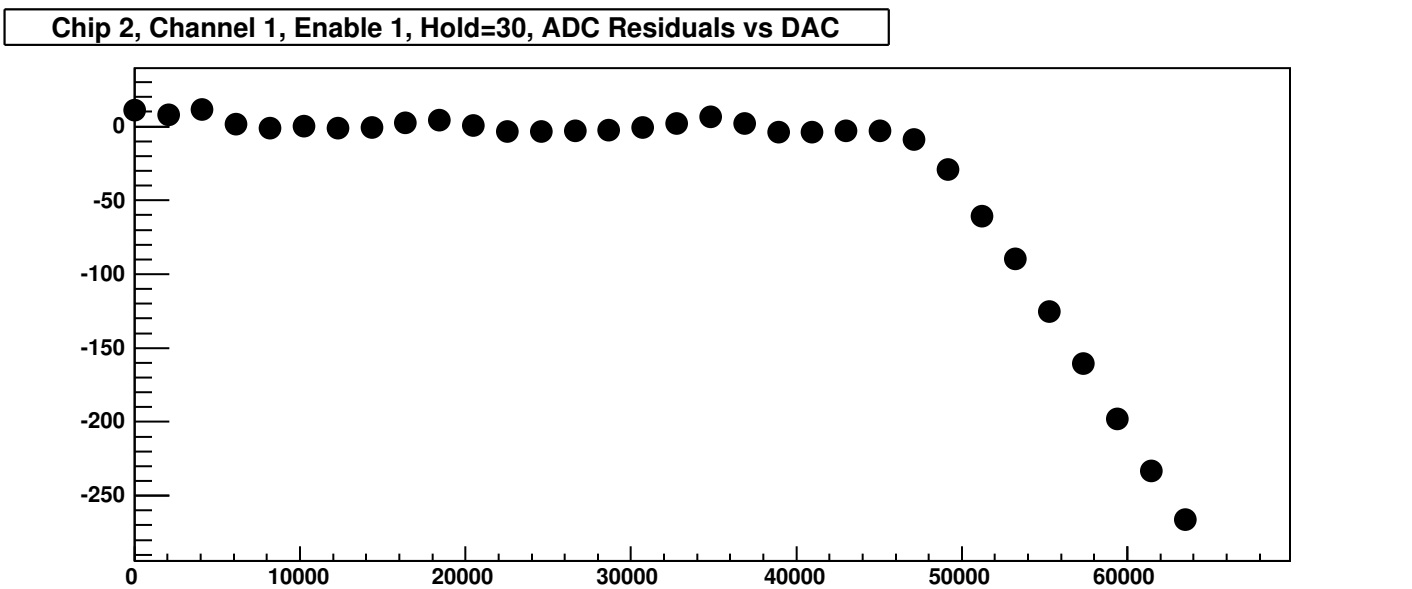

Chip 2, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3854 / 15
p0 882.8 ± 0.1262
p1 0.001673 ± 5.203e-06
980
960
940
920
900
880
860
840
820
800
780
760
0
10000
20000
30000
40000
50000
60000

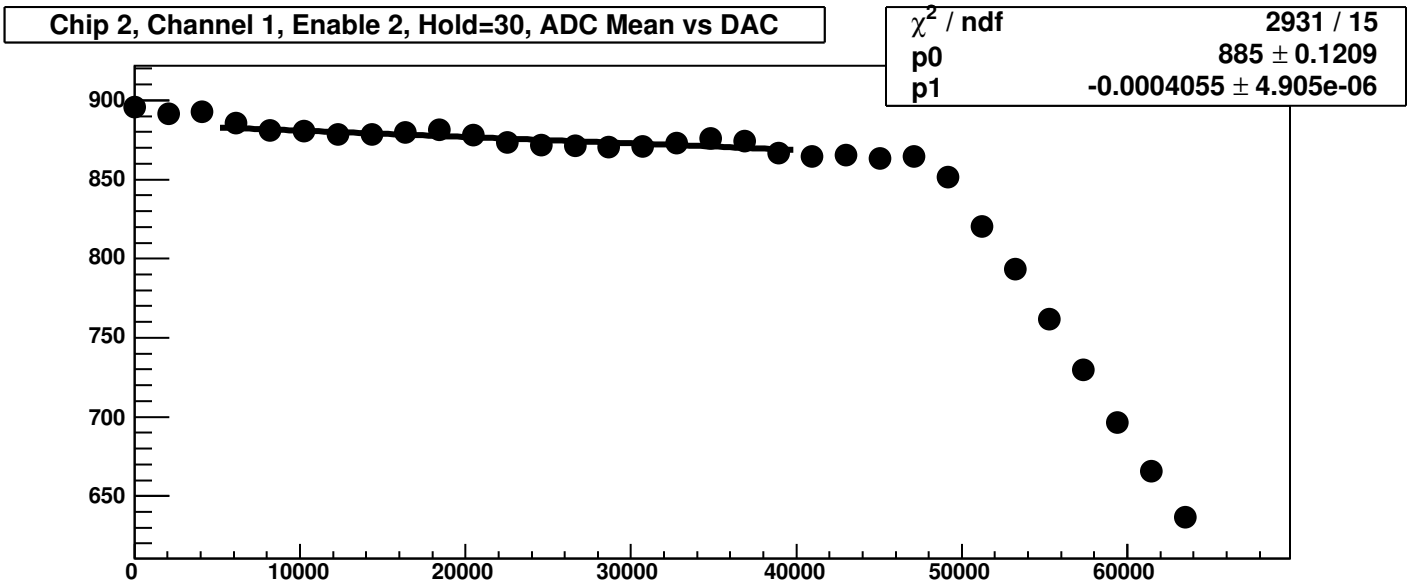

Chip 2, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
8
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

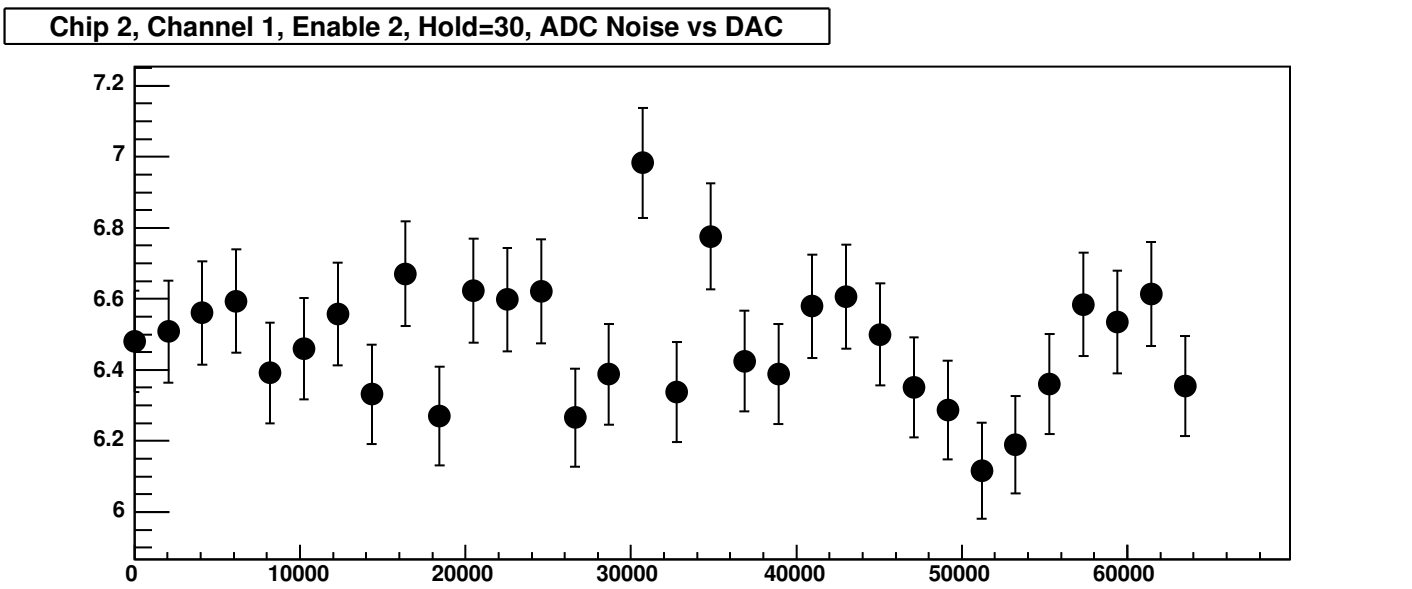

Chip 2, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

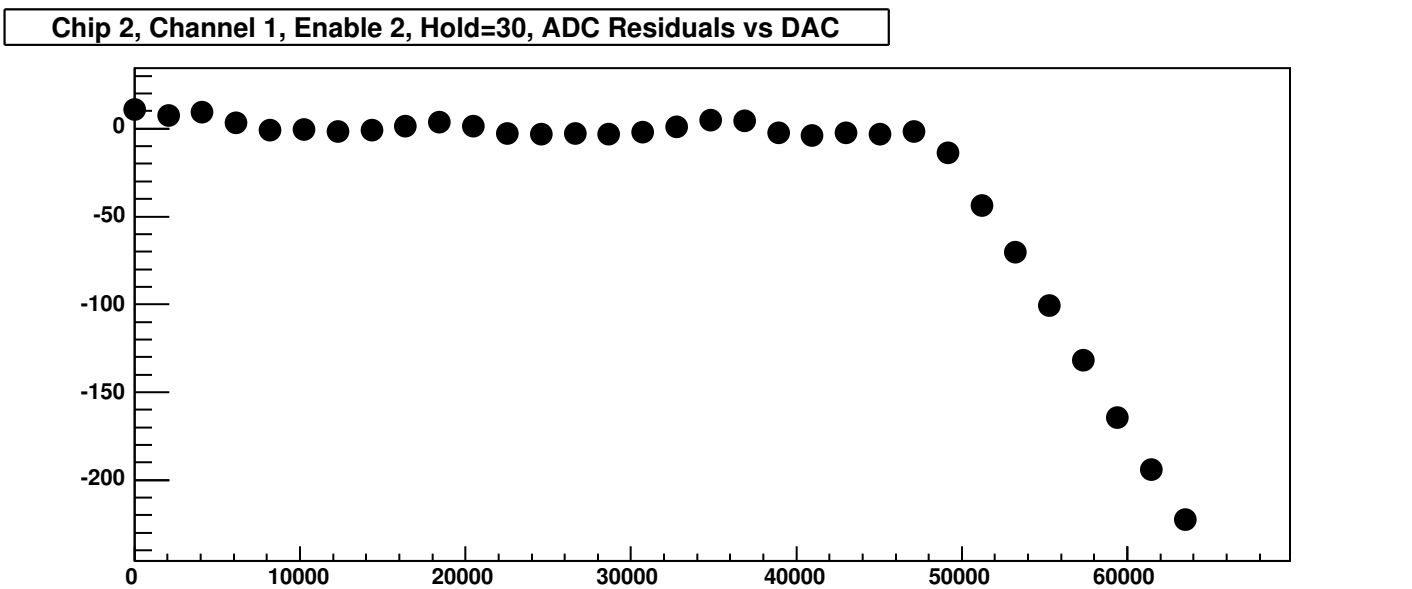

Chip 2, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3363 / 15
p0 885.7 ± 0.1201
p1 0.0008815 ± 4.846e-06
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

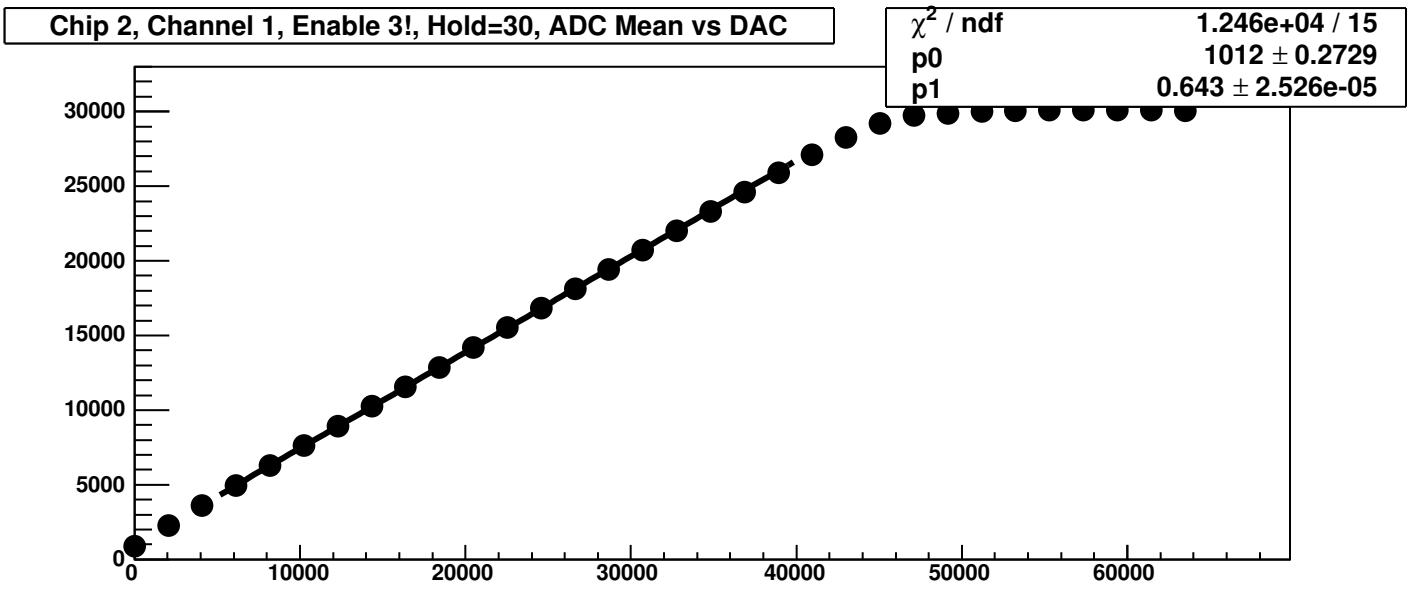

Chip 2, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

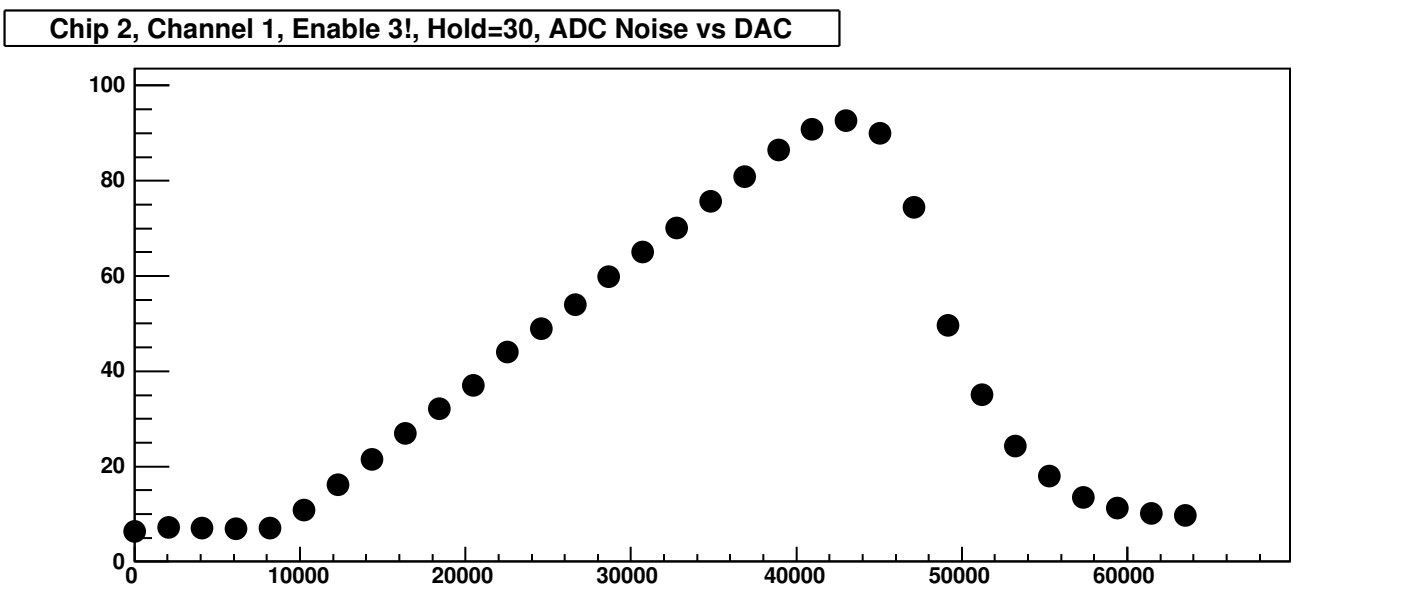

Chip 2, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

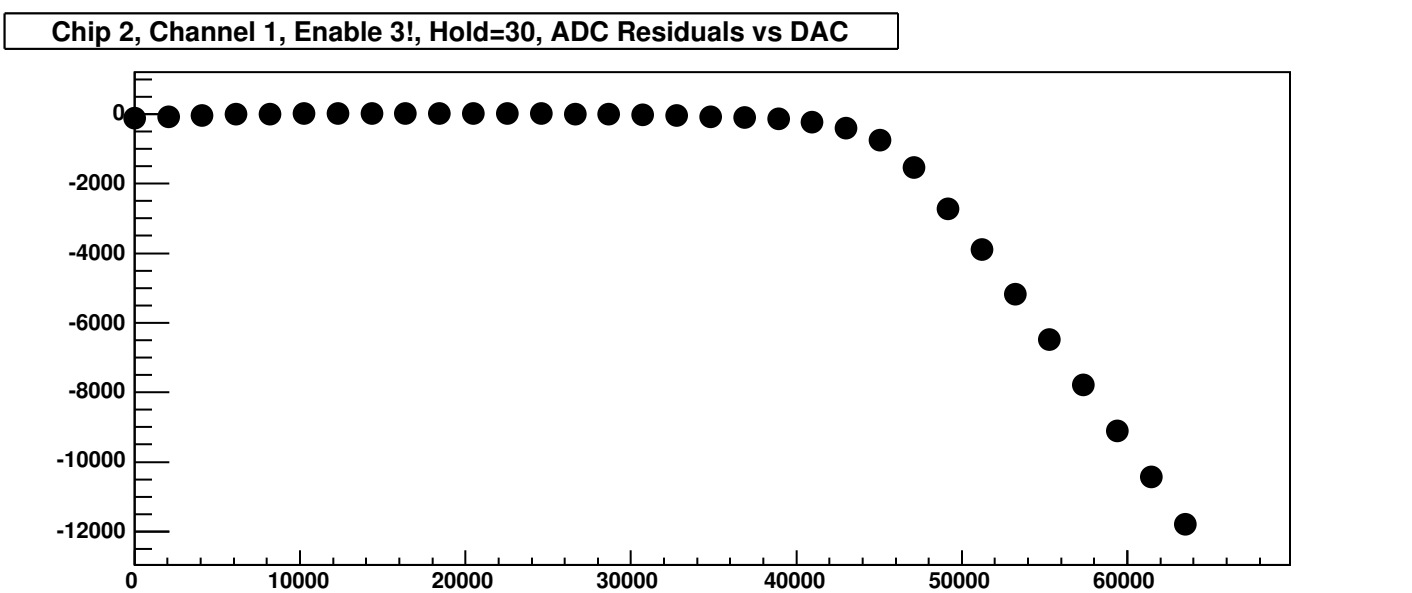

Chip 2, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2931 / 15
p0 885 ± 0.1209
p1 -0.0004055 ± 4.905e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

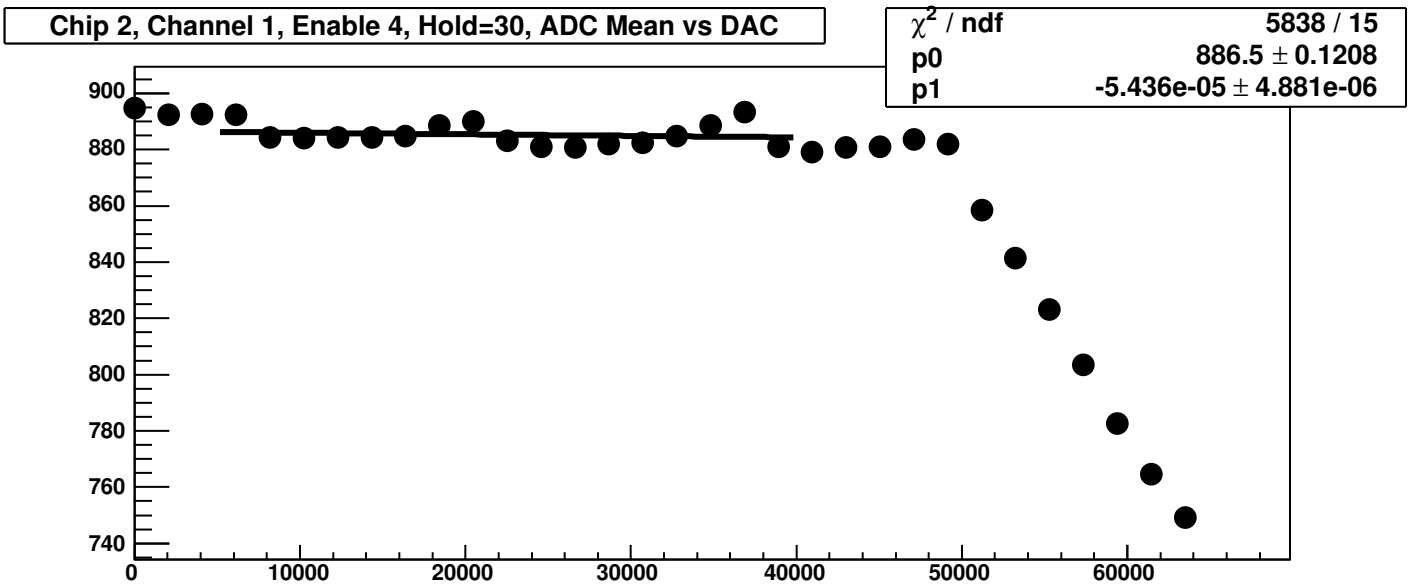

Chip 2, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

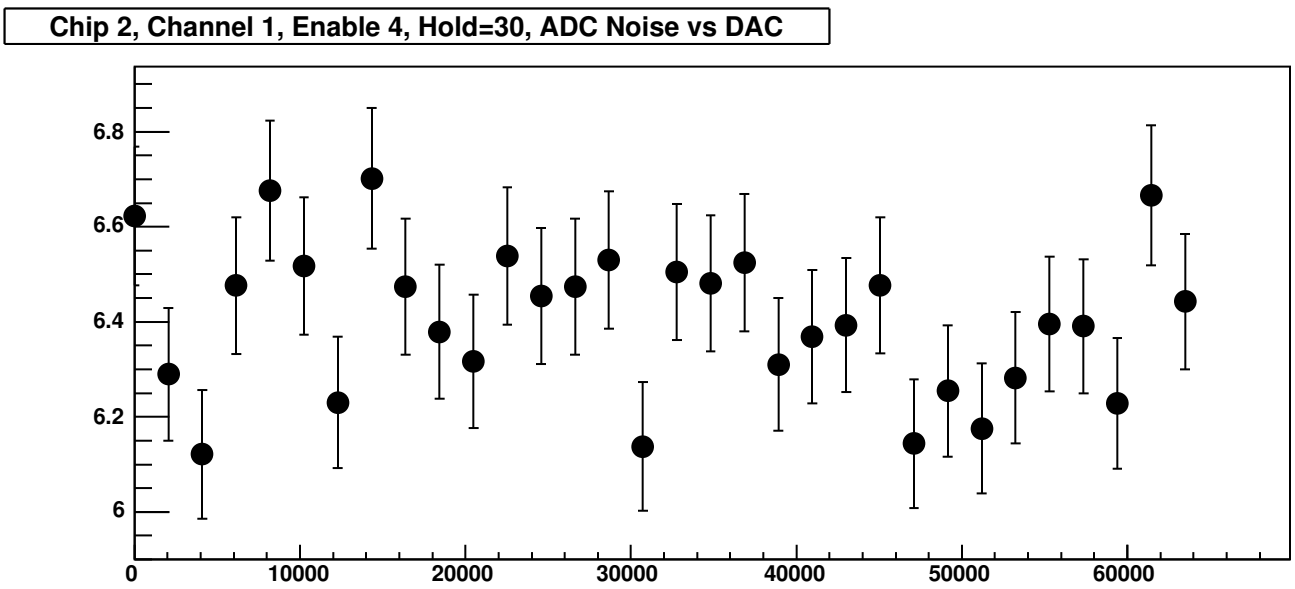

Chip 2, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

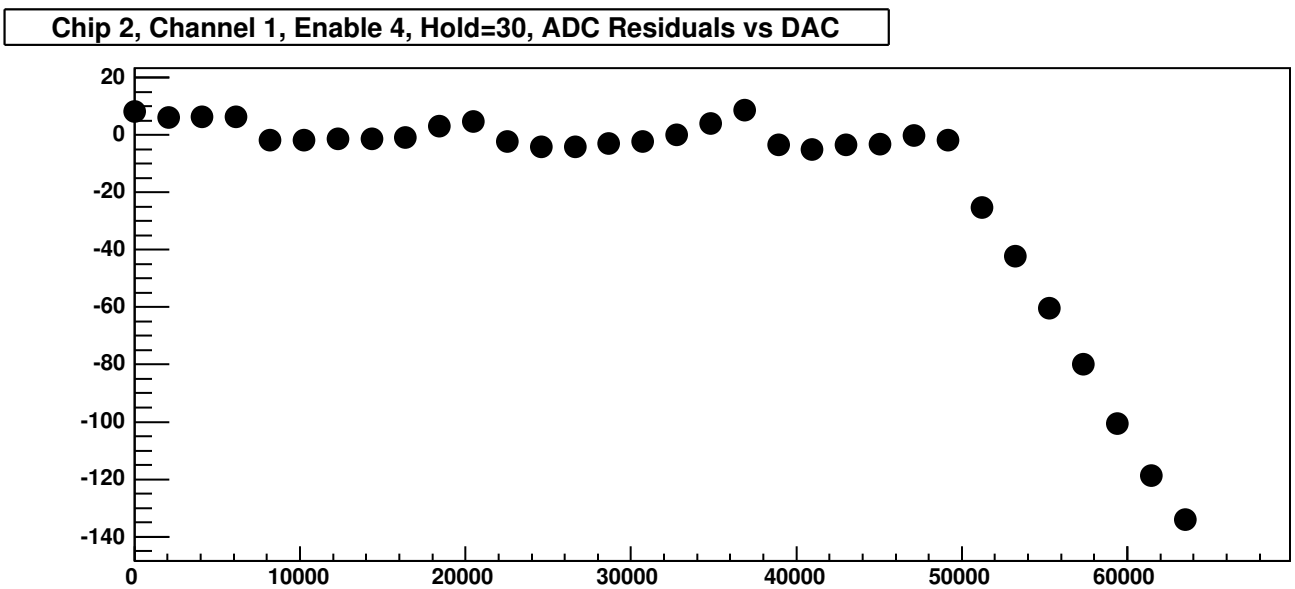

Chip 2, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.246e+04 / 15
p0 1012 ± 0.2729
p1 0.643 ± 2.526e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

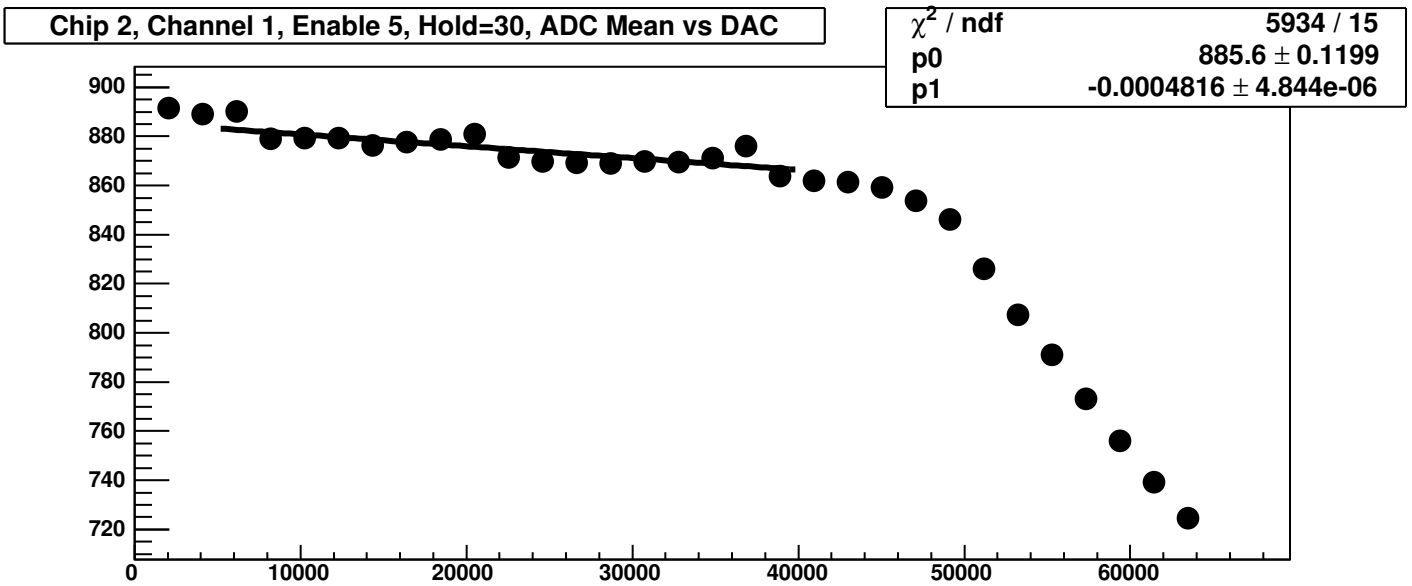

Chip 2, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

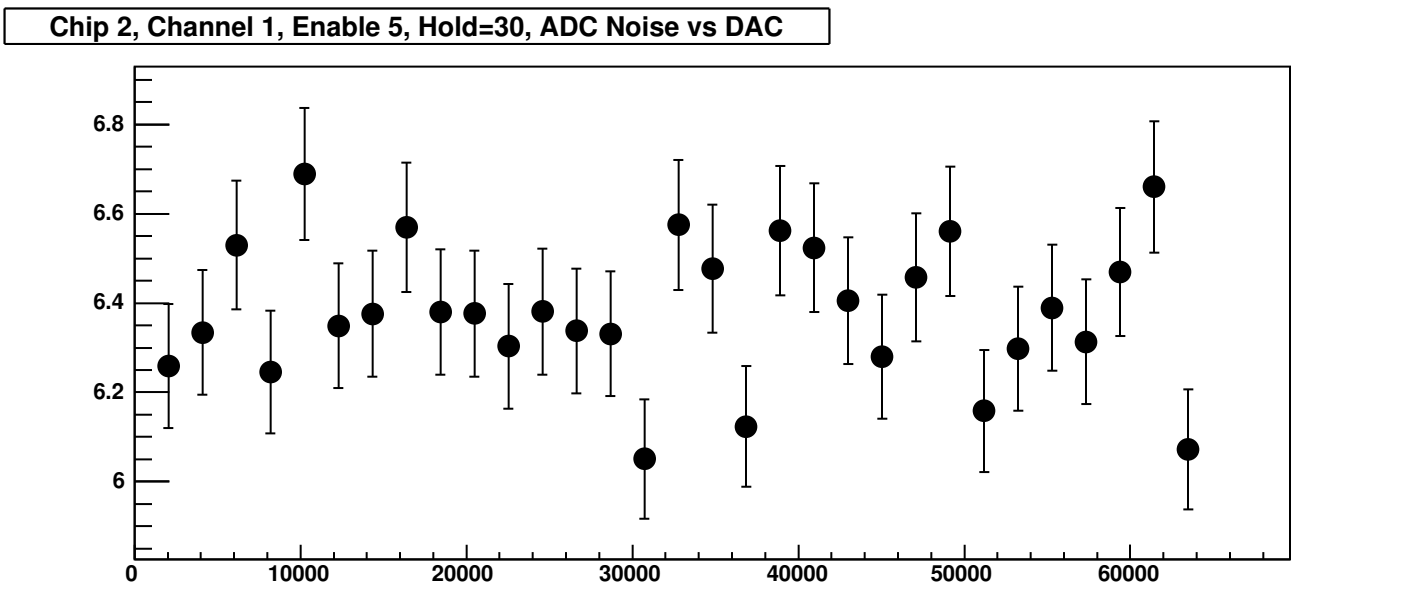

Chip 2, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

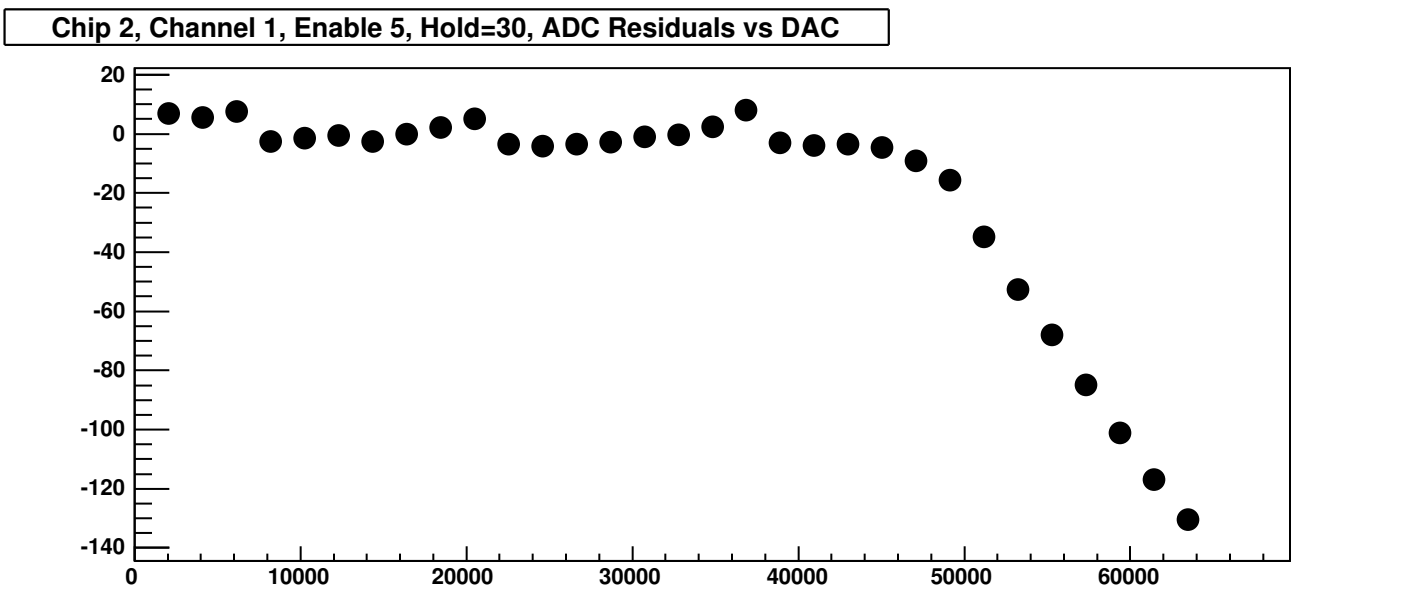

Chip 2, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5838 / 15
p0 886.5 ± 0.1208
p1 -5.436e-05 ± 4.881e-06
900
880
860
840
820
800
780
760
740
0
10000
20000
30000
40000
50000
60000

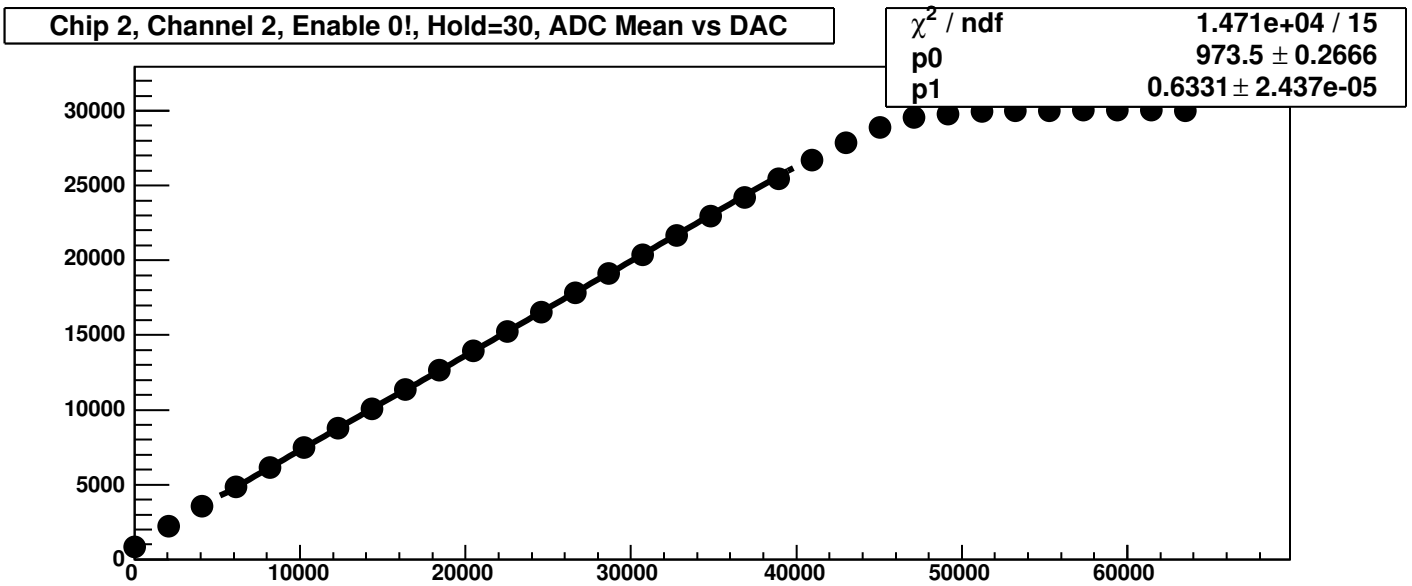

Chip 2, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

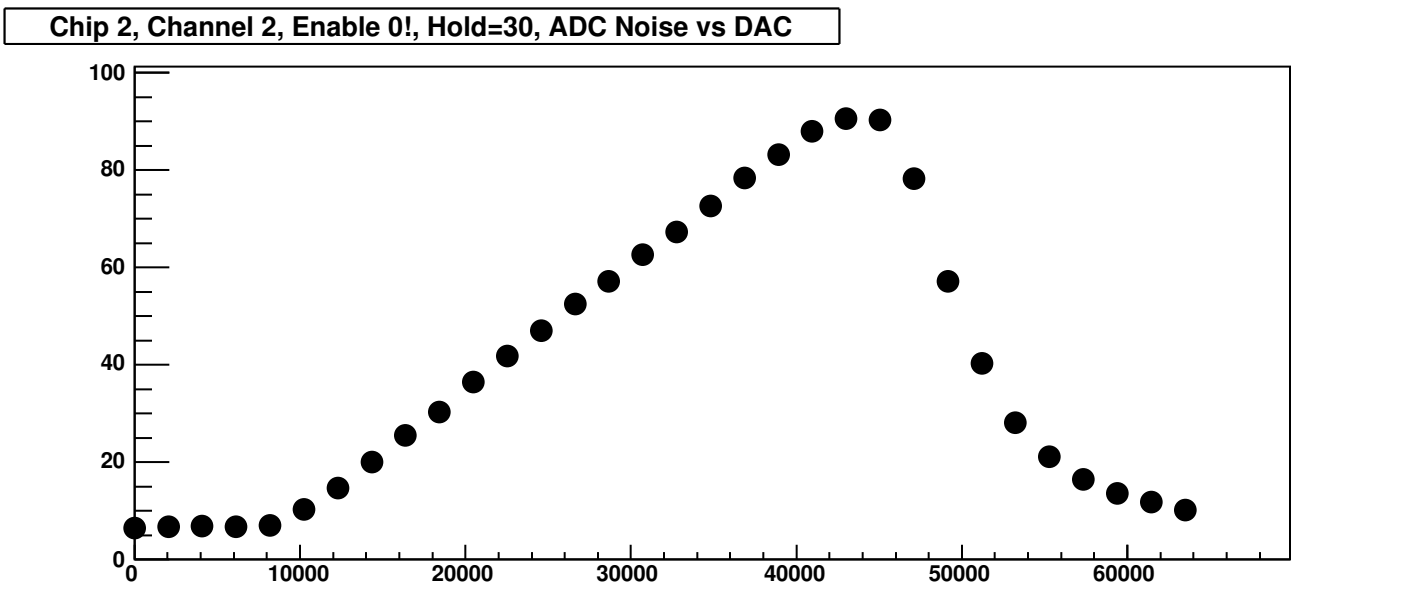

Chip 2, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

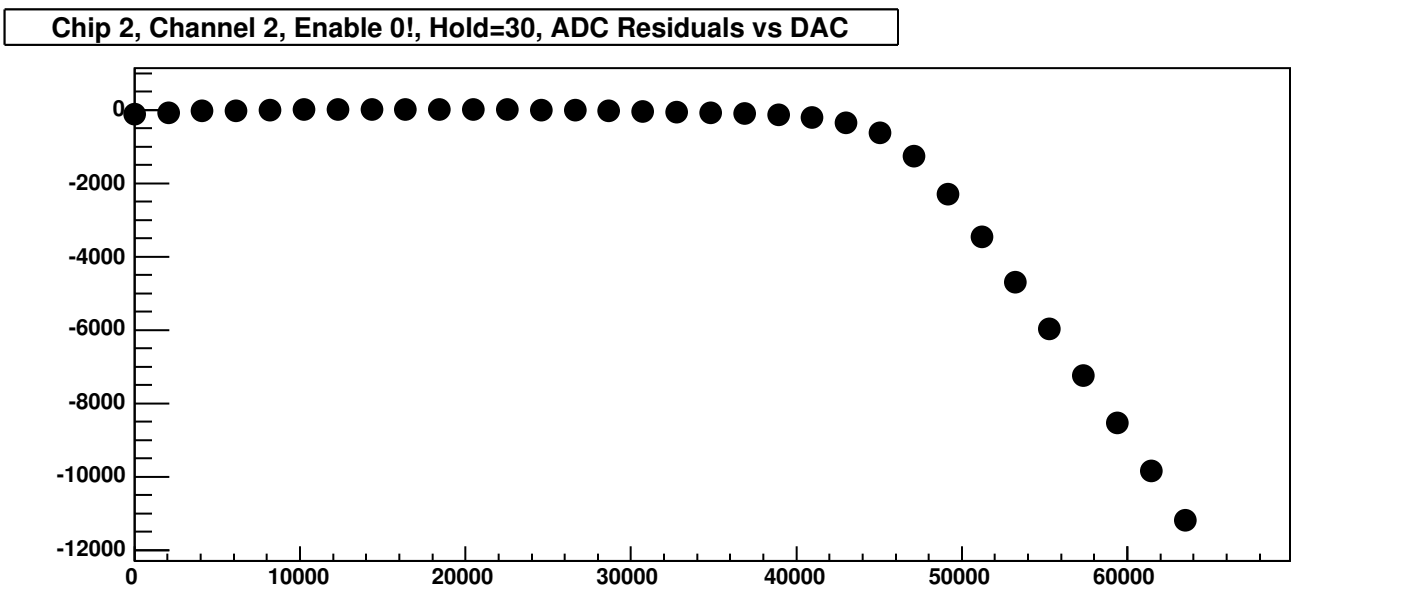

Chip 2, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5934 / 15
p0 885.6 ± 0.1199
p1 -0.0004816 ± 4.844e-06
900
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

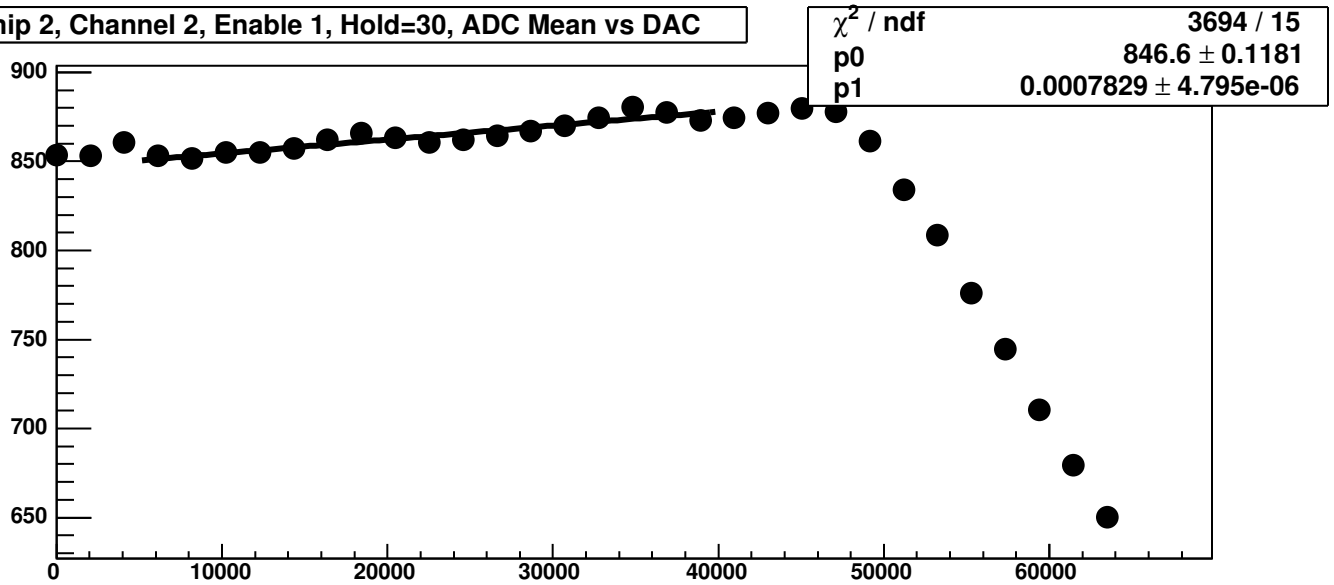

Chip 2, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.471e+04 / 15
p0 973.5 ± 0.2666
p1 0.6331 ± 2.437e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

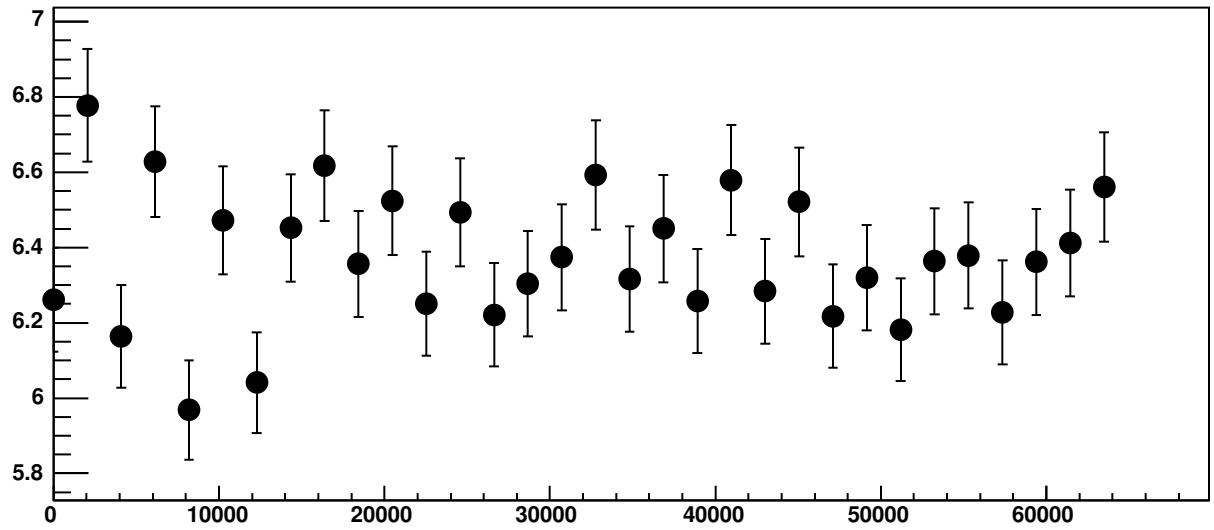

Chip 2, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

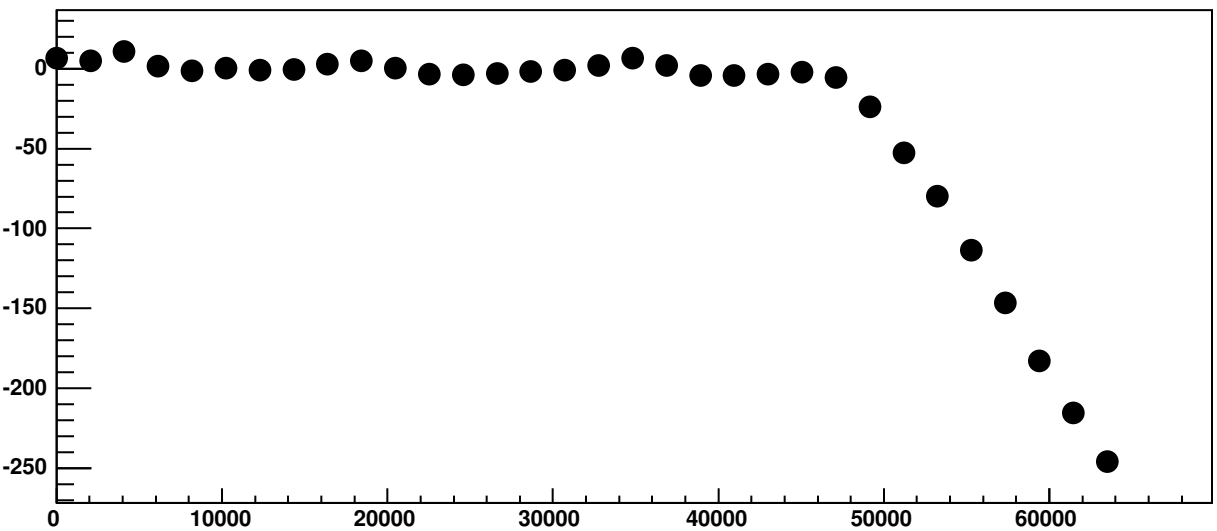
Chip 2, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC



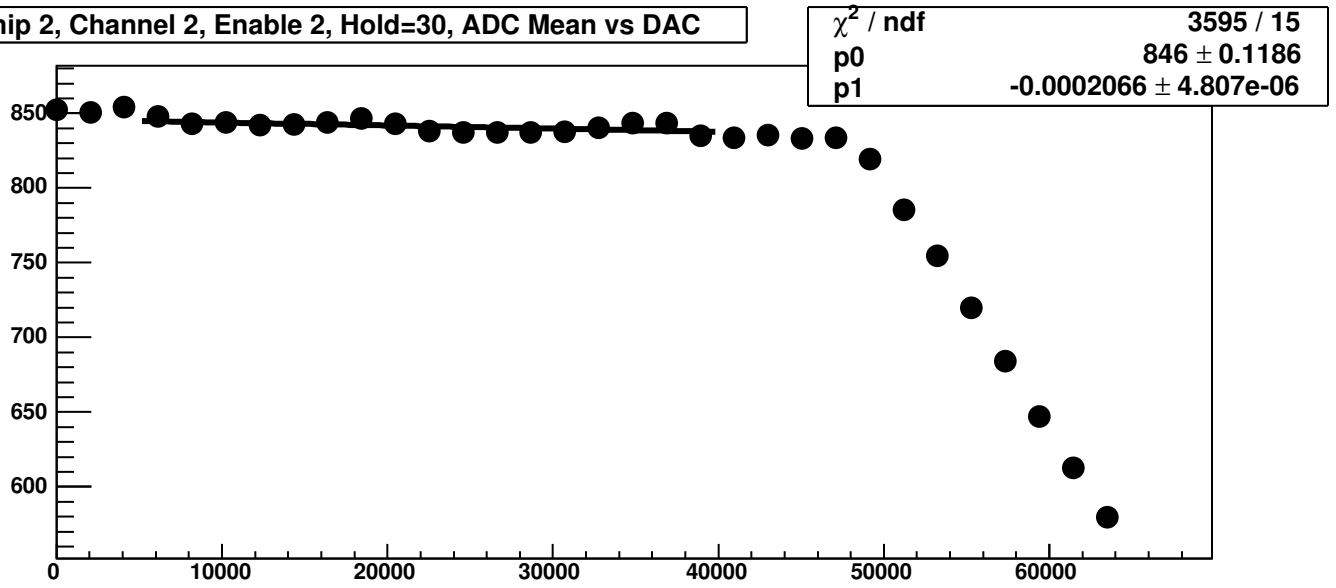
Chip 2, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC



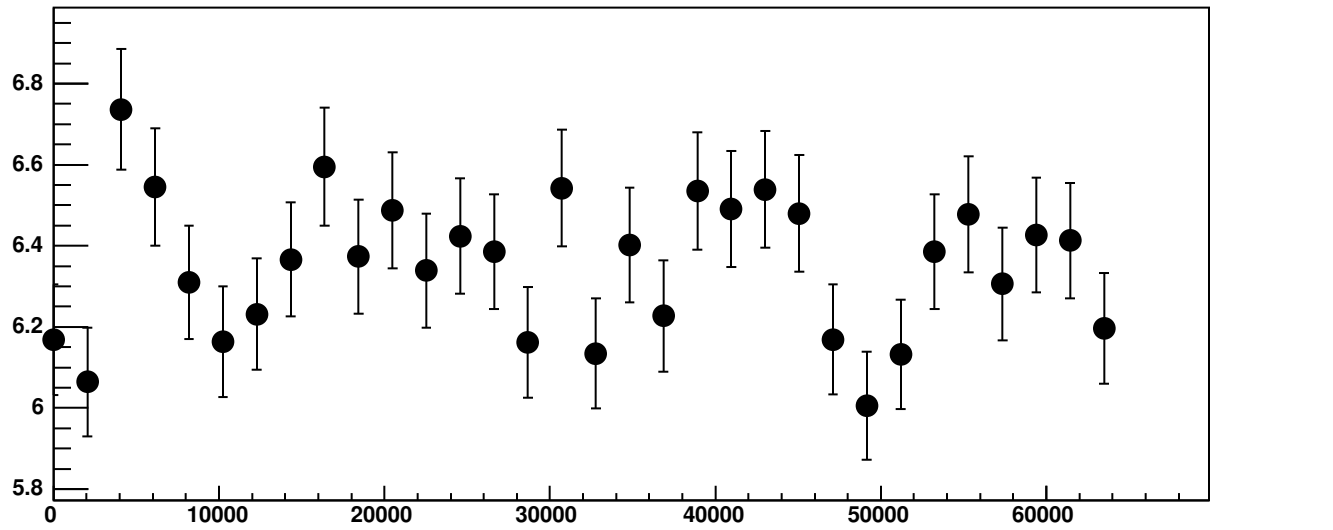
Chip 2, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC



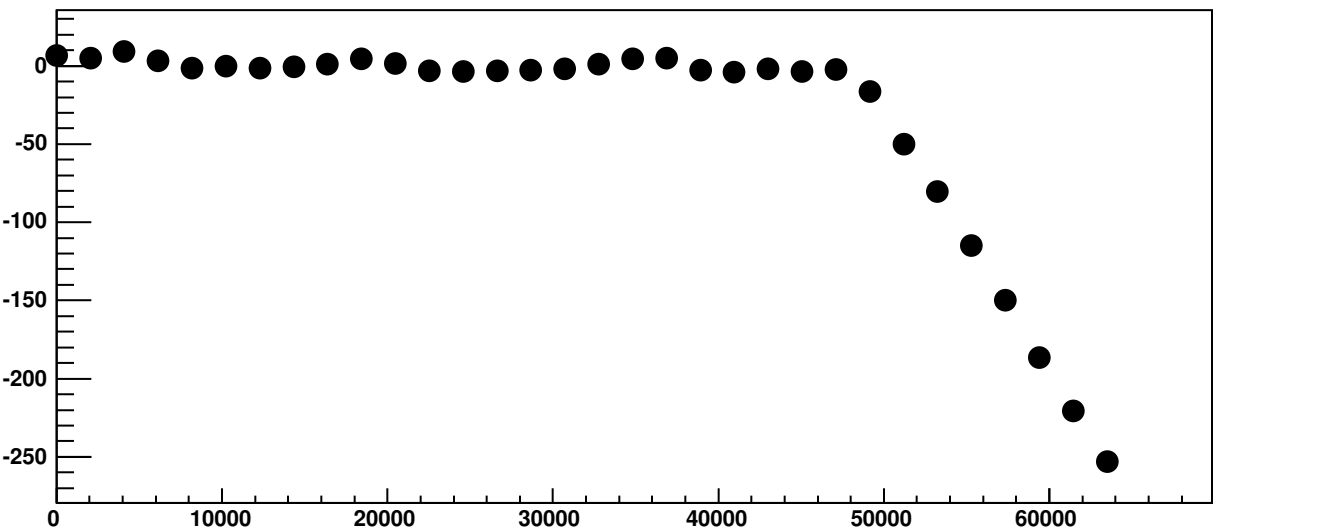
Chip 2, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC

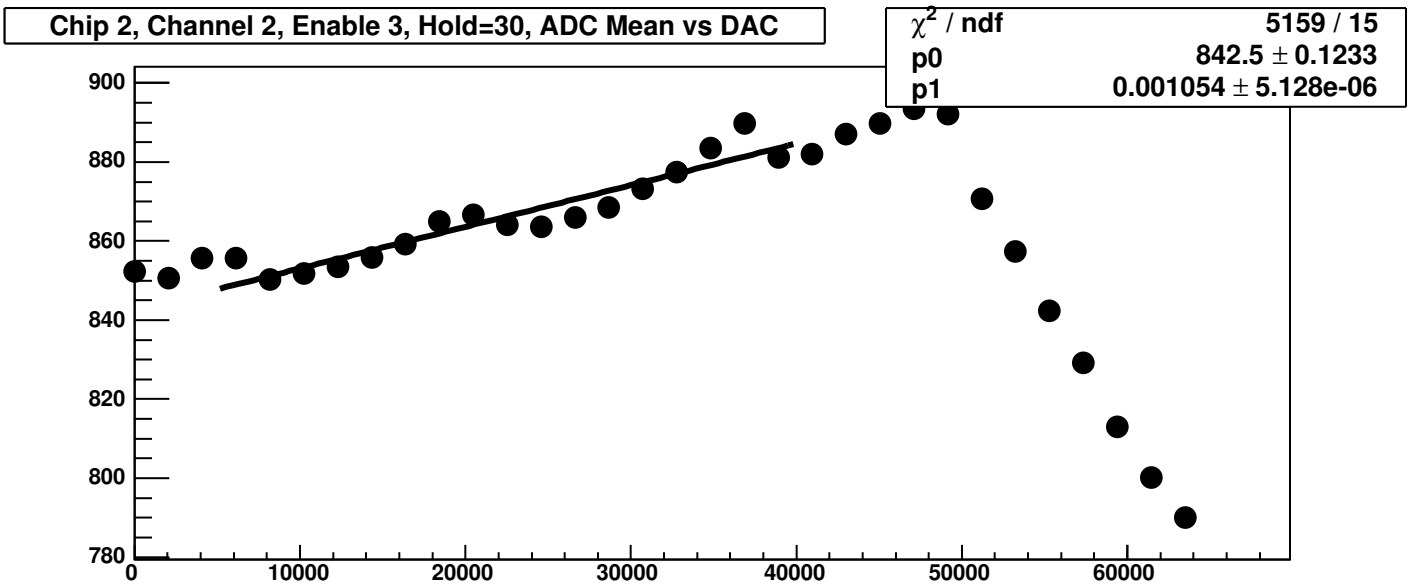


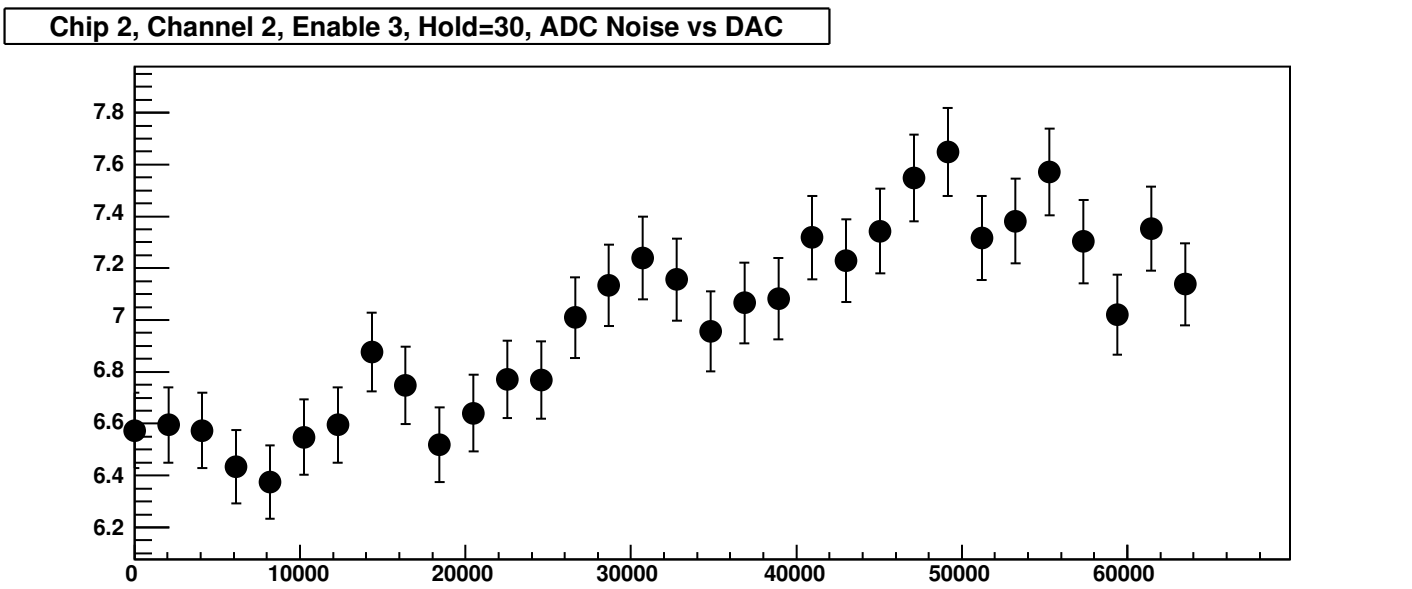
Chip 2, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC

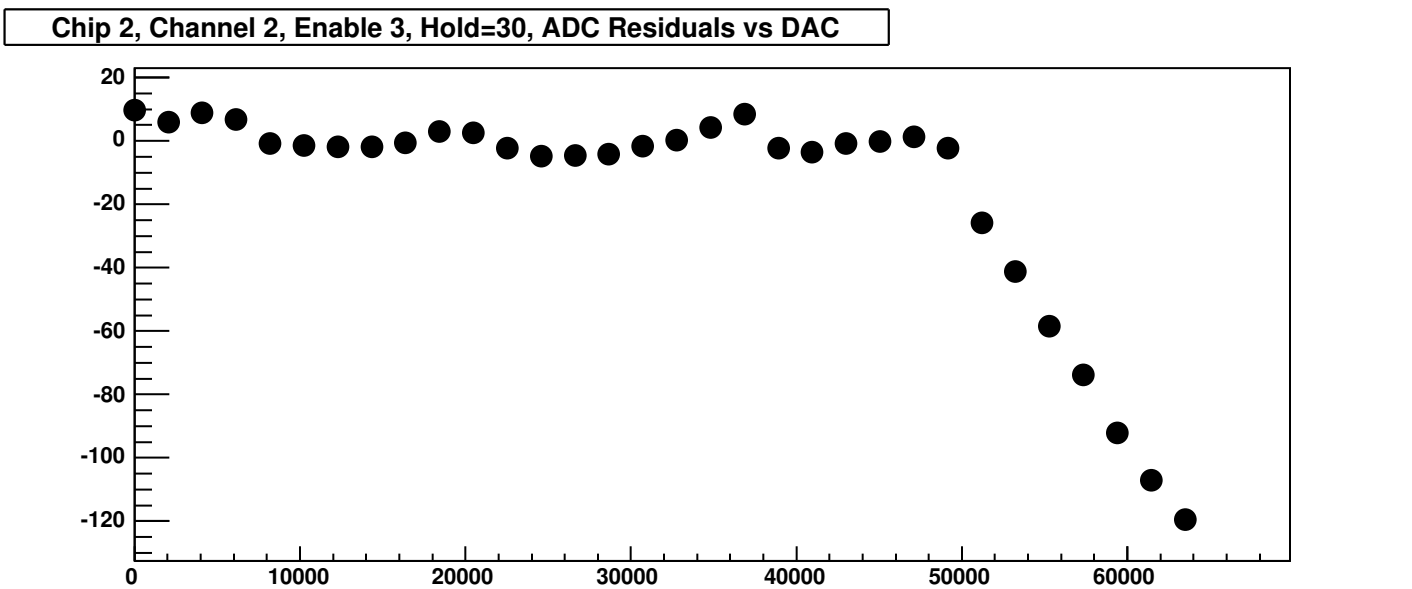


Chip 2, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC

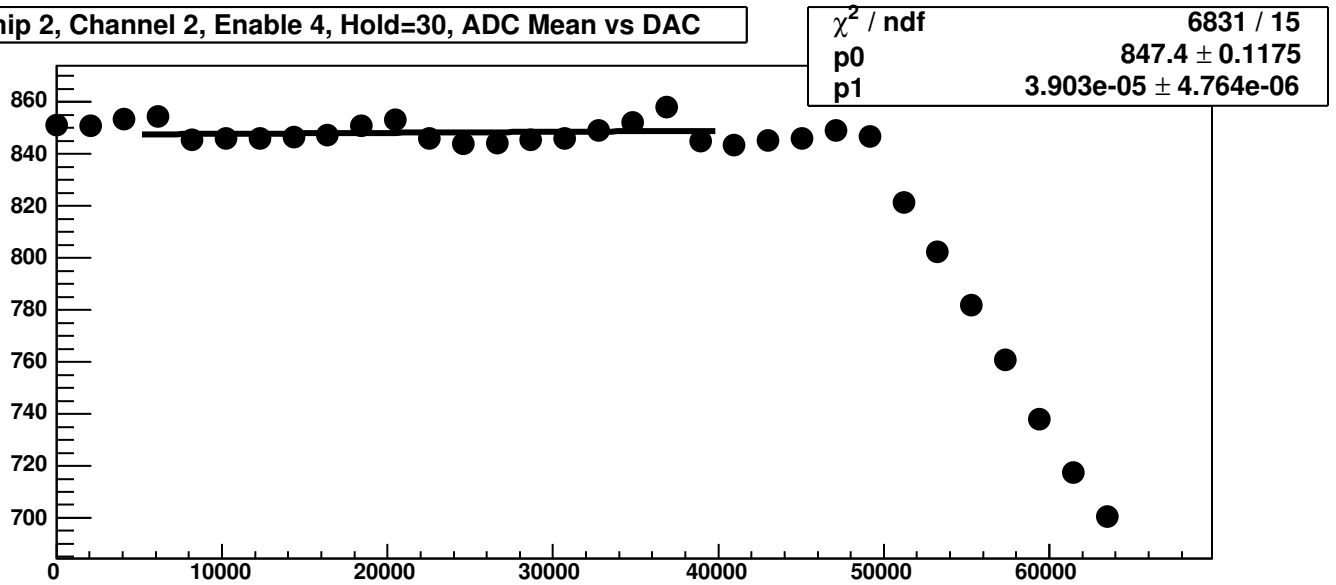



Chip 2, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5159 / 15
p0 842.5 ± 0.1233
p1 0.001054 ± 5.128e-06
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

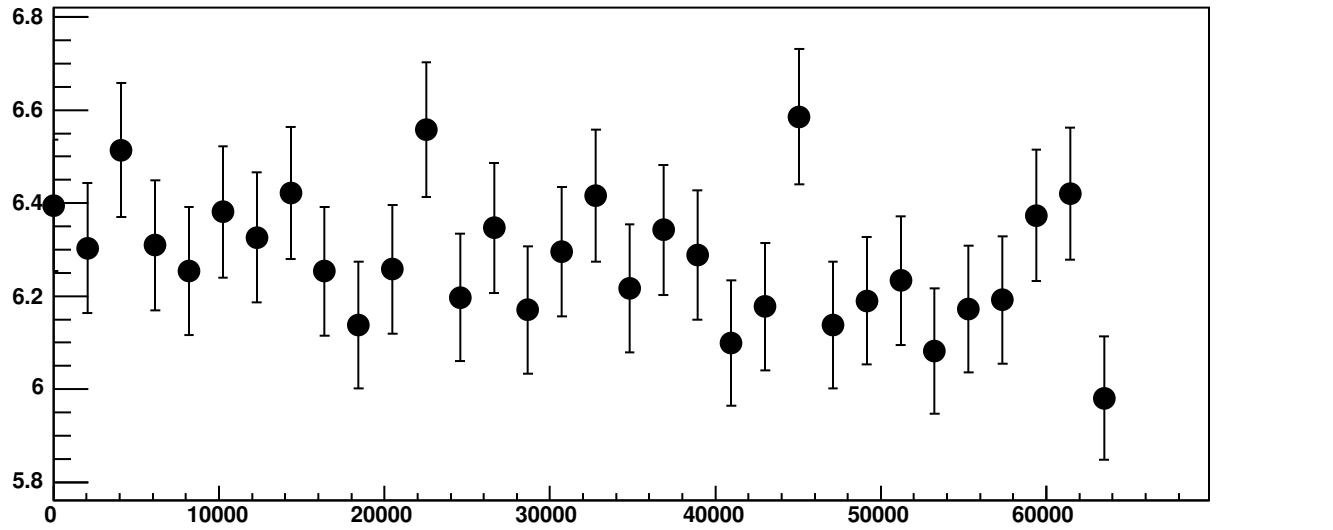

Chip 2, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

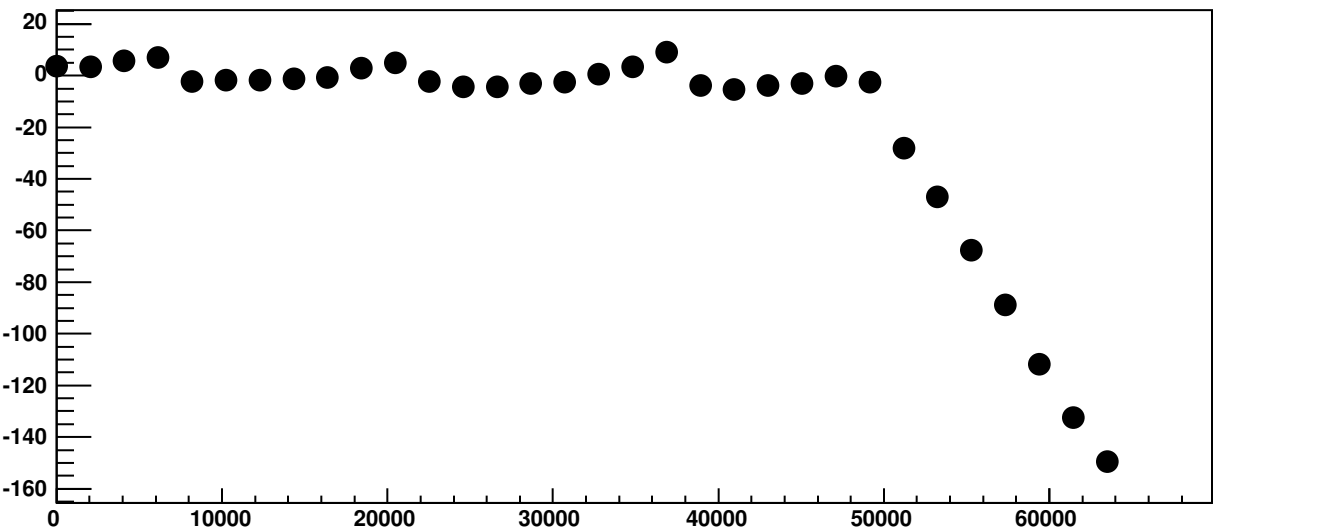
Chip 2, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

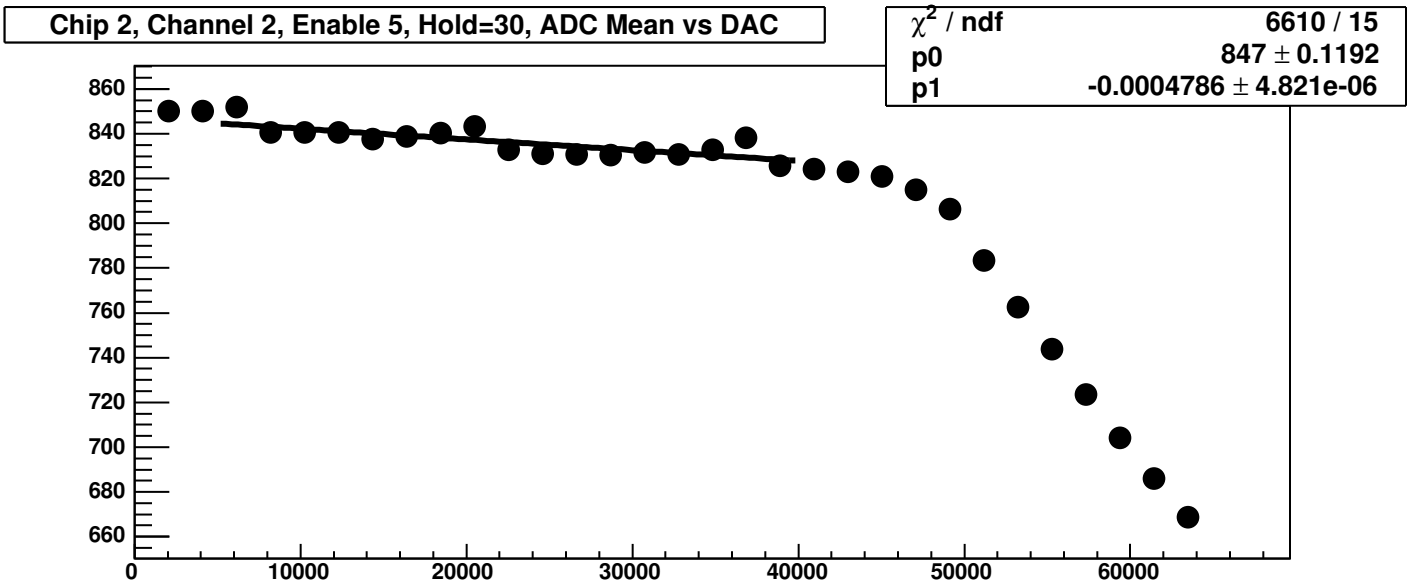


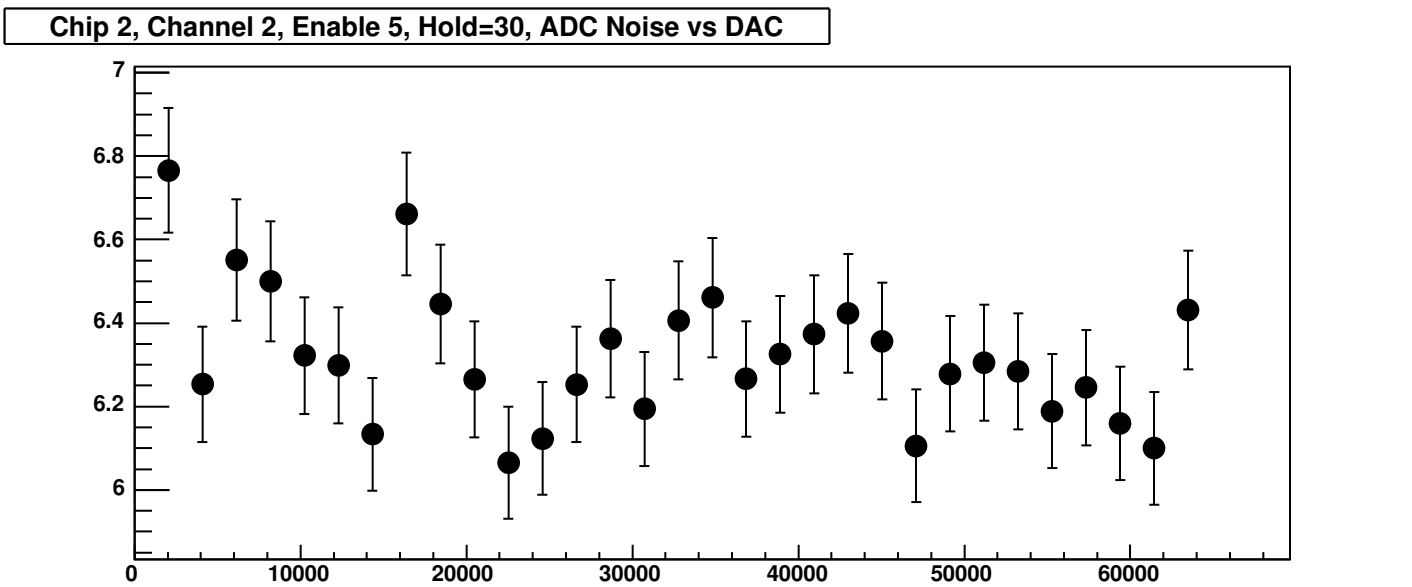
Chip 2, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC

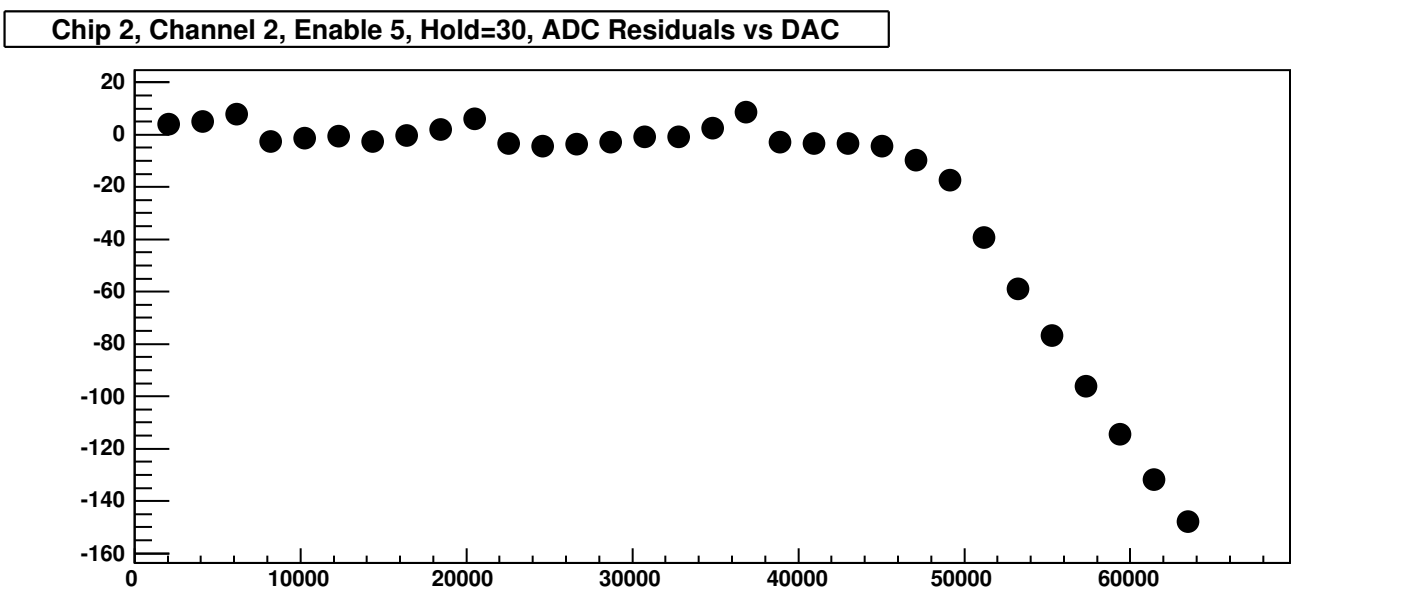


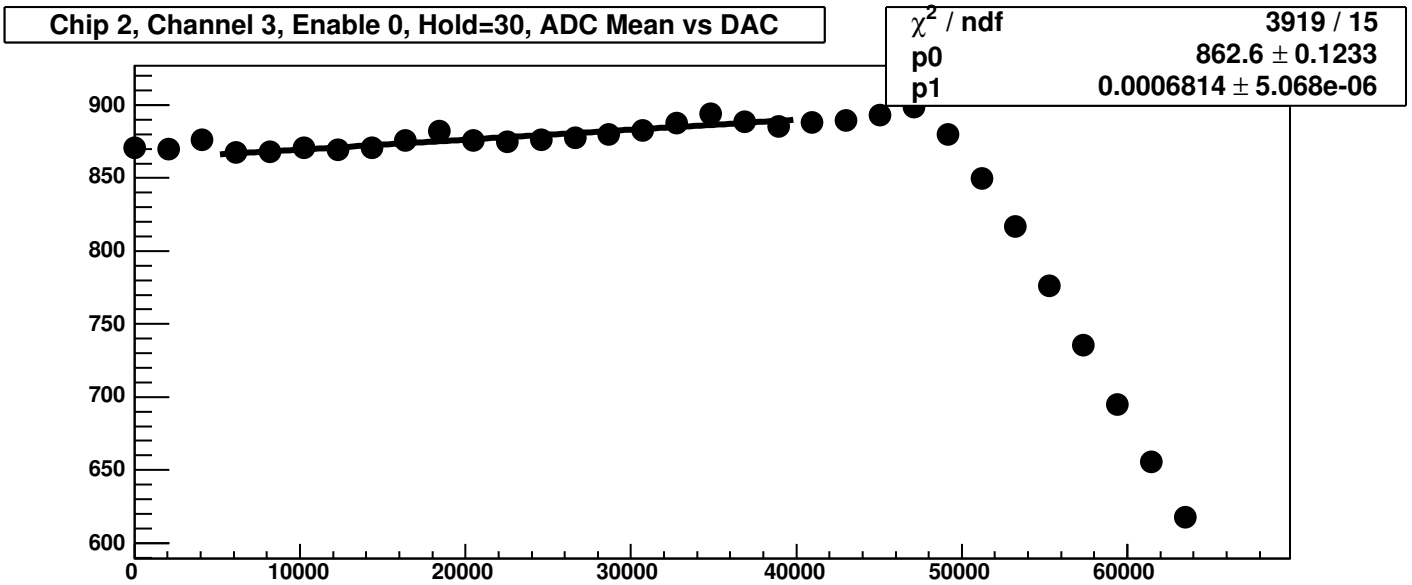
Chip 2, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

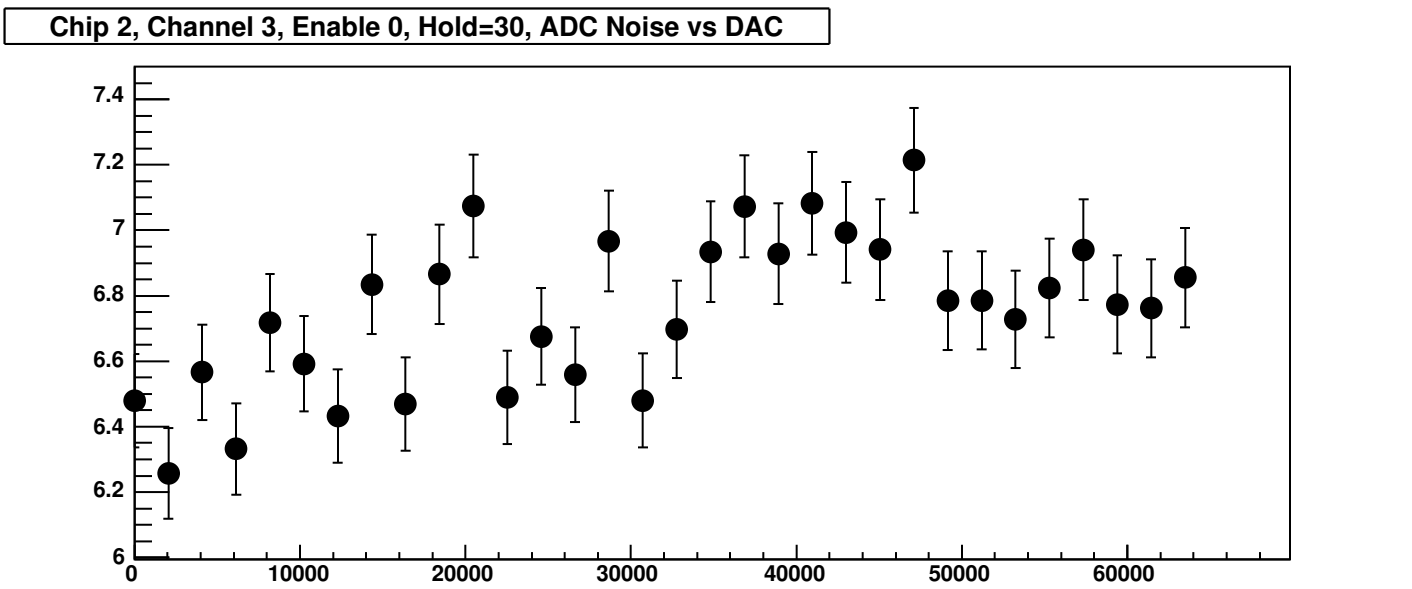


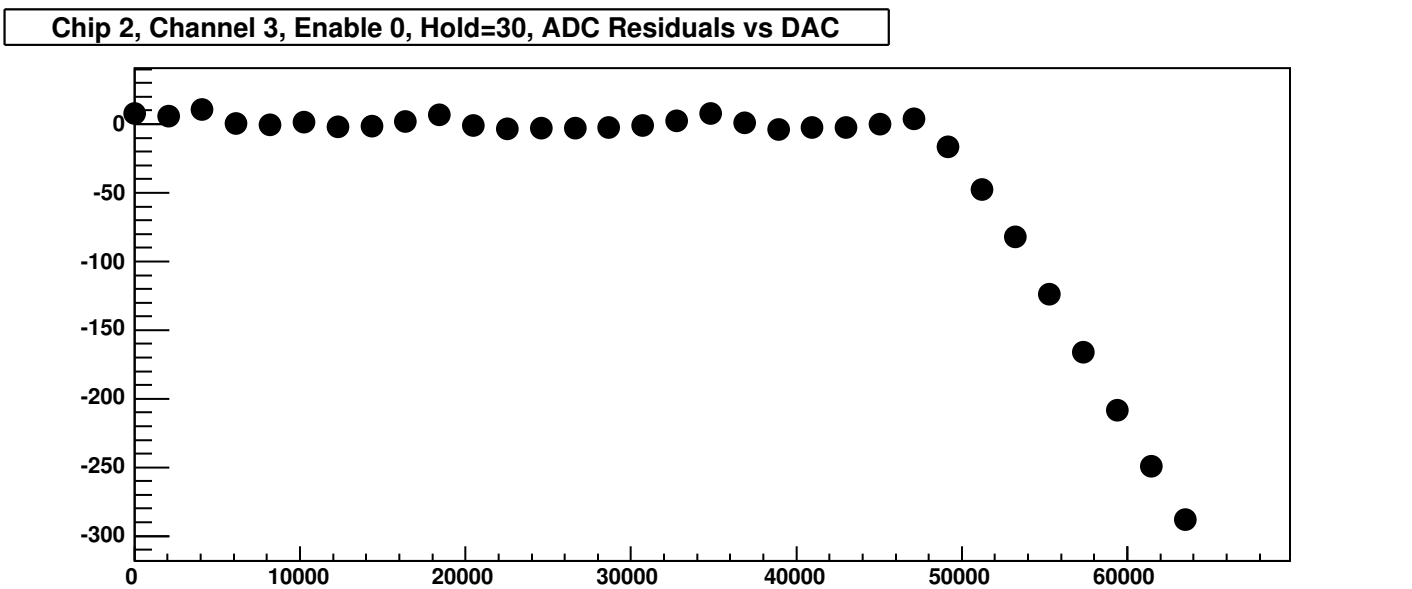

Chip 2, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6610 / 15
p0 847 ± 0.1192
p1 -0.0004786 ± 4.821e-06
860
840
820
800
780
760
740
720
700
680
660
0
10000
20000
30000
40000
50000
60000

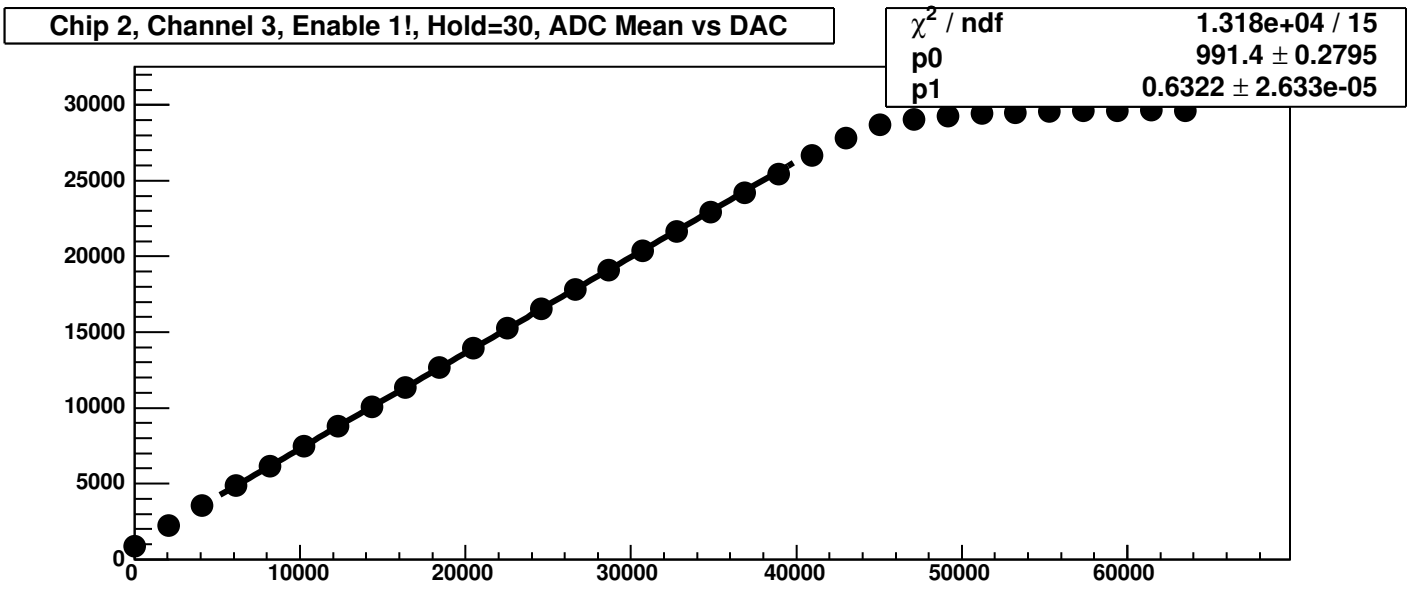

Chip 2, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

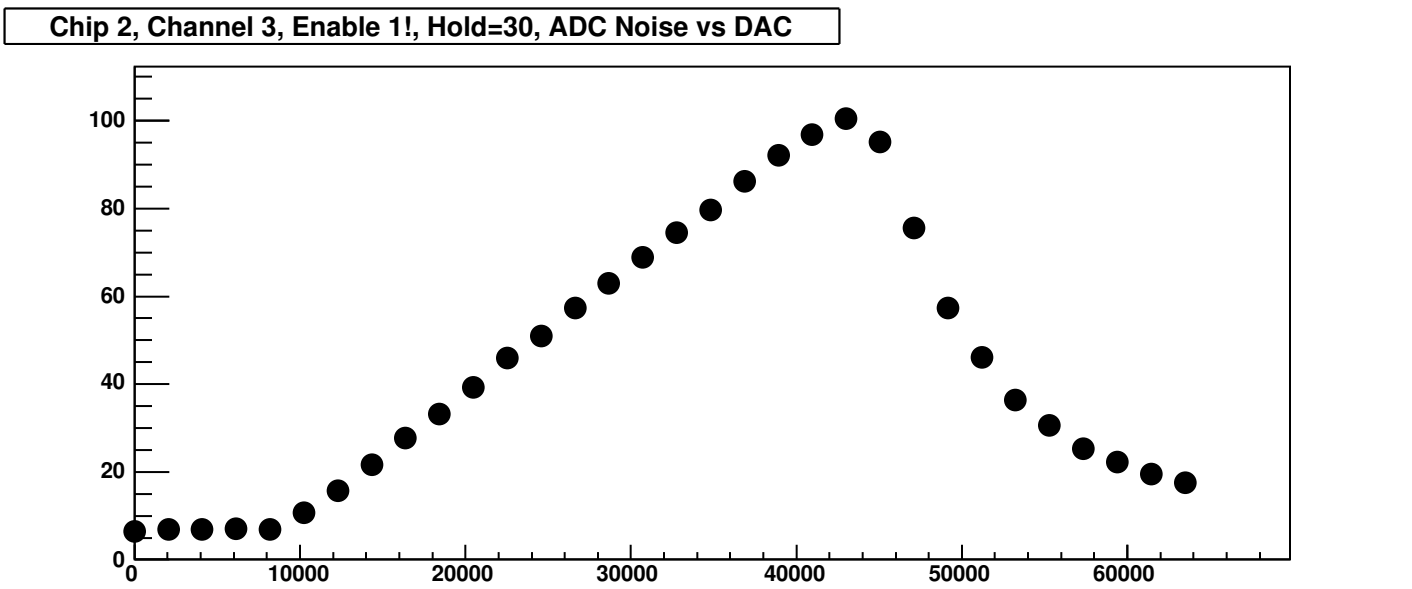

Chip 2, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

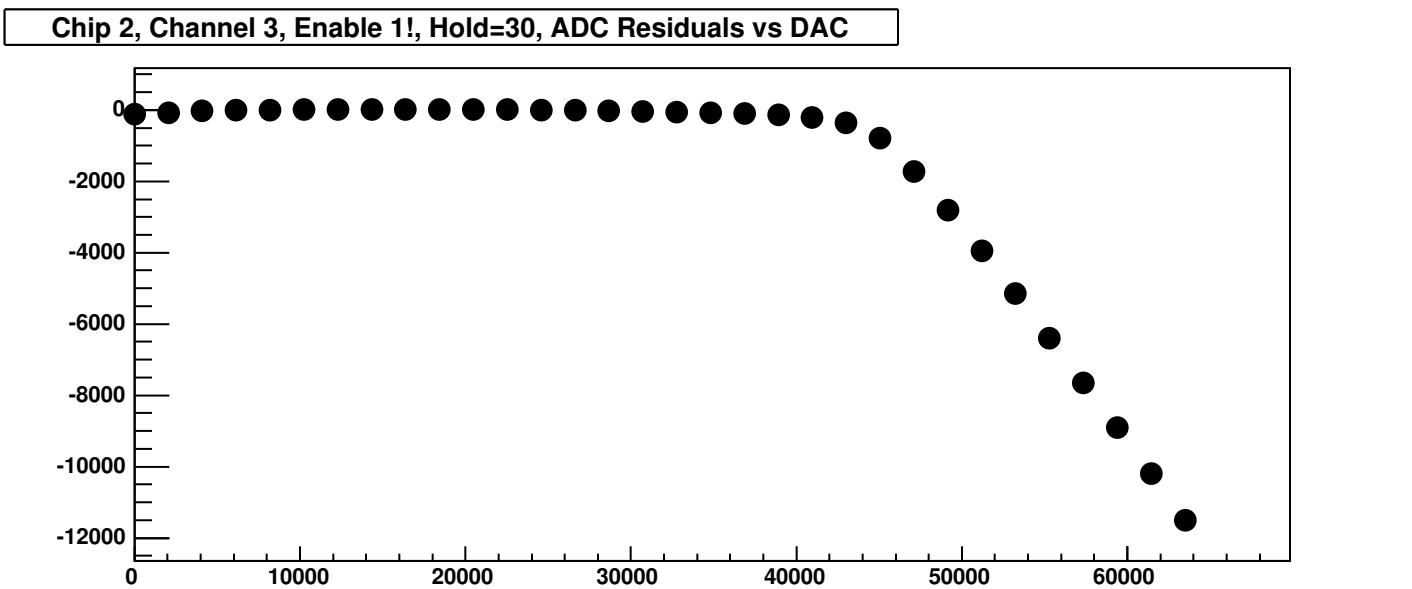

Chip 2, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3919 / 15
p0 862.6 ± 0.1233
p1 0.0006814 ± 5.068e-06
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

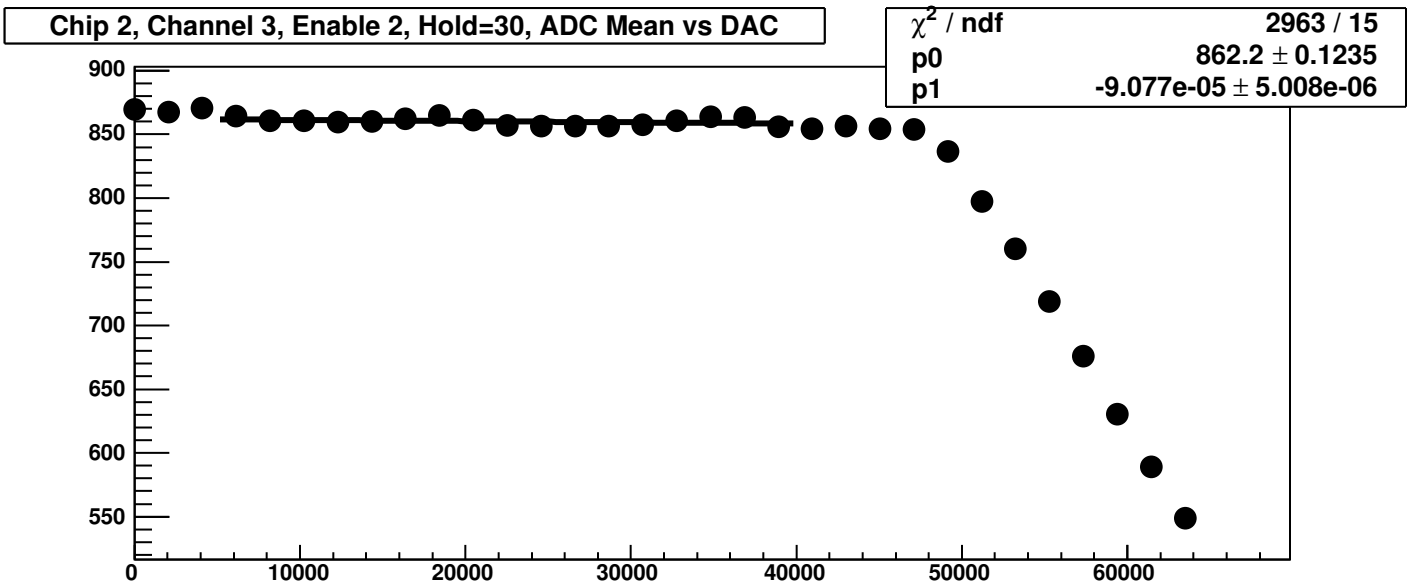

Chip 2, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

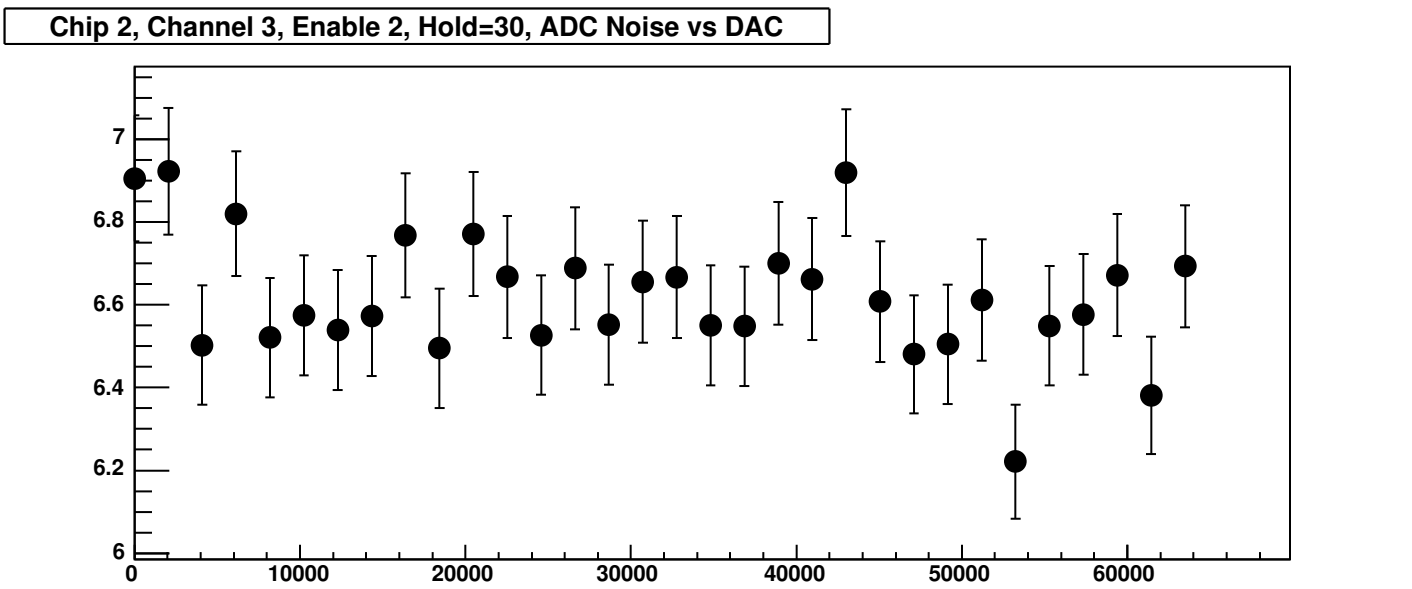

Chip 2, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

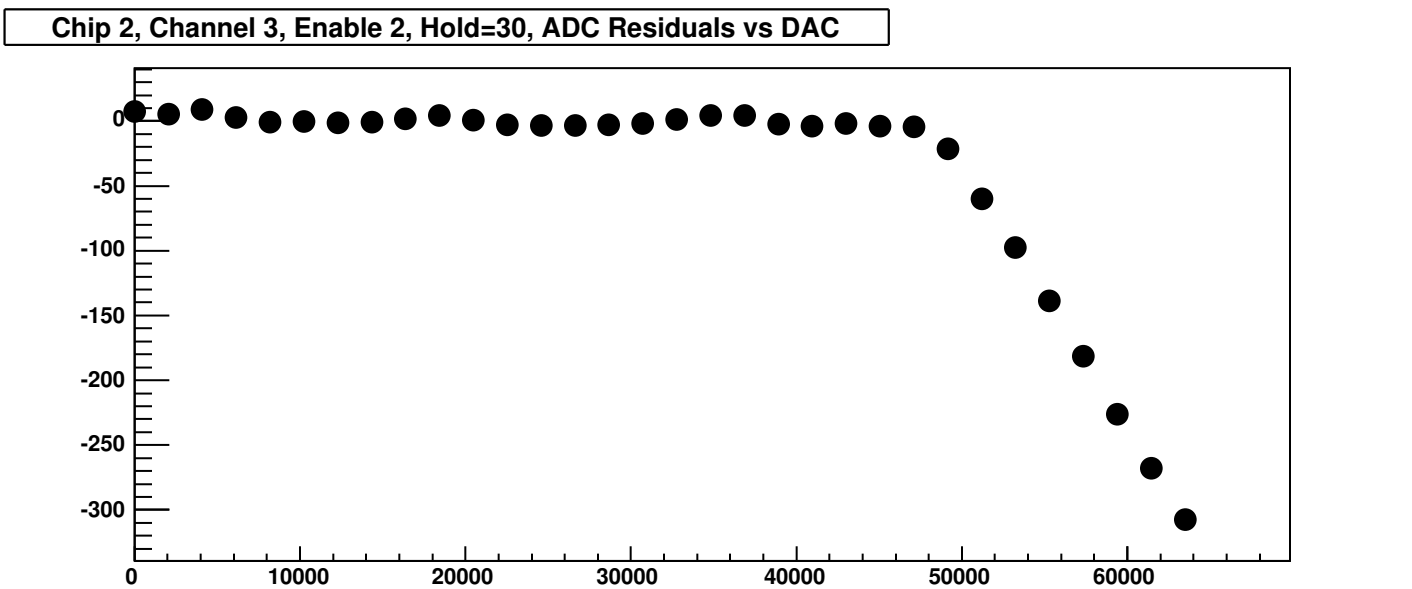

Chip 2, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.318e+04 / 15
p0 991.4 ± 0.2795
p1 0.6322 ± 2.633e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

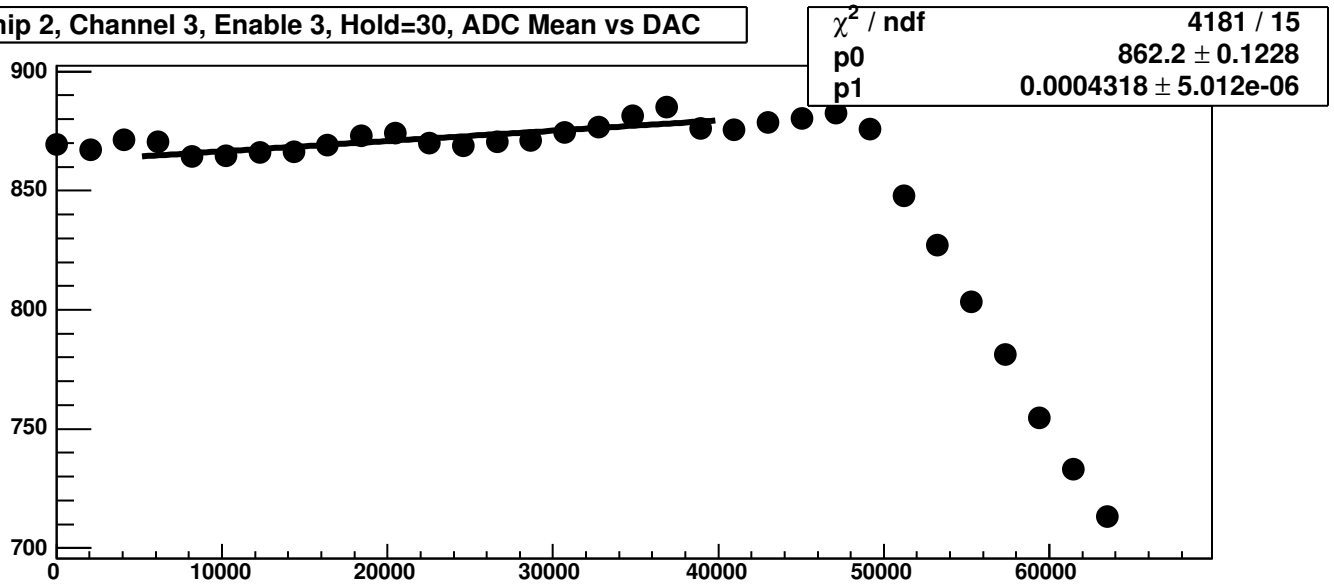

Chip 2, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2963 / 15
p0 862.2 ± 0.1235
p1 -9.077e-05 ± 5.008e-06
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

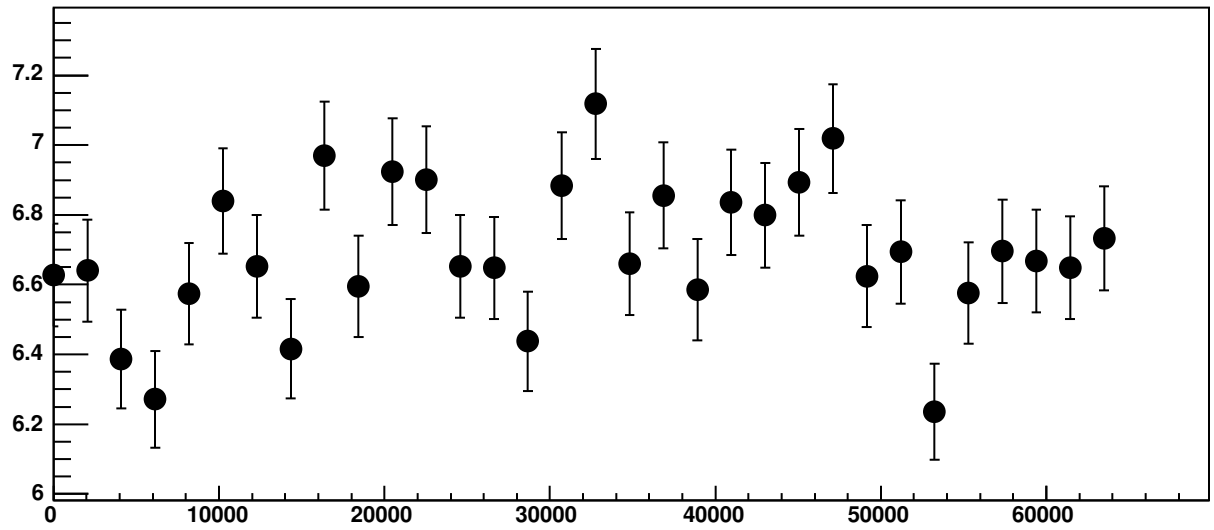

Chip 2, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

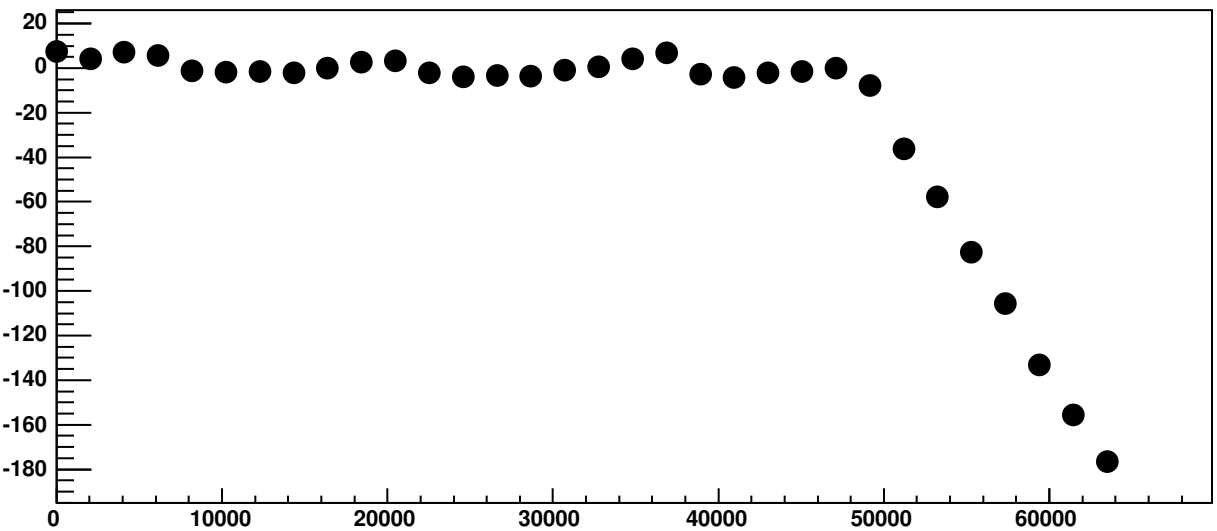
Chip 2, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC

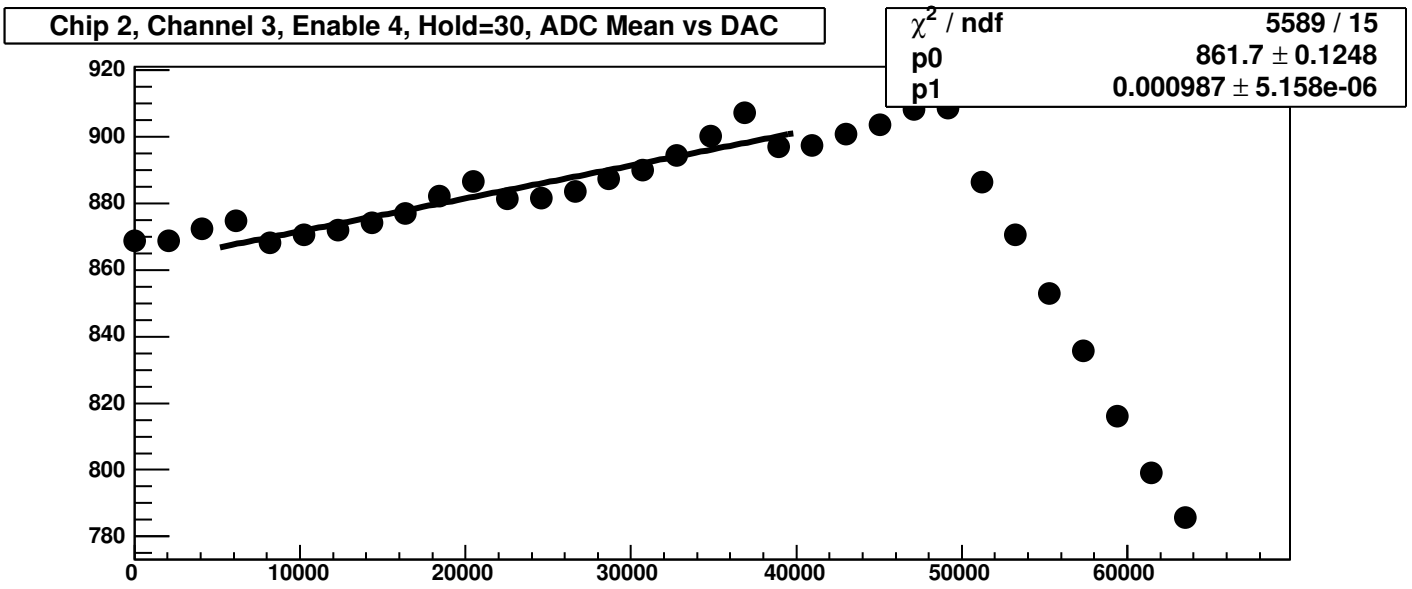


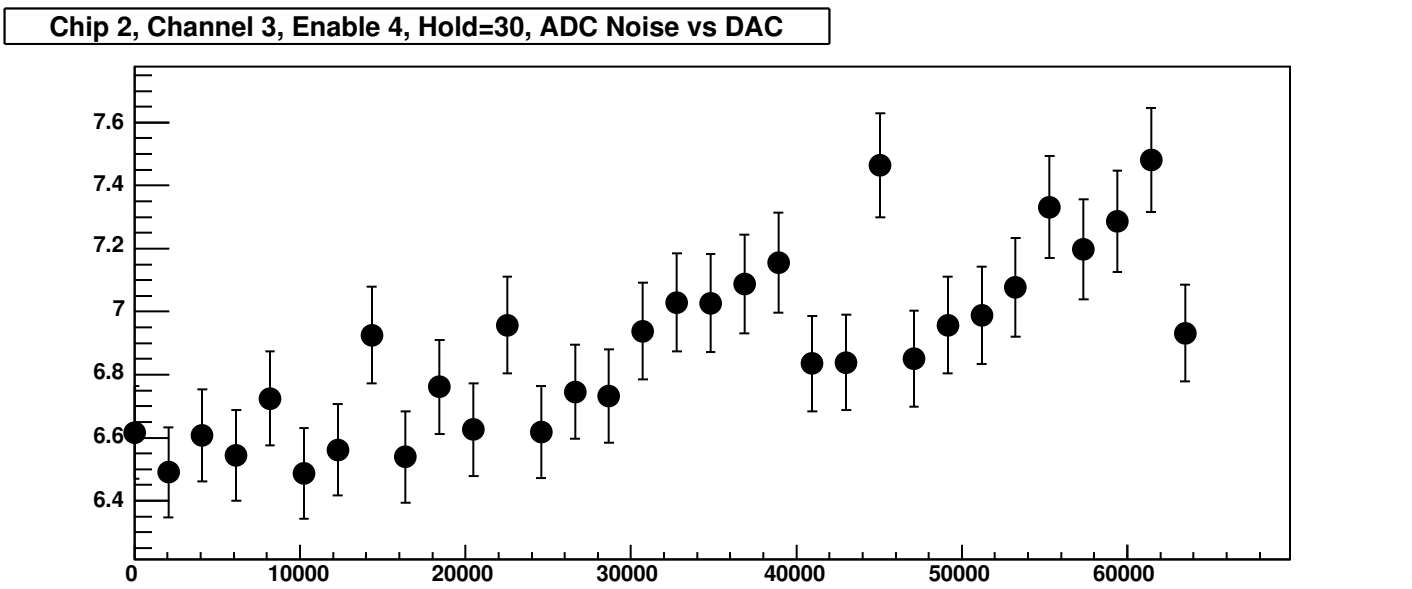
Chip 2, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC

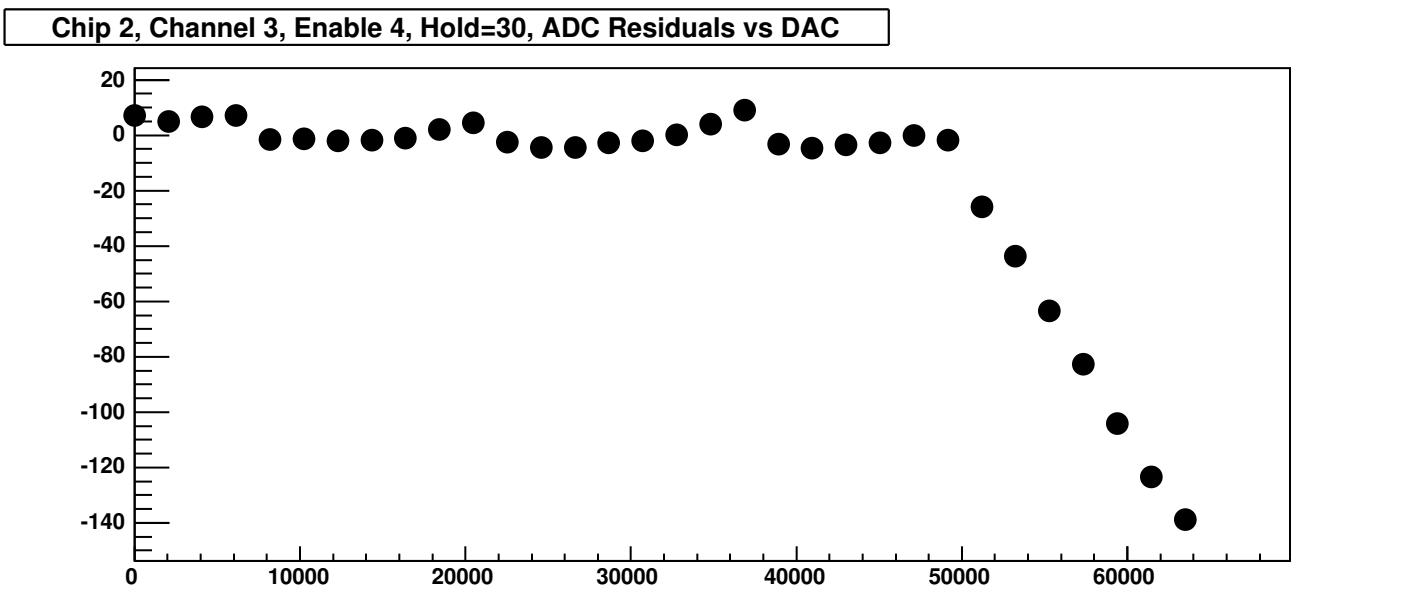


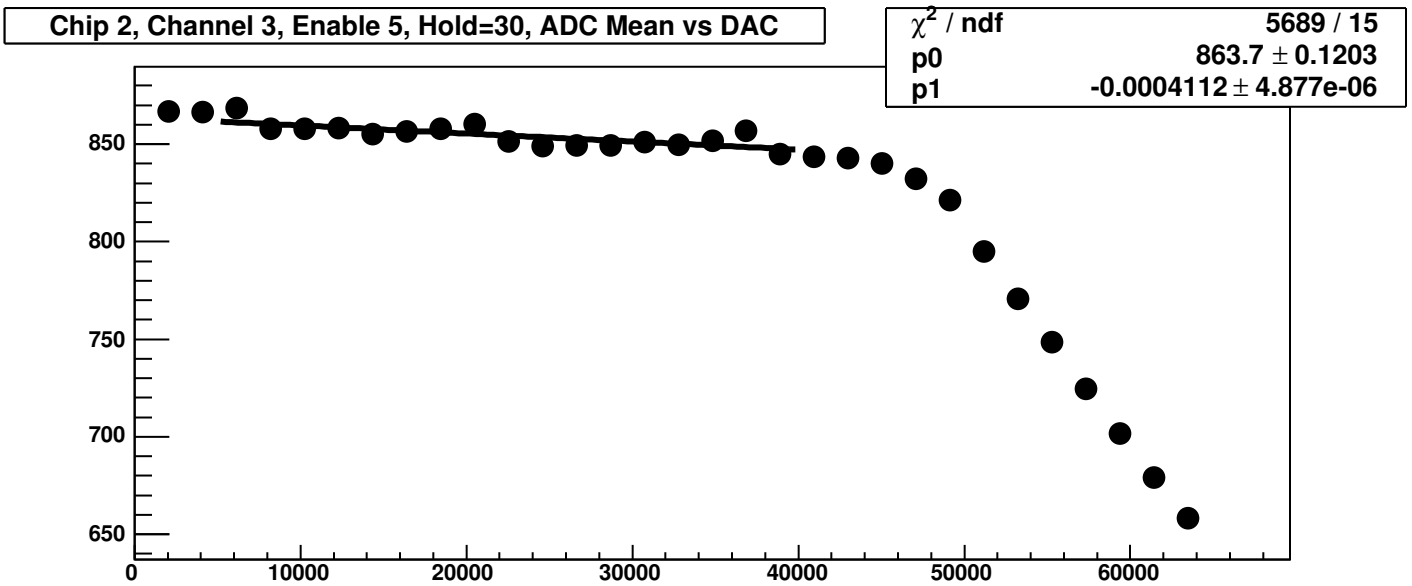
Chip 2, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC

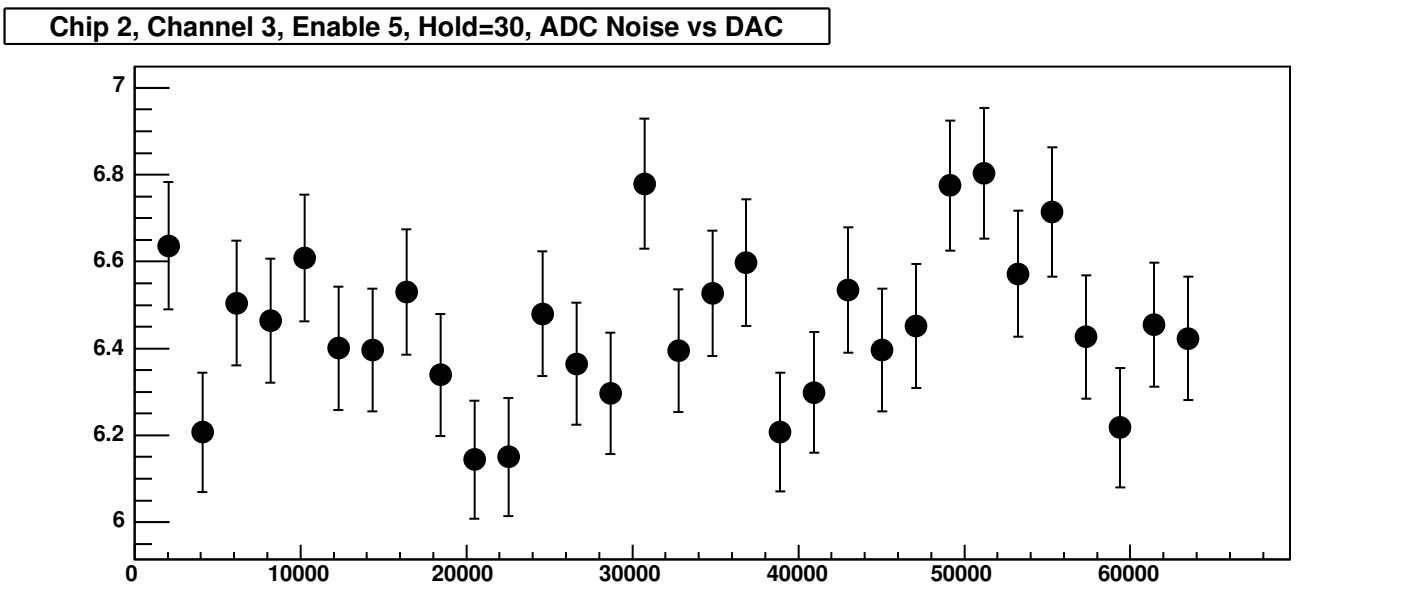


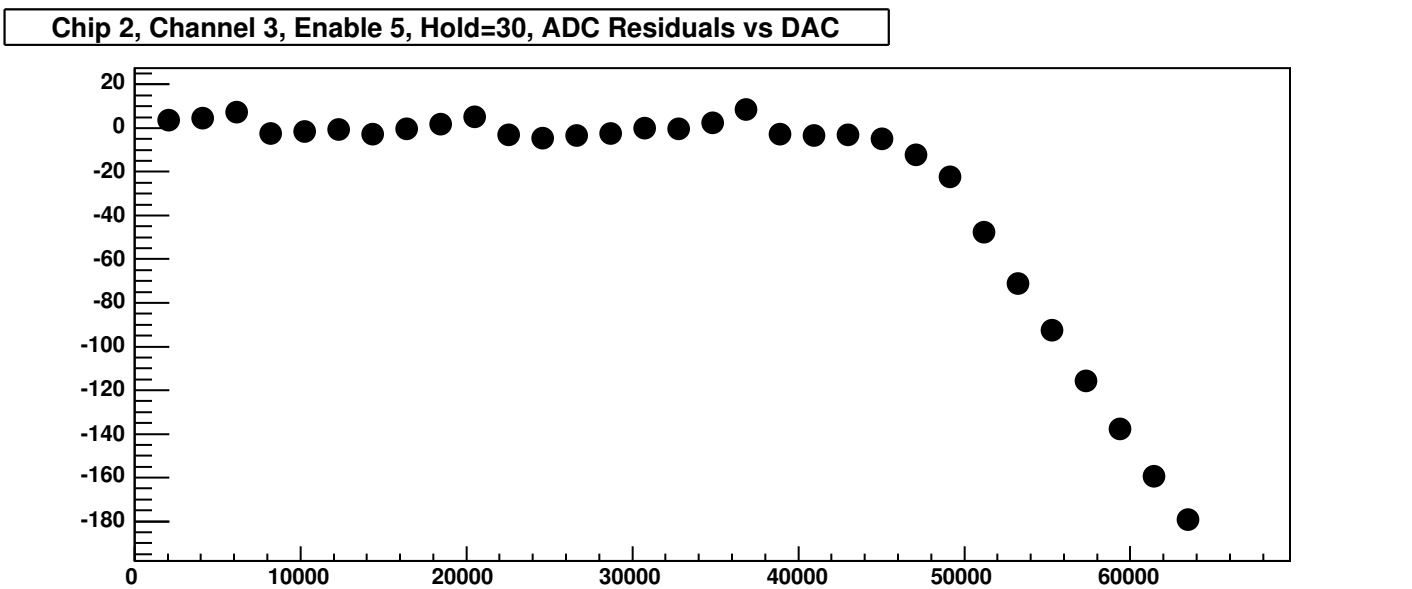

Chip 2, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5589 / 15
p0 861.7 ± 0.1248
p1 0.000987 ± 5.158e-06
920
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

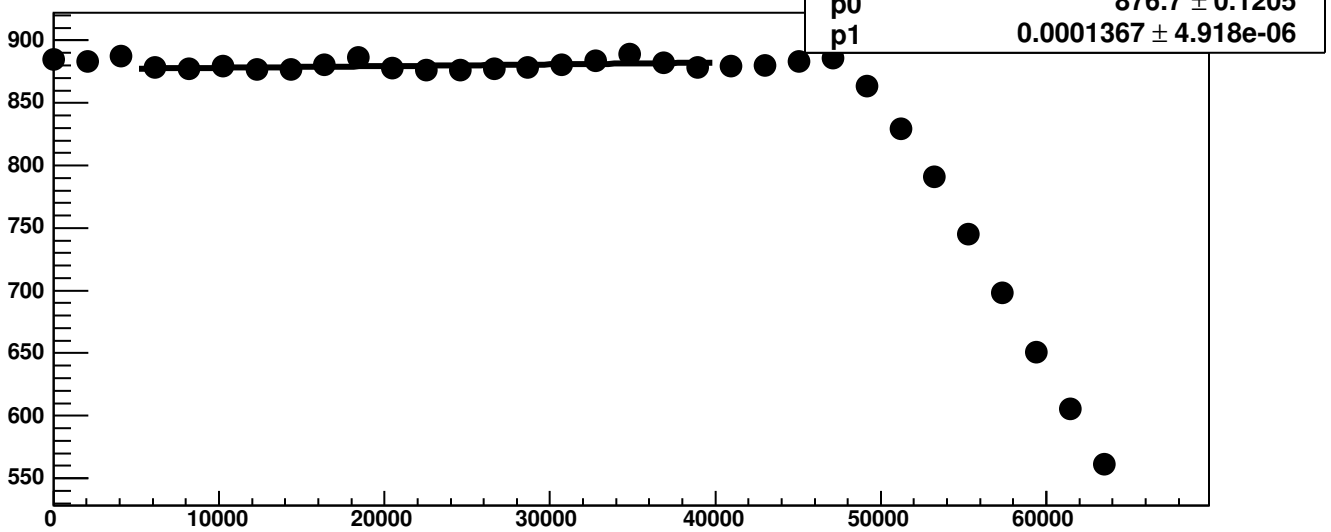

Chip 2, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5689 / 15
p0 863.7 ± 0.1203
p1 -0.0004112 ± 4.877e-06
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

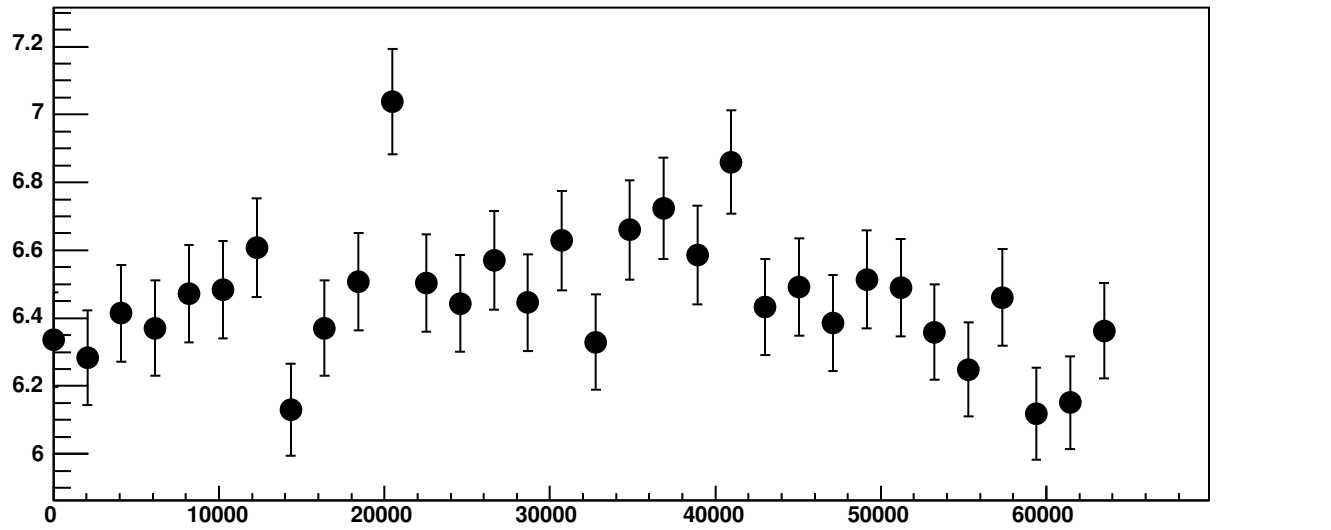

Chip 2, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

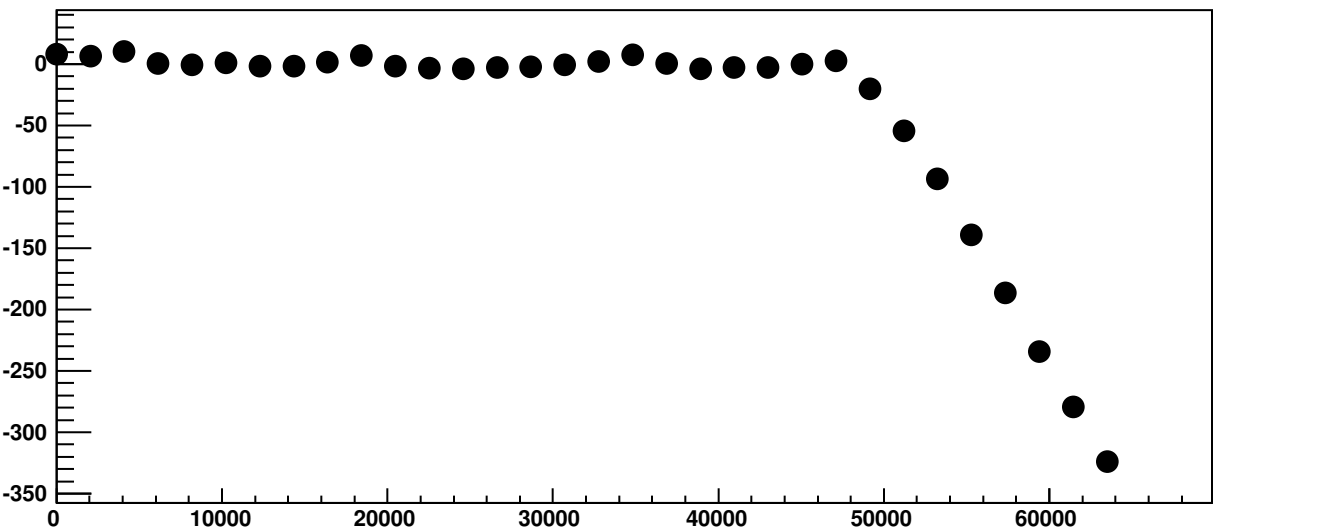
Chip 2, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

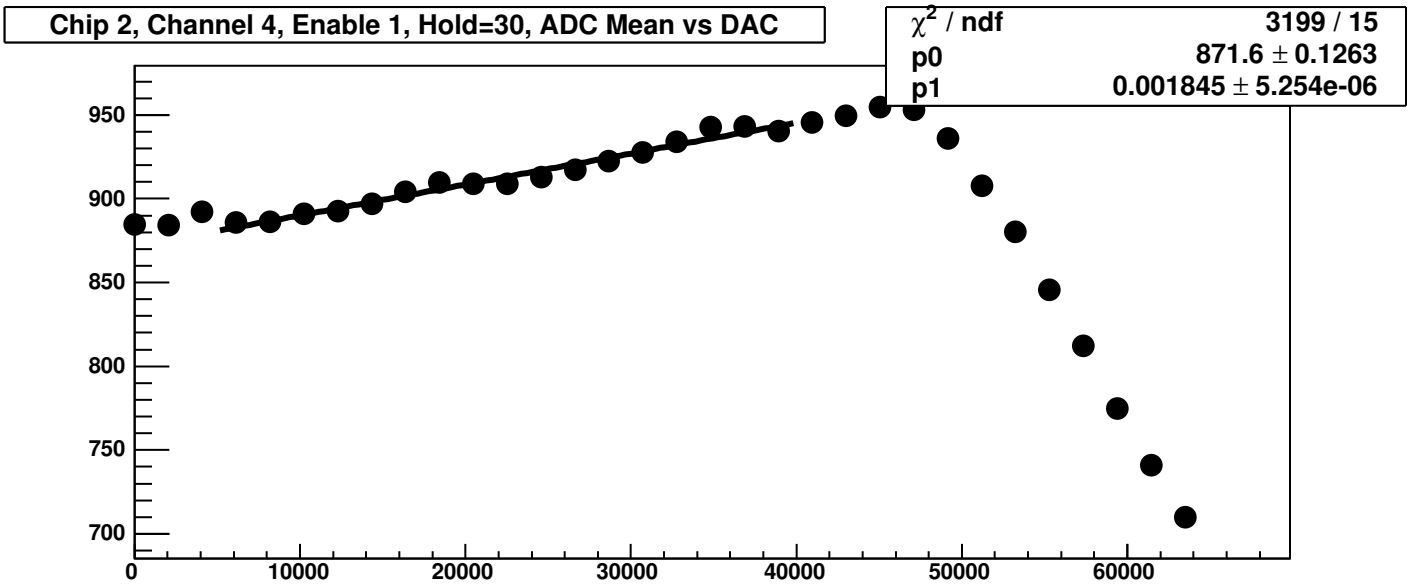


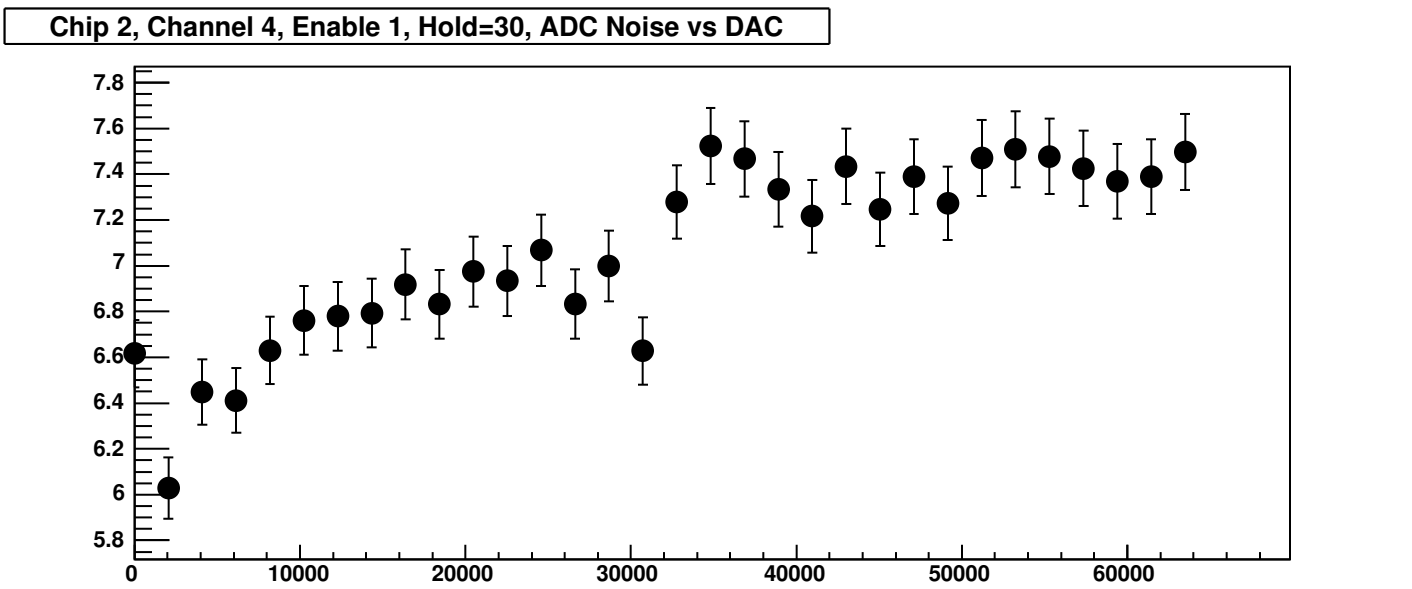
Chip 2, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC

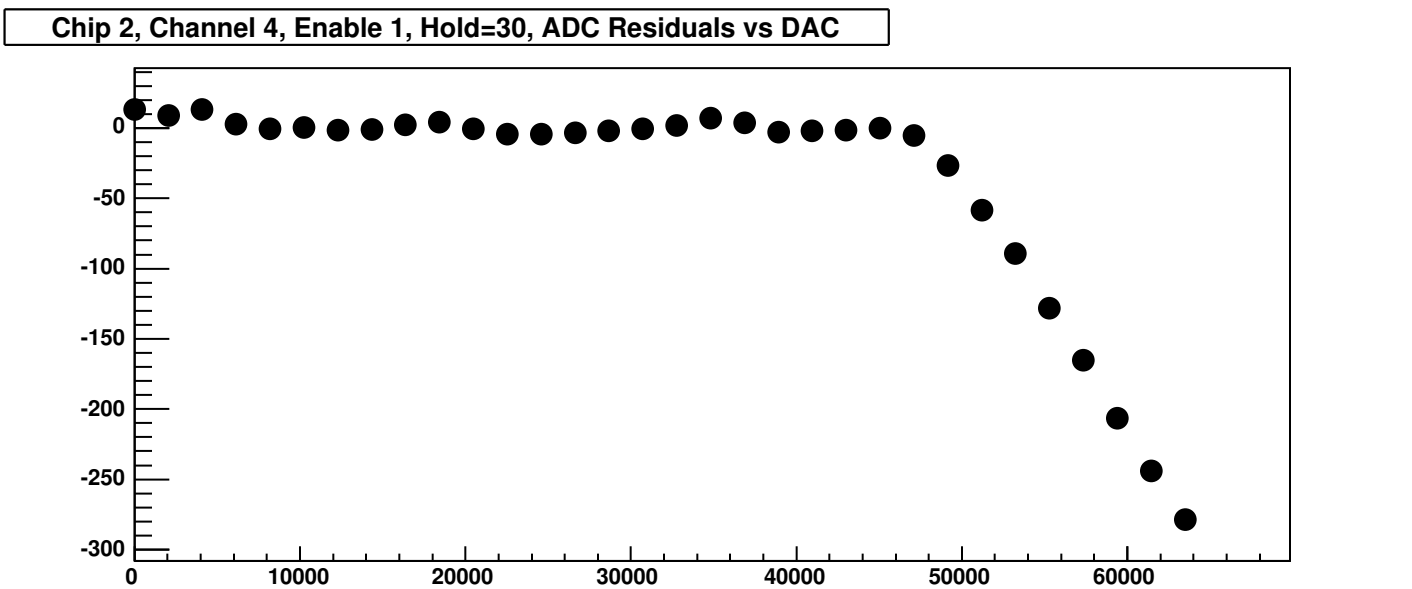


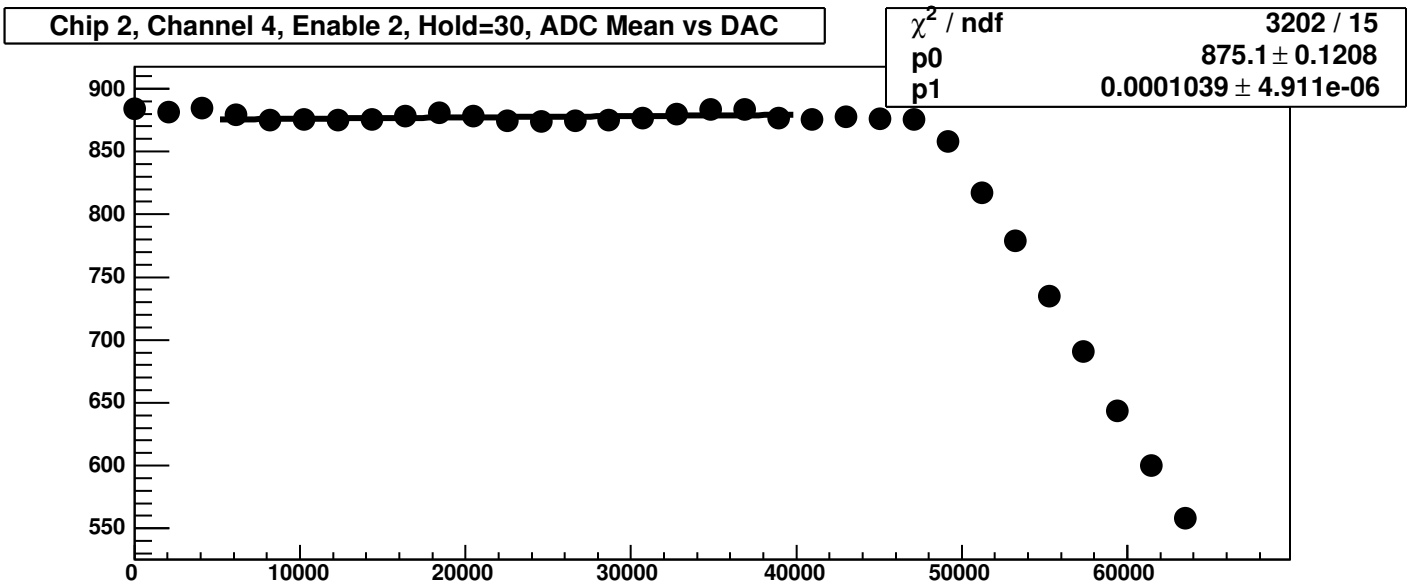
Chip 2, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

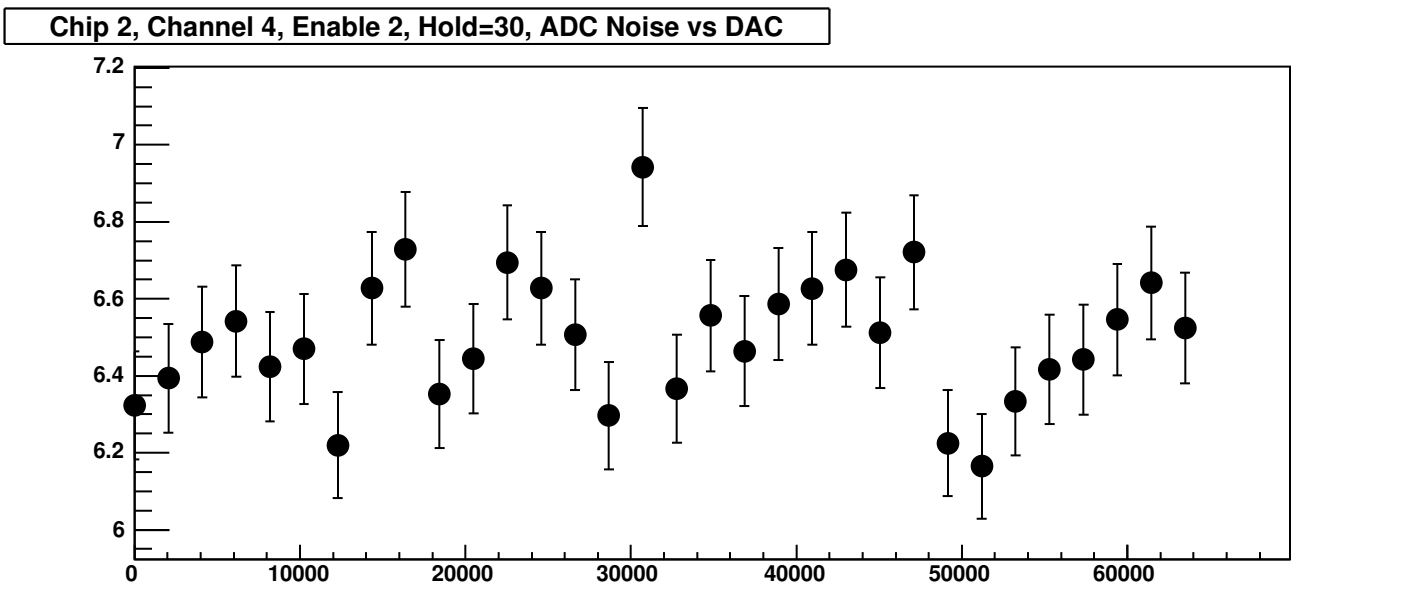


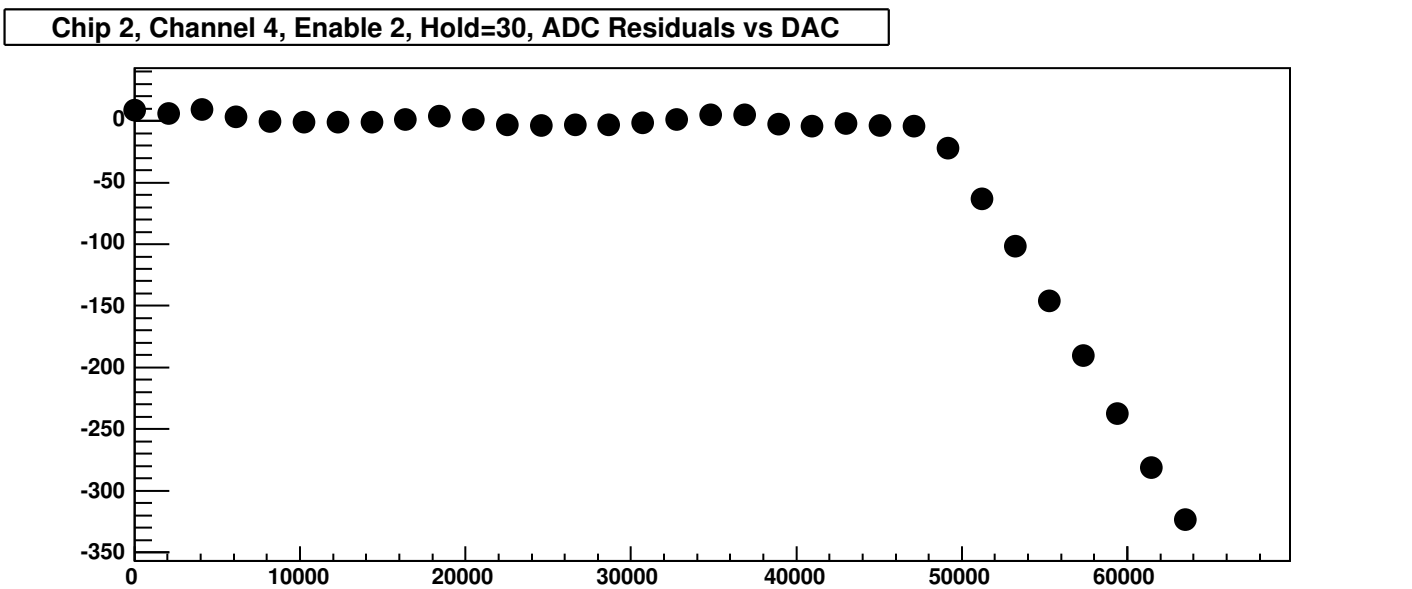

Chip 2, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3199 / 15
p0 871.6 ± 0.1263
p1 0.001845 ± 5.254e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

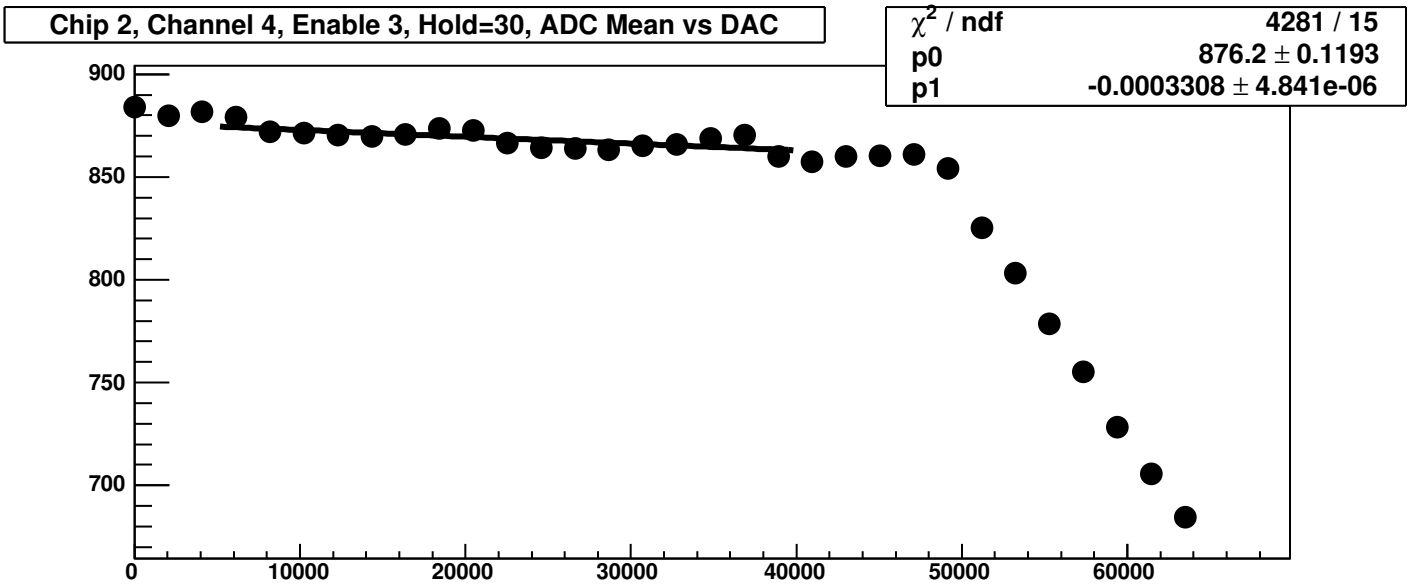

Chip 2, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

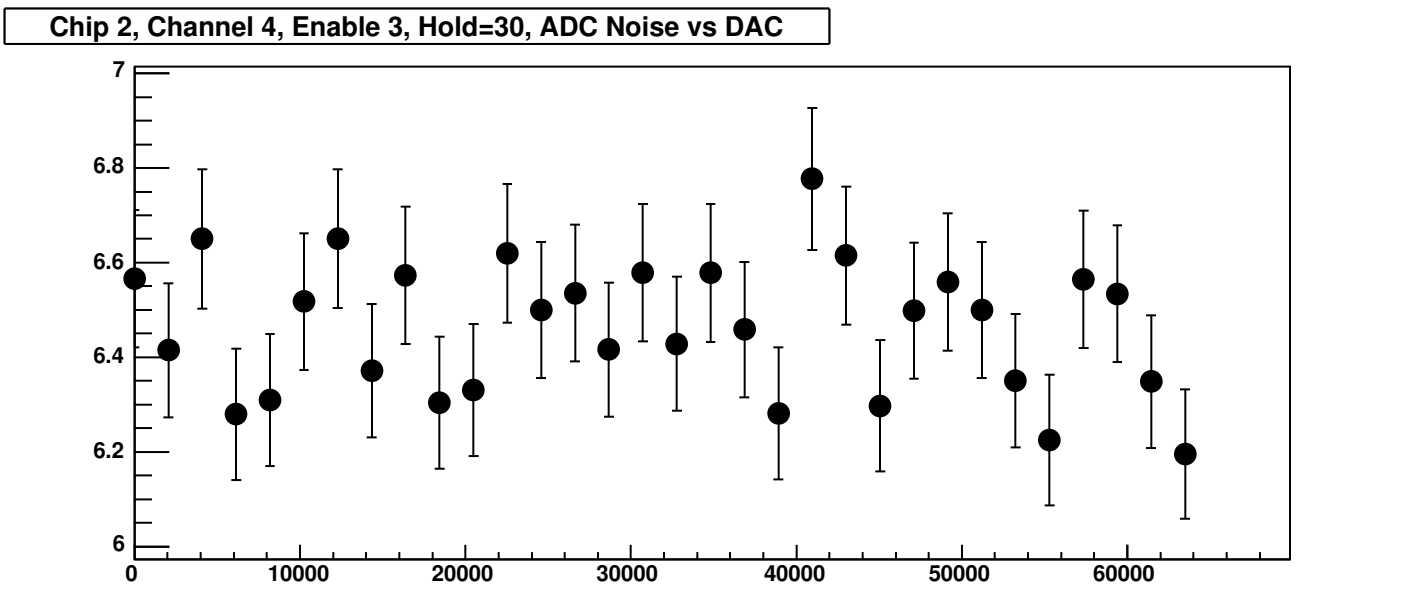

Chip 2, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

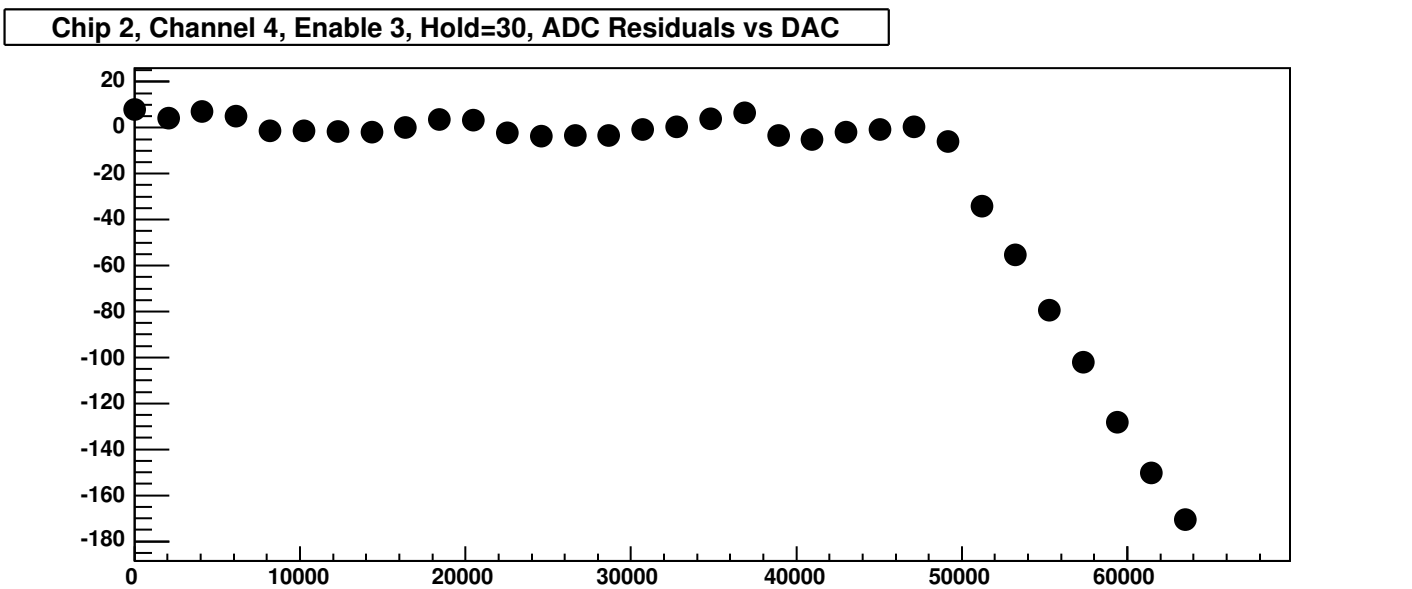

Chip 2, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3202 / 15
p0 875.1 ± 0.1208
p1 0.0001039 ± 4.911e-06
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

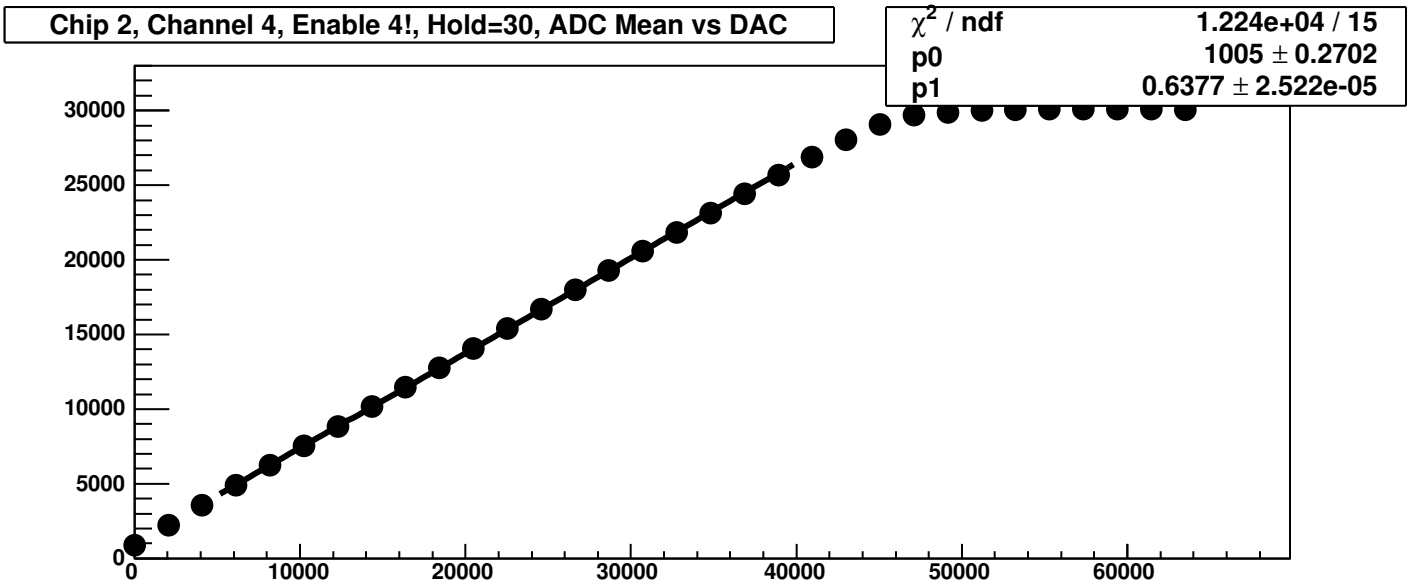

Chip 2, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

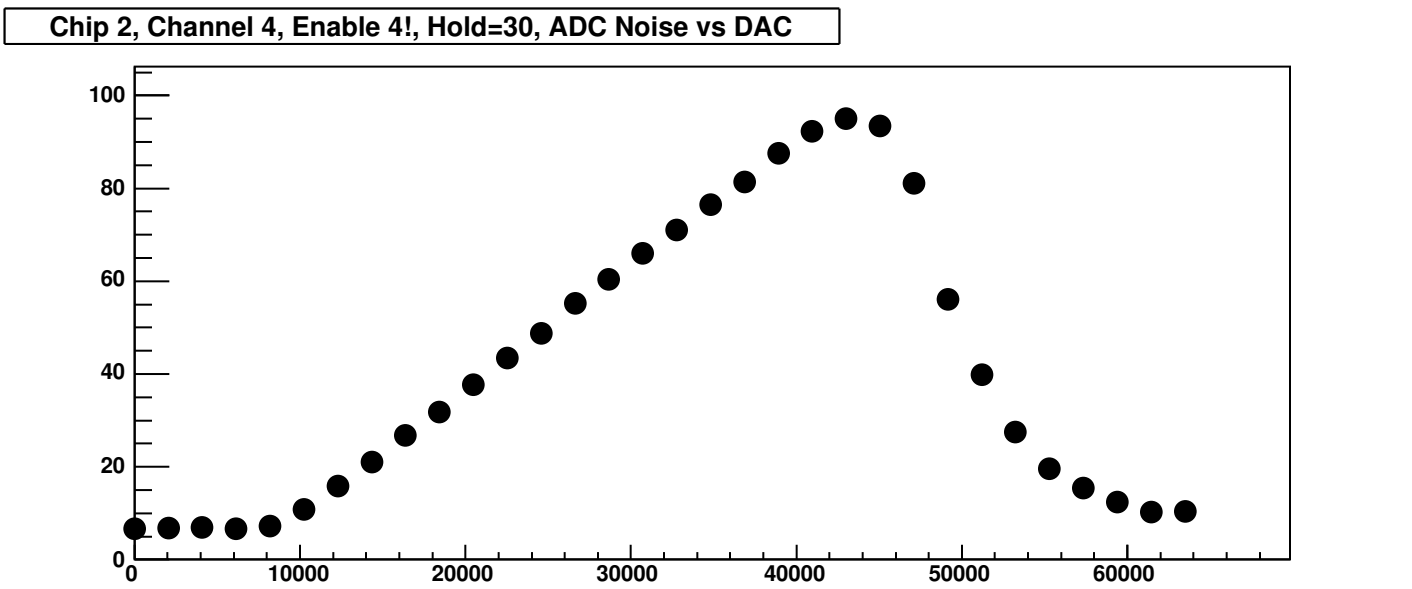

Chip 2, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

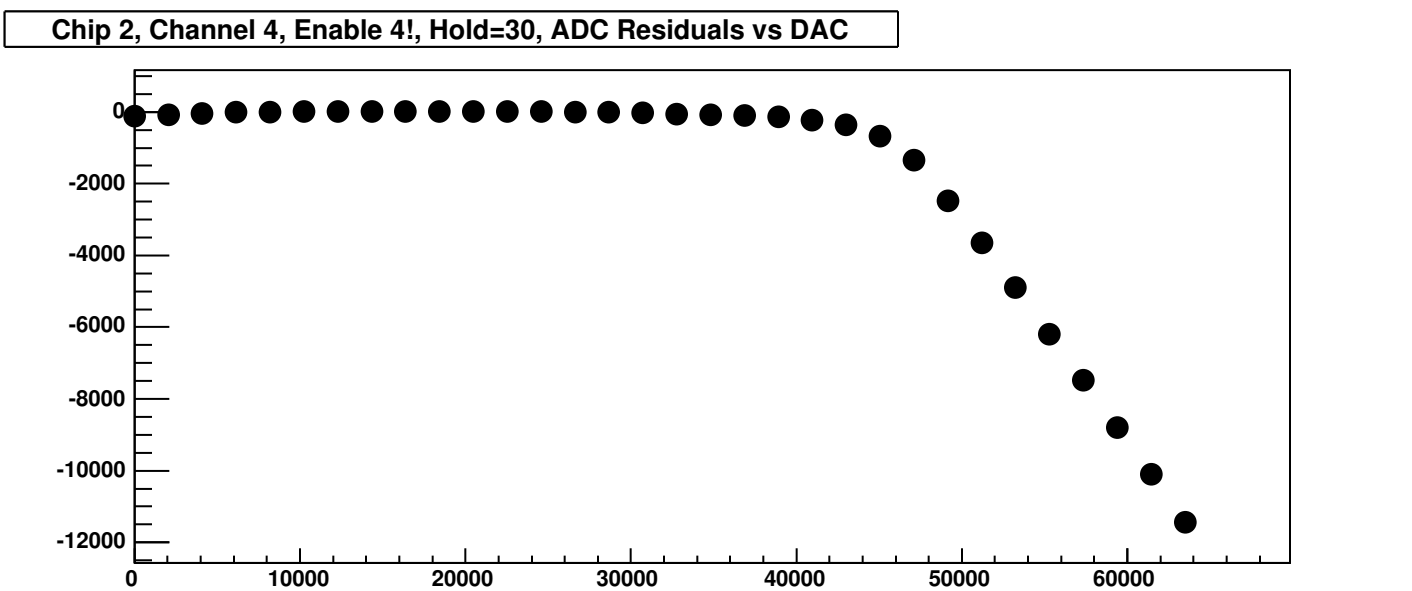

Chip 2, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4281 / 15
p0 876.2 ± 0.1193
p1 -0.0003308 ± 4.841e-06
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

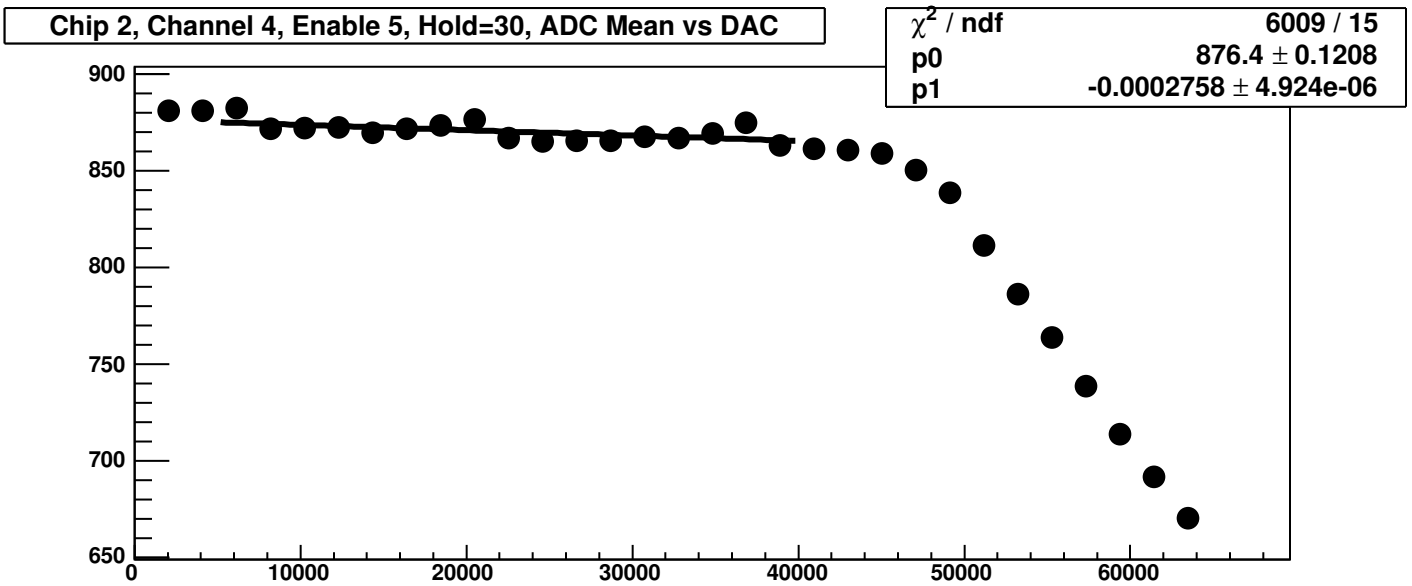

Chip 2, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

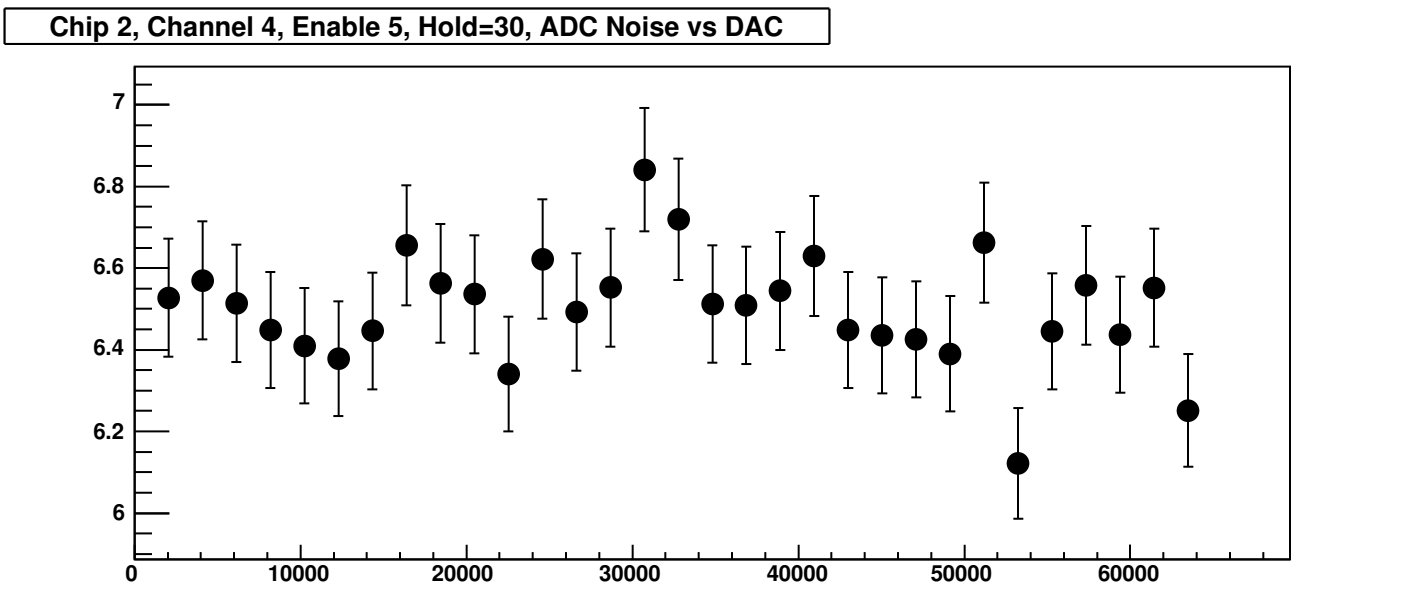

Chip 2, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

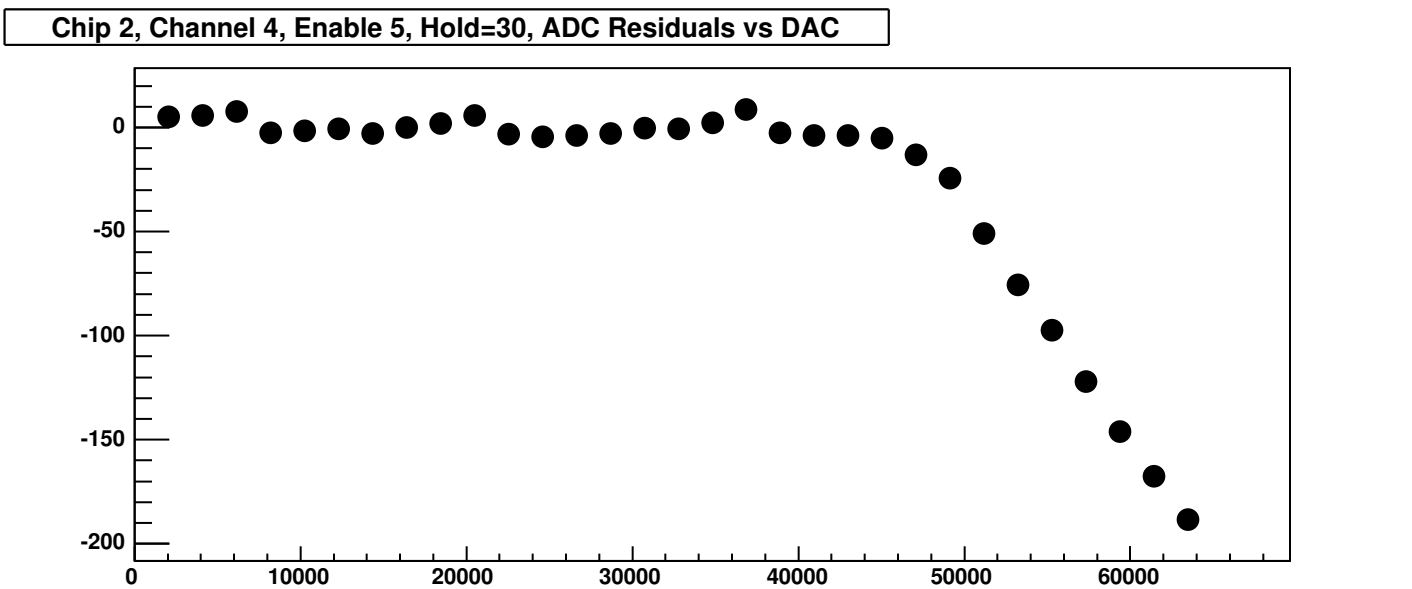

Chip 2, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.224e+04 / 15
p0 1005 ± 0.2702
p1 0.6377 ± 2.522e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

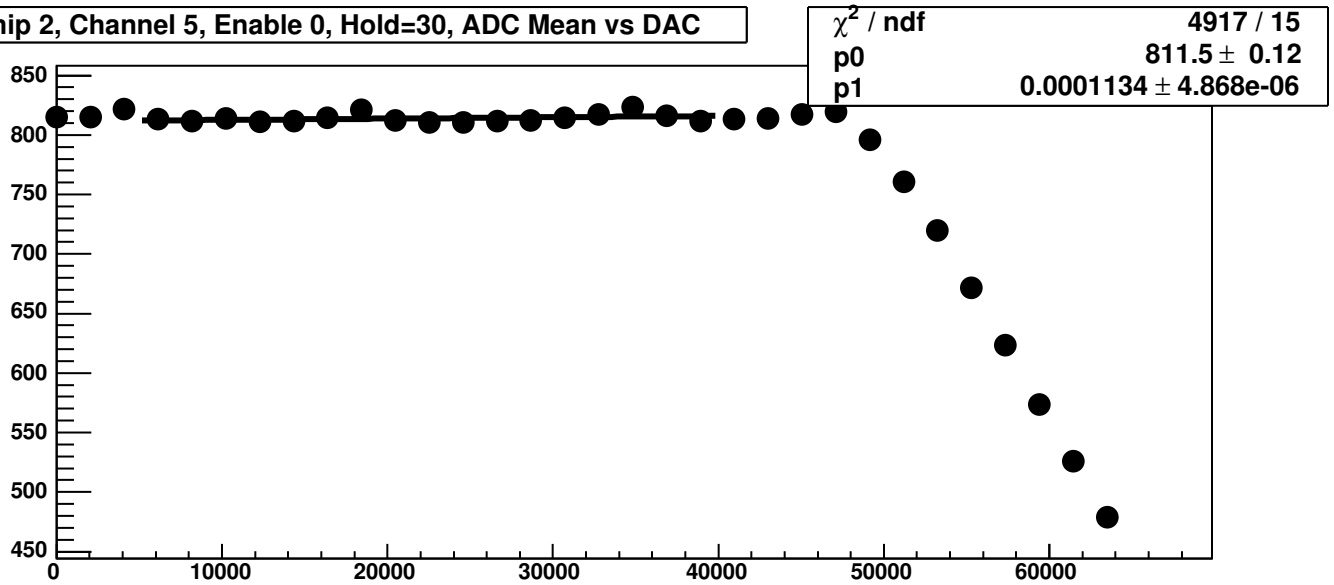

Chip 2, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6009 / 15
p0 876.4 ± 0.1208
p1 -0.0002758 ± 4.924e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

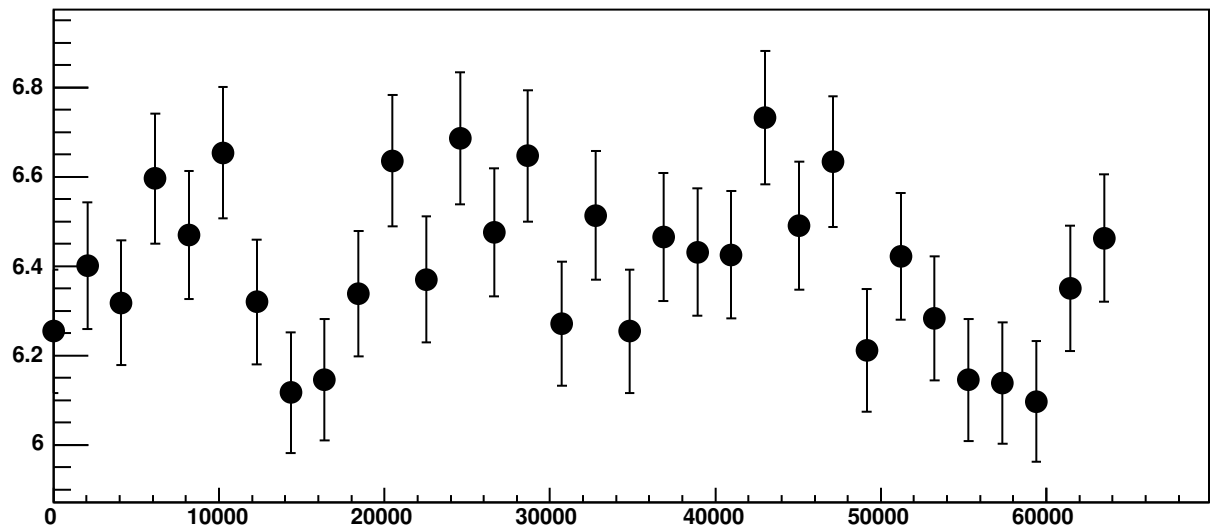

Chip 2, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

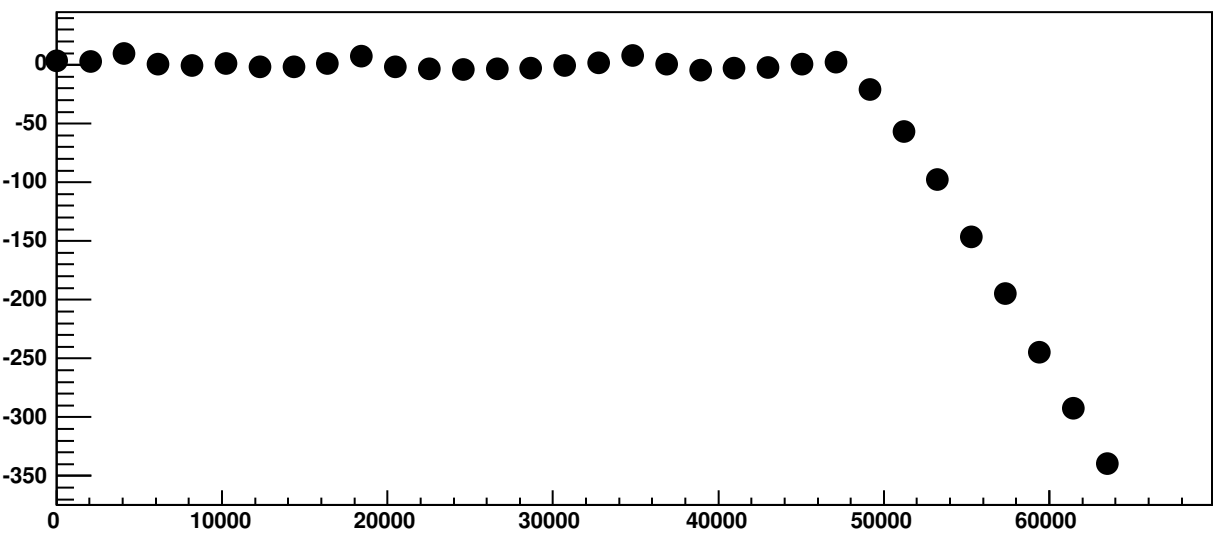
Chip 2, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC

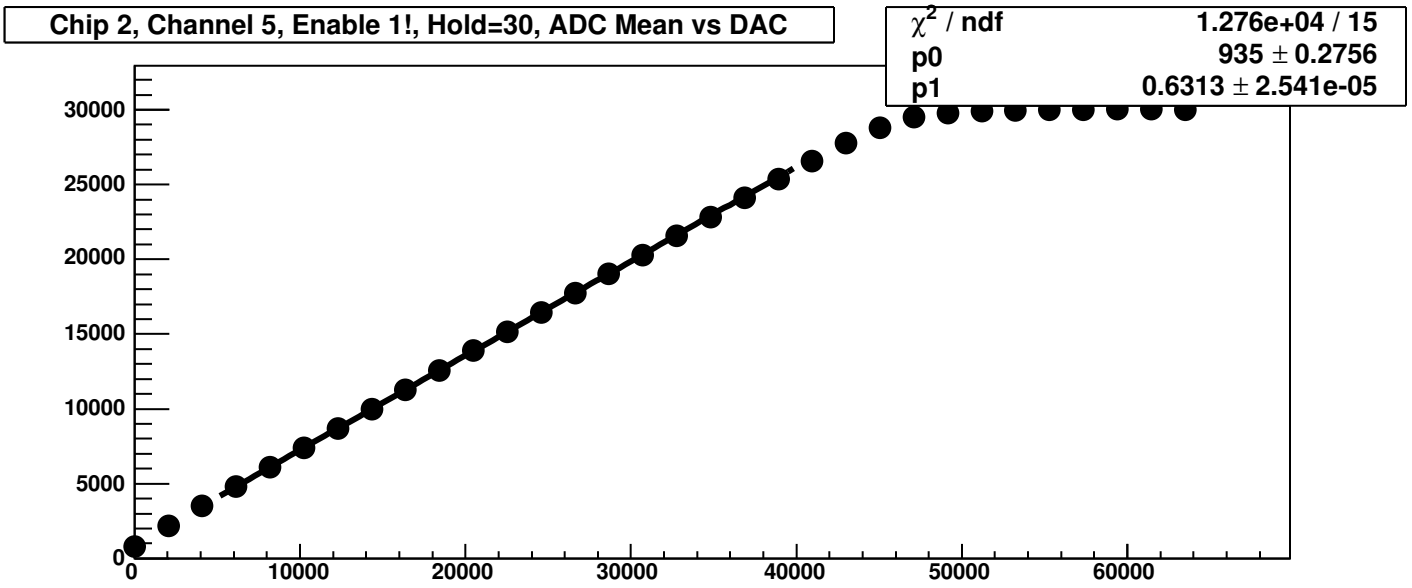


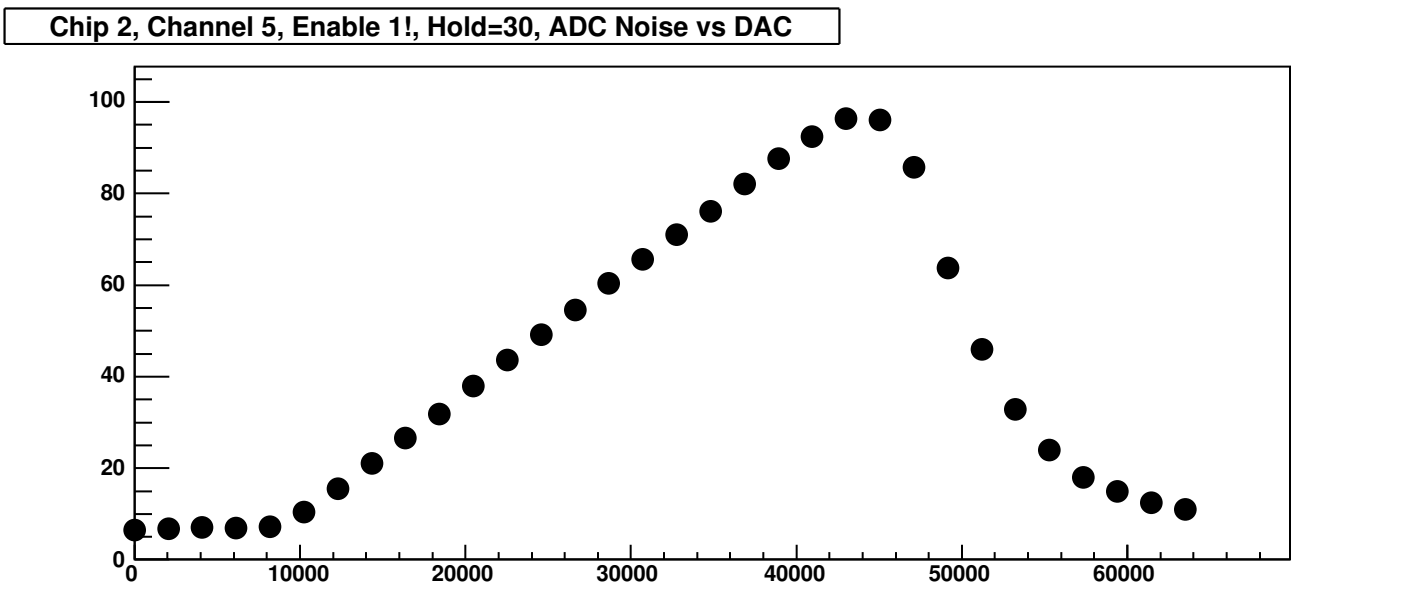
Chip 2, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC

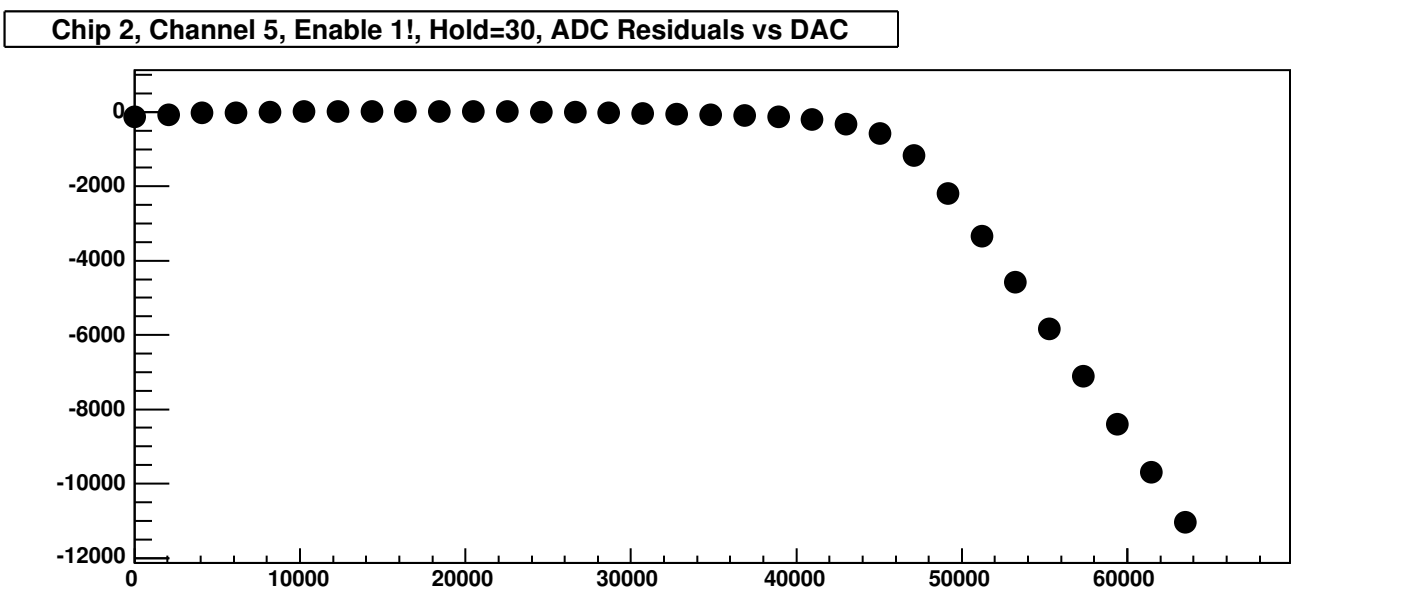


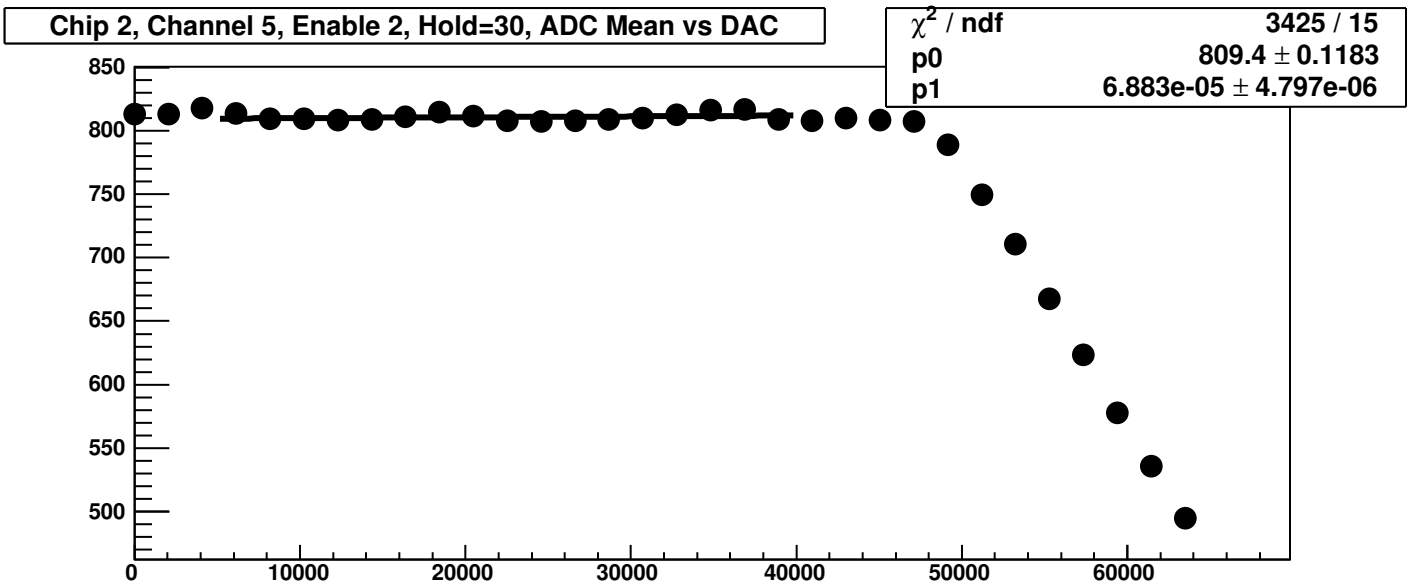
Chip 2, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

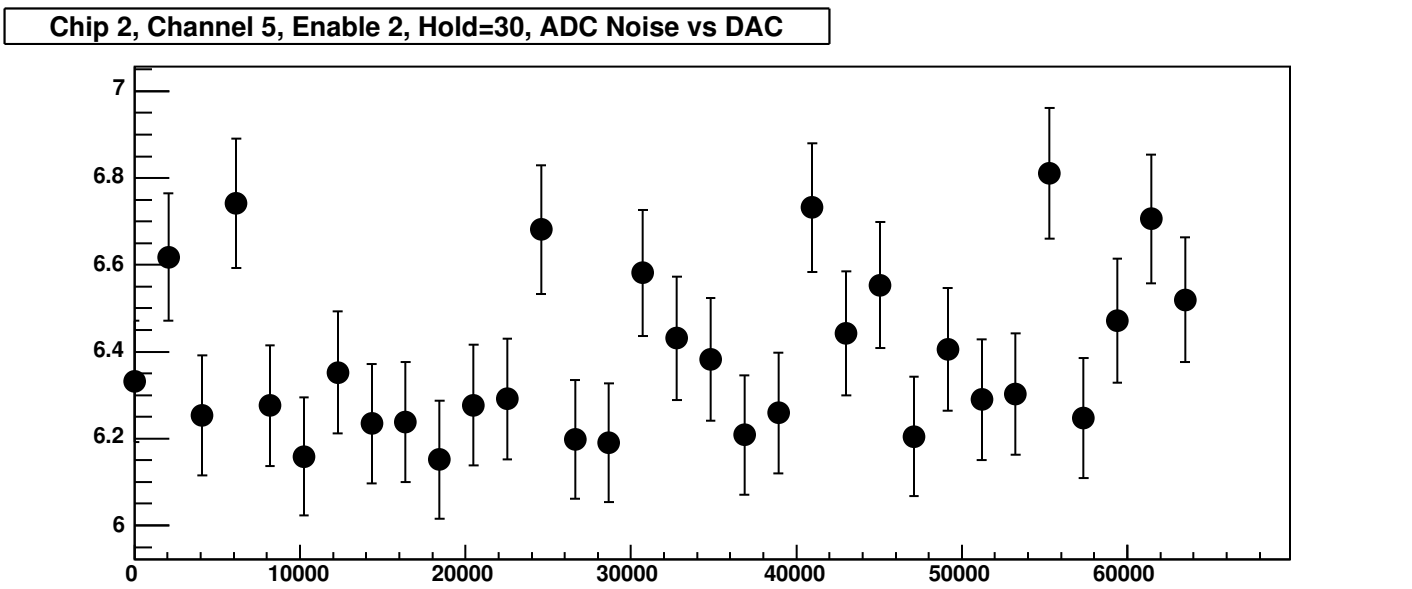


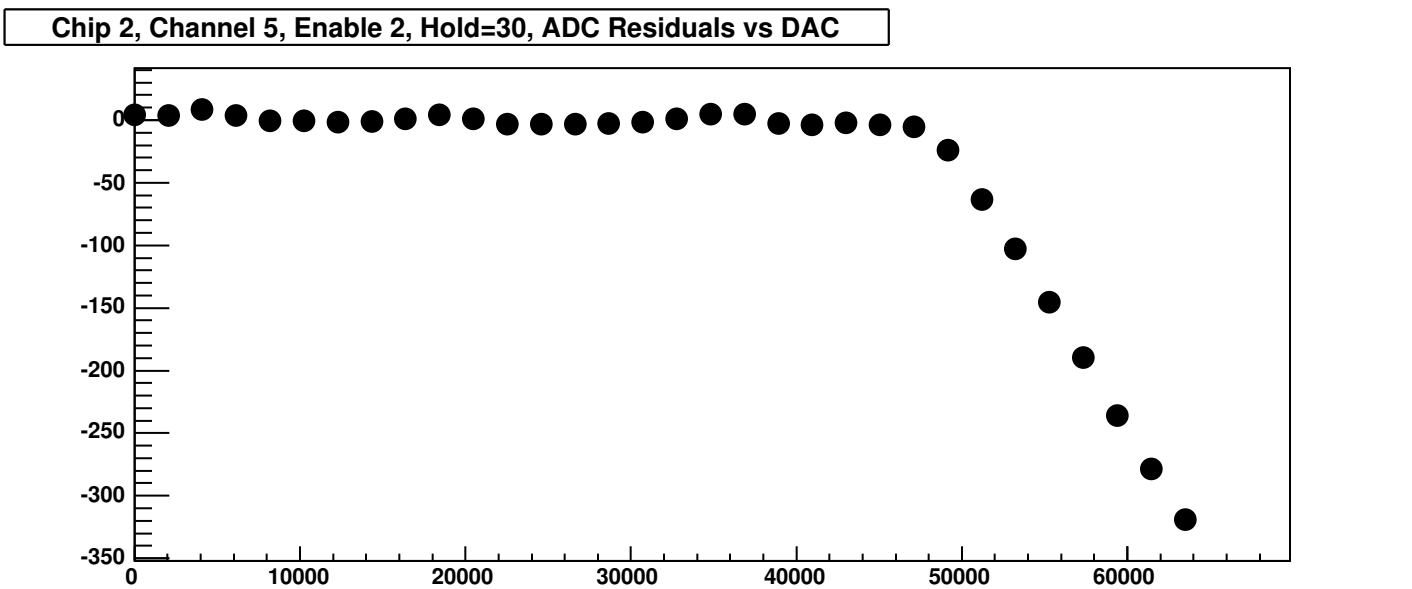

Chip 2, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.276e+04 / 15
p0 935 ± 0.2756
p1 0.6313 ± 2.541e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

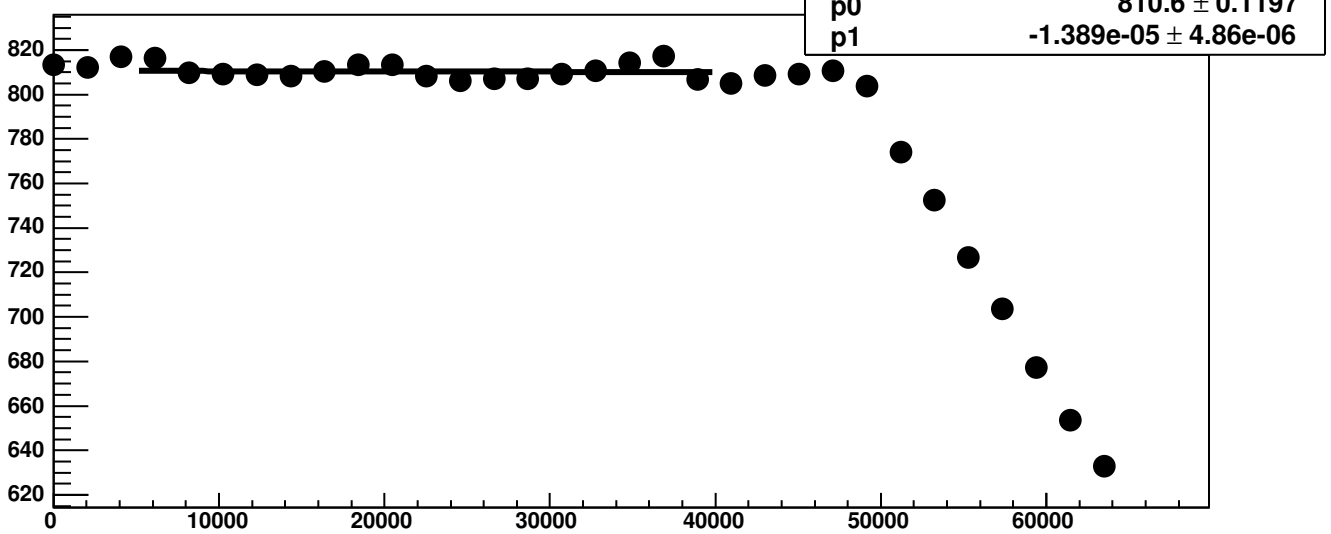

Chip 2, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3425 / 15
p0 809.4 ± 0.1183
p1 6.883e-05 ± 4.797e-06
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

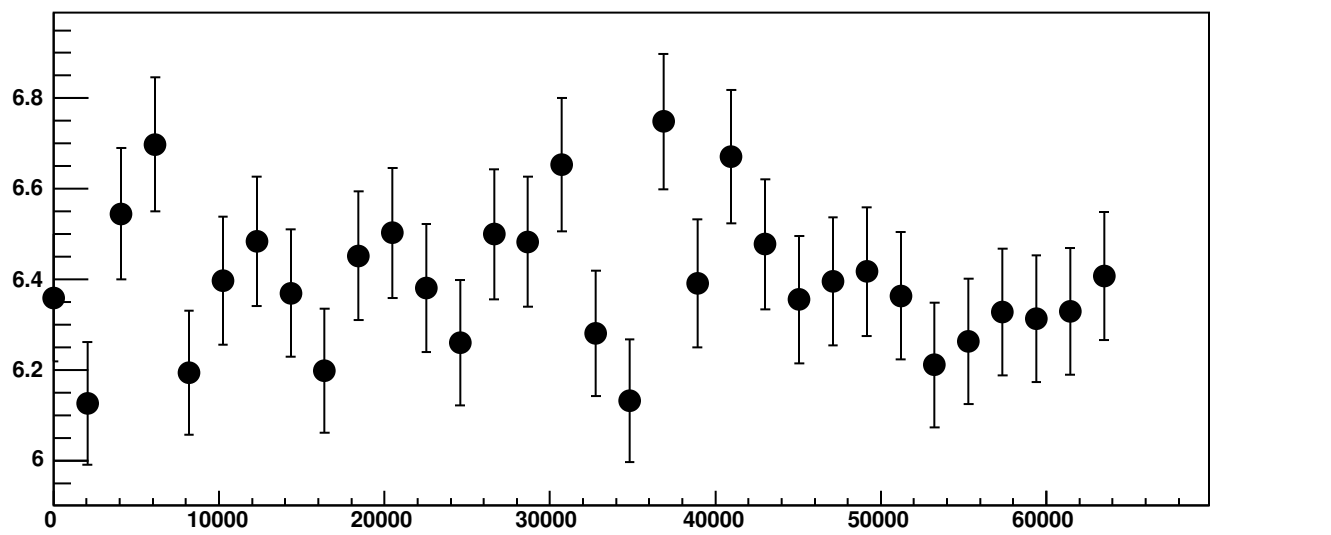

Chip 2, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

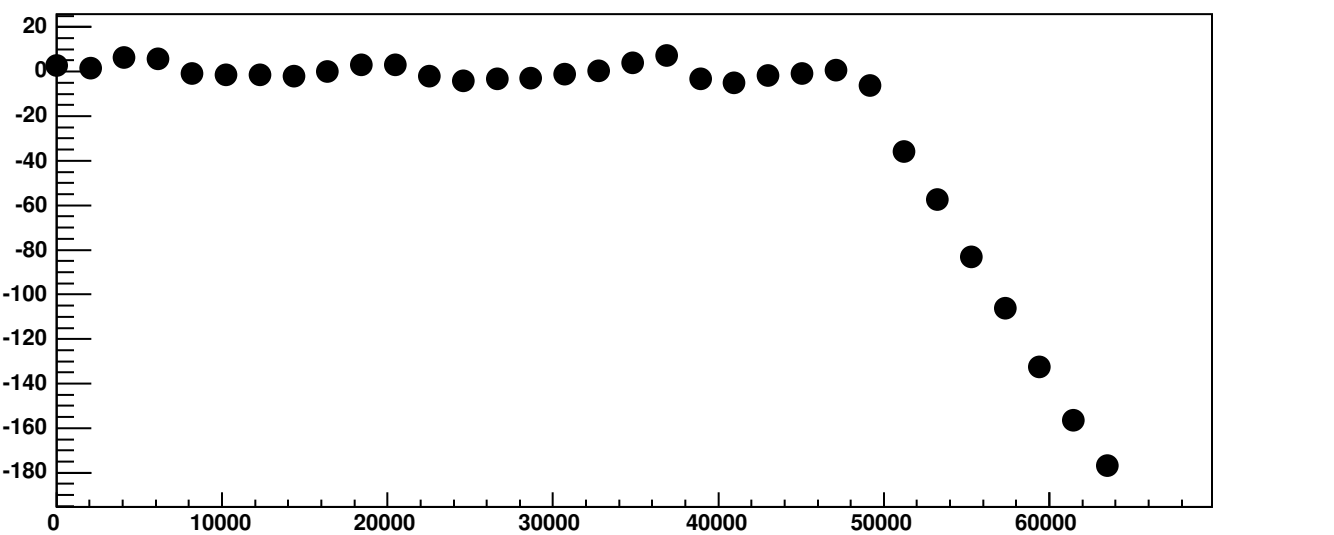
Chip 2, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

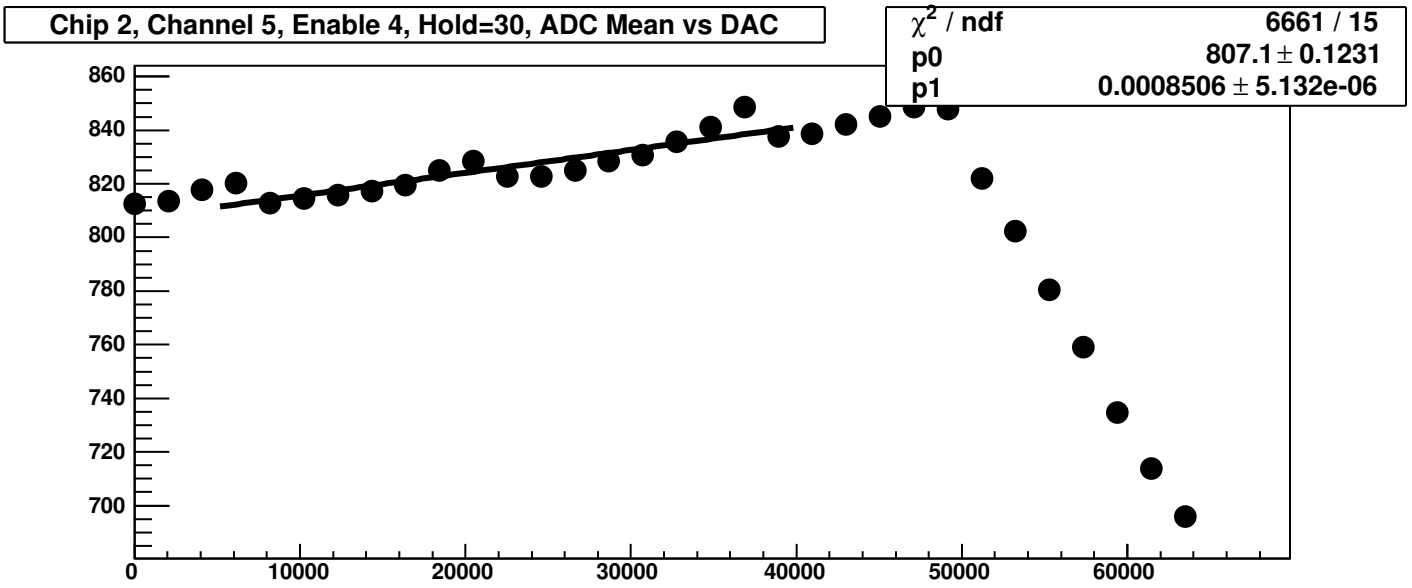


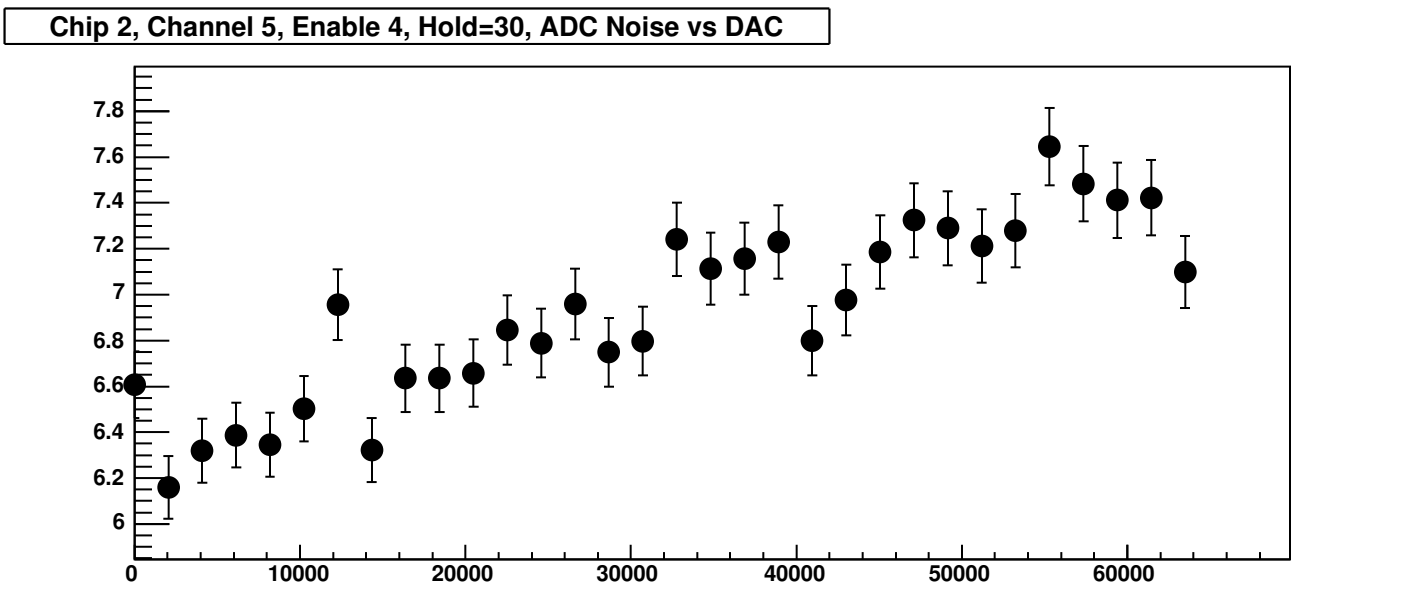
Chip 2, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

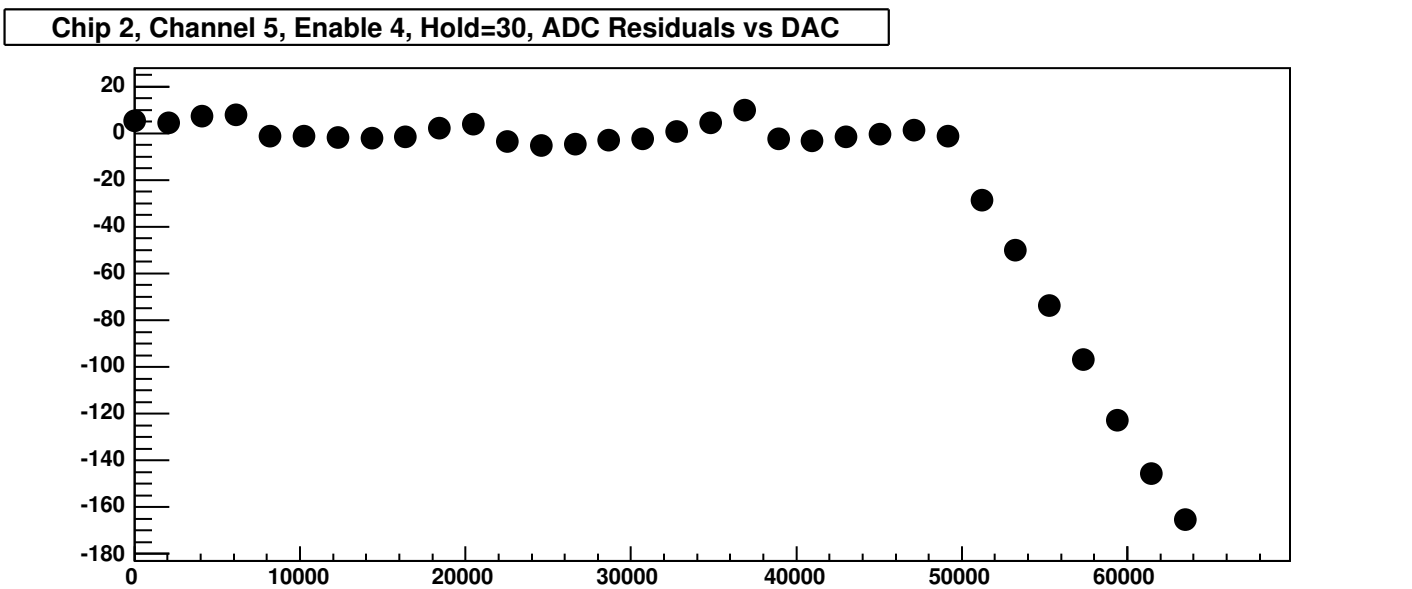


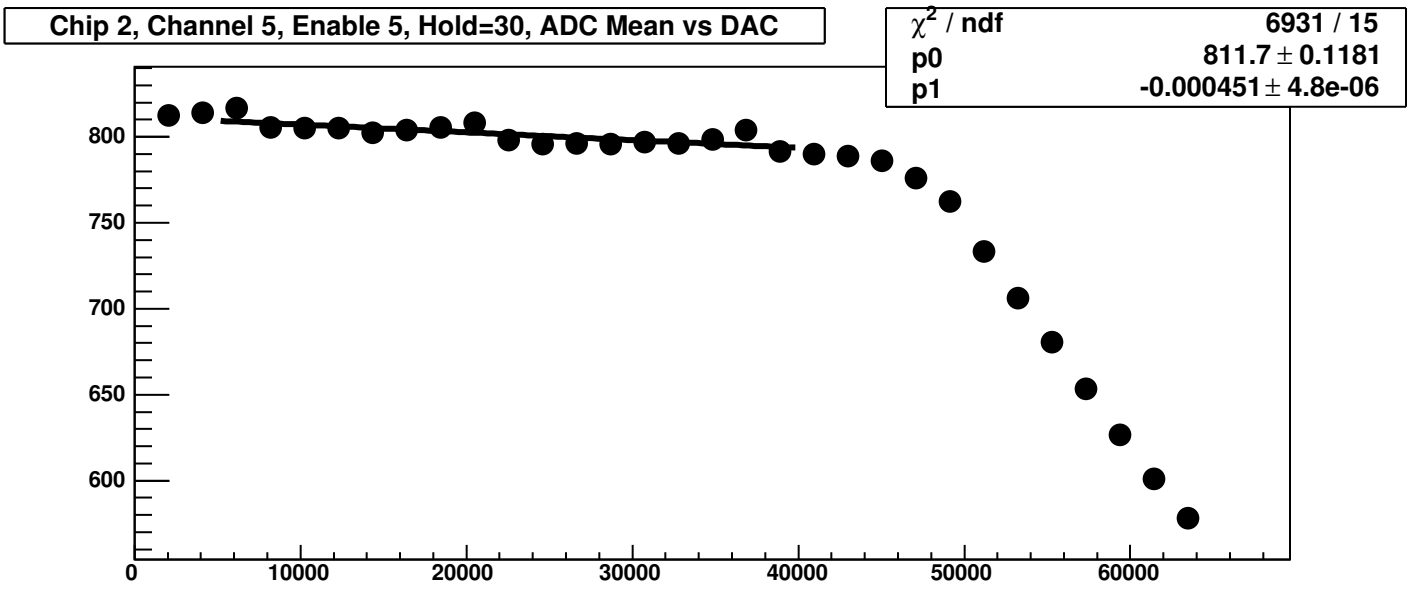
Chip 2, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

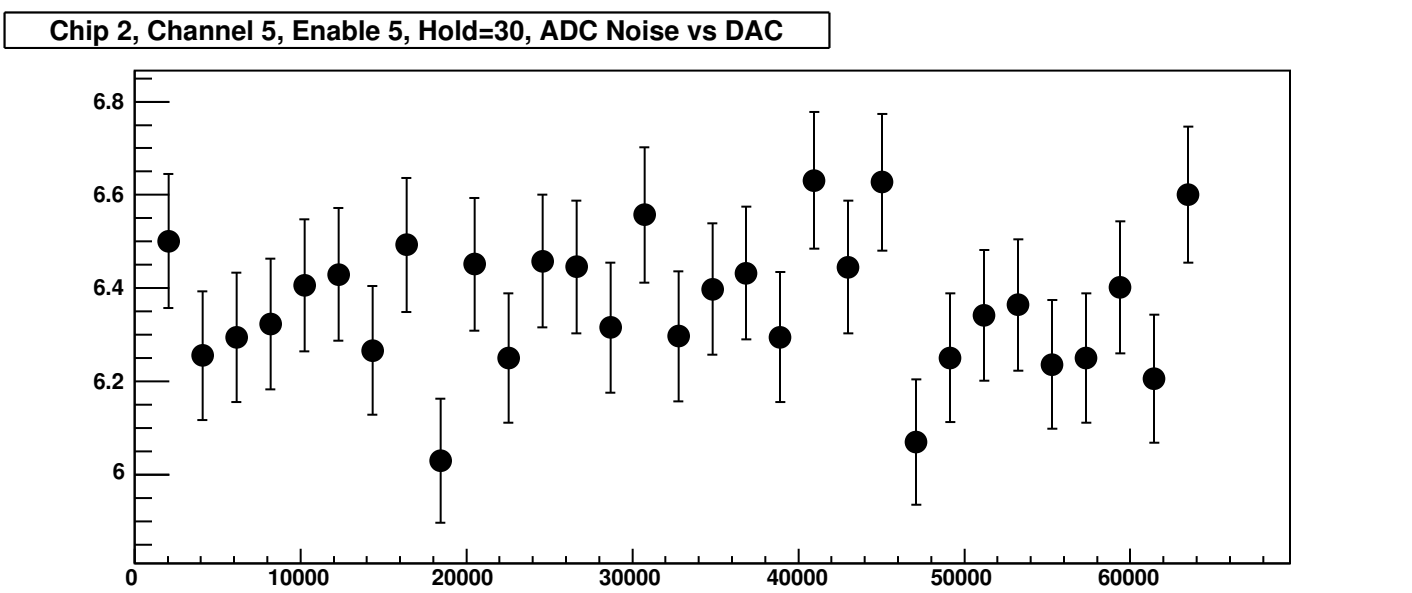


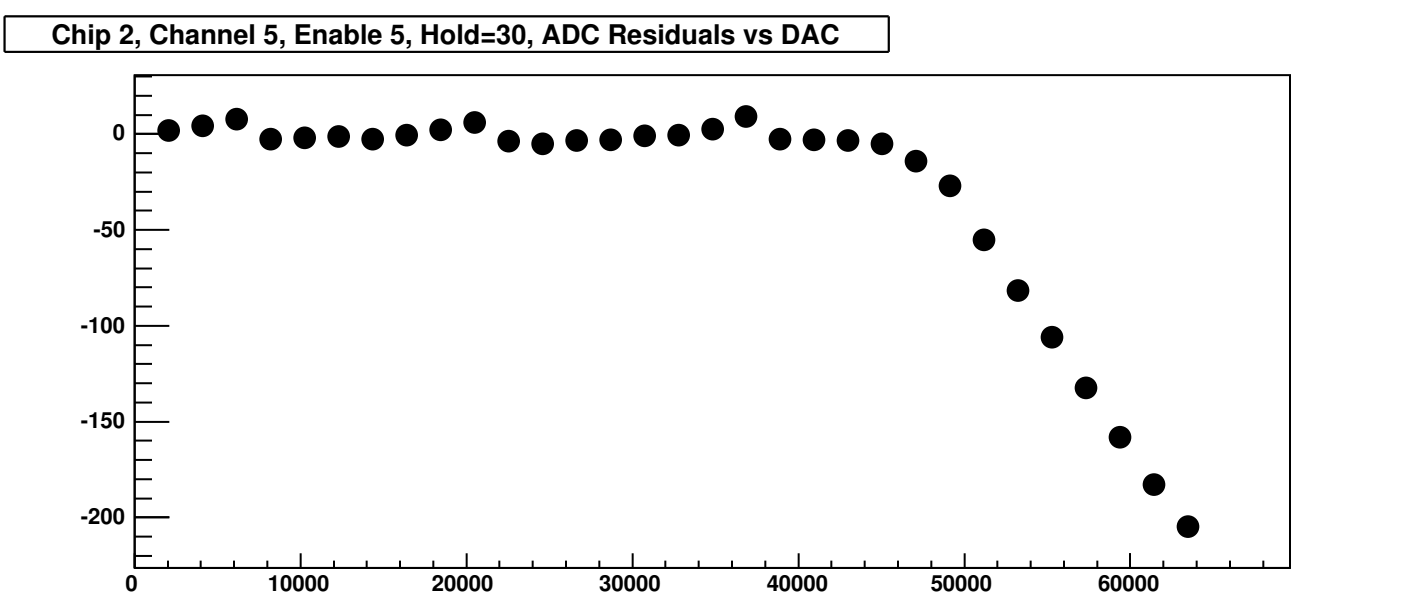

Chip 2, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6661 / 15
p0 807.1 ± 0.1231
p1 0.0008506 ± 5.132e-06
860
840
820
800
780
760
740
720
700
0
10000
20000
30000
40000
50000
60000

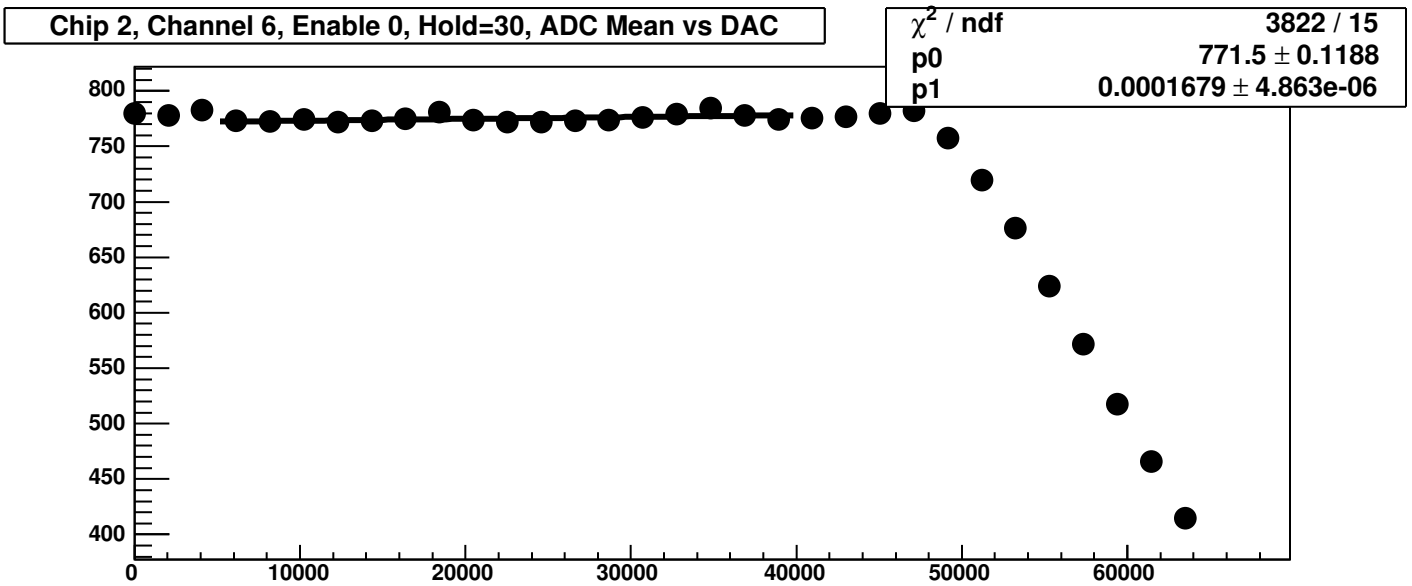

Chip 2, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

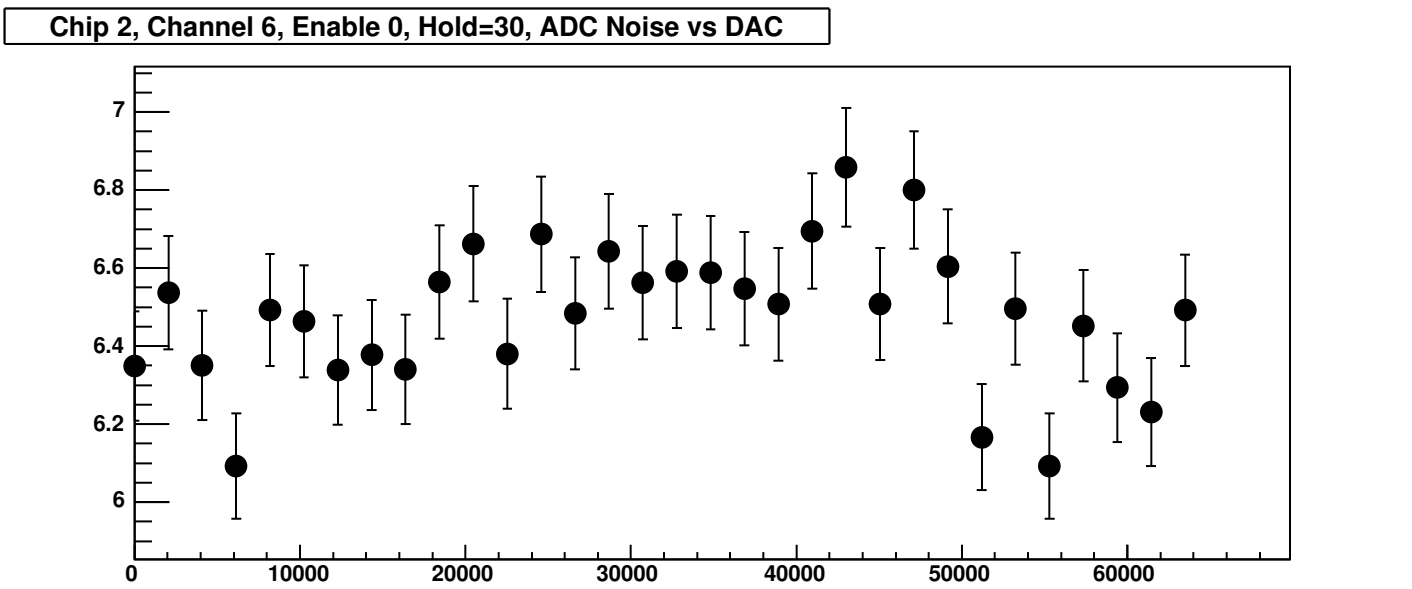

Chip 2, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

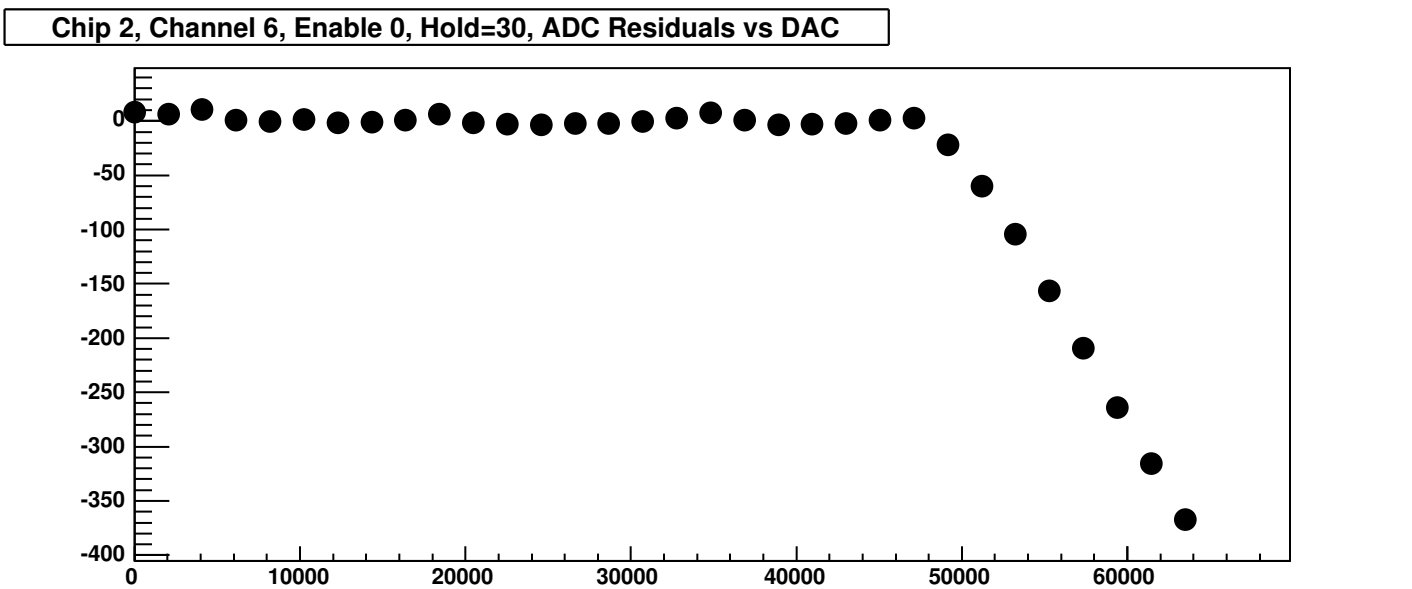

Chip 2, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6931 / 15
p0 811.7 ± 0.1181
p1 -0.000451 ± 4.8e-06
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

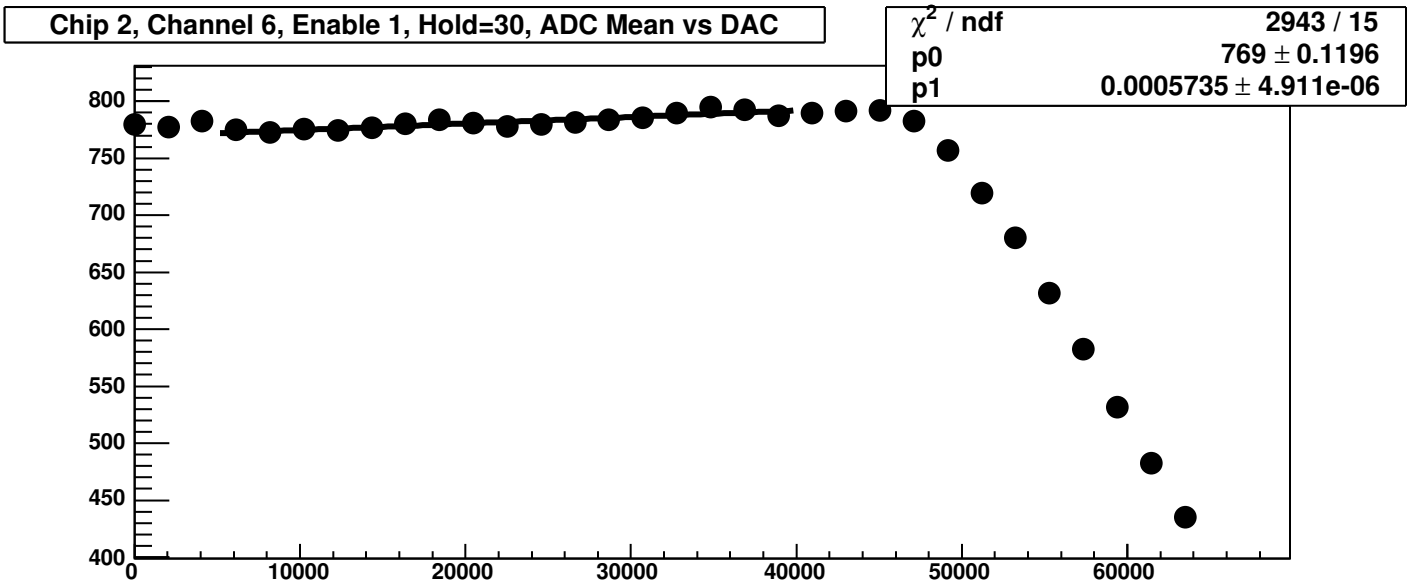

Chip 2, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

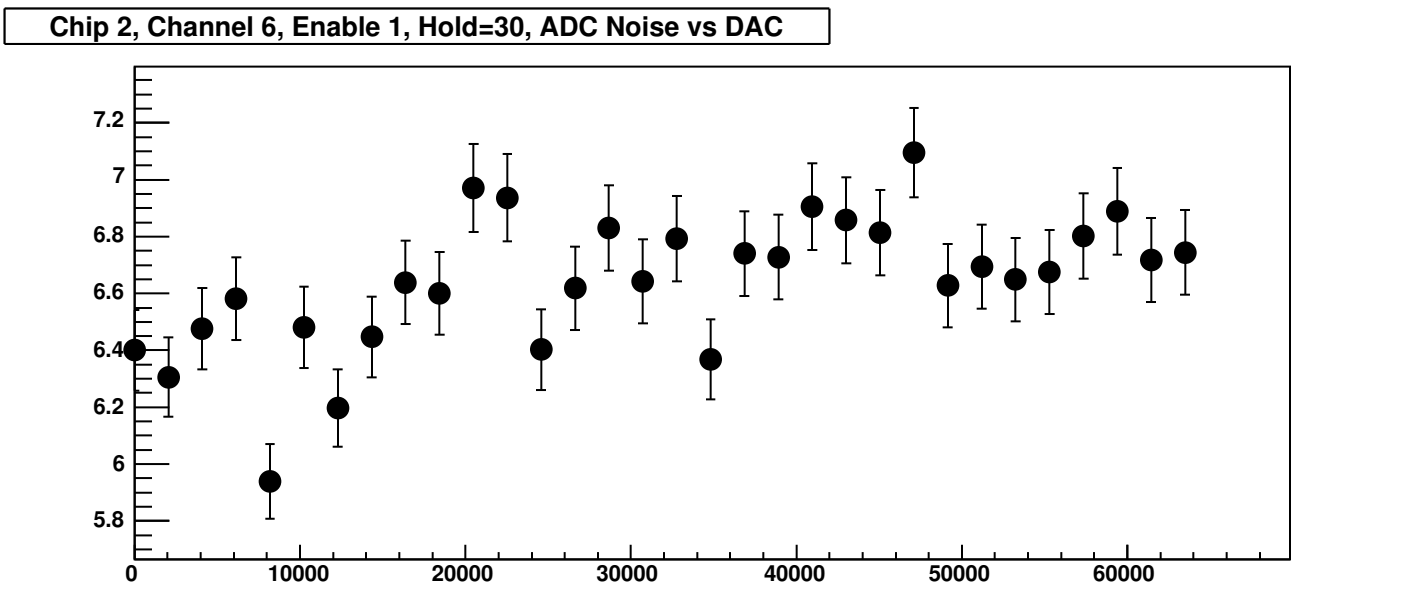

Chip 2, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

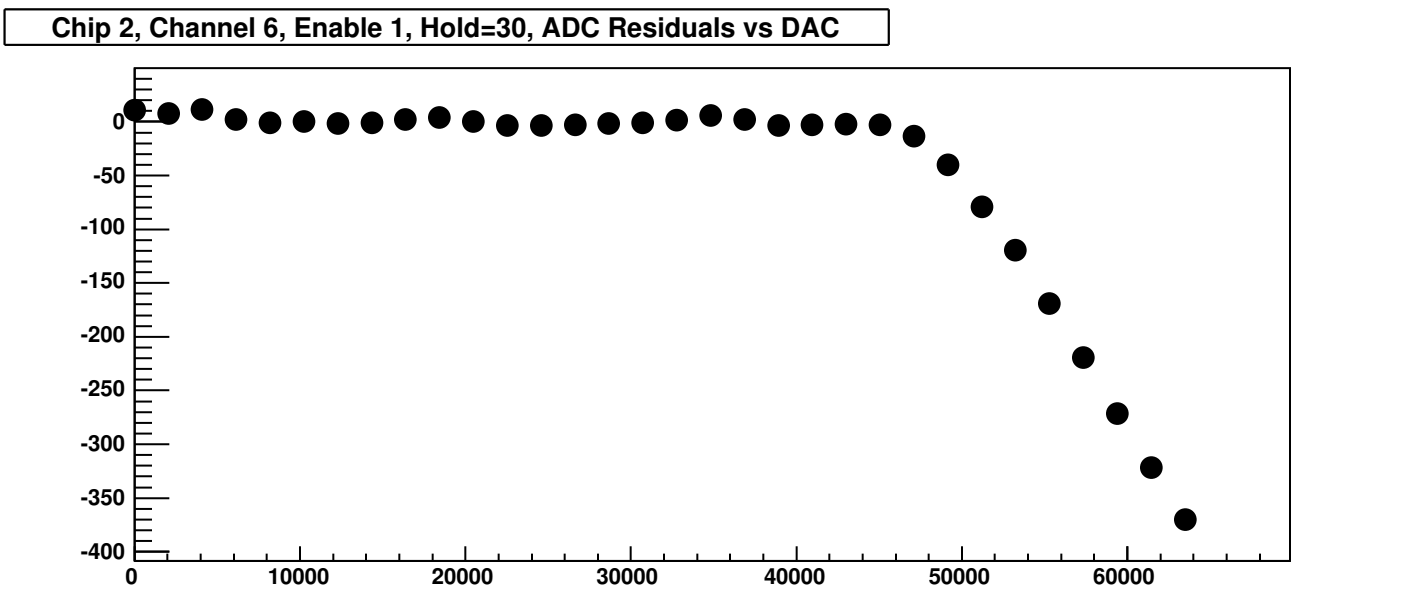

Chip 2, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3822 / 15
p0 771.5 ± 0.1188
p1 0.0001679 ± 4.863e-06
800
750
700
650
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000

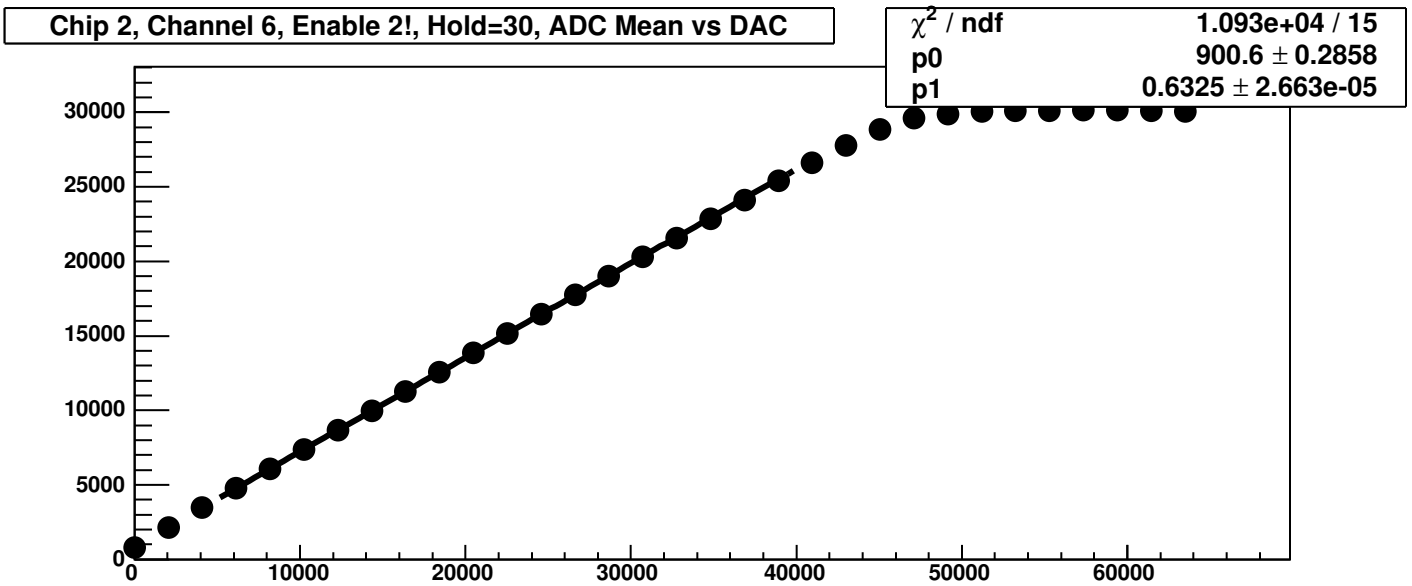

Chip 2, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

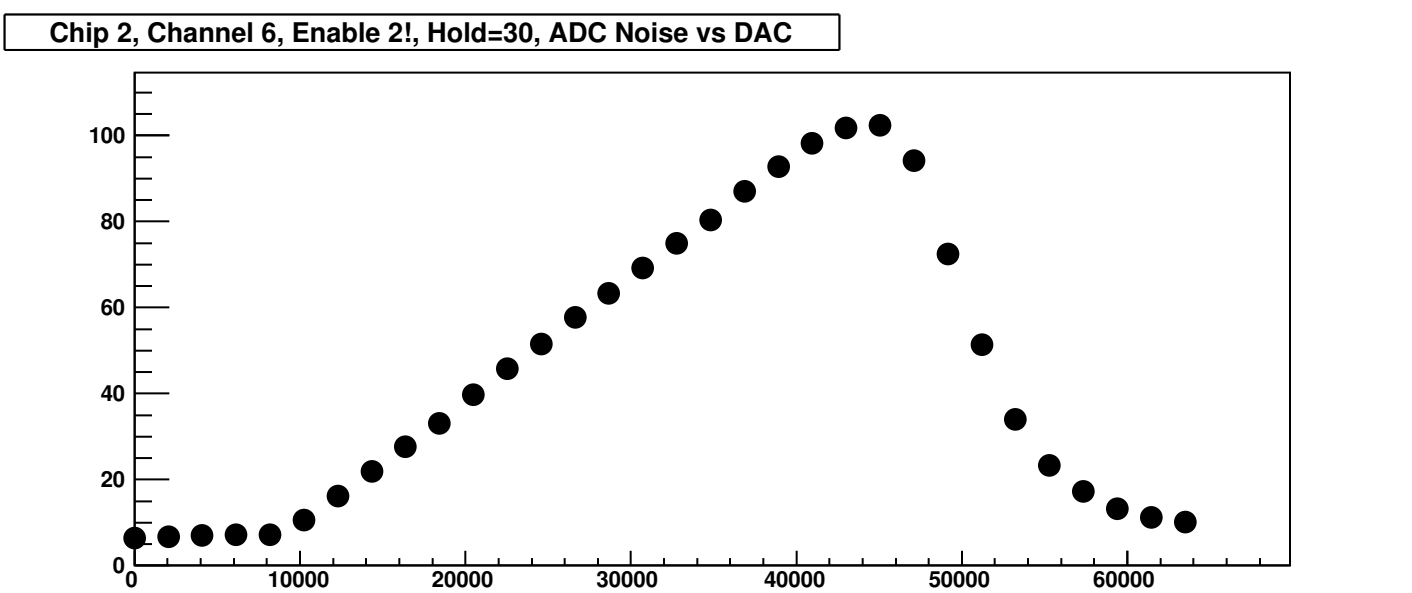

Chip 2, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

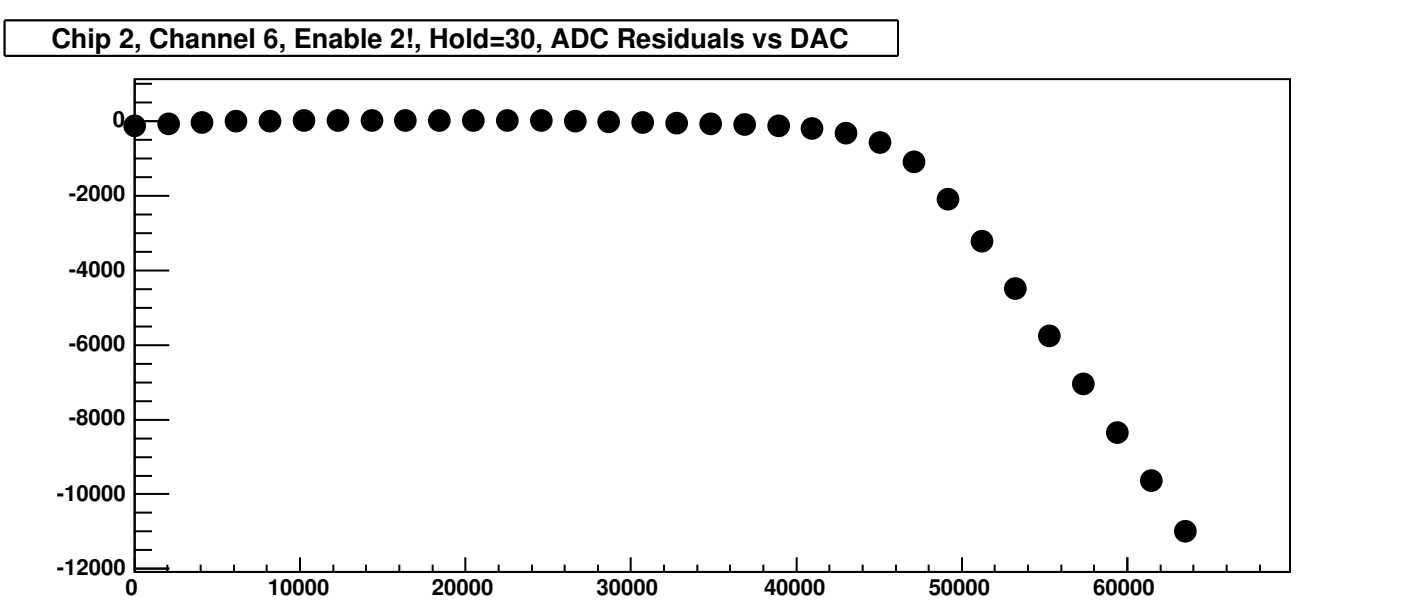

Chip 2, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2943 / 15
p0 769 ± 0.1196
p1 0.0005735 ± 4.911e-06
800
750
700
650
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000

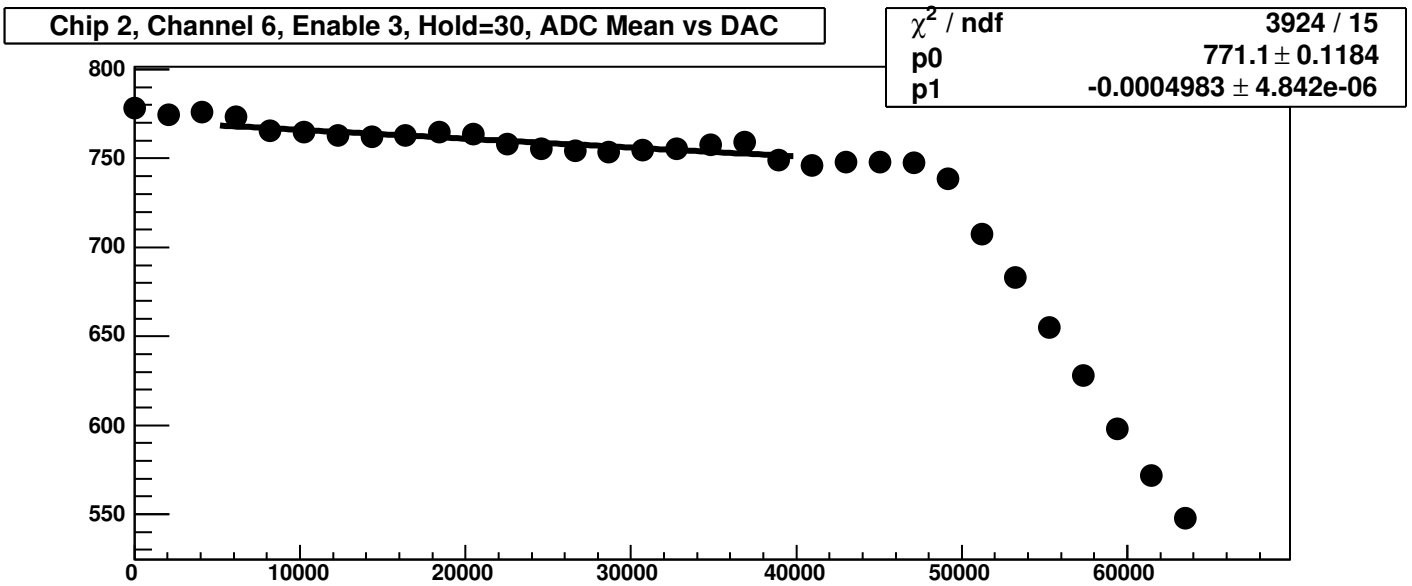

Chip 2, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

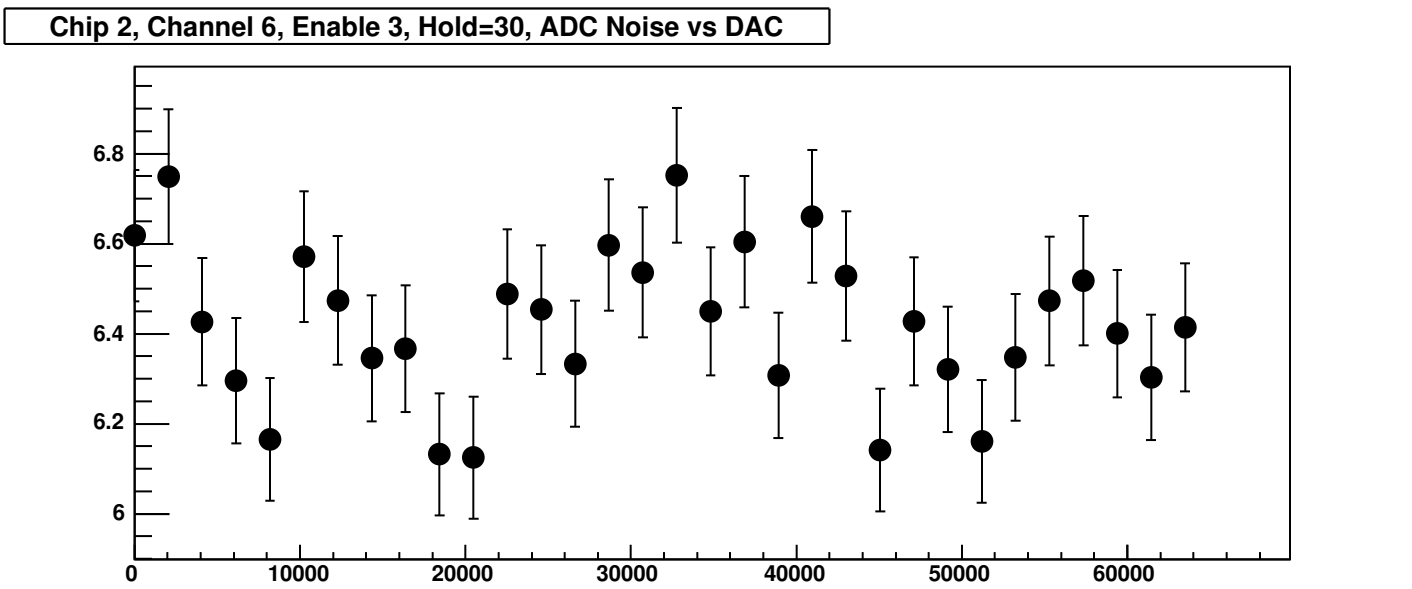

Chip 2, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

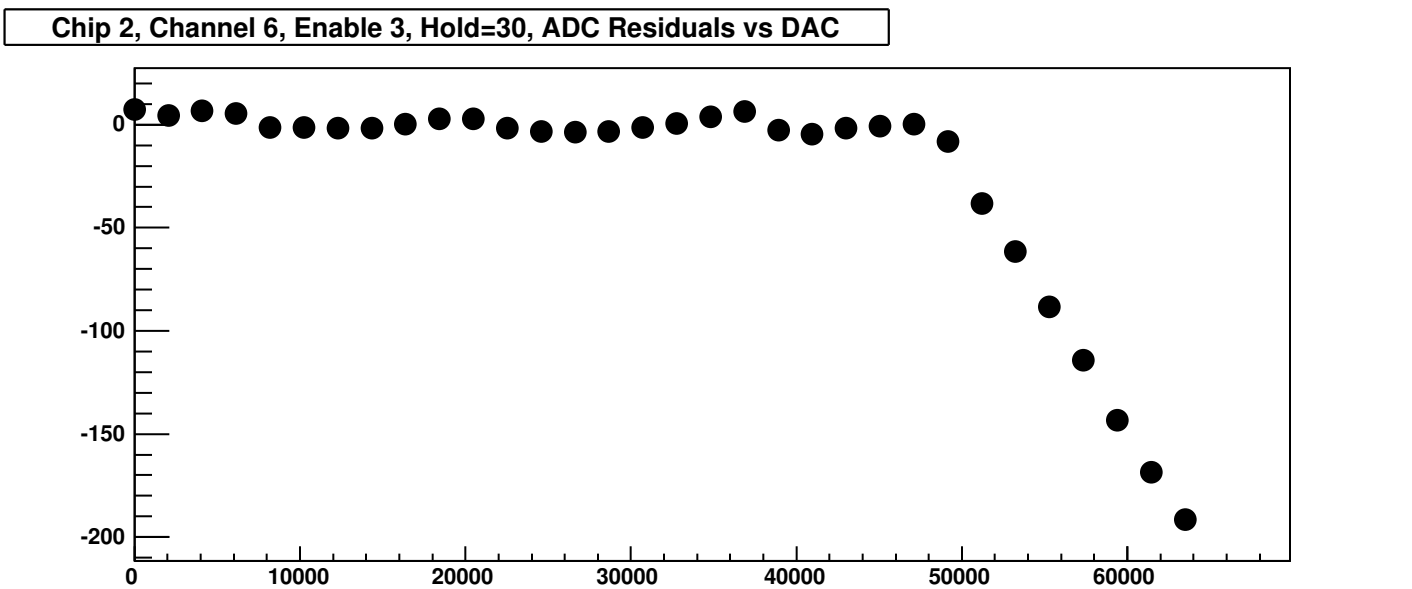

Chip 2, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.093e+04 / 15
p0 900.6 ± 0.2858
p1 0.6325 ± 2.663e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

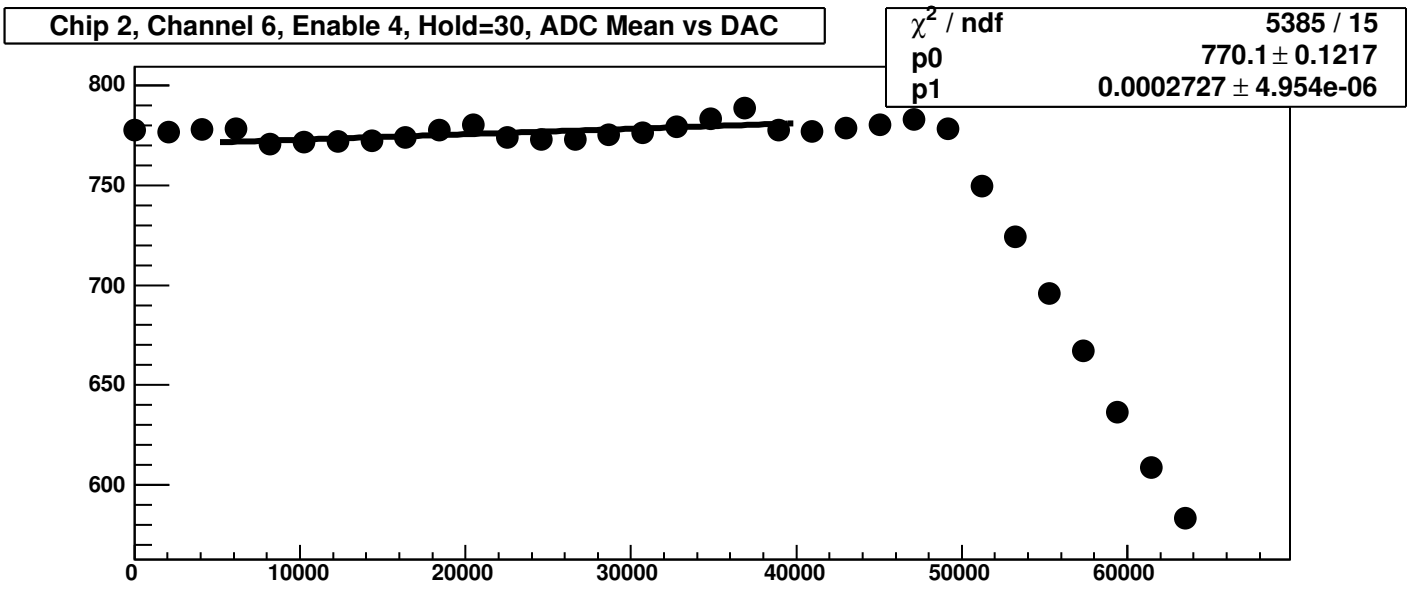

Chip 2, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

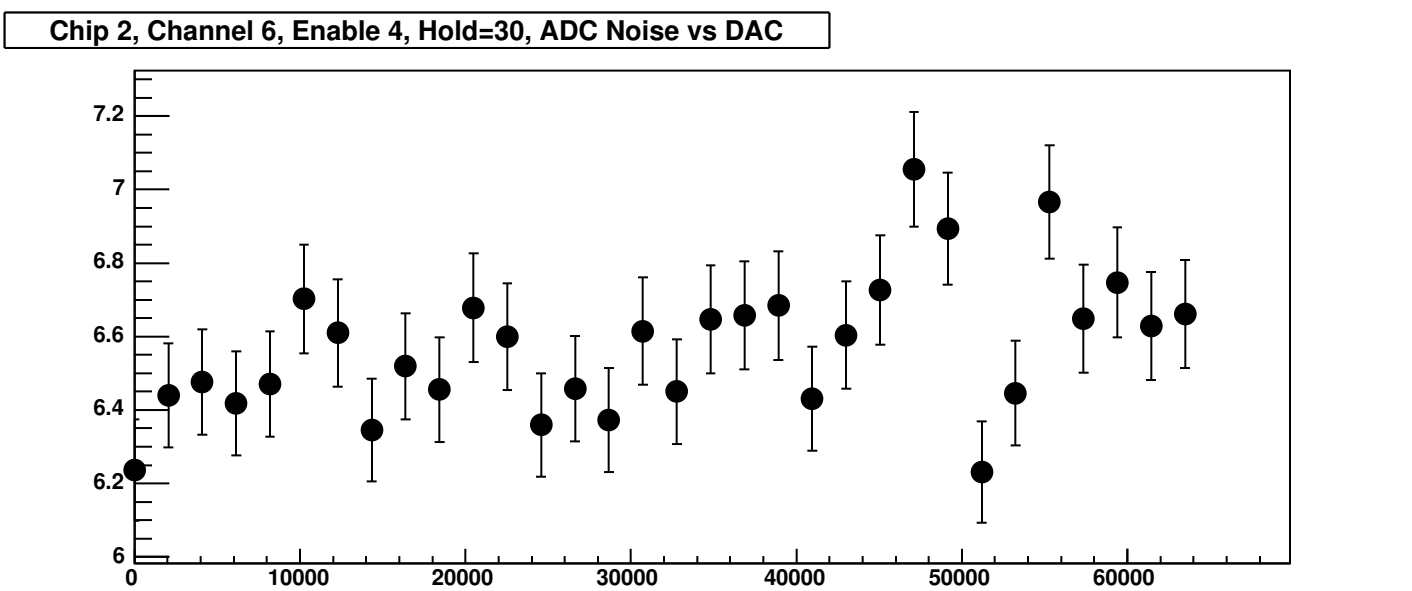

Chip 2, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

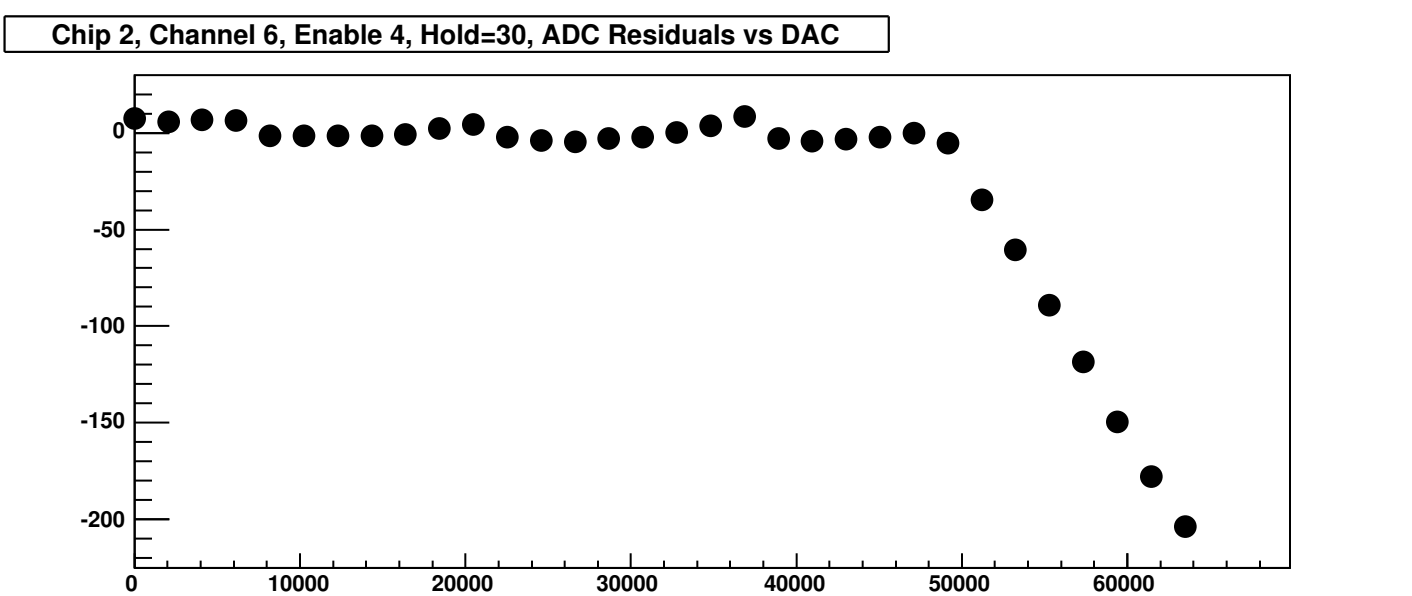

Chip 2, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3924 / 15
p0 771.1 ± 0.1184
p1 -0.0004983 ± 4.842e-06
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

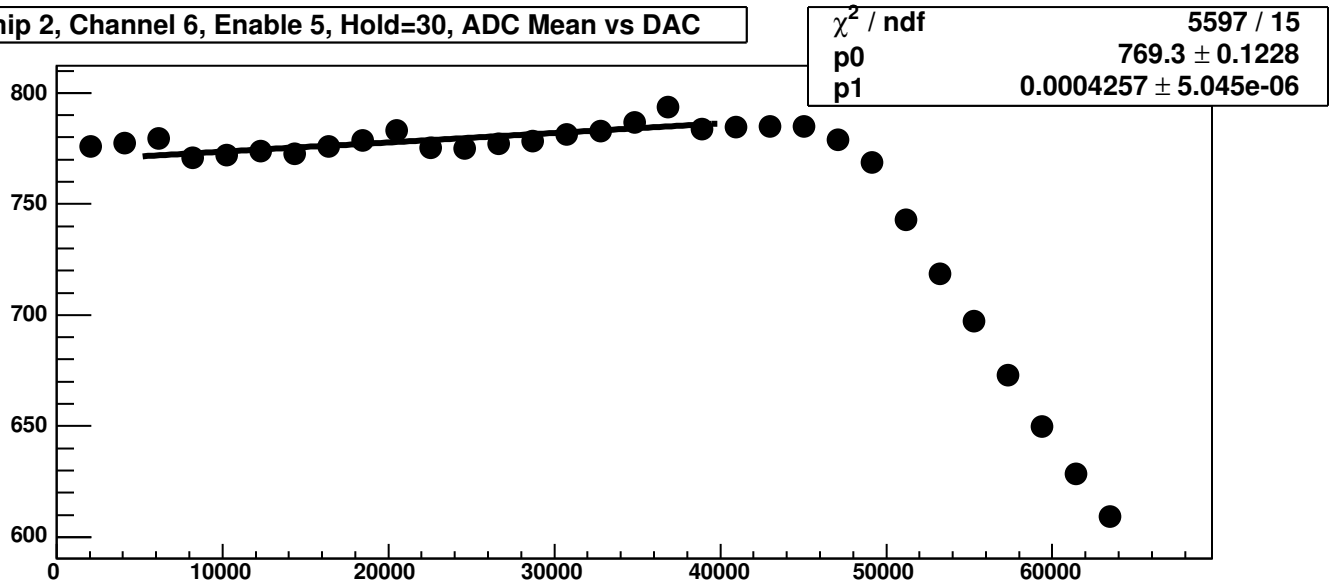

Chip 2, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5385 / 15
p0 770.1 ± 0.1217
p1 0.0002727 ± 4.954e-06
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

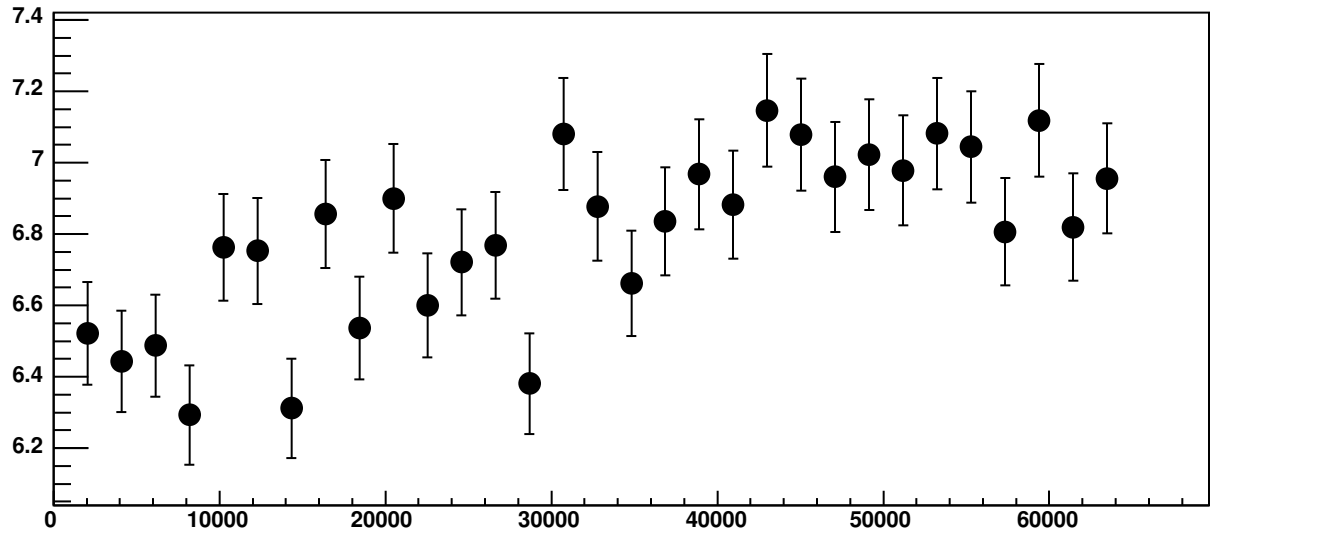

Chip 2, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

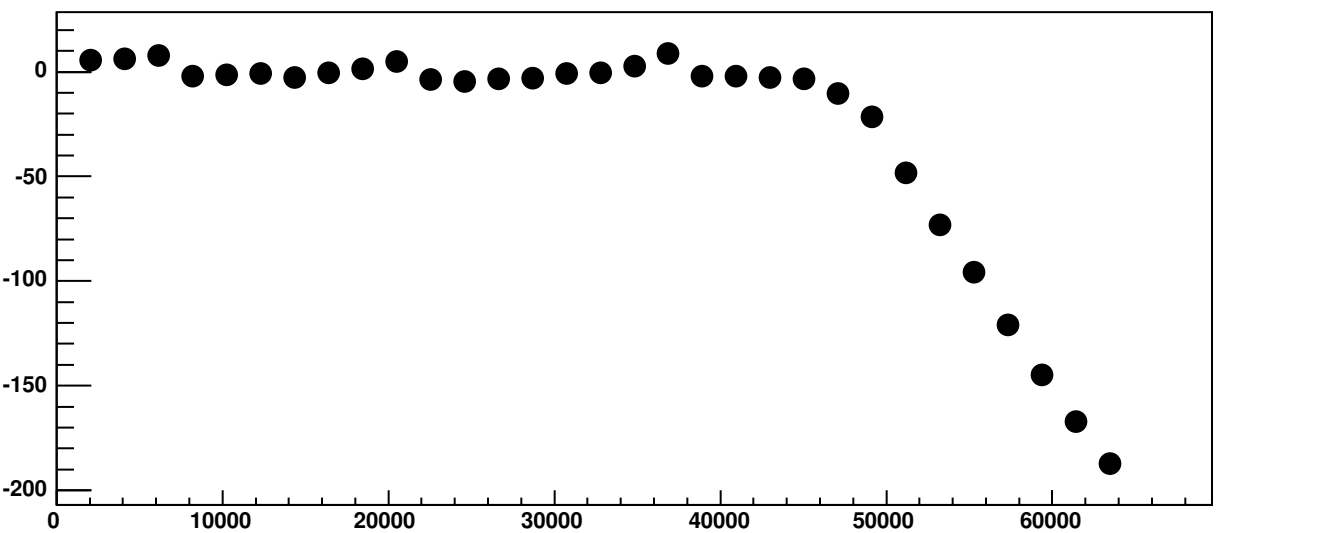
Chip 2, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC



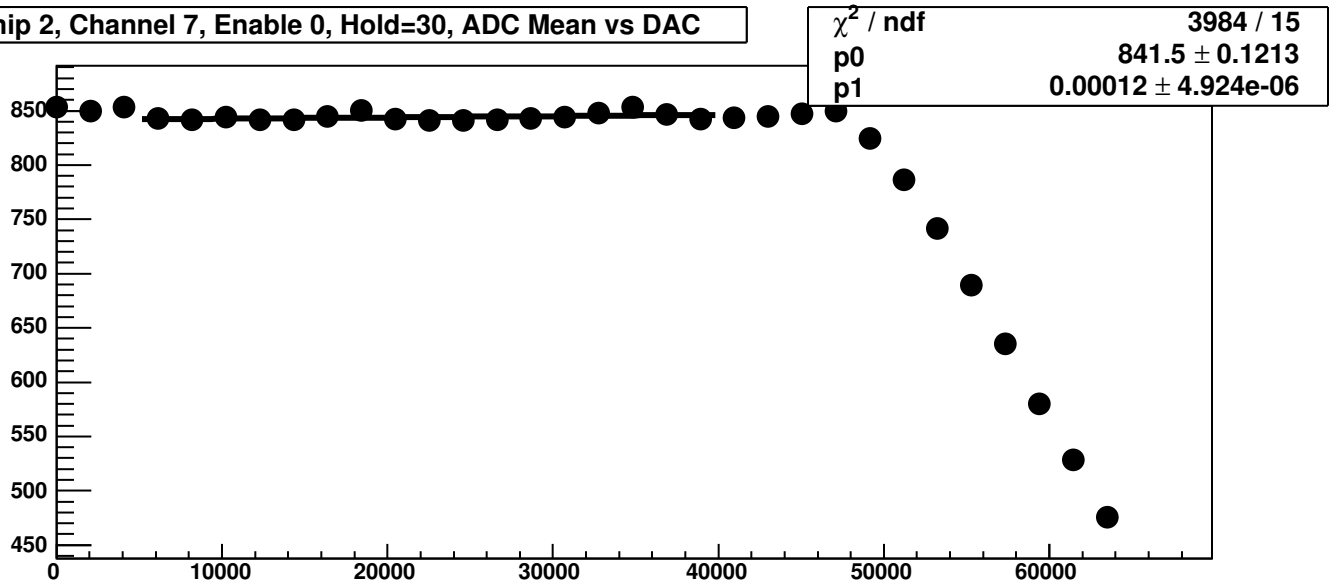
Chip 2, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC



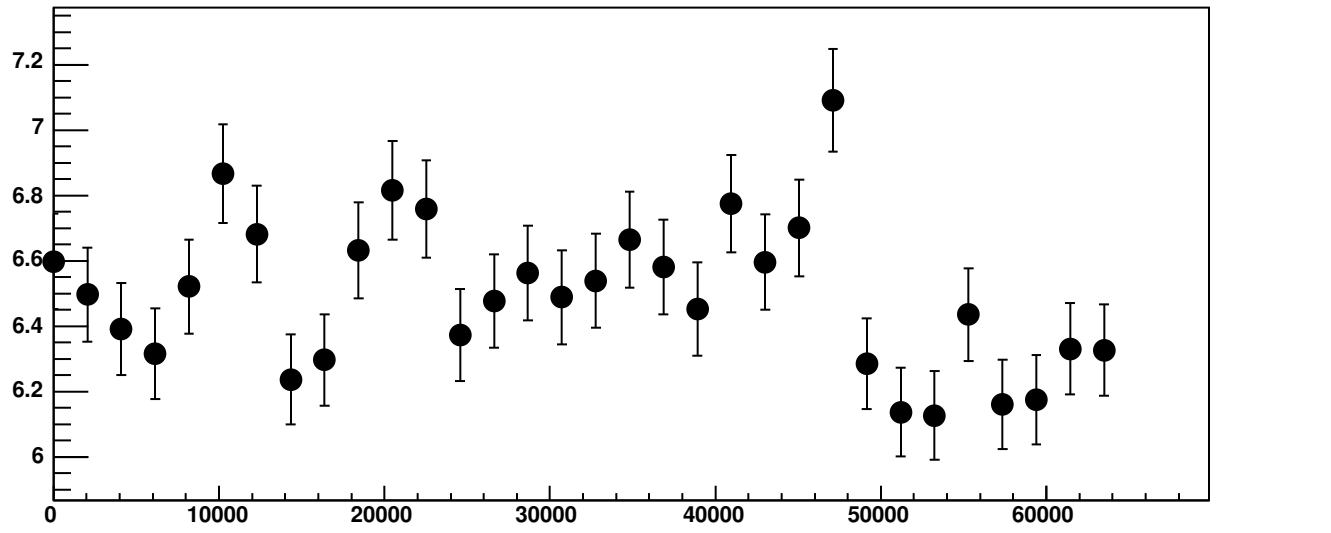
Chip 2, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC



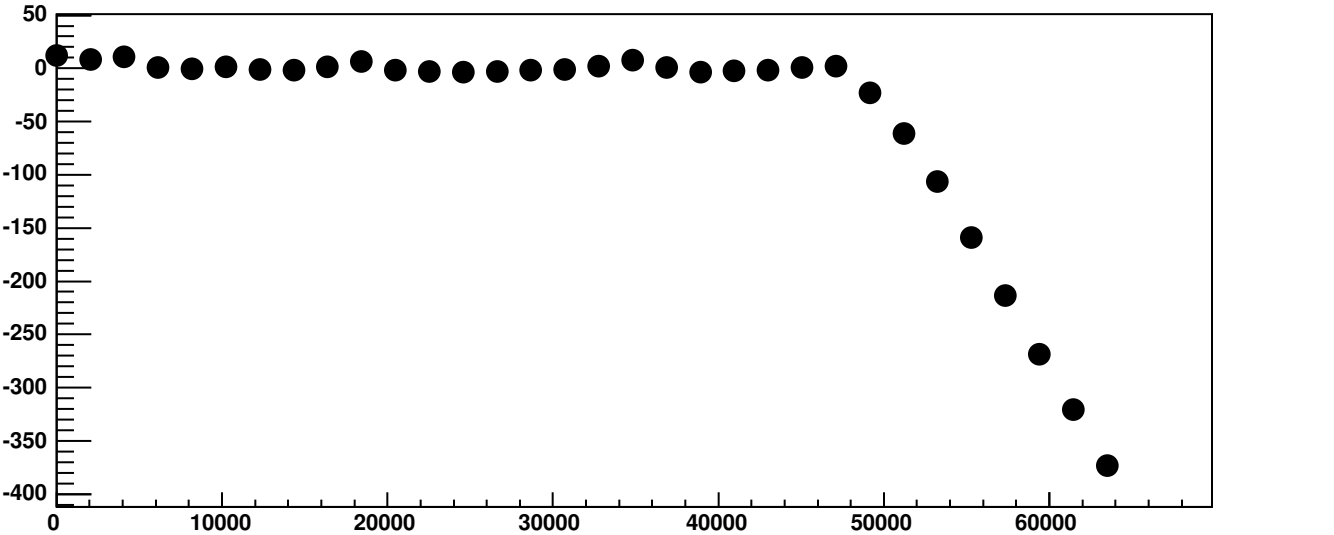
Chip 2, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC



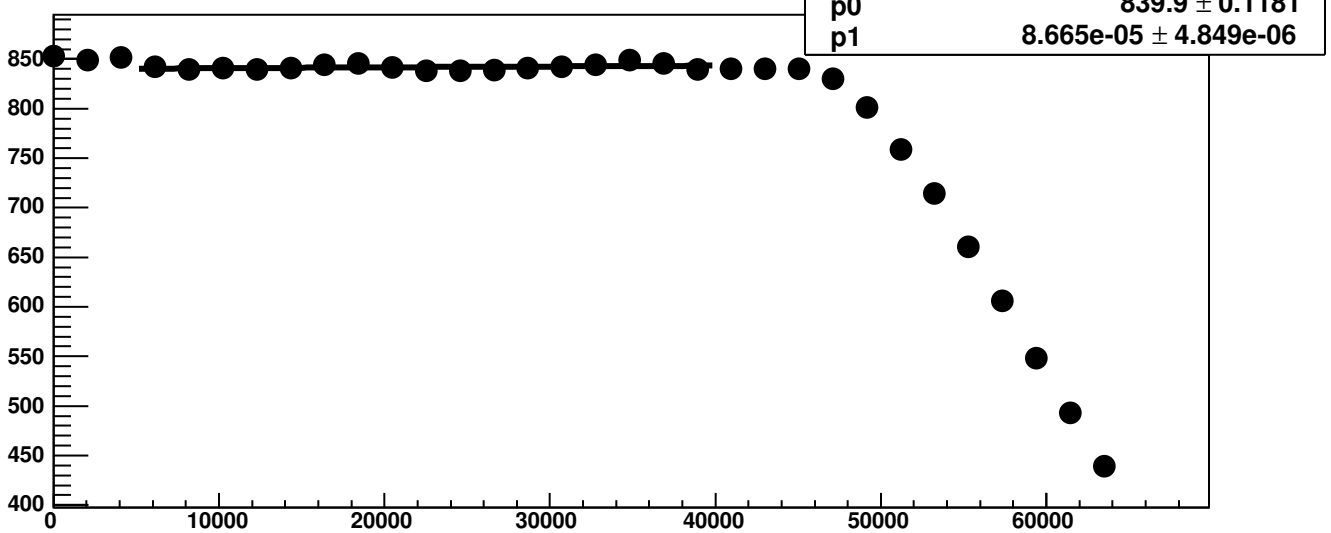
Chip 2, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



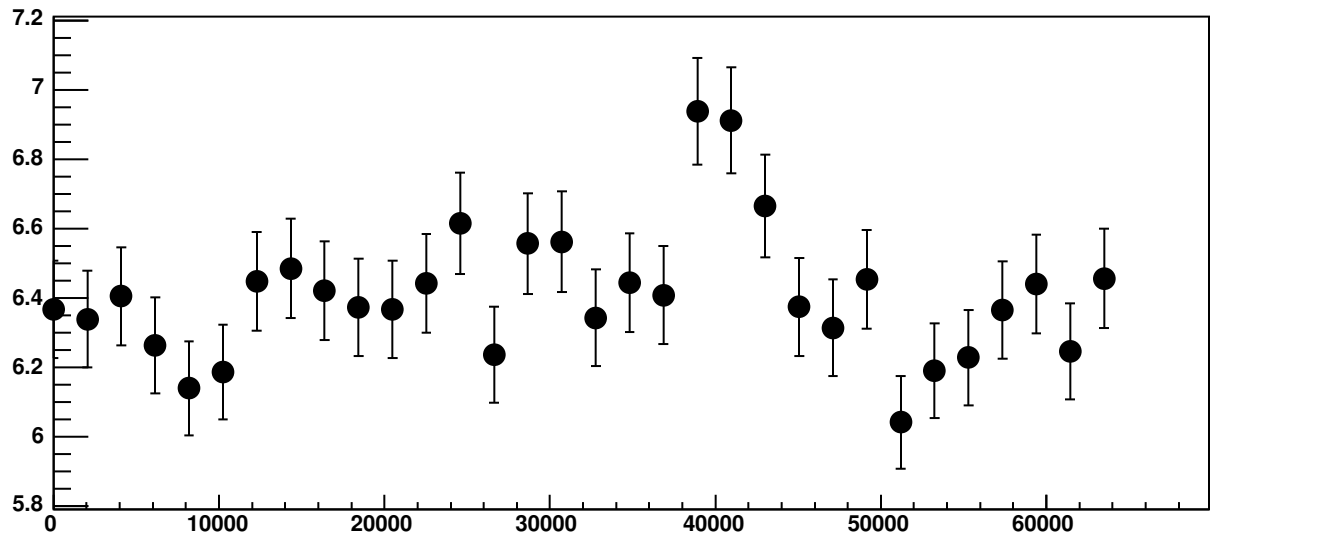
Chip 2, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC



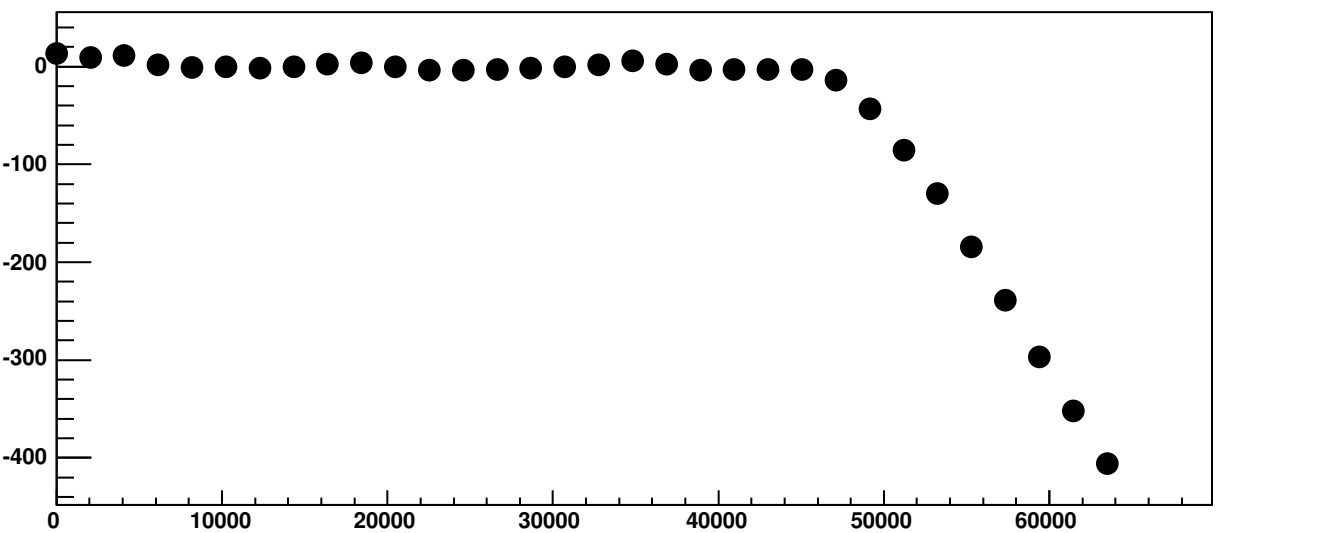
Chip 2, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

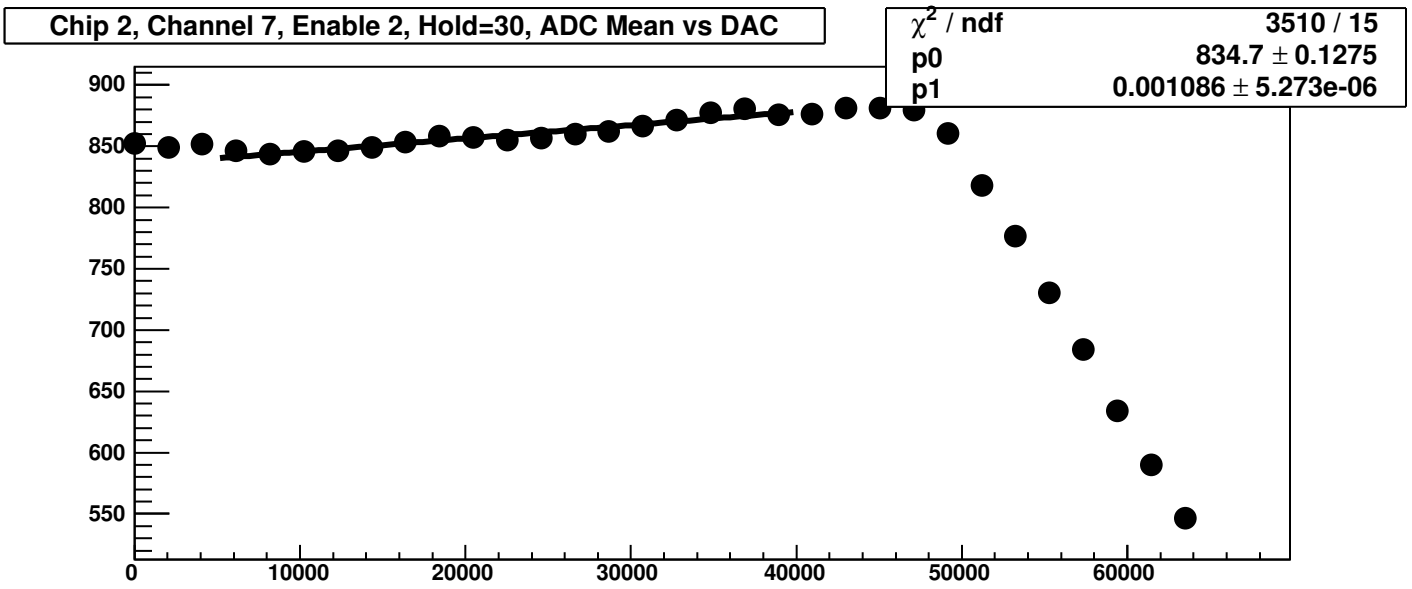


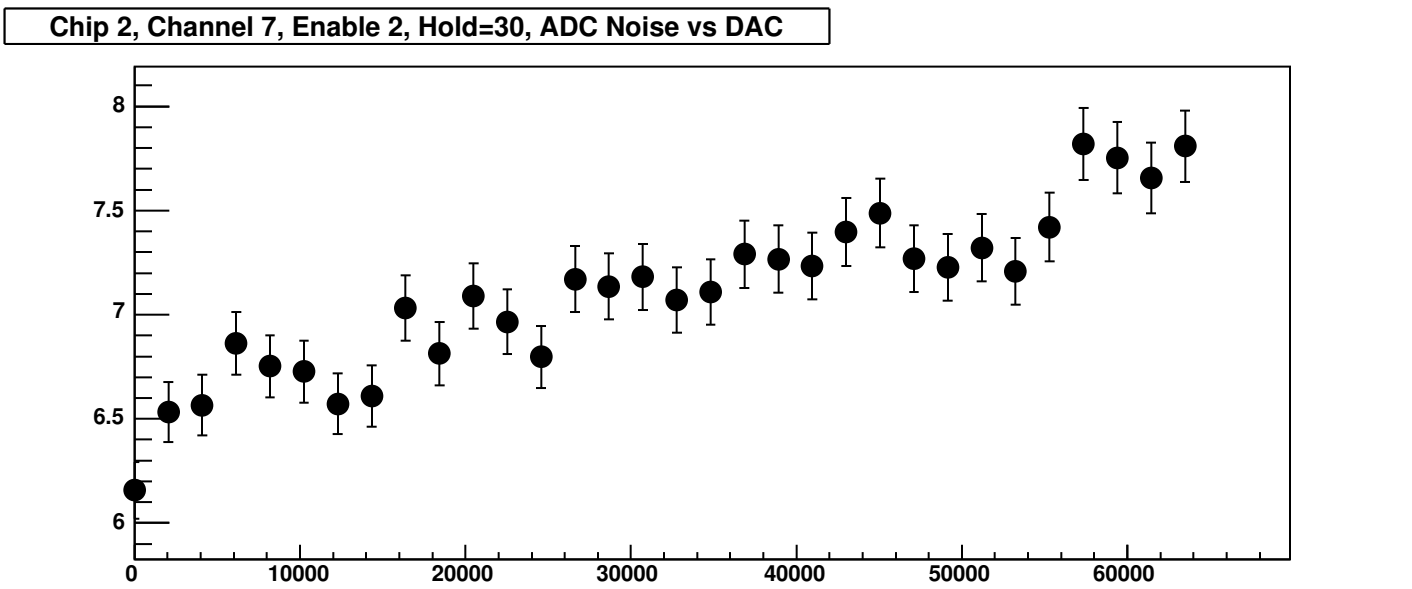
Chip 2, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC

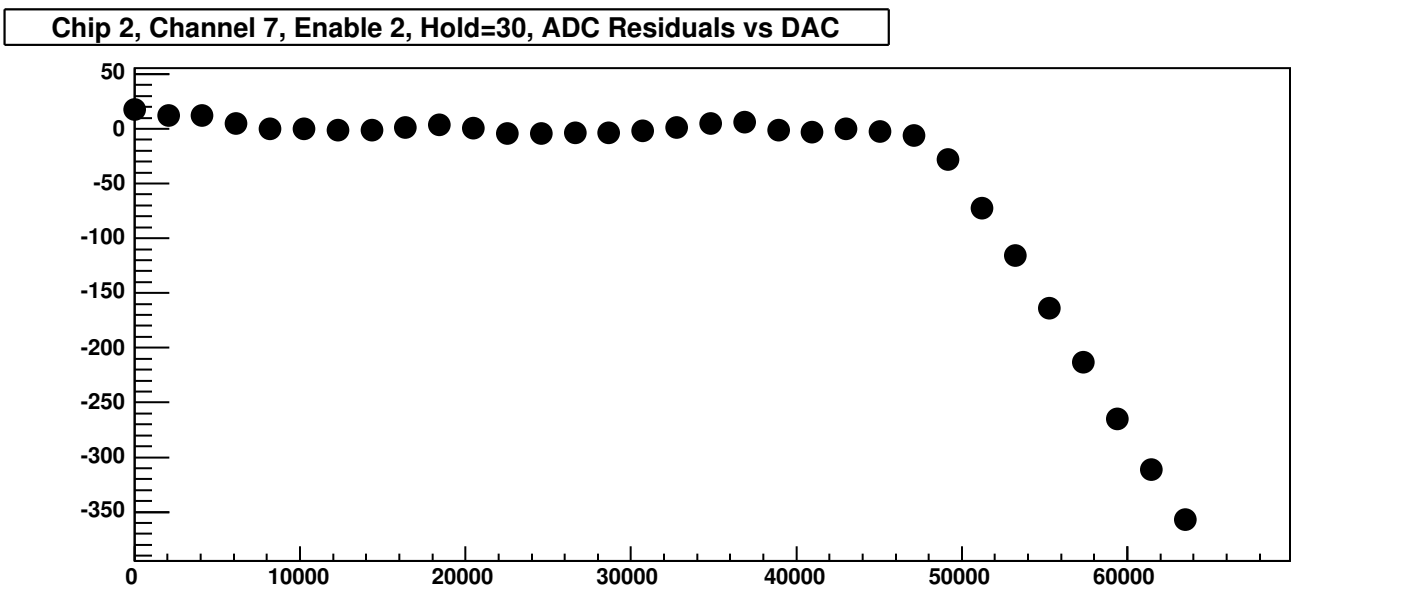


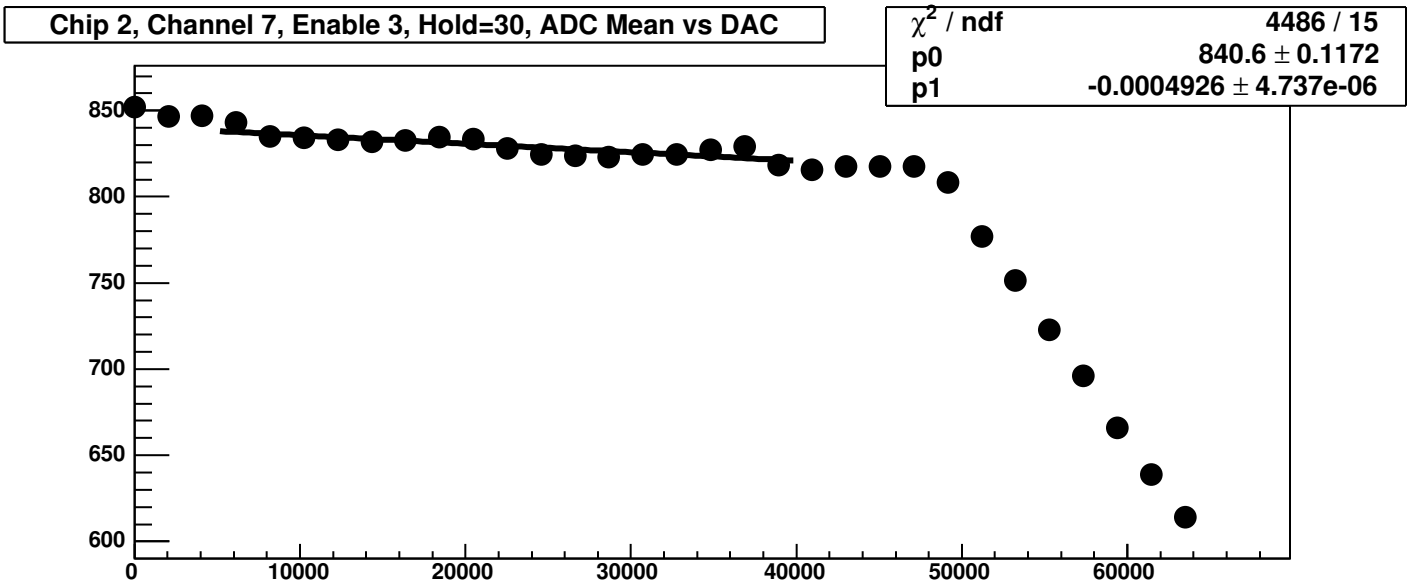
Chip 2, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

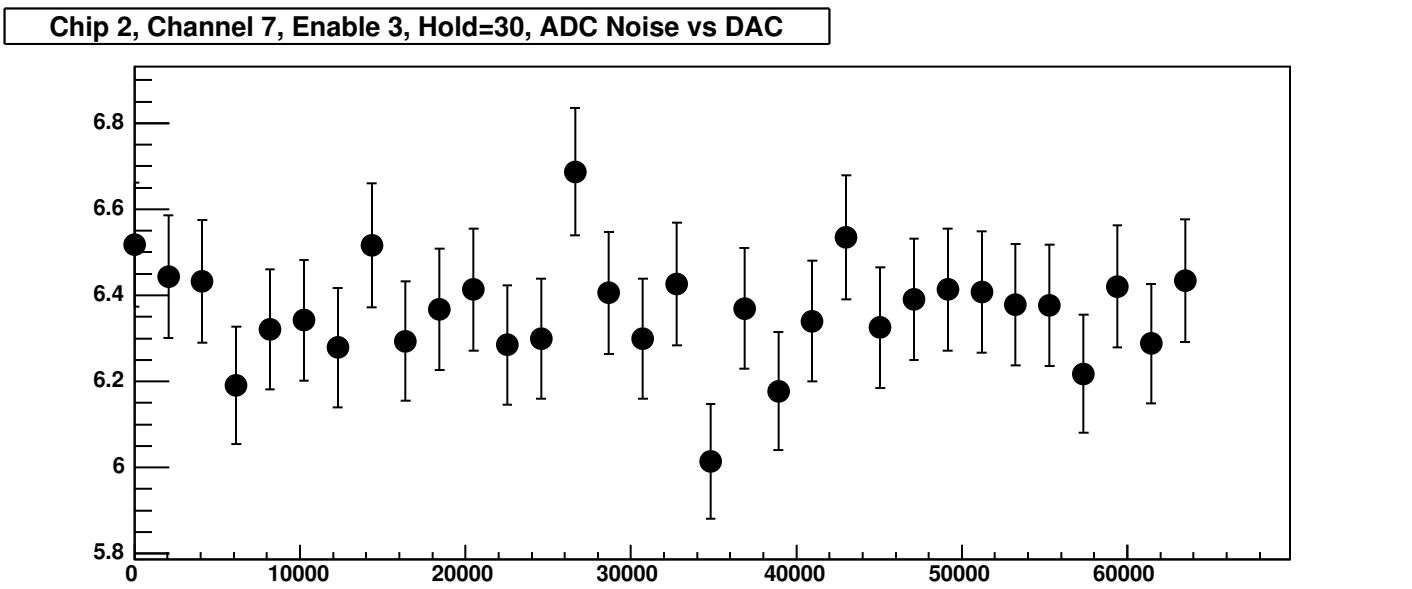


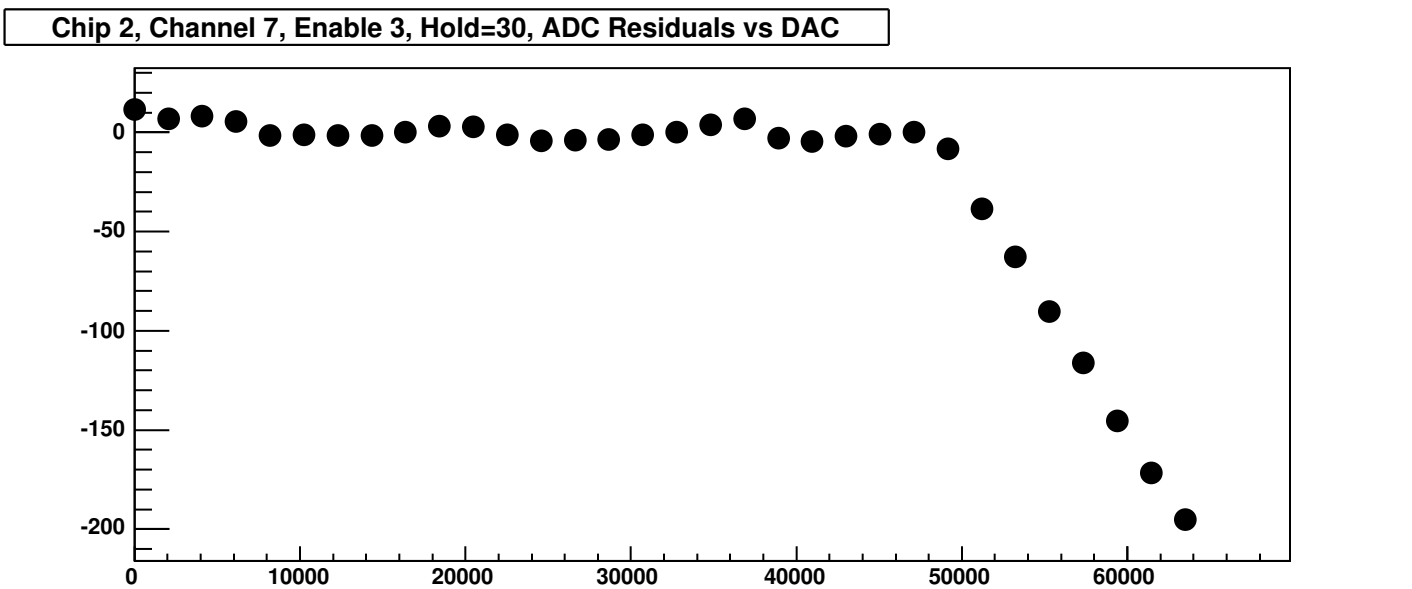

Chip 2, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3510 / 15
p0 834.7 ± 0.1275
p1 0.001086 ± 5.273e-06
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC
8
7.5
7
6.5
6
0
10000
20000
30000
40000
50000
60000

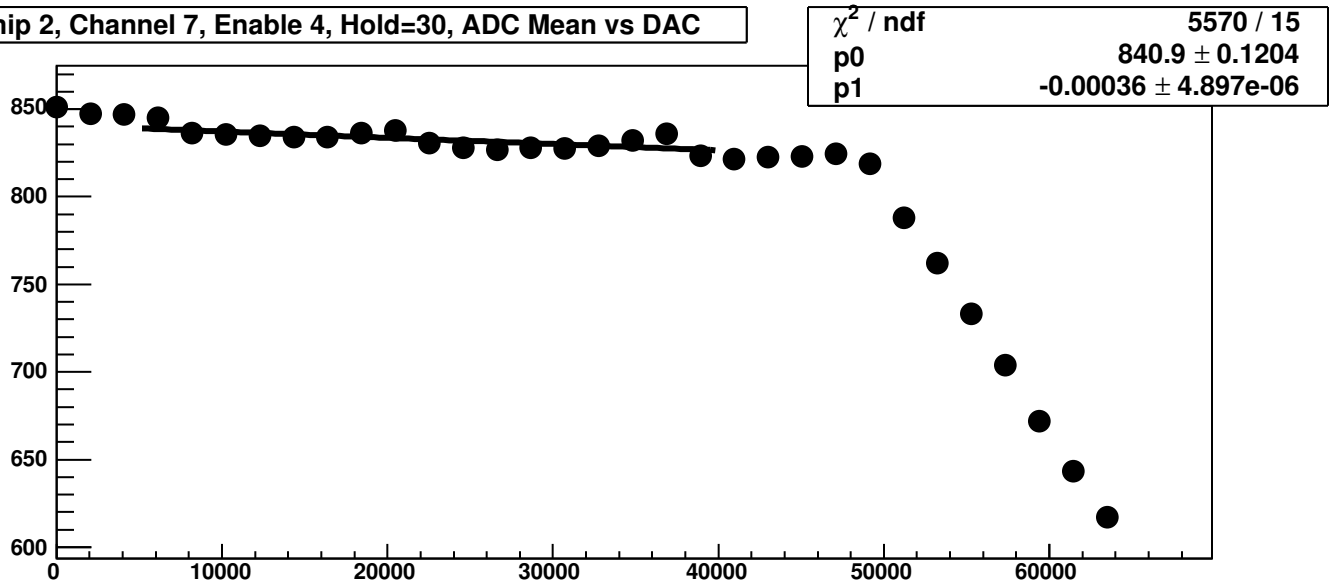

Chip 2, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4486 / 15
p0 840.6 ± 0.1172
p1 -0.0004926 ± 4.737e-06
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

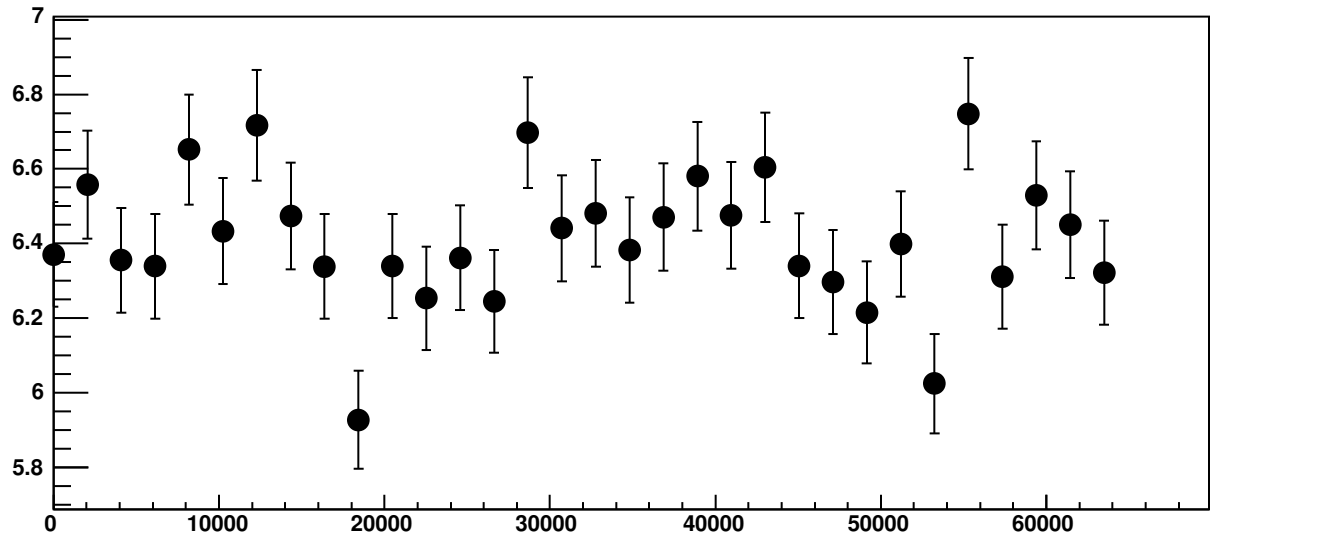

Chip 2, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

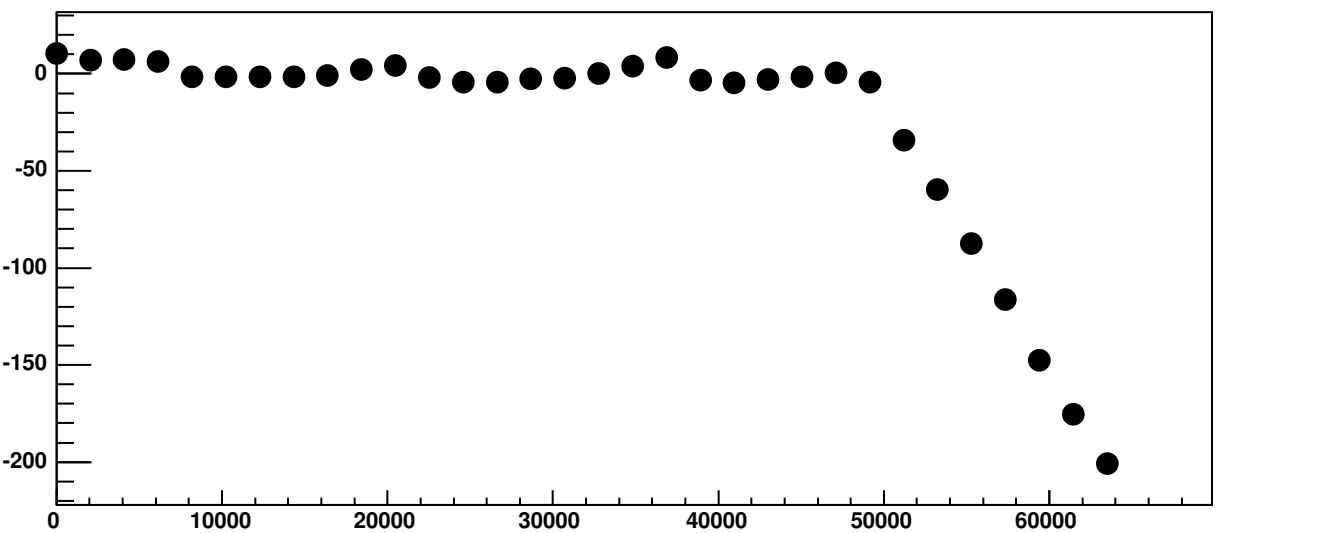
Chip 2, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC

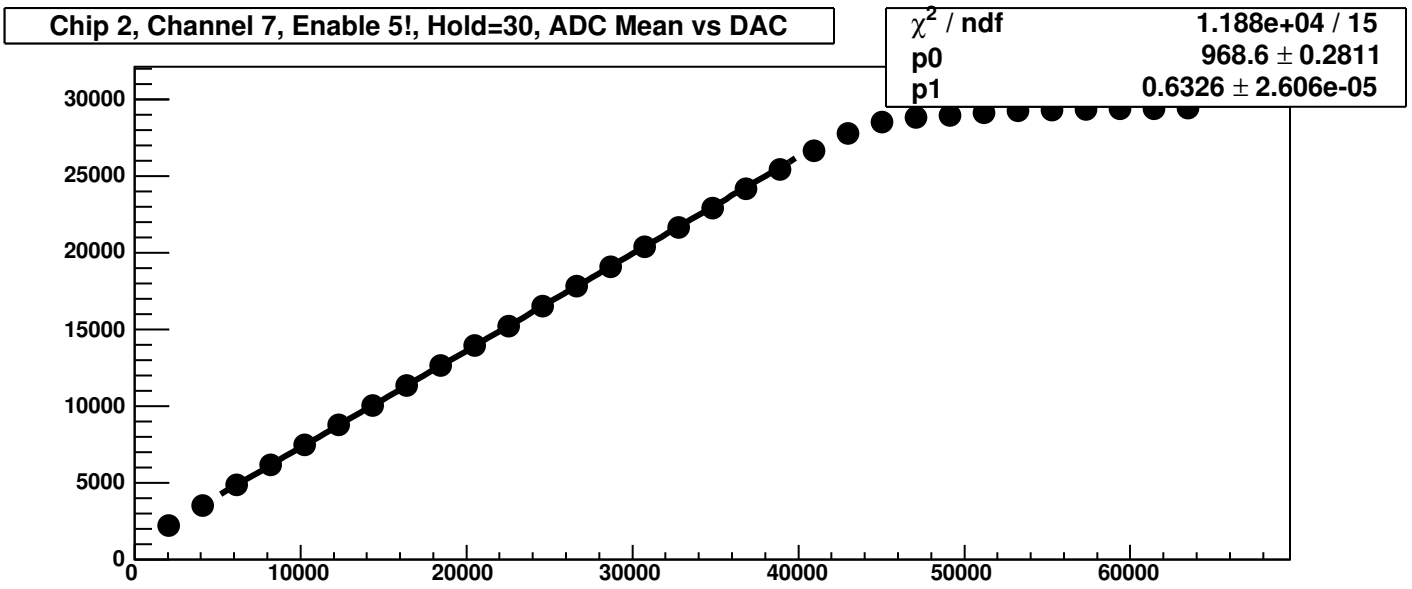


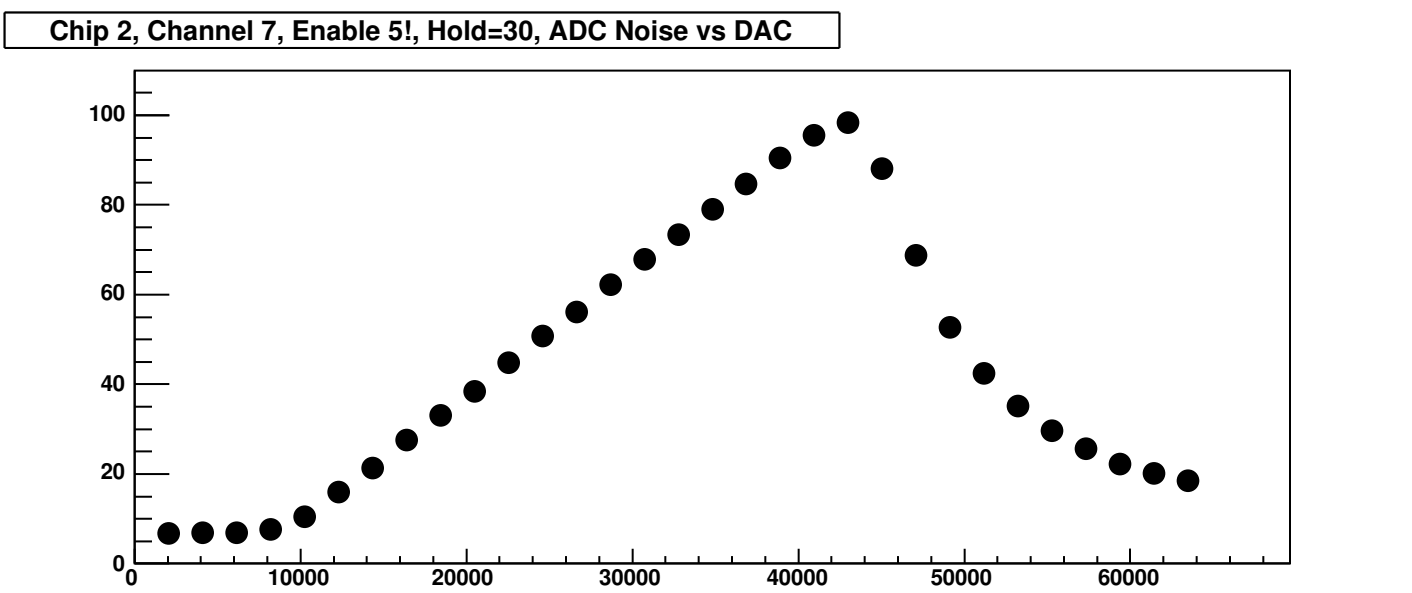
Chip 2, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC

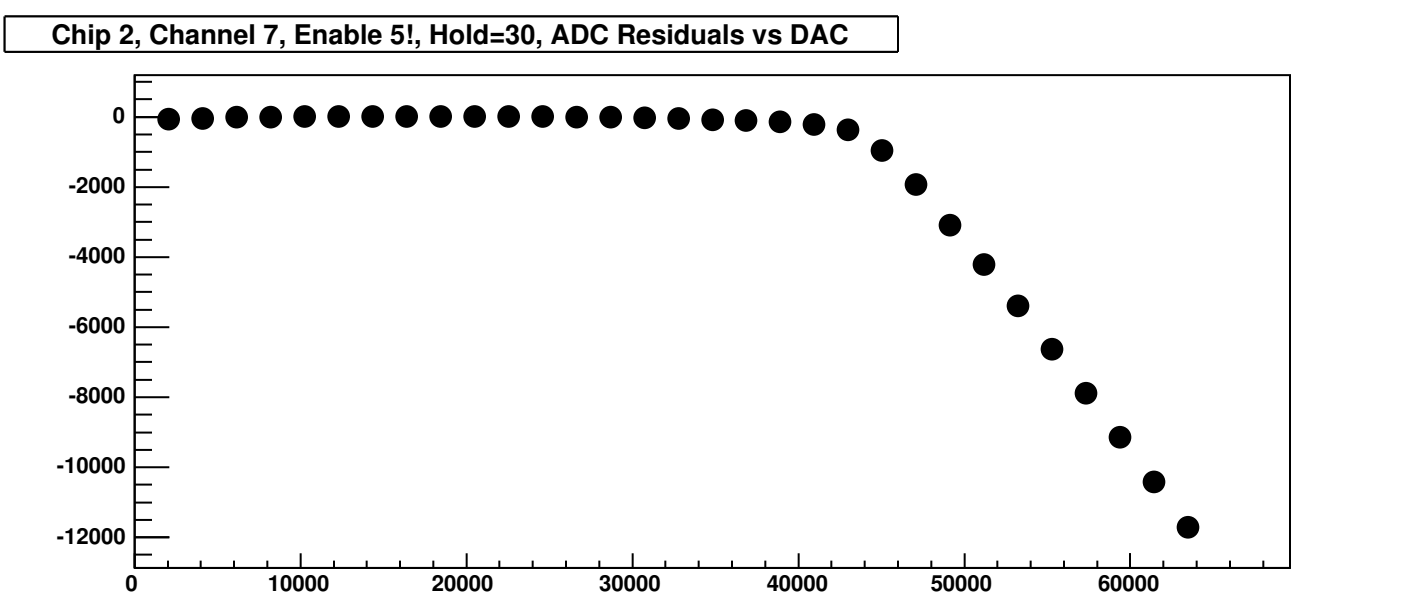


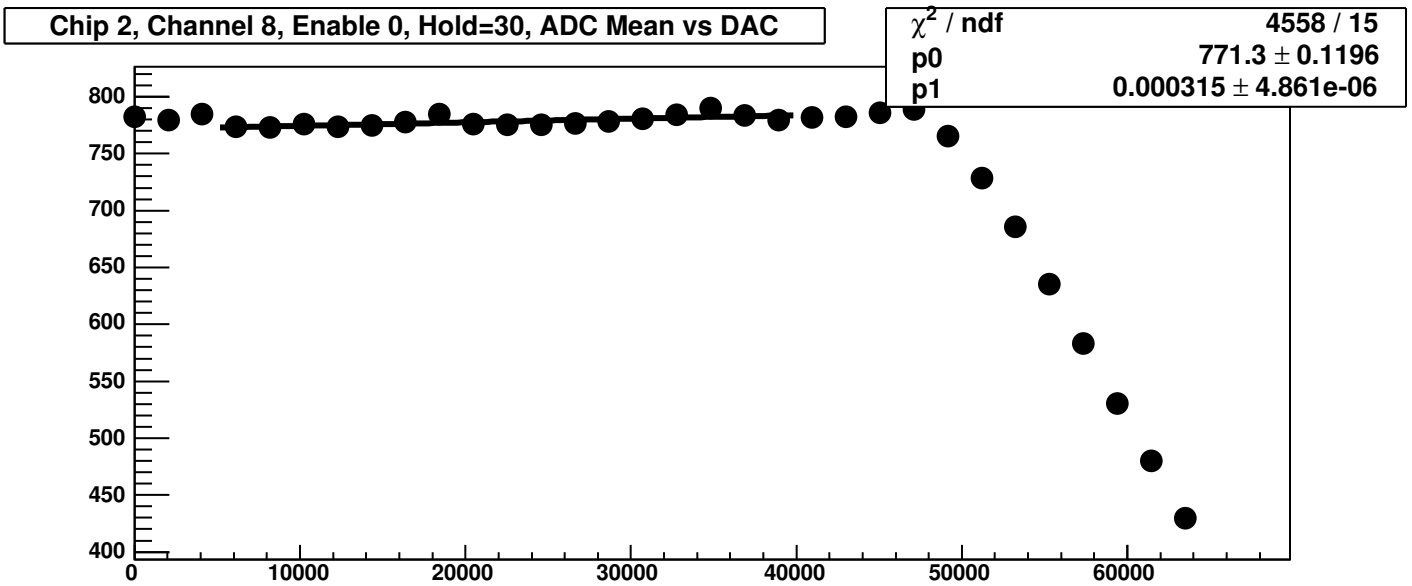
Chip 2, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC

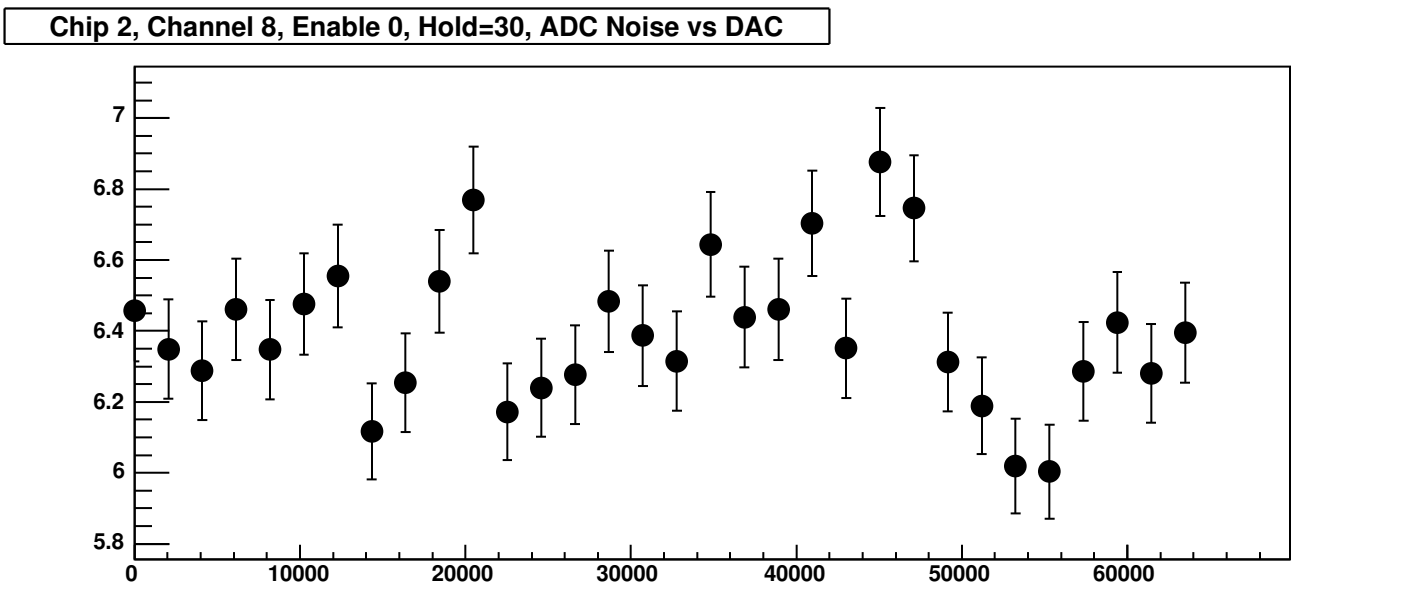


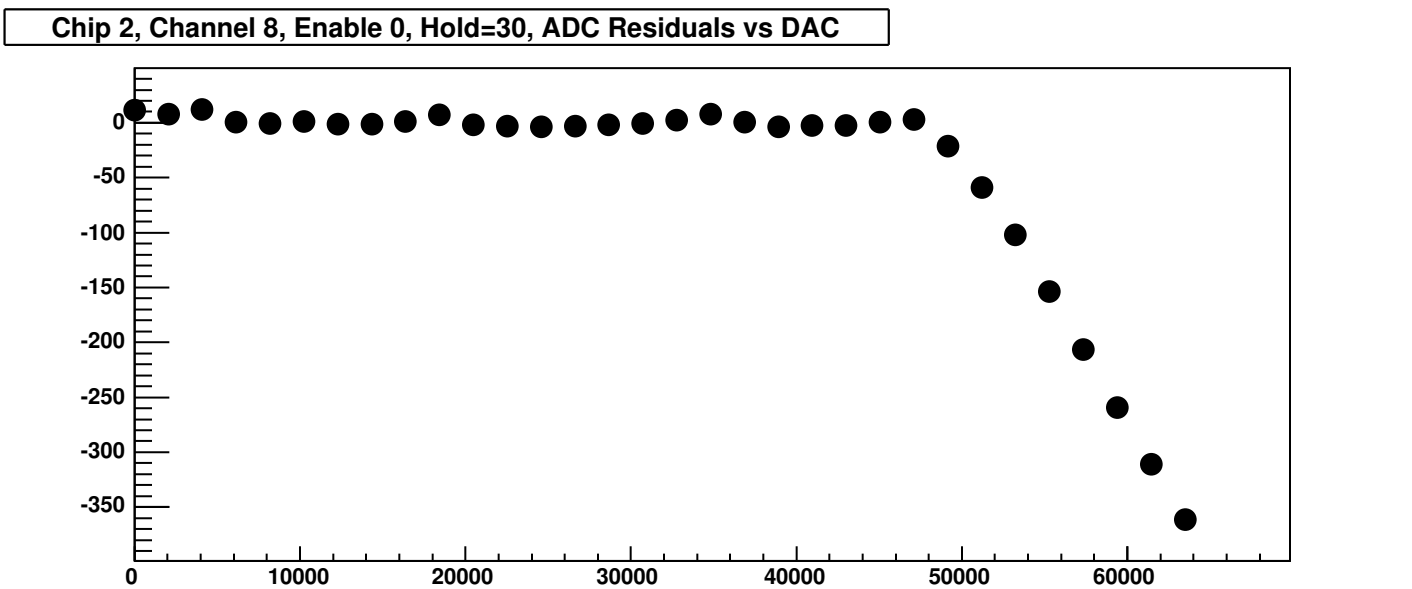

Chip 2, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.188e+04 / 15
p0 968.6 ± 0.2811
p1 0.6326 ± 2.606e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

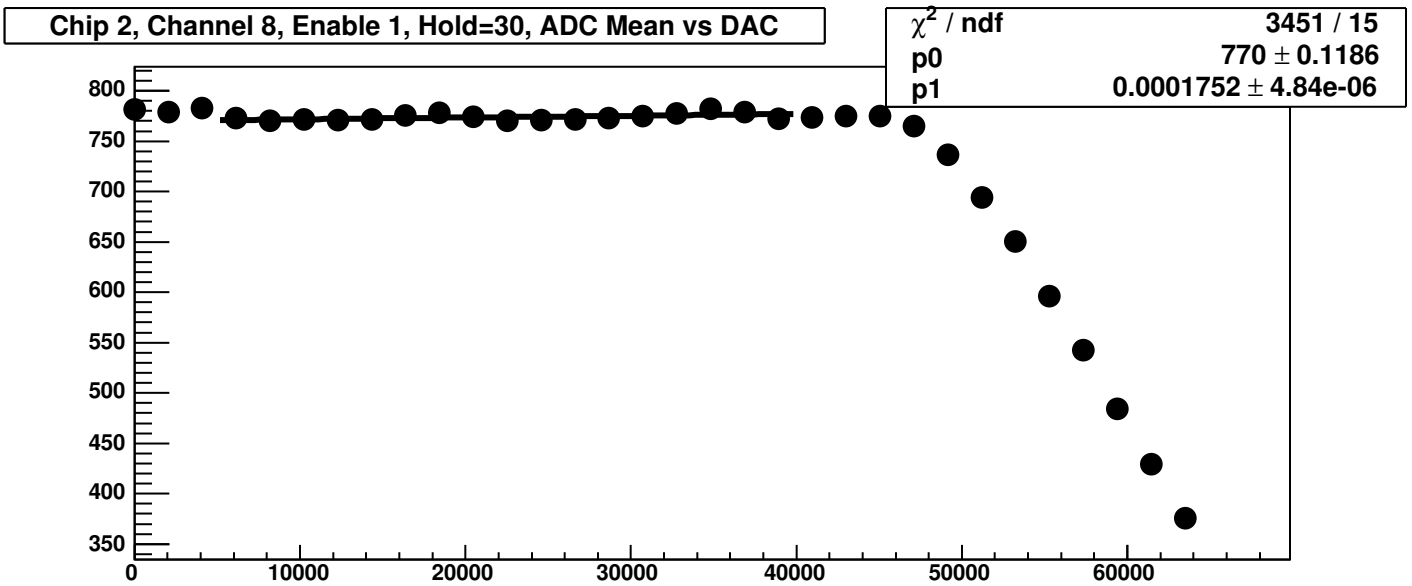

Chip 2, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

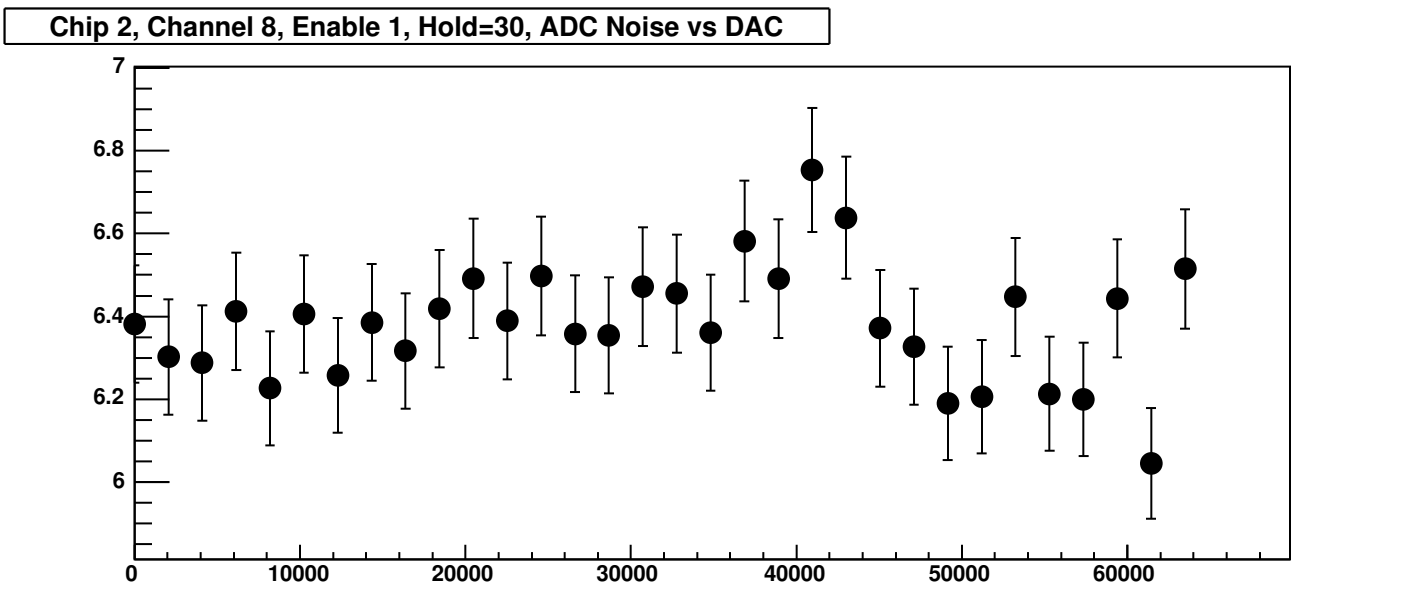

Chip 2, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

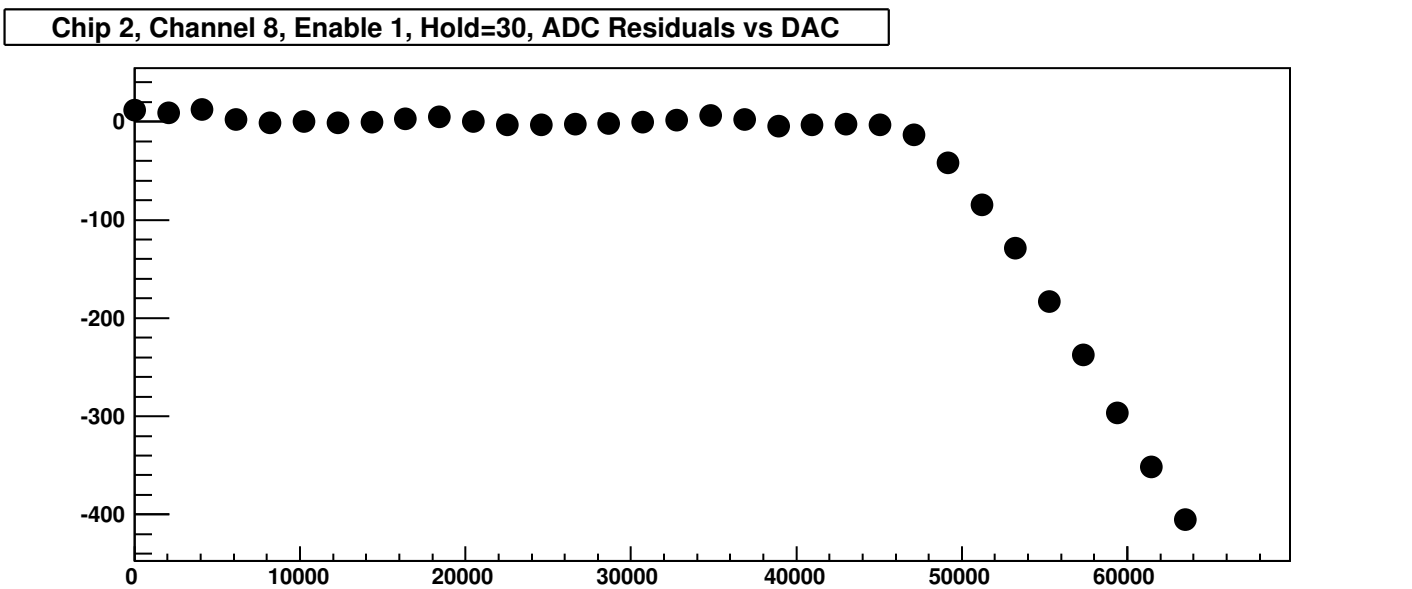

Chip 2, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4558 / 15
p0 771.3 ± 0.1196
p1 0.000315 ± 4.861e-06
800
750
700
650
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000

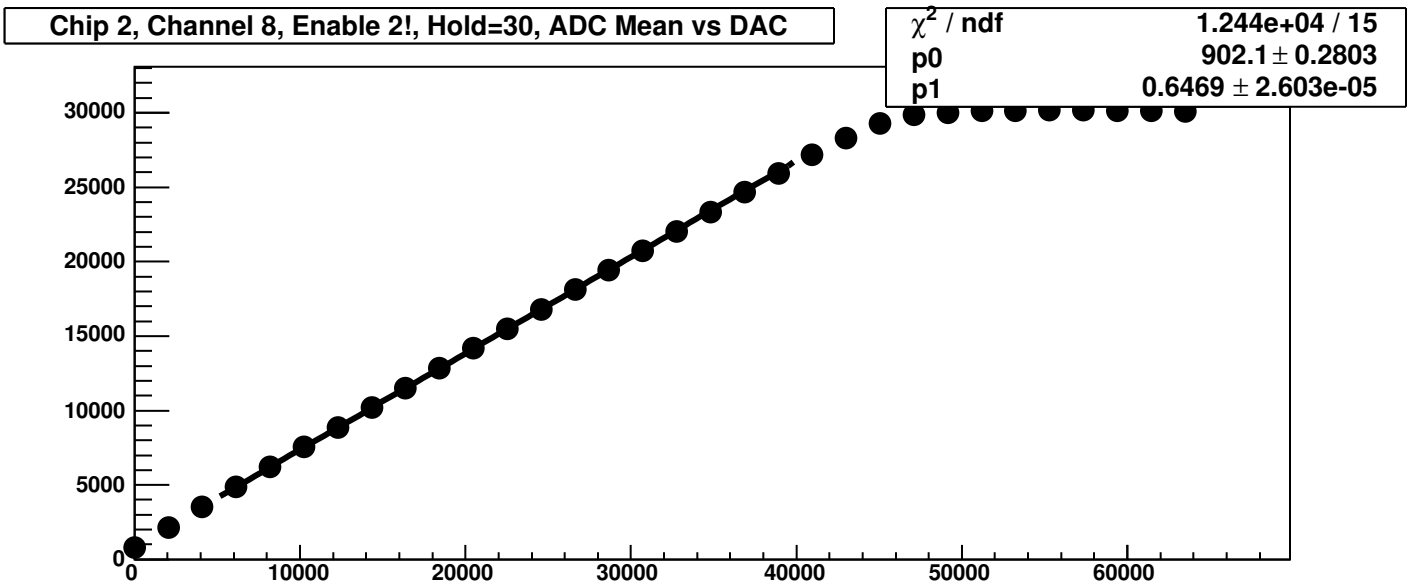

Chip 2, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

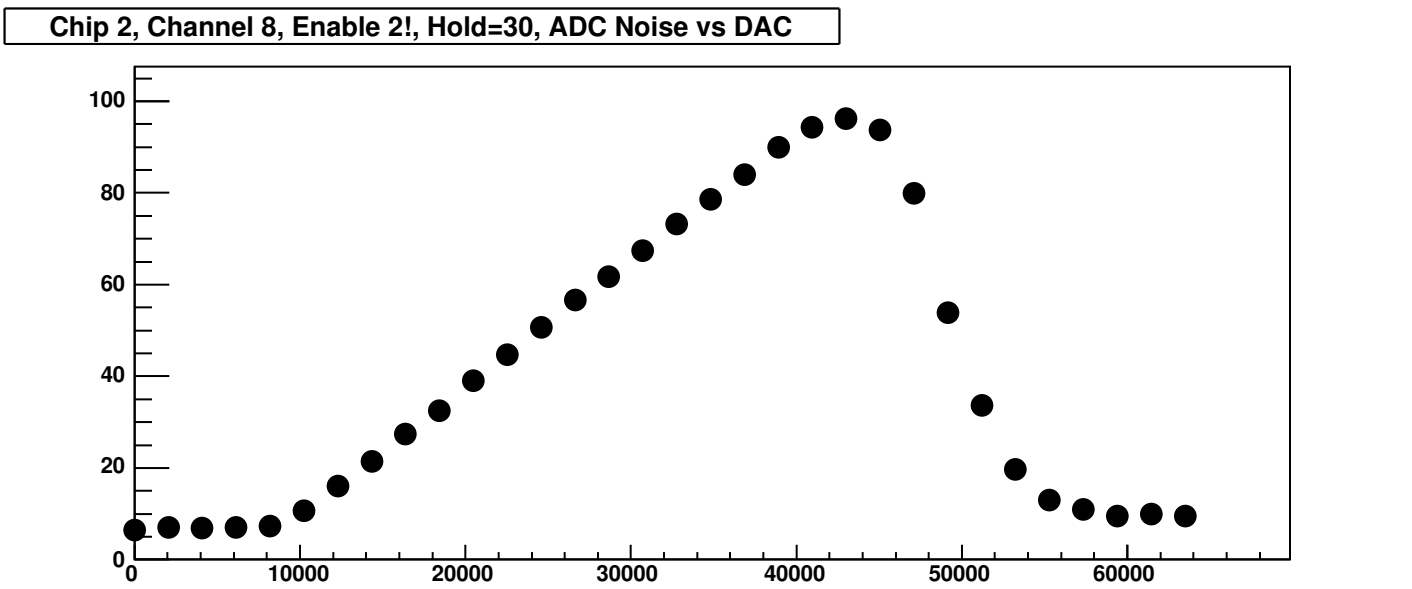

Chip 2, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

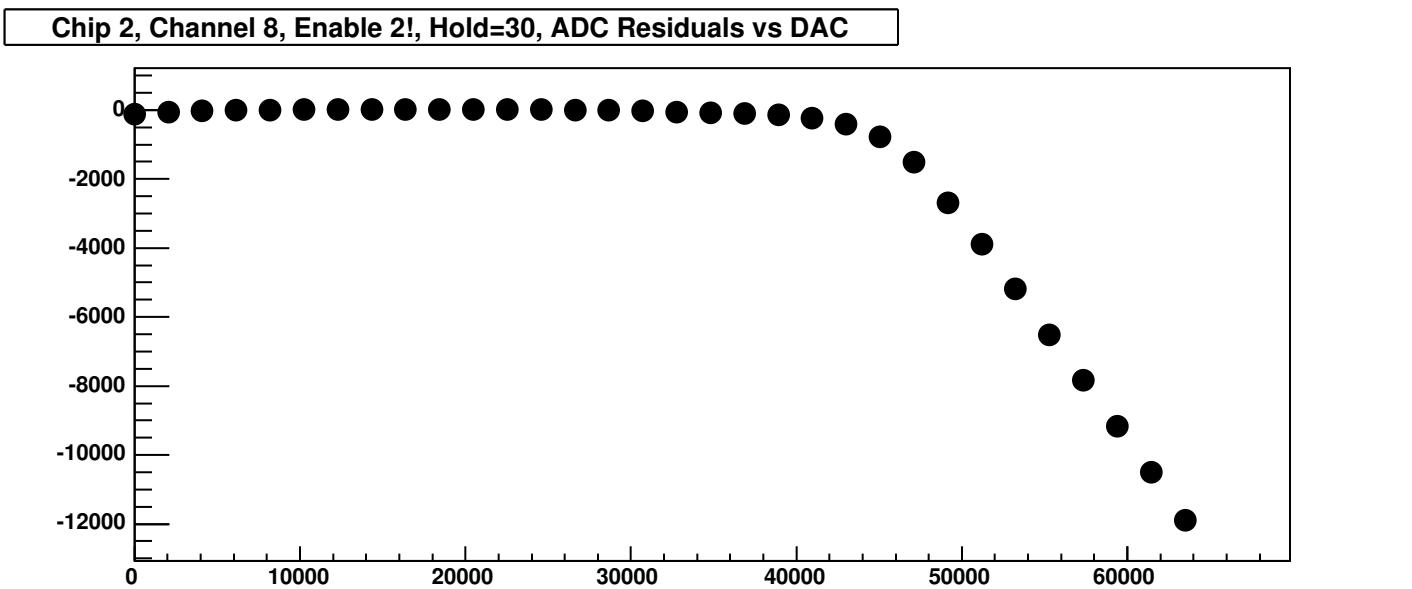

Chip 2, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3451 / 15
p0 770 ± 0.1186
p1 0.0001752 ± 4.84e-06
800
750
700
650
600
550
500
450
400
350
0
10000
20000
30000
40000
50000
60000

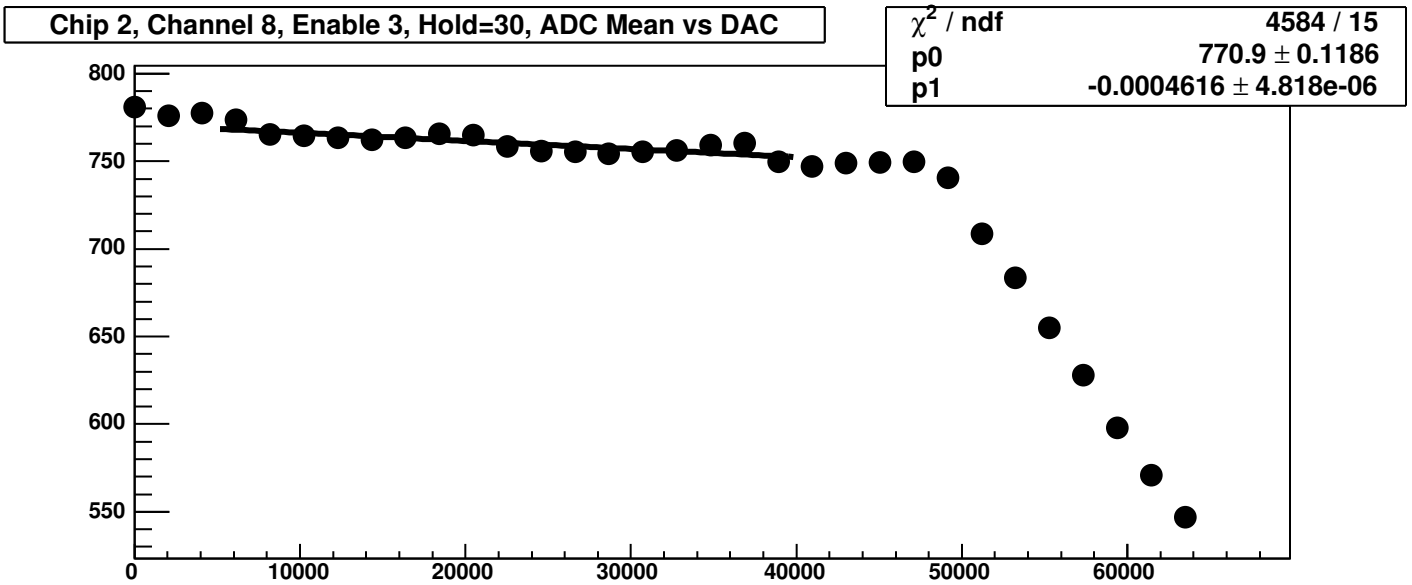

Chip 2, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

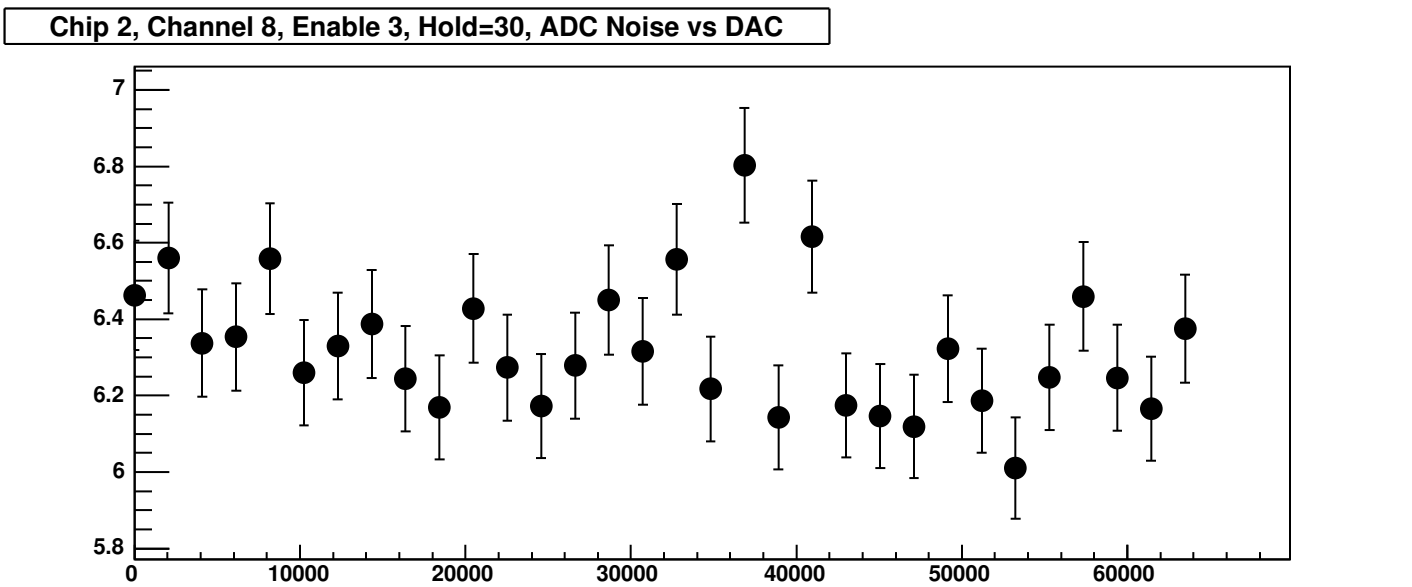

Chip 2, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

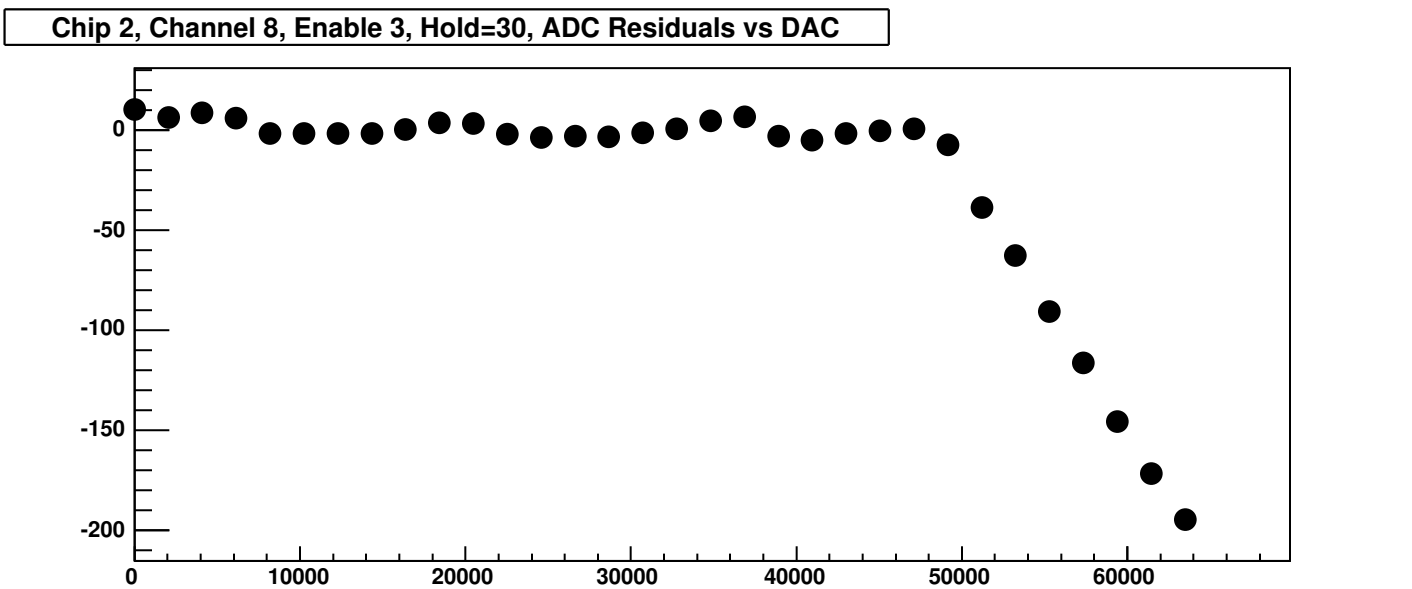

Chip 2, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.244e+04 / 15
p0 902.1 ± 0.2803
p1 0.6469 ± 2.603e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

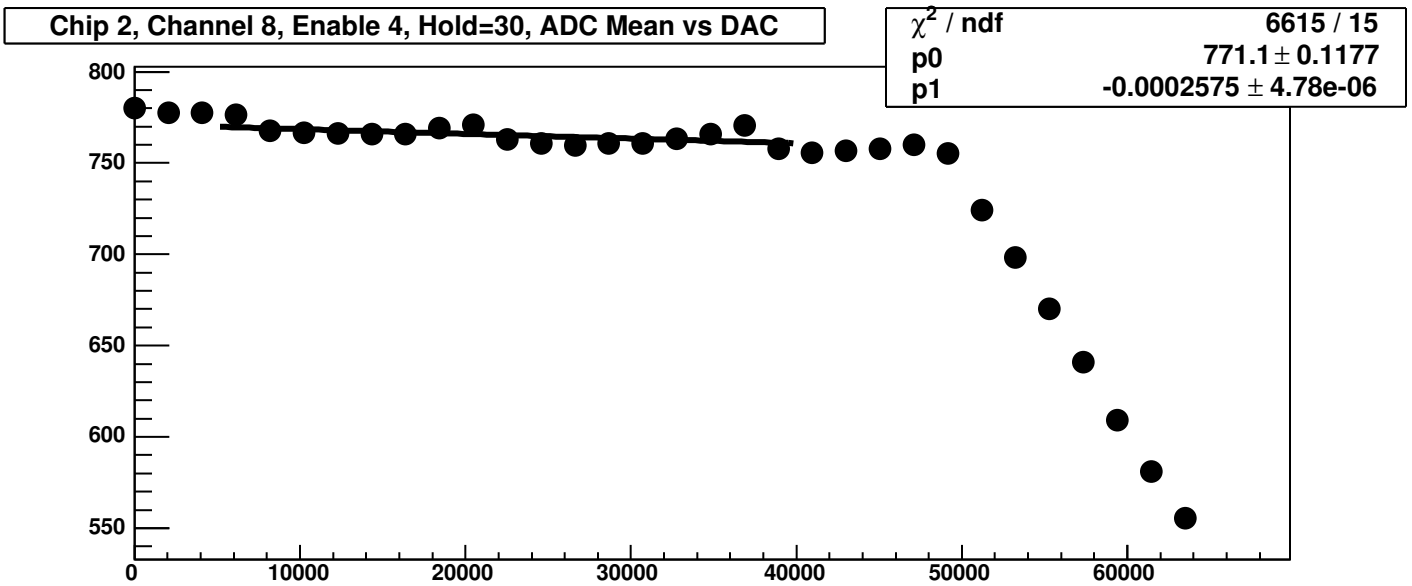

Chip 2, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

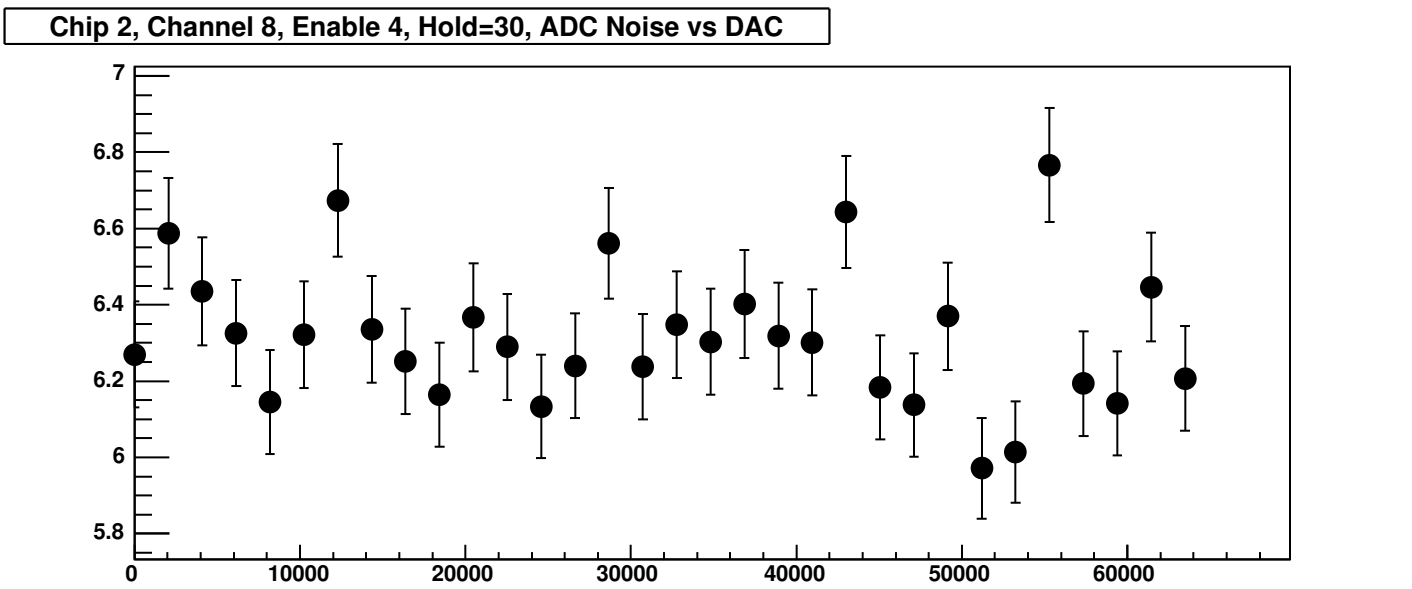

Chip 2, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

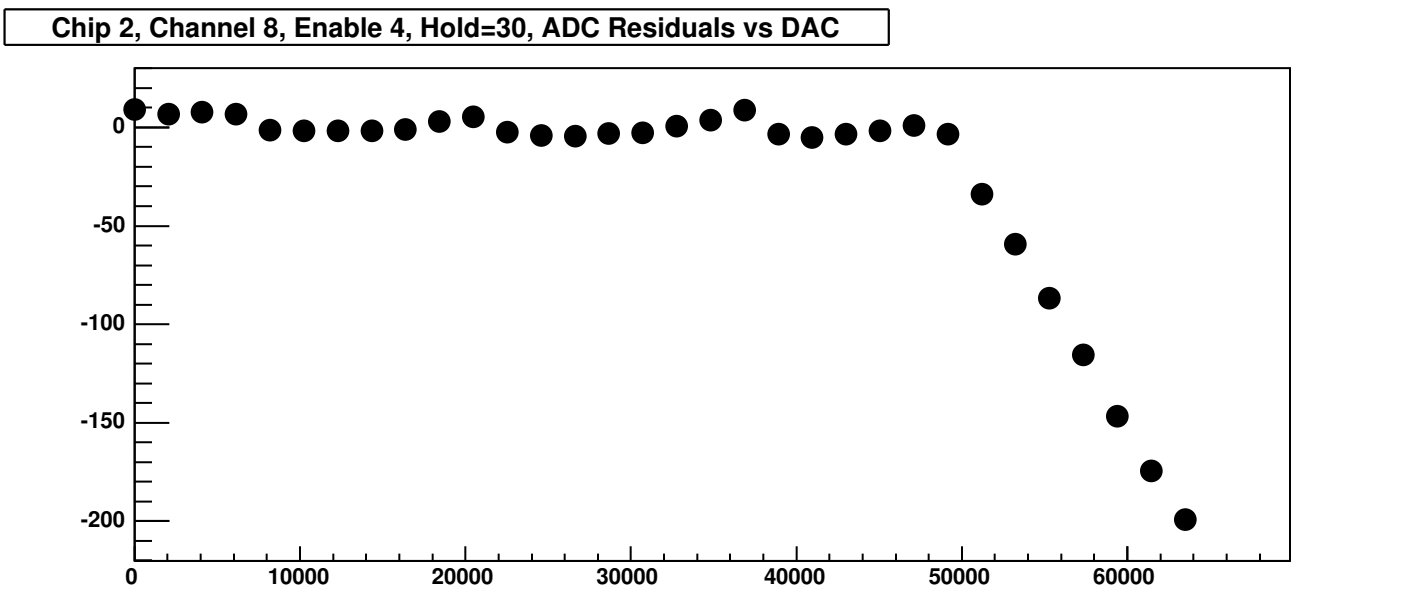

Chip 2, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4584 / 15
p0 770.9 ± 0.1186
p1 -0.0004616 ± 4.818e-06
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

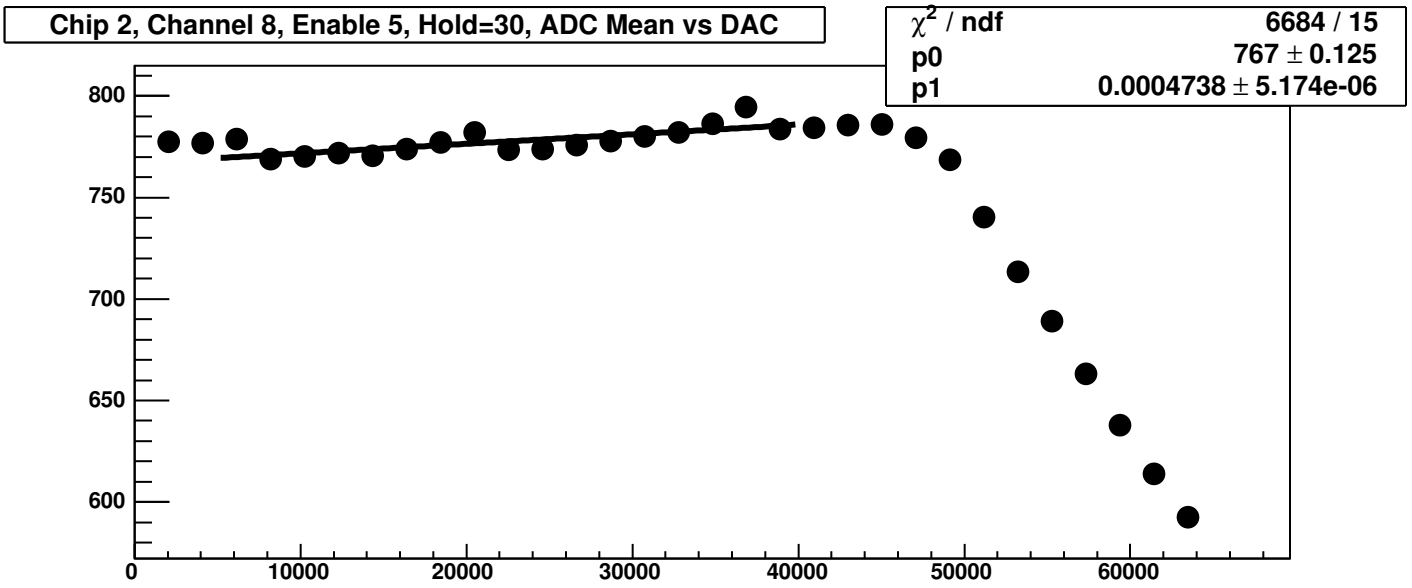

Chip 2, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

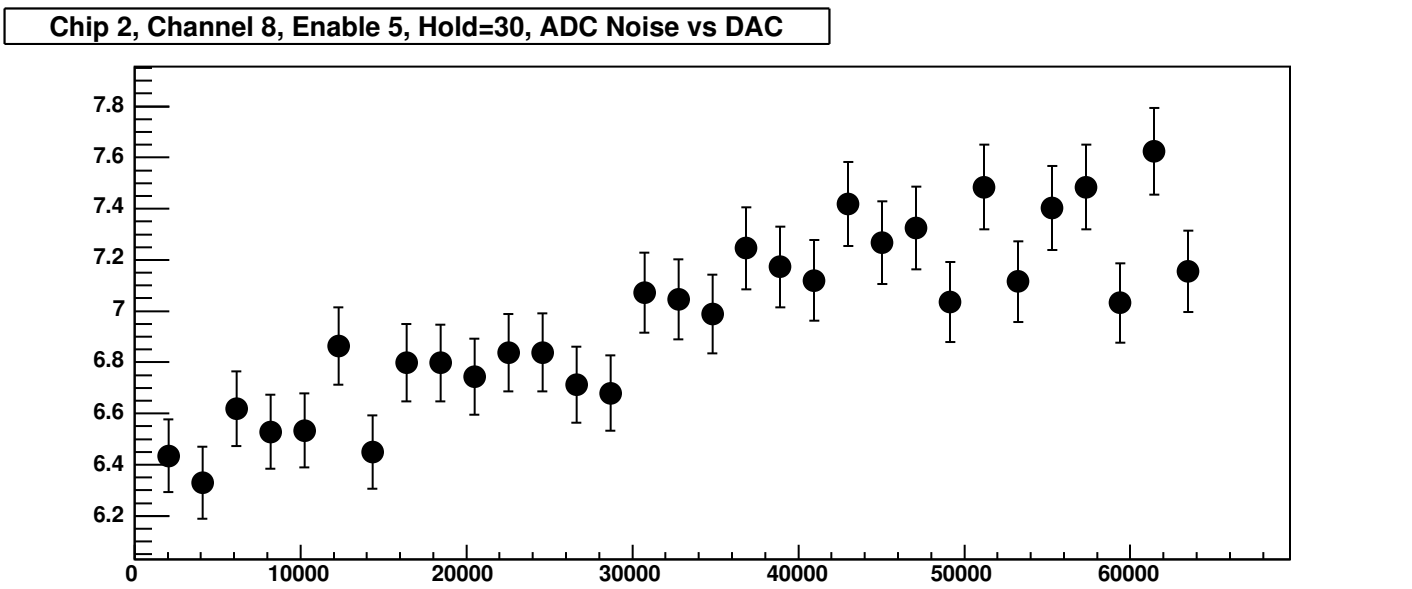

Chip 2, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

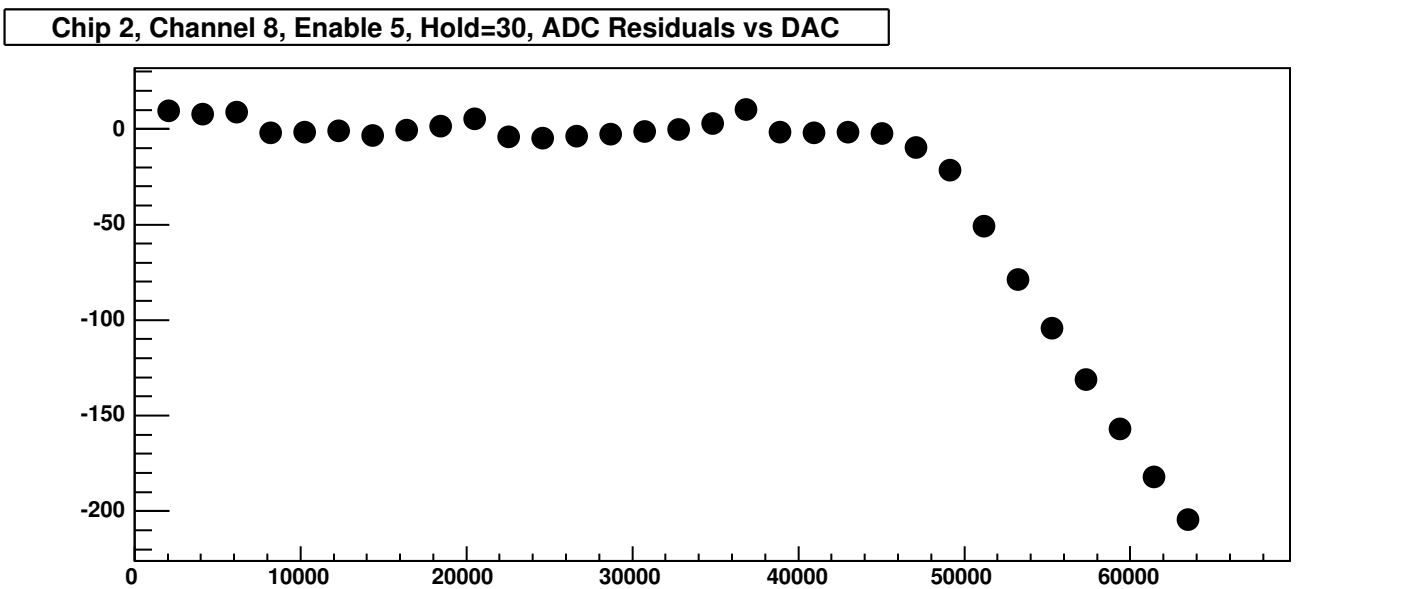

Chip 2, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6615 / 15
p0 771.1 ± 0.1177
p1 -0.0002575 ± 4.78e-06
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

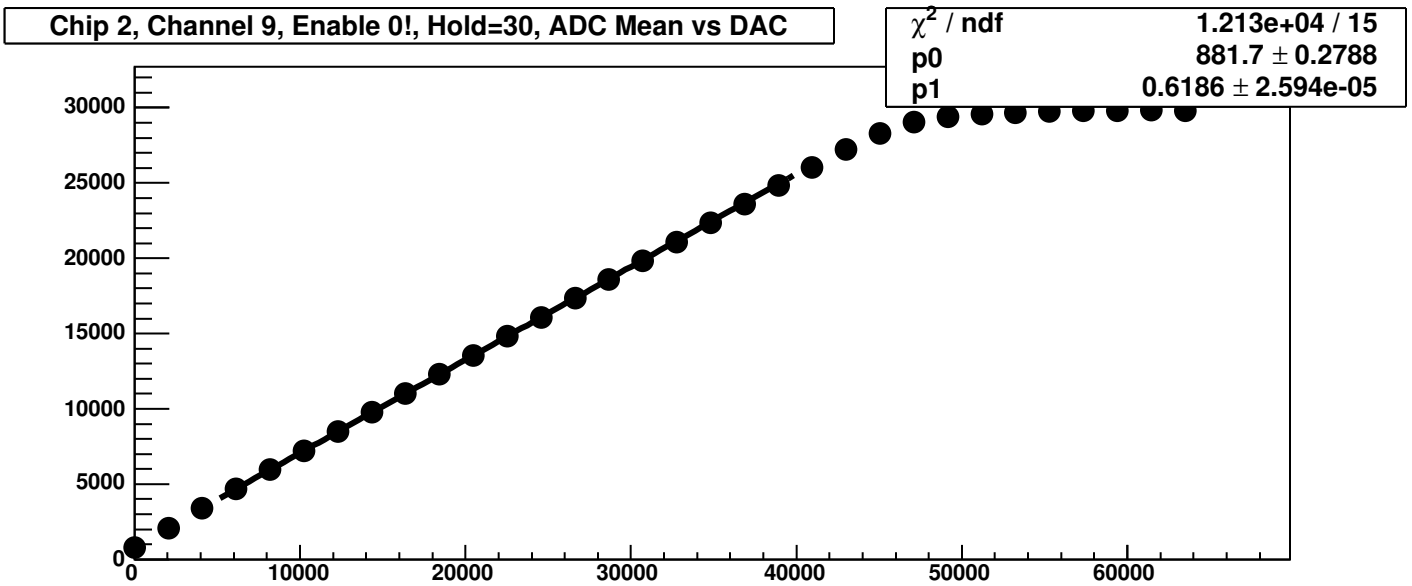

Chip 2, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

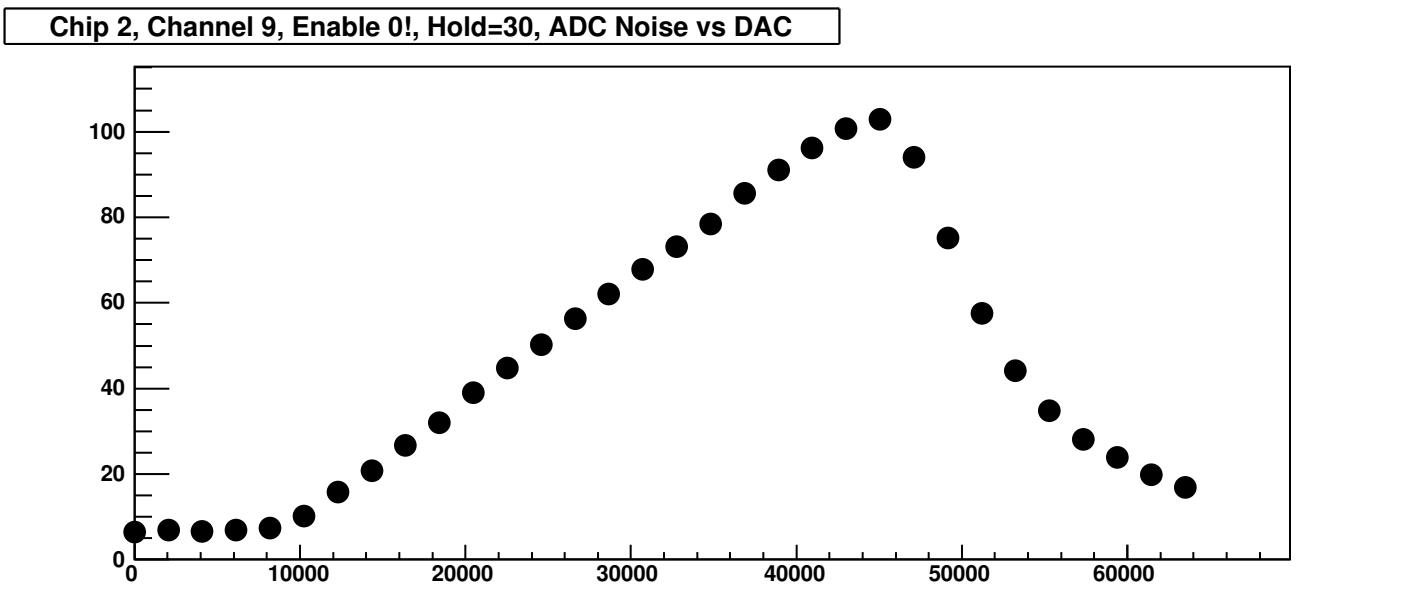

Chip 2, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

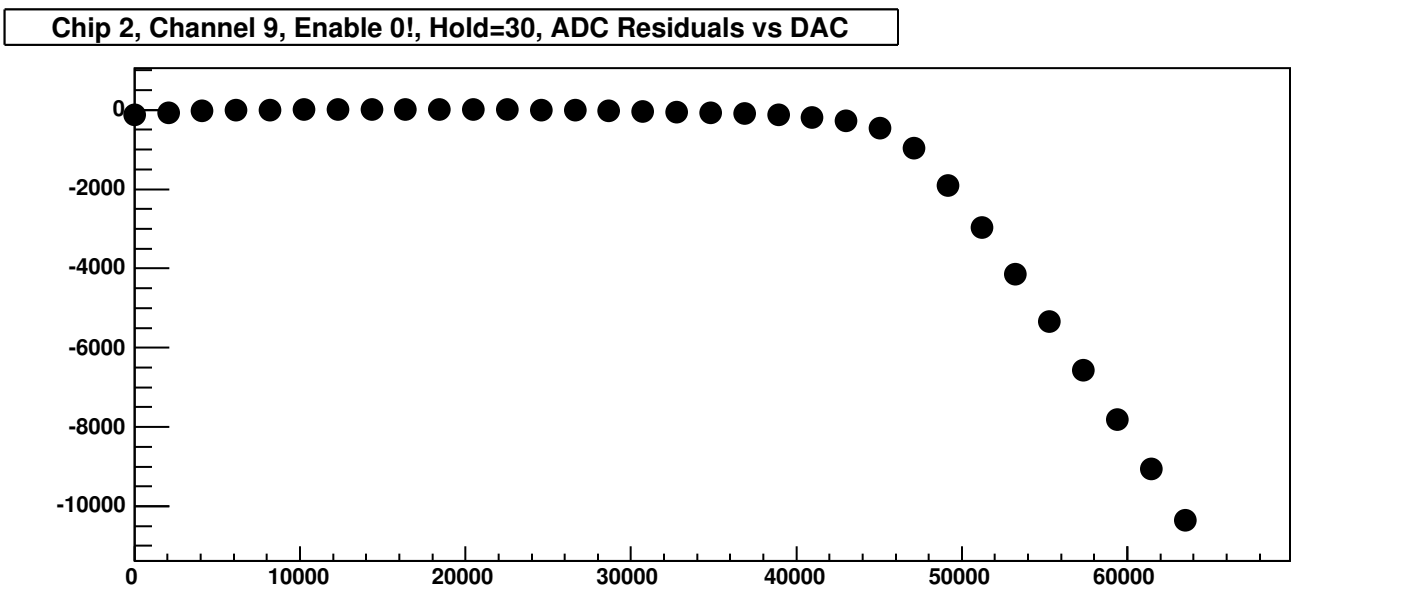

Chip 2, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6684 / 15
p0 767 ± 0.125
p1 0.0004738 ± 5.174e-06
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

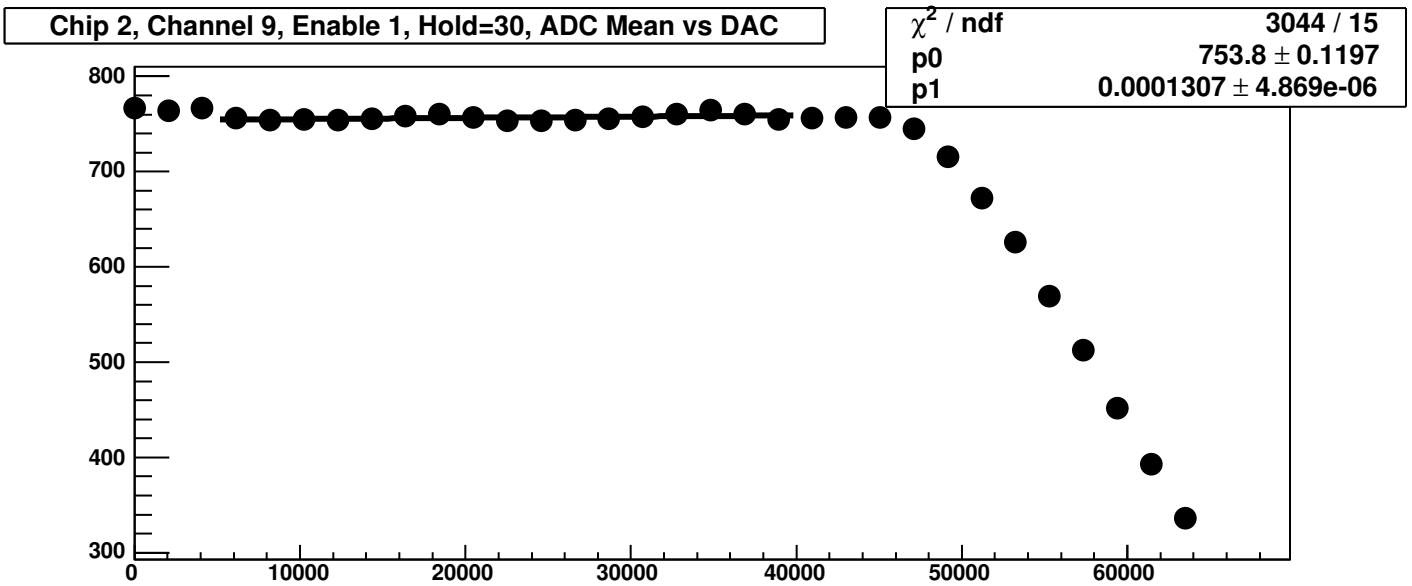

Chip 2, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

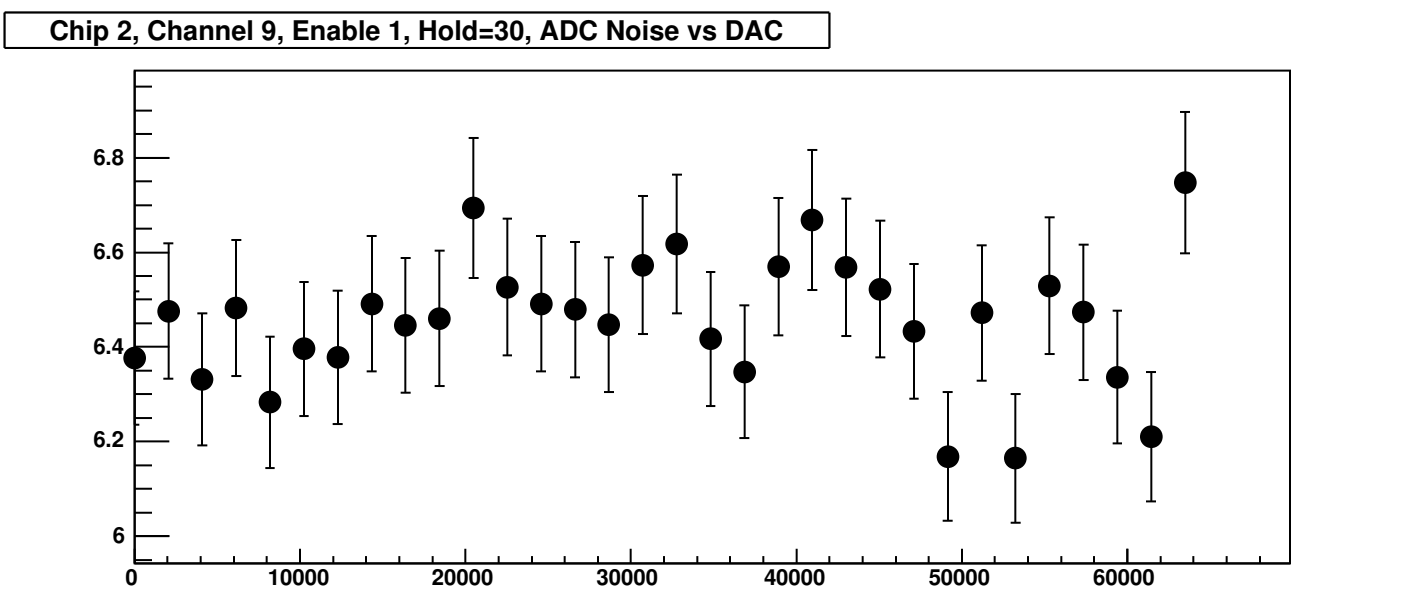

Chip 2, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

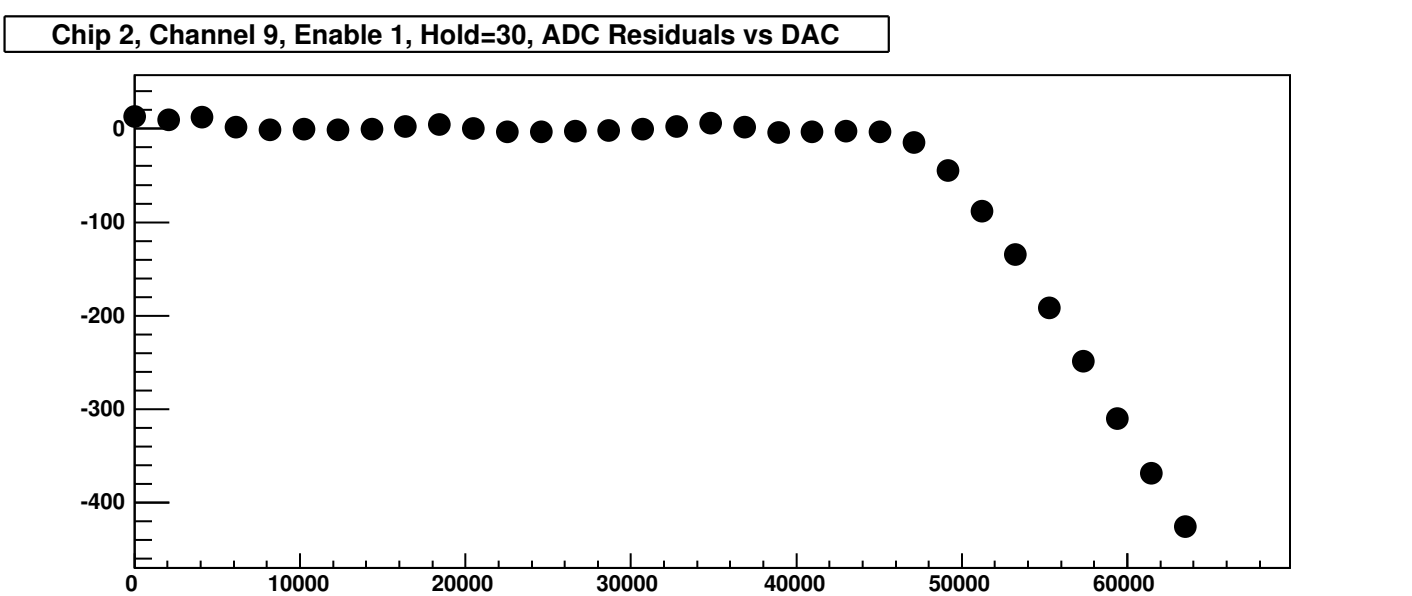

Chip 2, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.213e+04 / 15
p0 881.7 ± 0.2788
p1 0.6186 ± 2.594e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

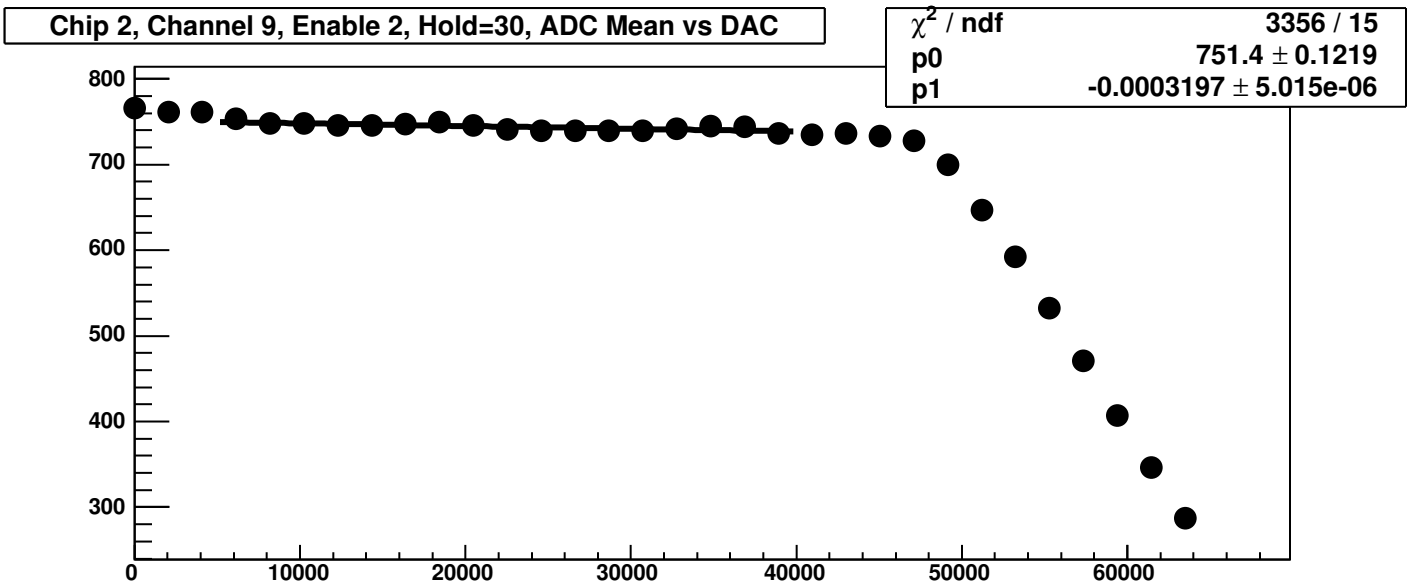

Chip 2, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

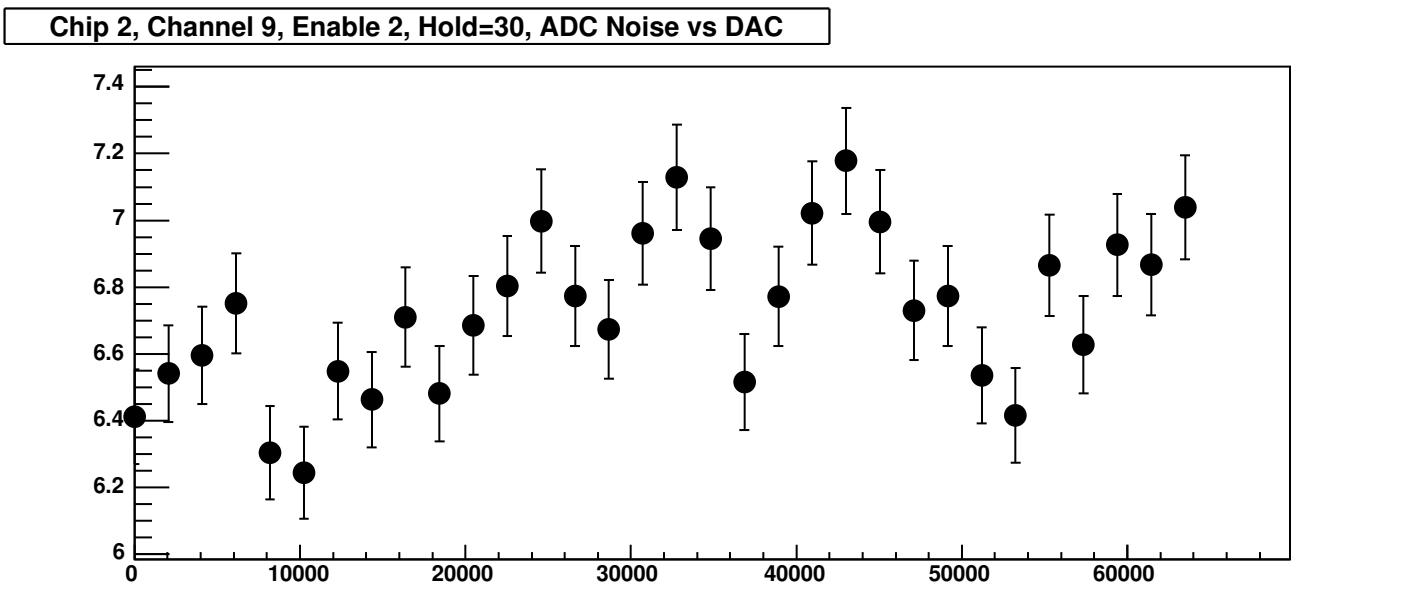

Chip 2, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

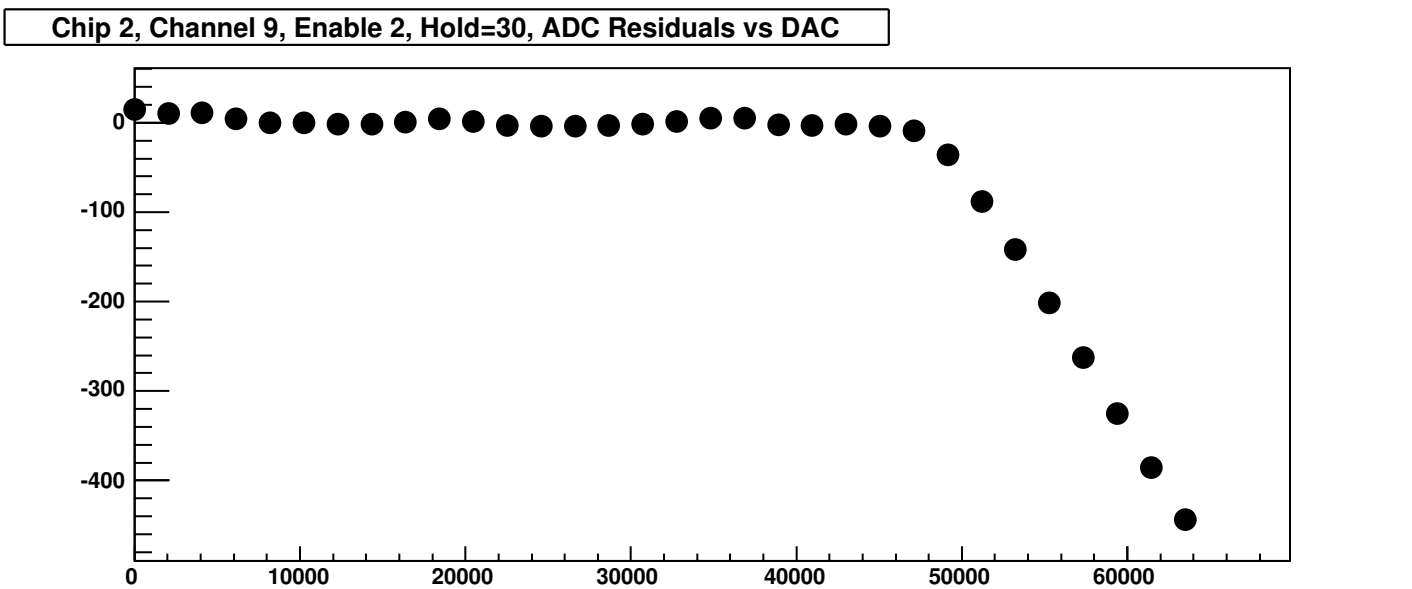

Chip 2, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3044 / 15
p0 753.8 ± 0.1197
p1 0.0001307 ± 4.869e-06
800
700
600
500
400
300
0
10000
20000
30000
40000
50000
60000

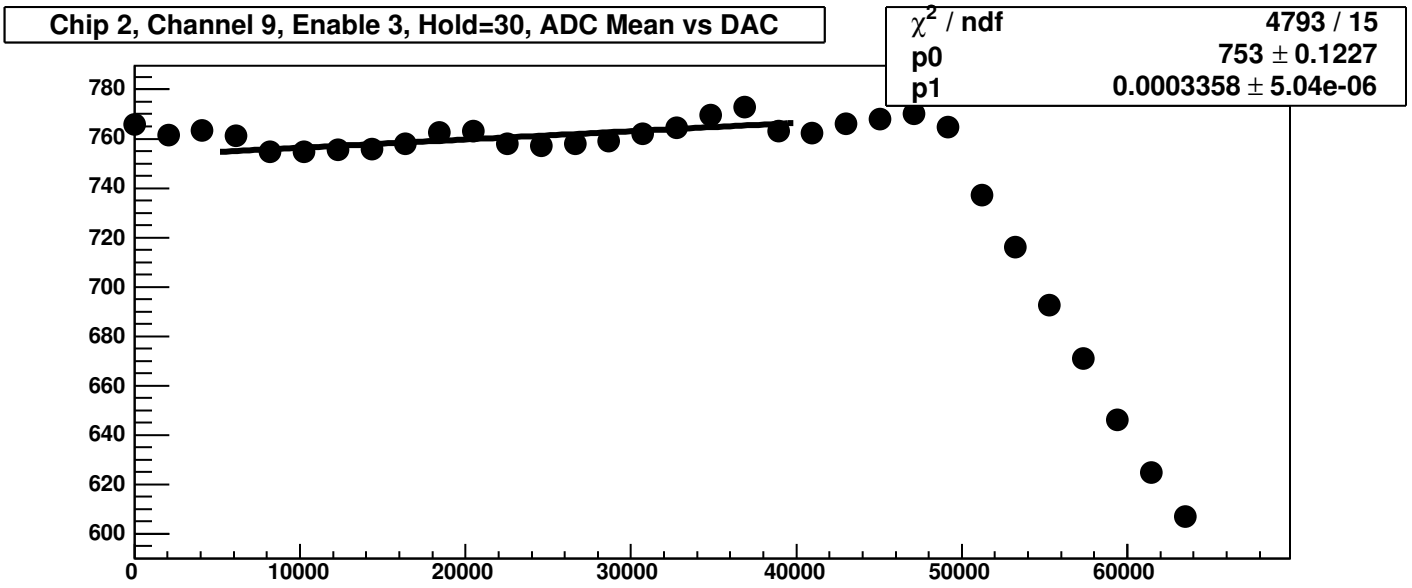

Chip 2, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

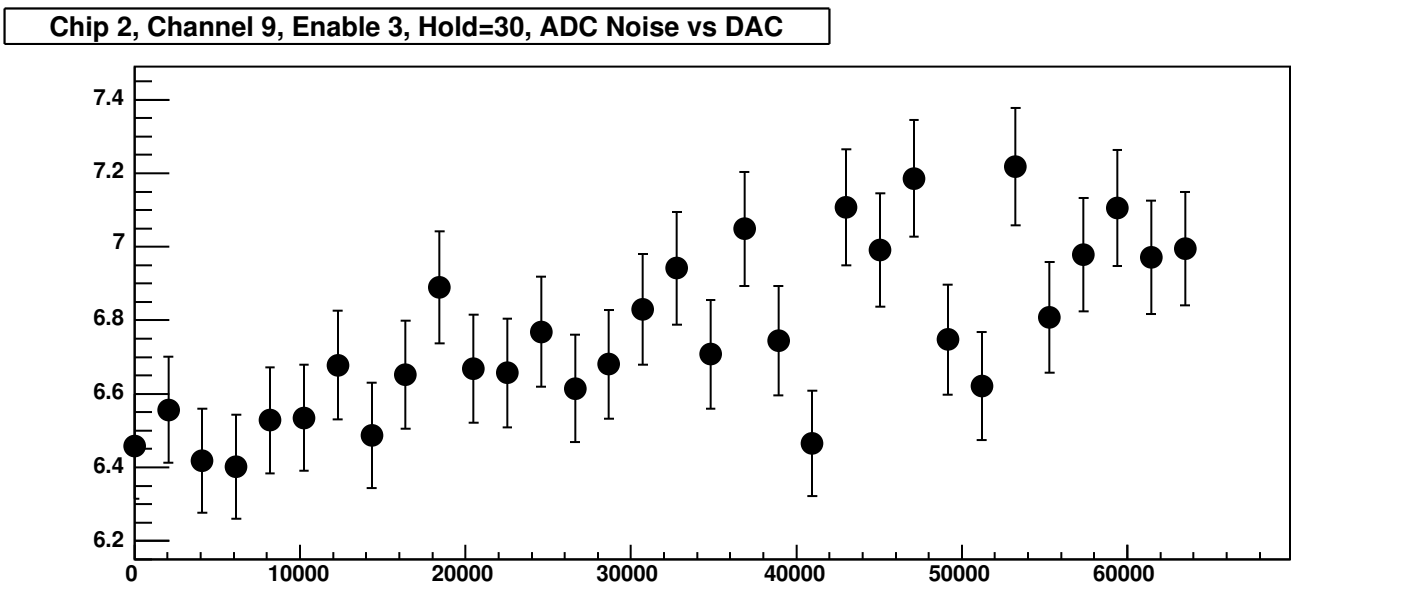

Chip 2, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

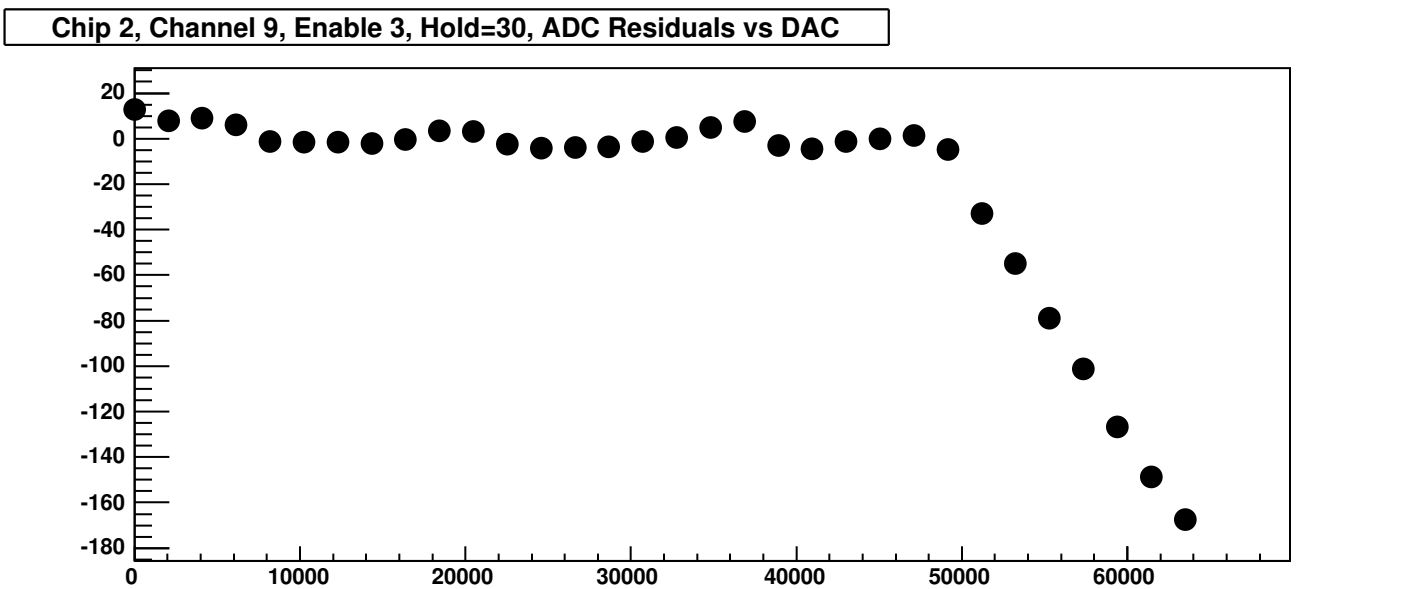

Chip 2, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3356 / 15
p0 751.4 ± 0.1219
p1 -0.0003197 ± 5.015e-06
800
700
600
500
400
300
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

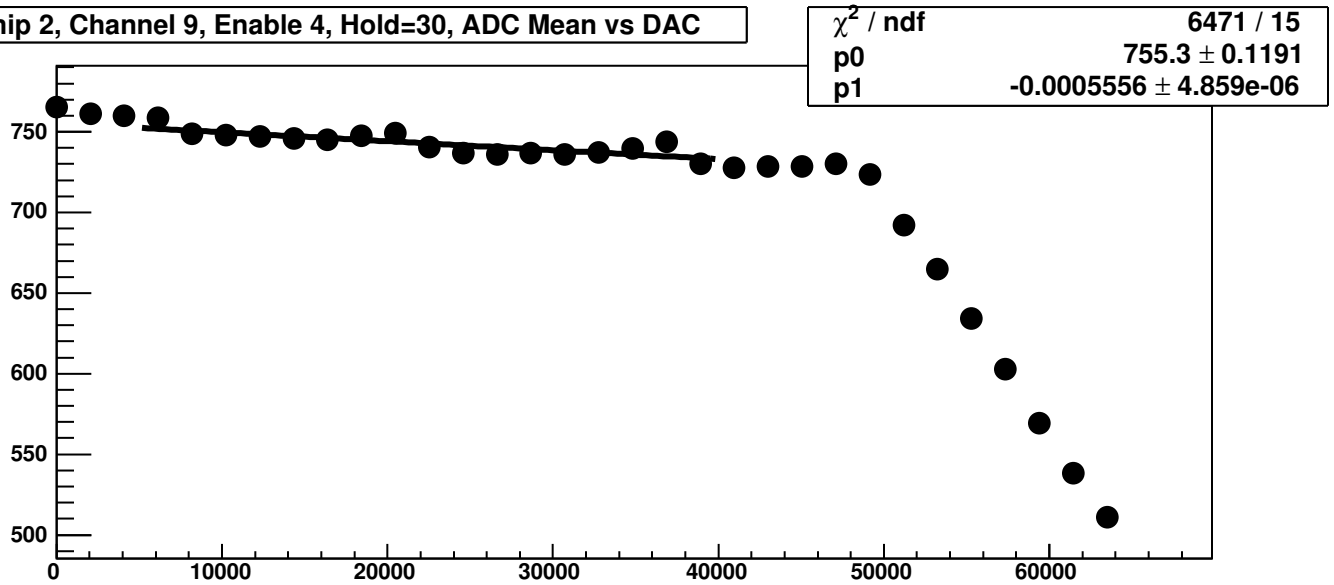

Chip 2, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4793 / 15
p0 753 ± 0.1227
p1 0.0003358 ± 5.04e-06
780
760
740
720
700
680
660
640
620
600
0
10000
20000
30000
40000
50000
60000

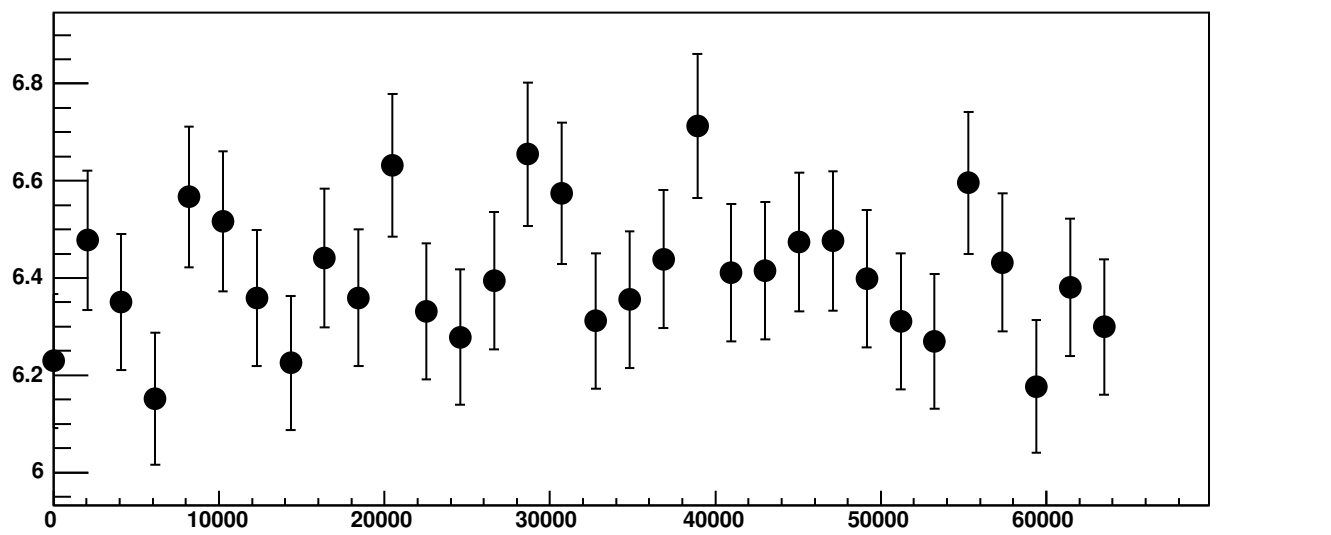

Chip 2, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

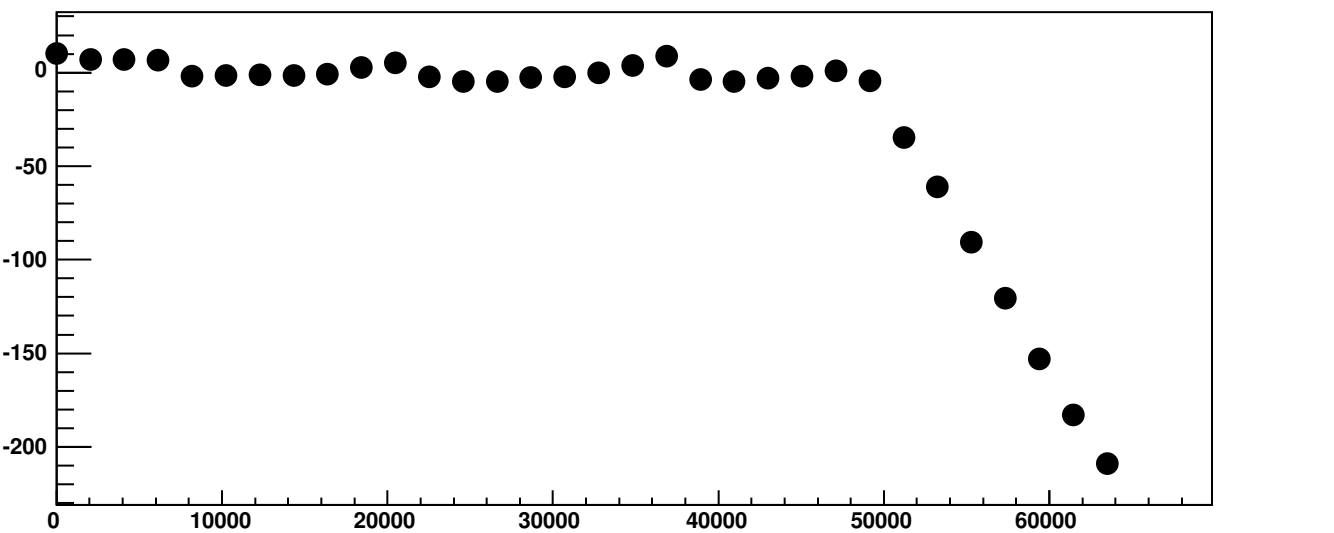
Chip 2, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC



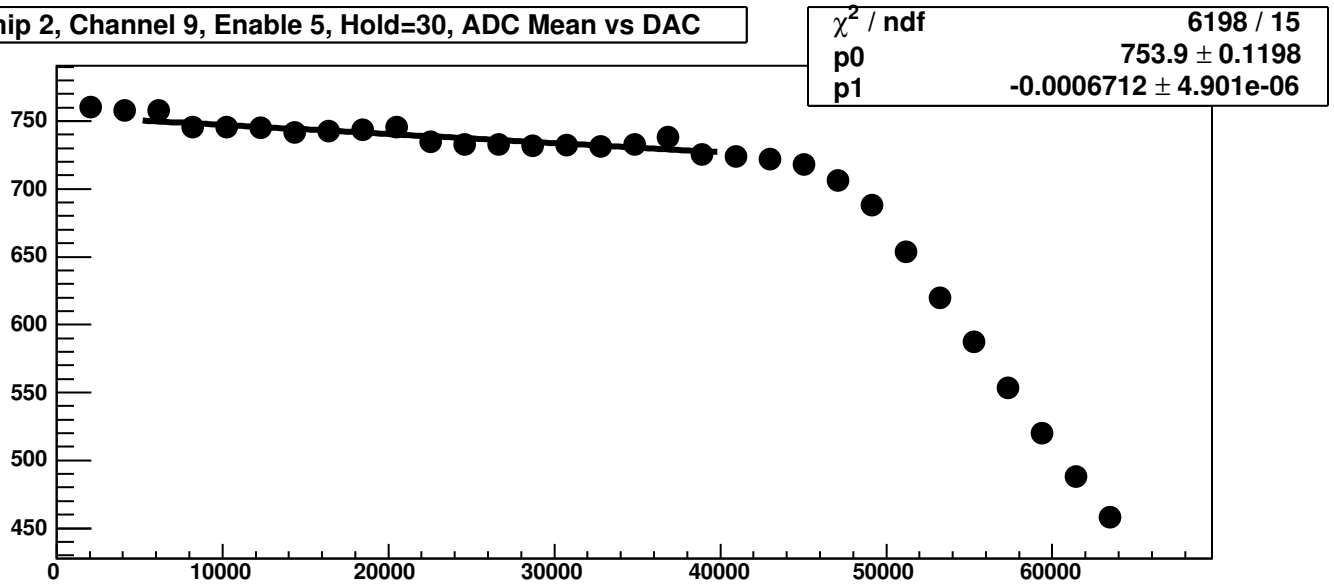
Chip 2, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



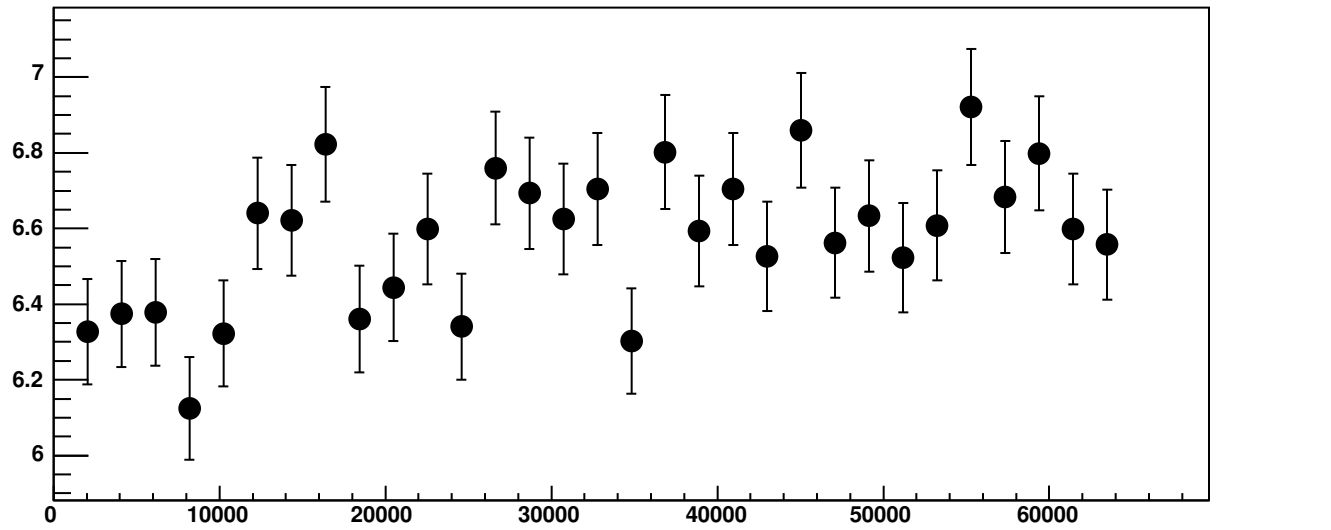
Chip 2, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



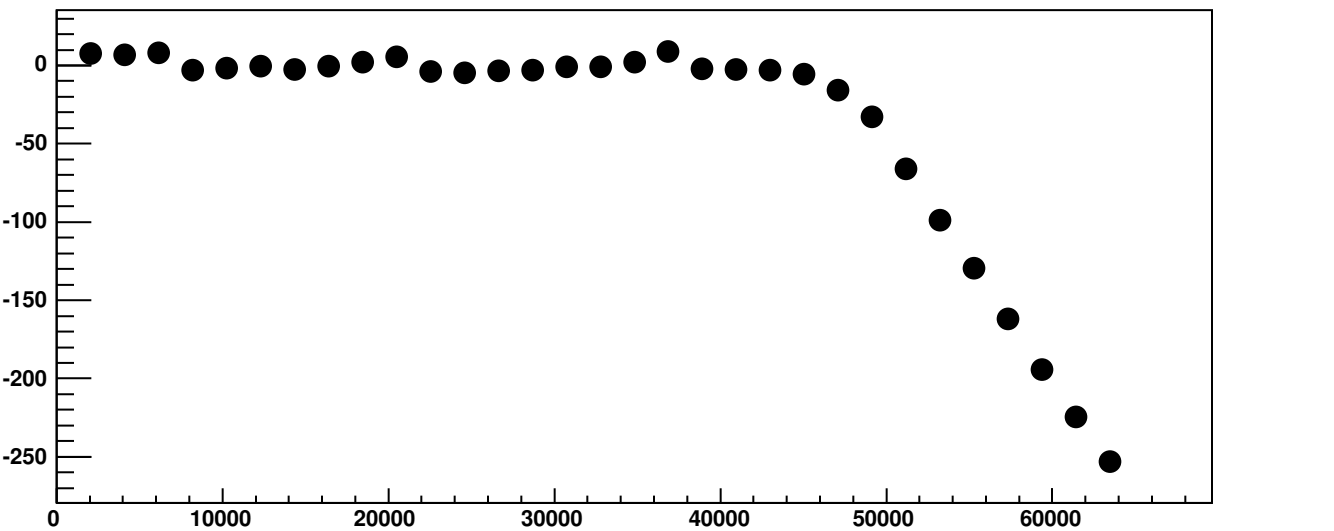
Chip 2, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC

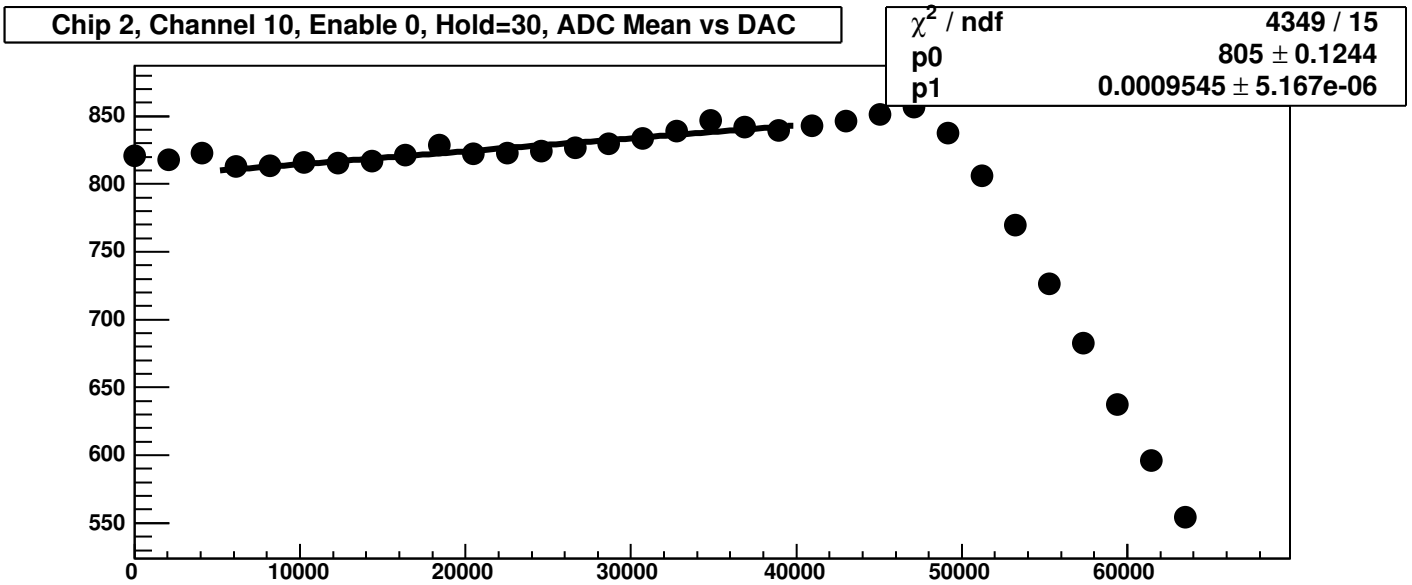


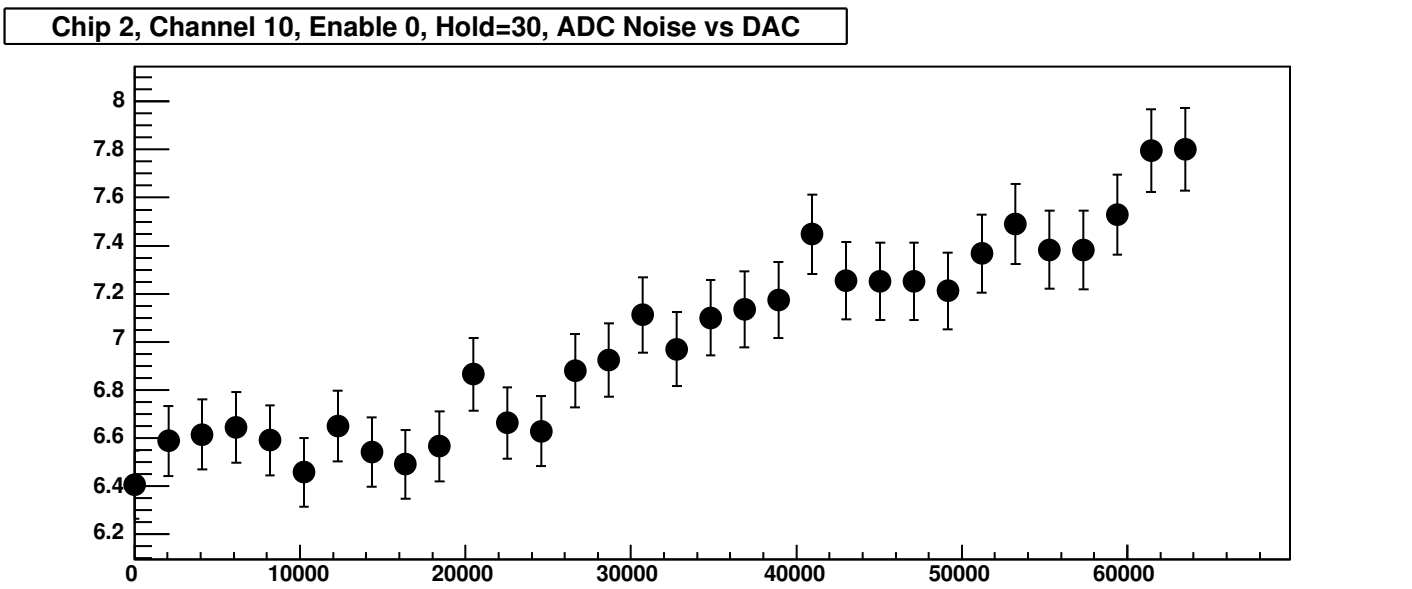
Chip 2, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC

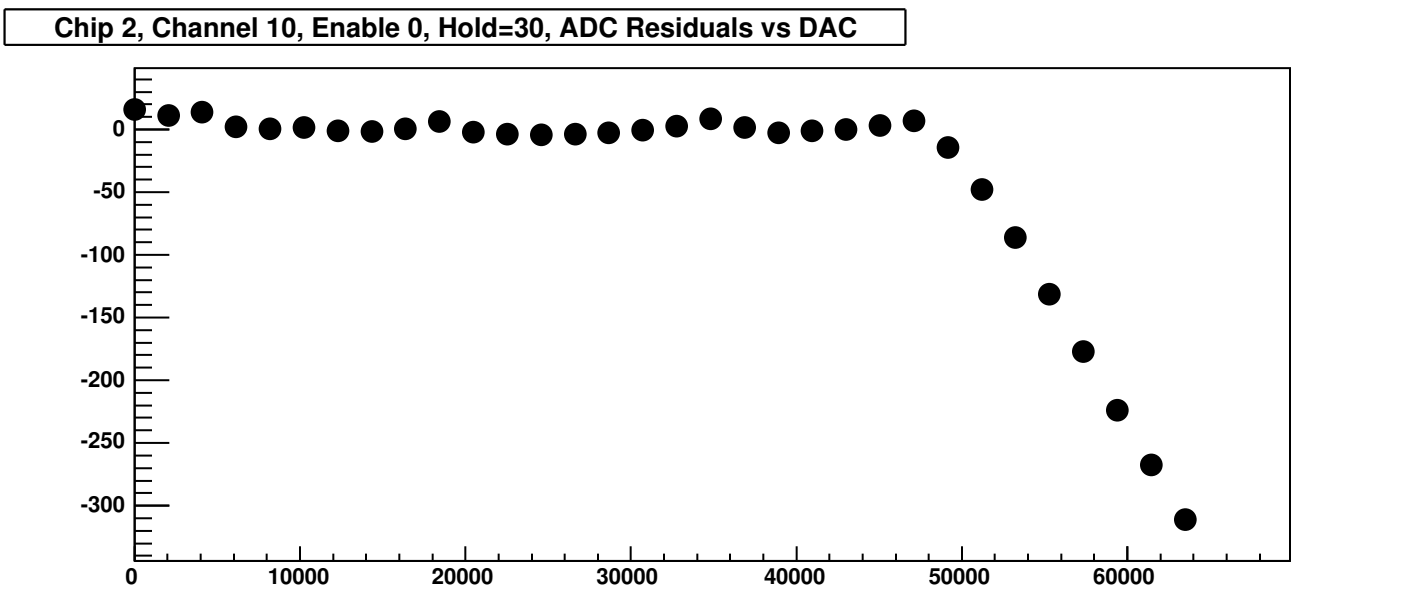


Chip 2, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

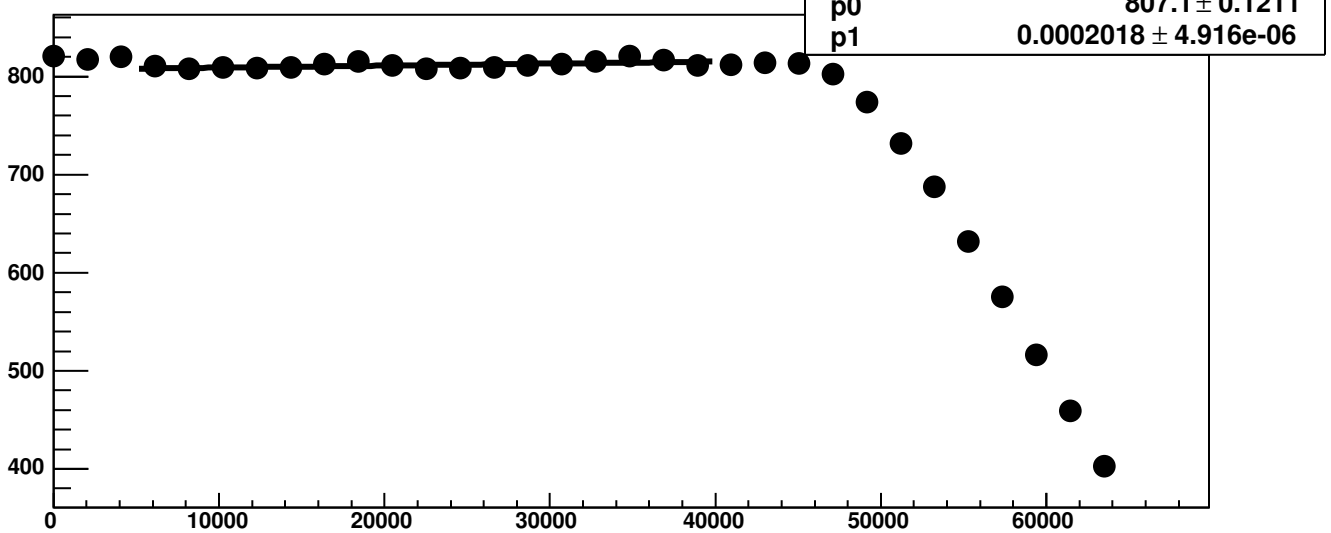



Chip 2, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4349 / 15
p0 805 ± 0.1244
p1 0.0009545 ± 5.167e-06
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

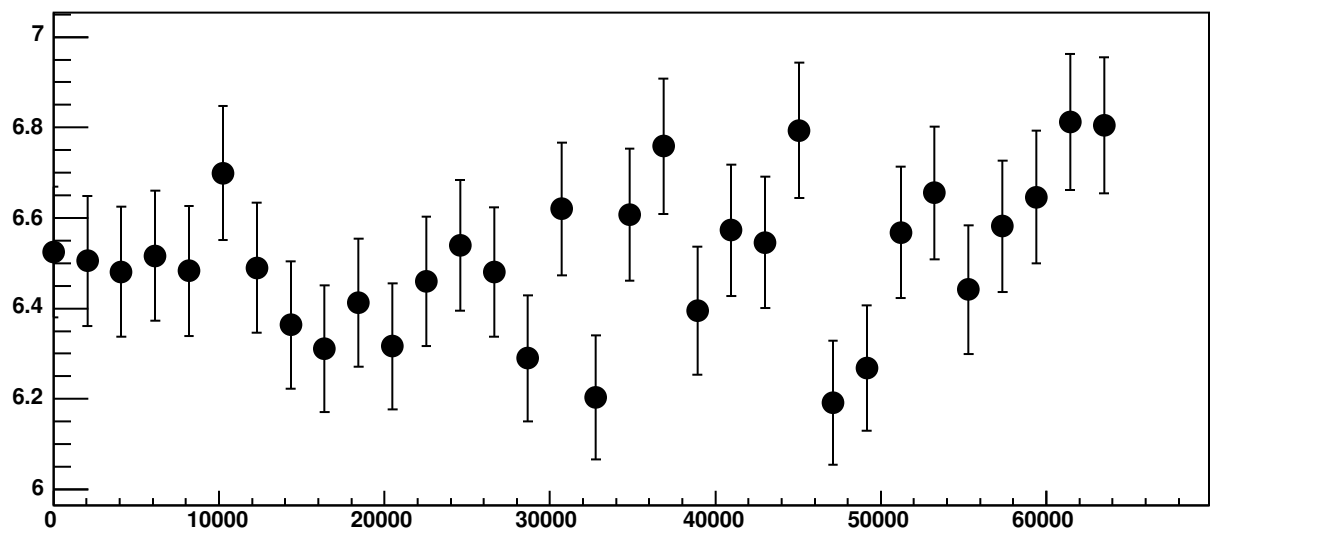

Chip 2, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
8
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

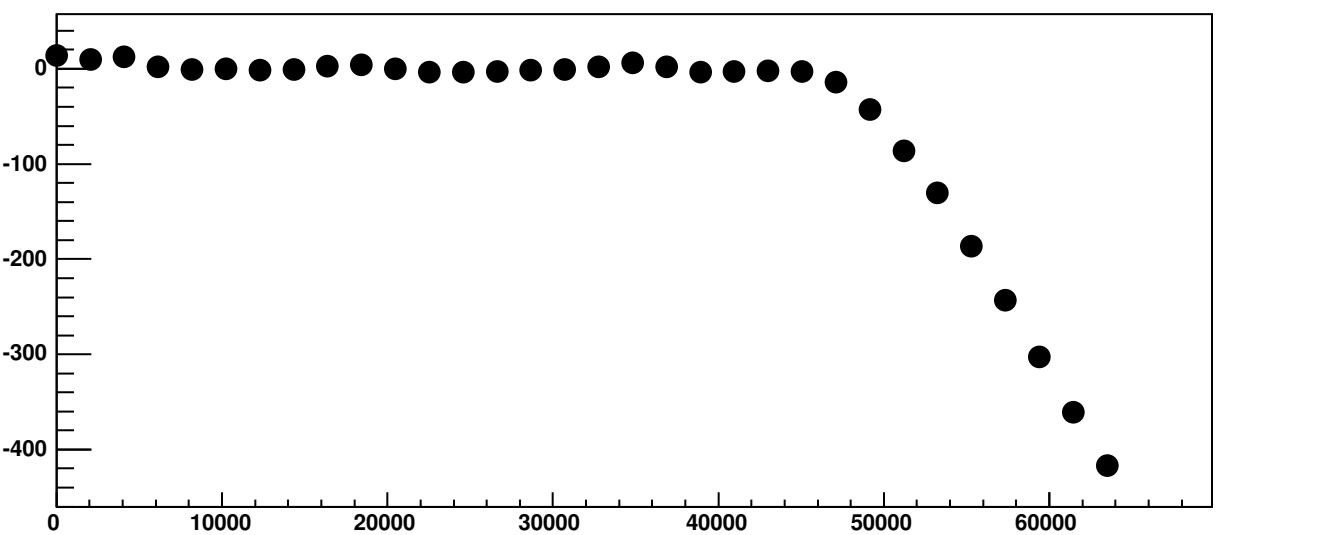
Chip 2, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

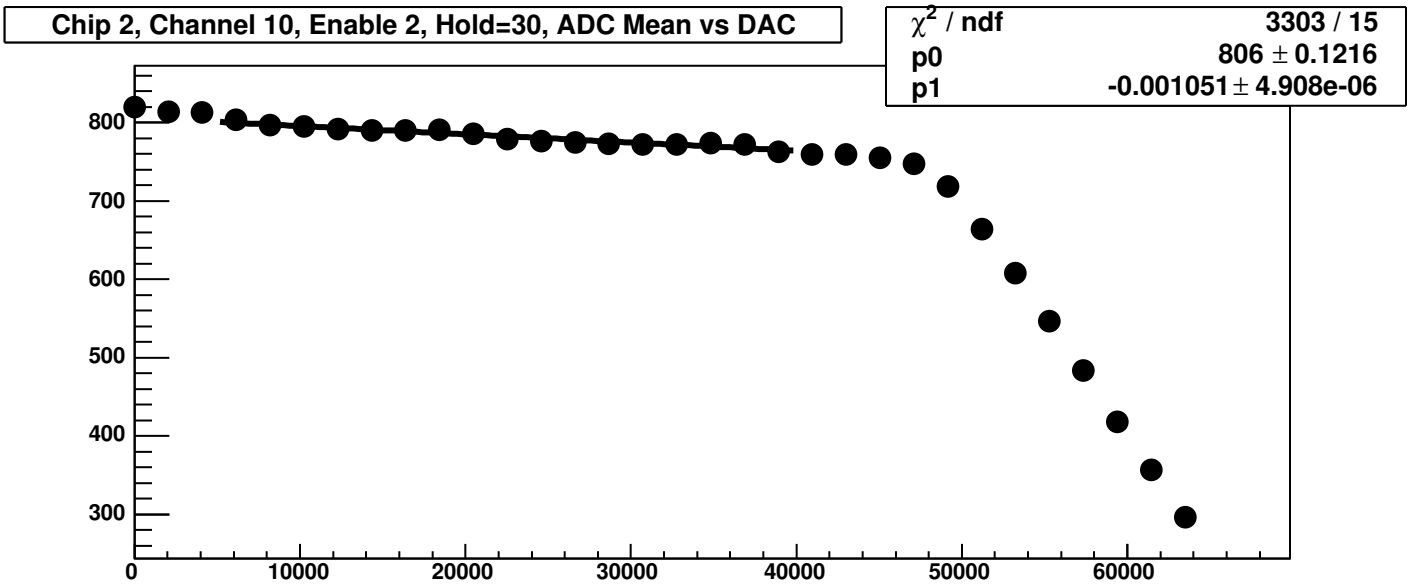


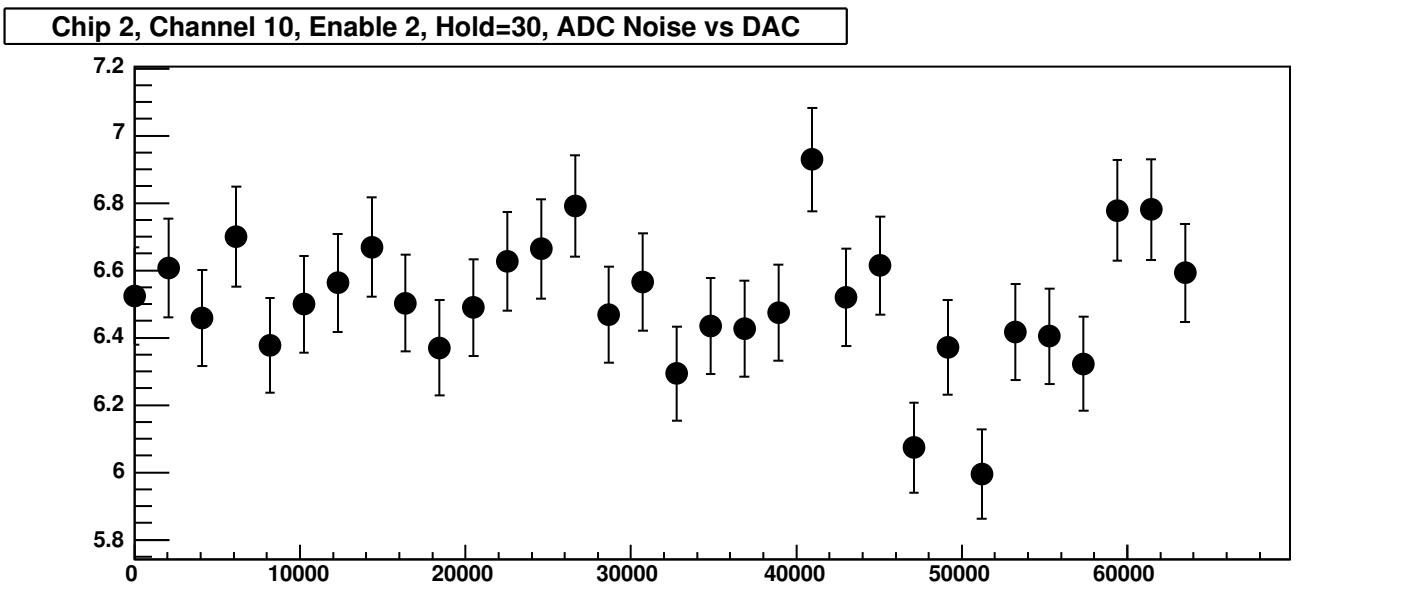
Chip 2, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC

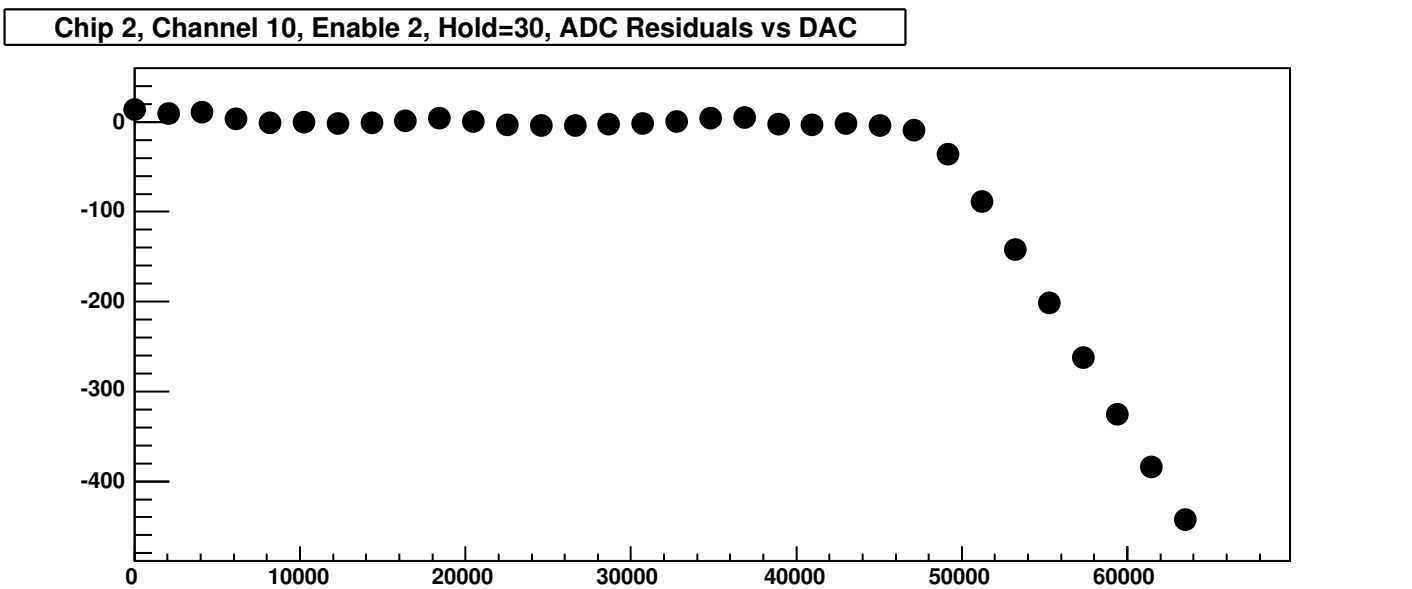


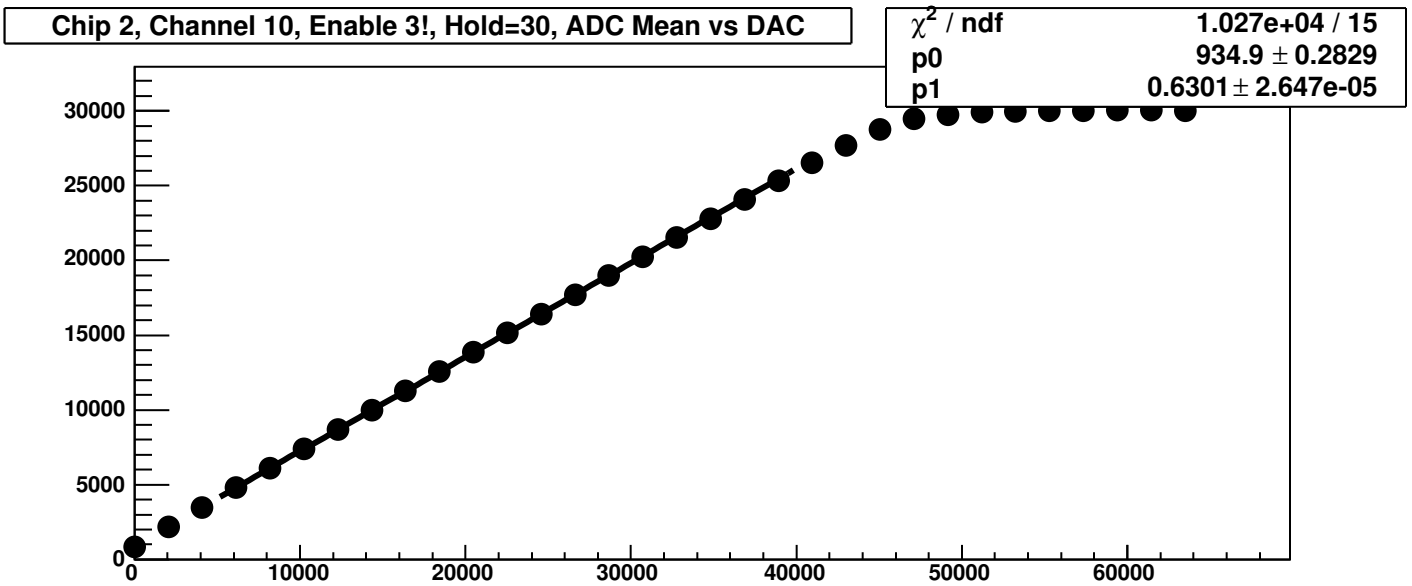
Chip 2, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC

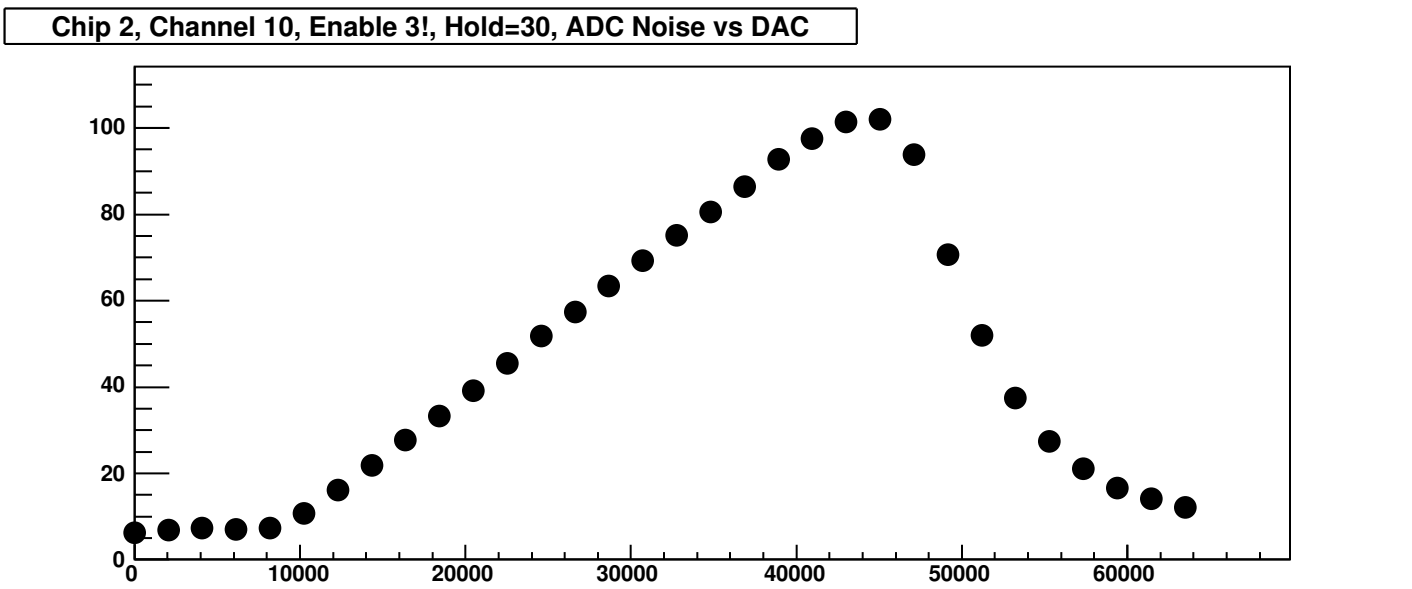


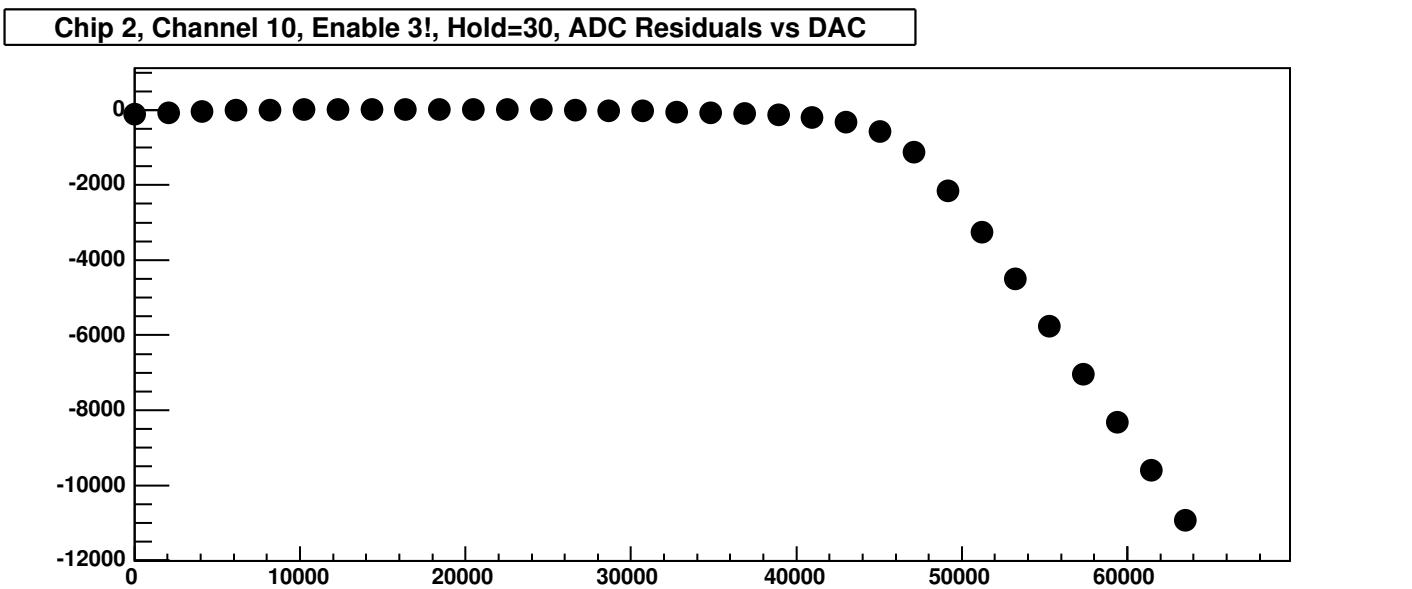

Chip 2, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3303 / 15
p0 806 ± 0.1216
p1 -0.001051 ± 4.908e-06
800
700
600
500
400
300
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

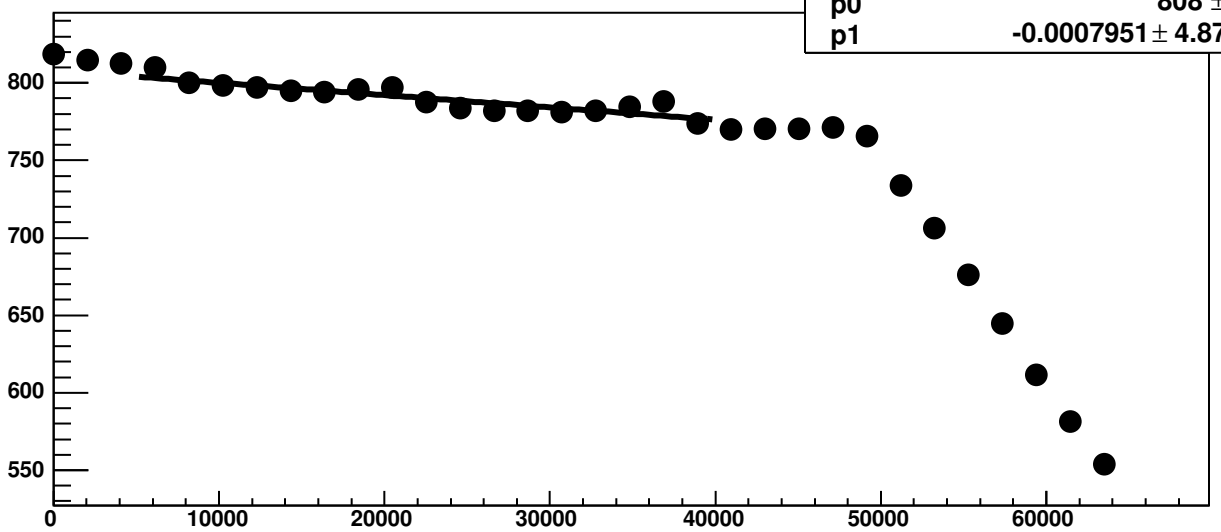

Chip 2, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.027e+04 / 15
p0 934.9 ± 0.2829
p1 0.6301 ± 2.647e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

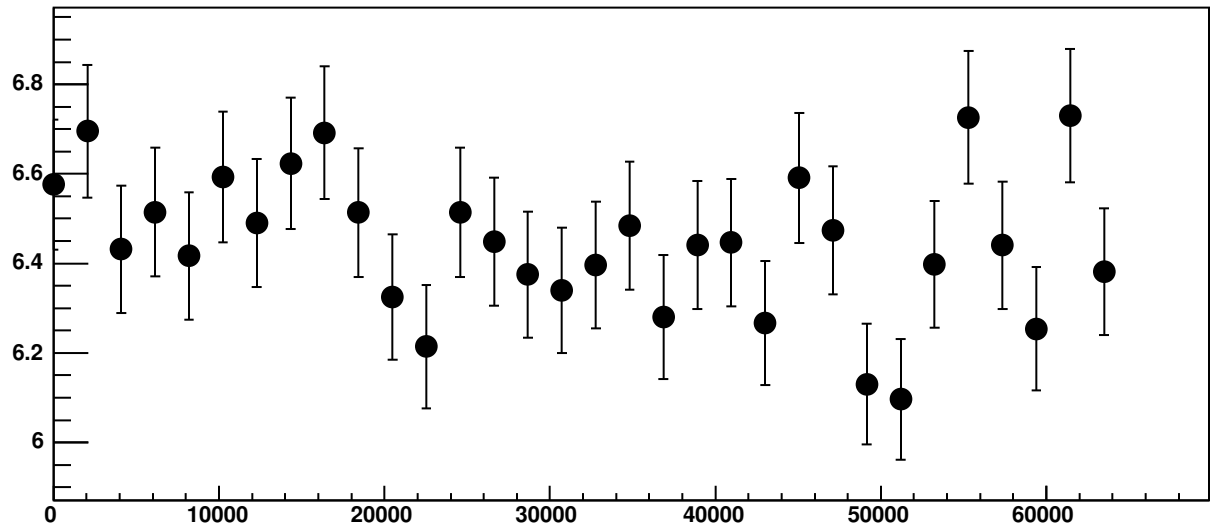

Chip 2, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

Chip 2, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

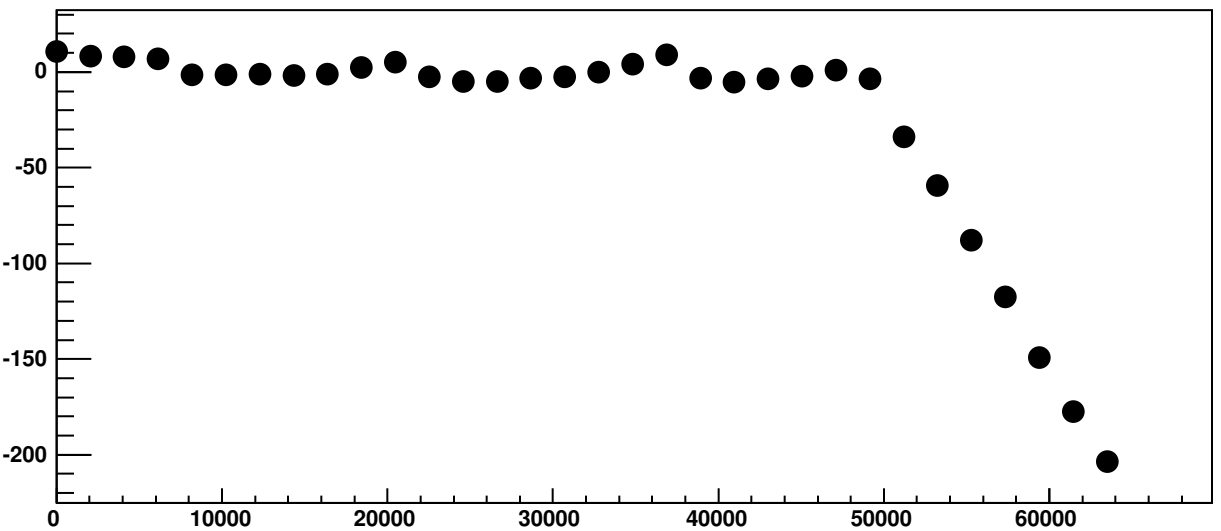
χ^2 / ndf	6907 / 15
p0	808 ± 0.121
p1	-0.0007951 ± 4.876e-06



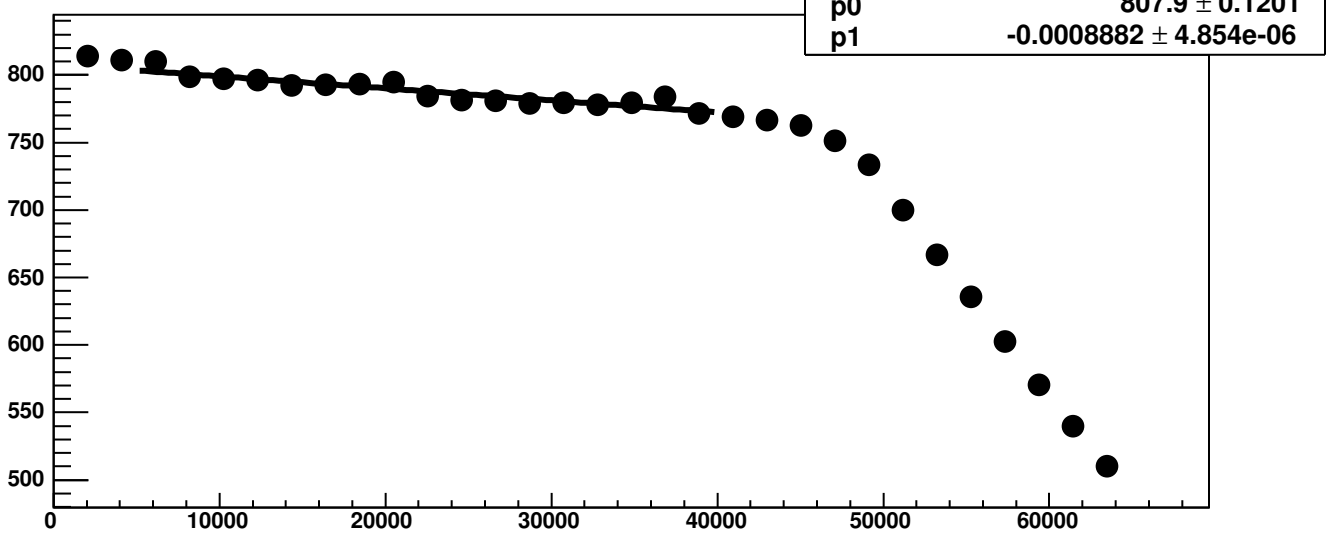
Chip 2, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



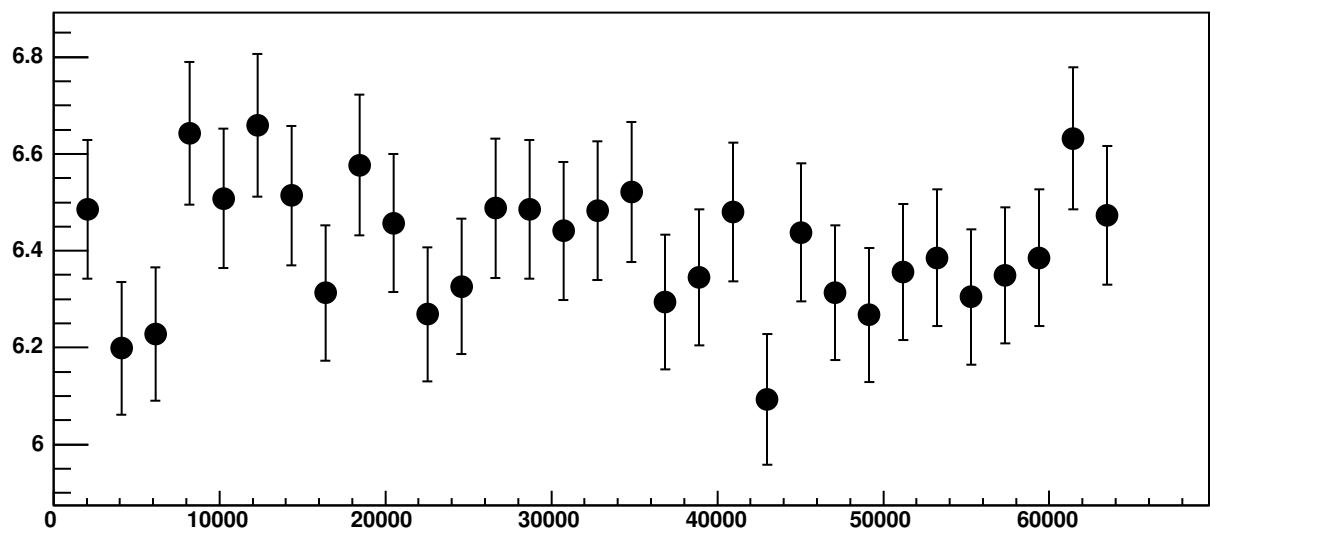
Chip 2, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



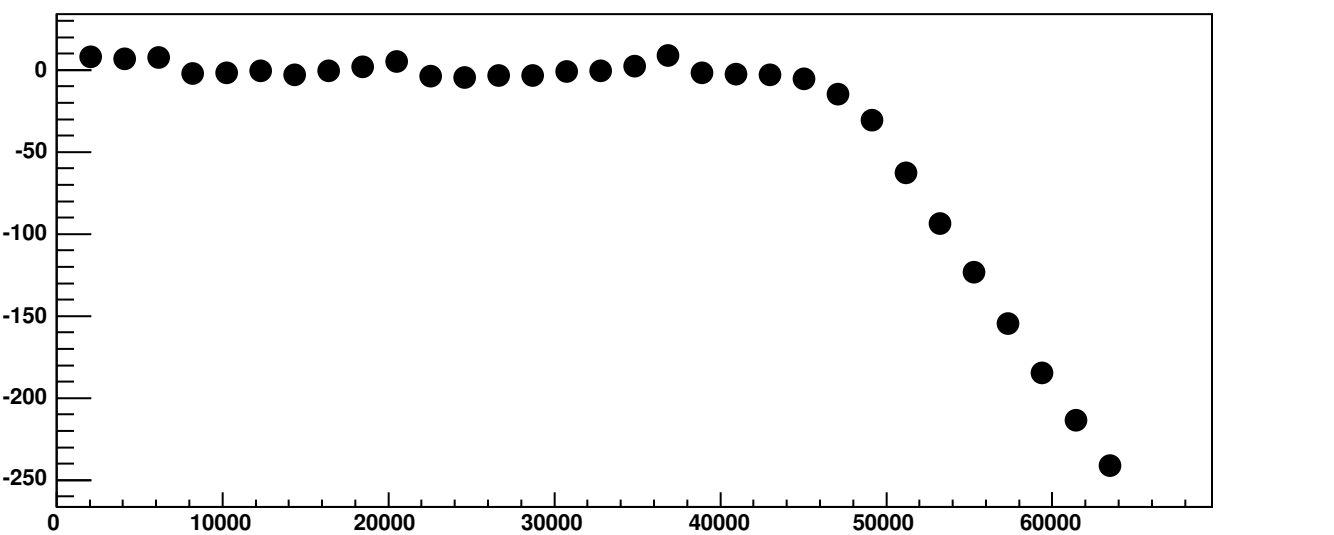
Chip 2, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC

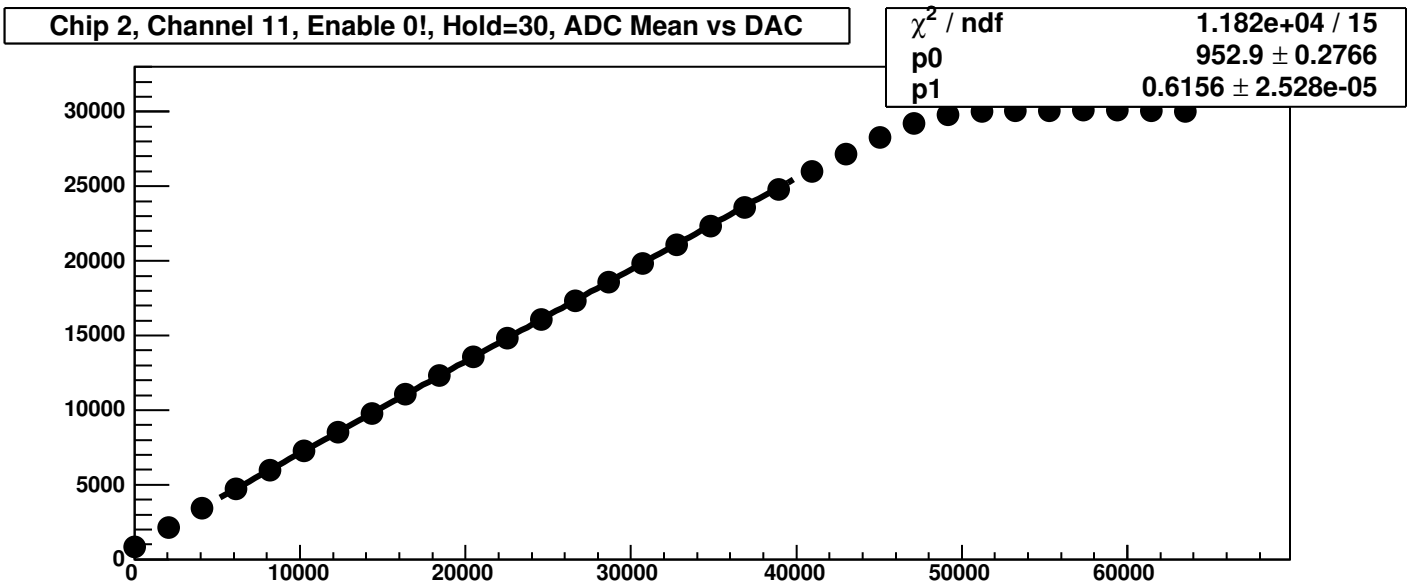


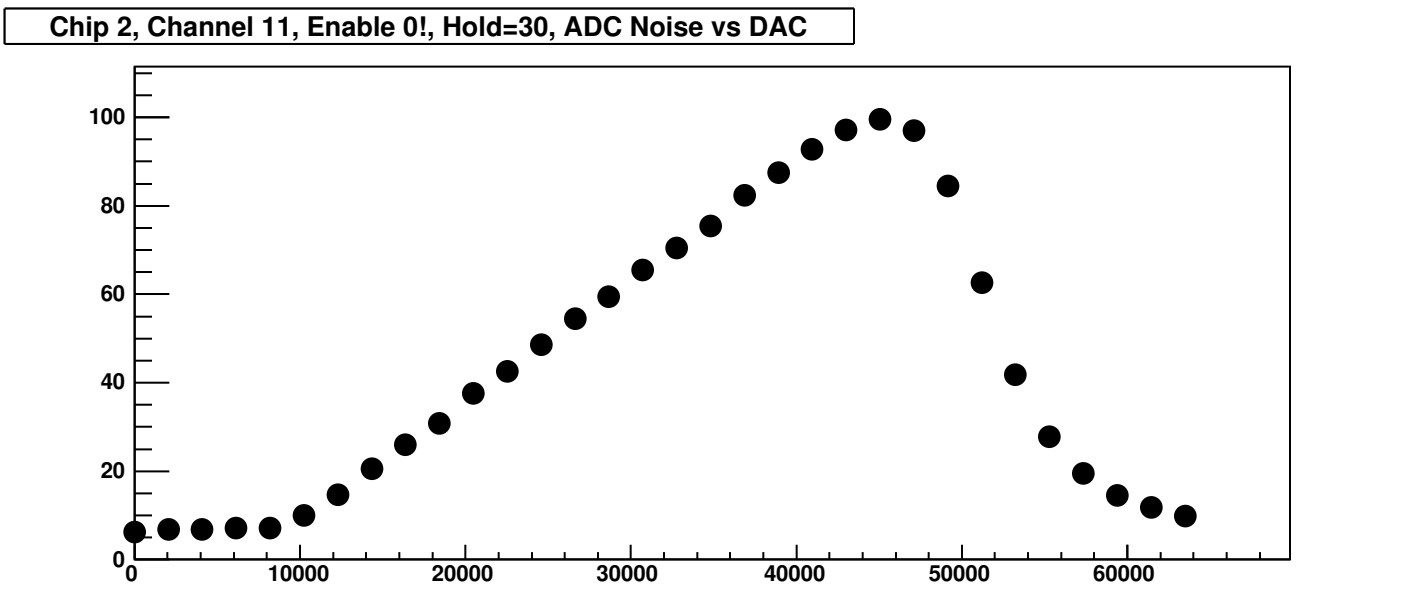
Chip 2, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC

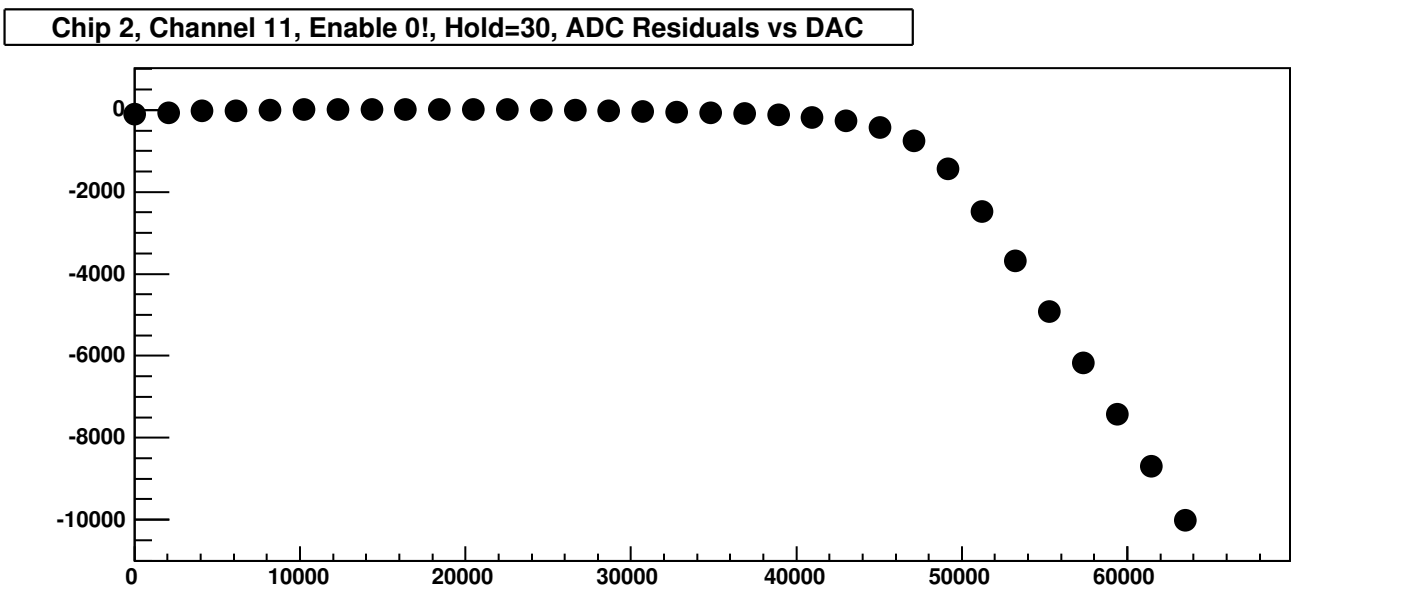


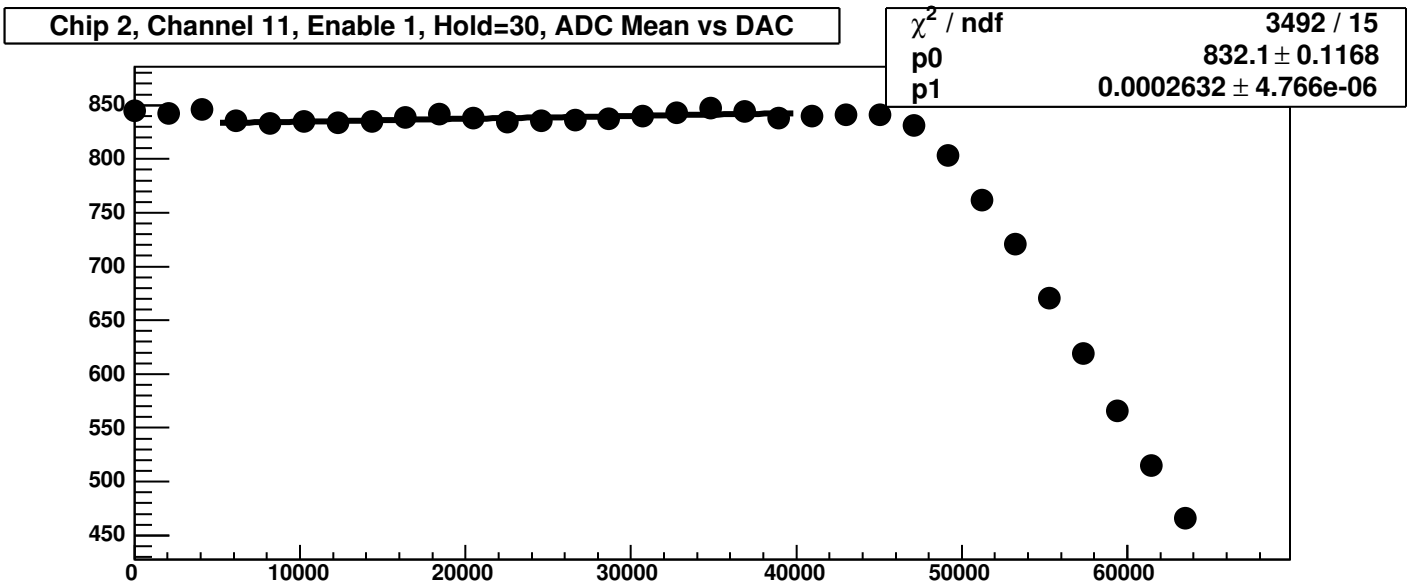
Chip 2, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC

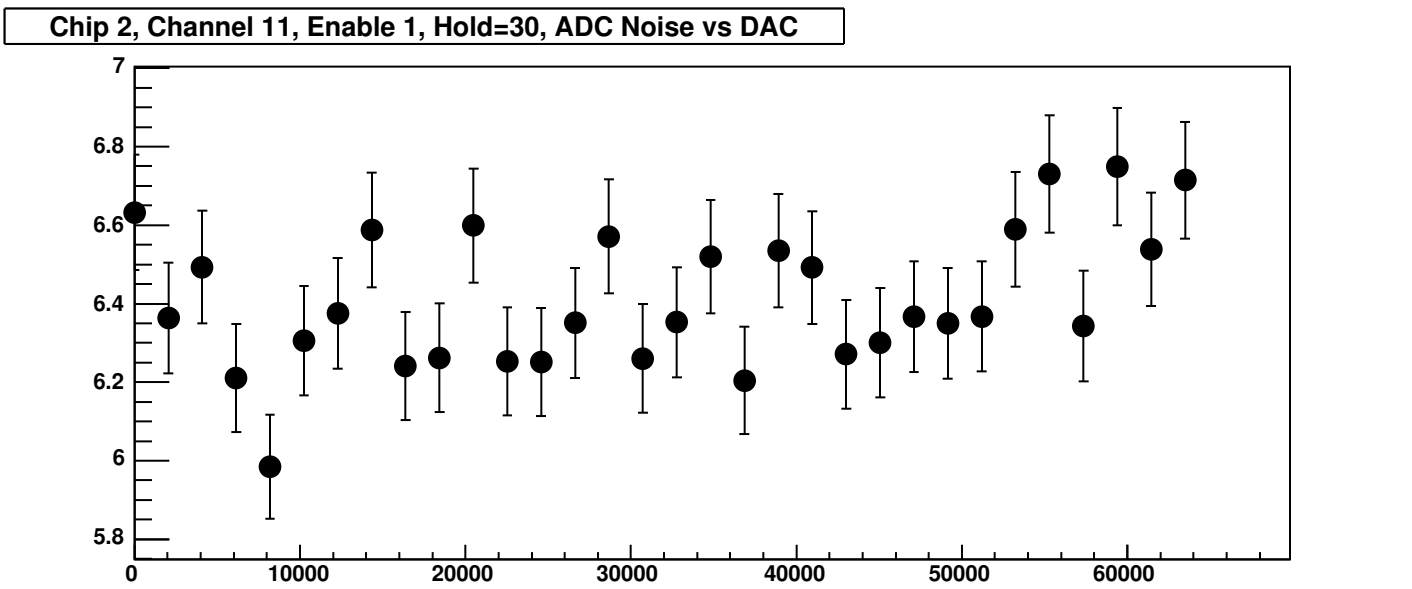


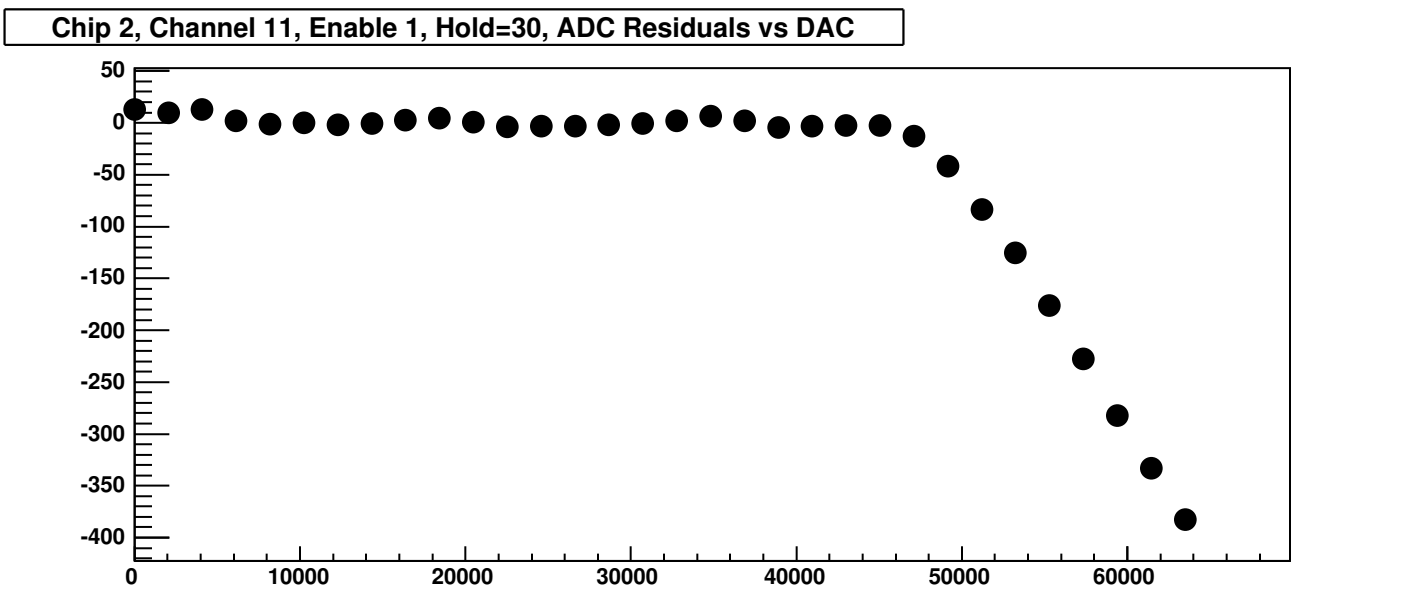

Chip 2, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.182e+04 / 15
p0 952.9 ± 0.2766
p1 0.6156 ± 2.528e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

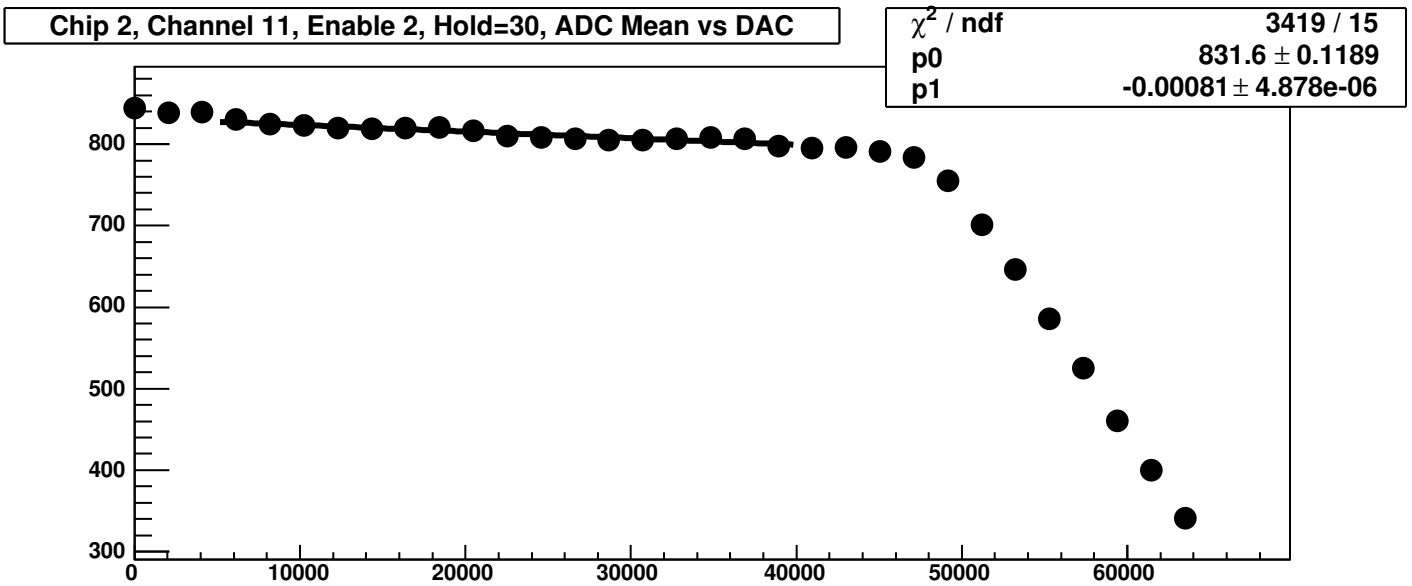

Chip 2, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

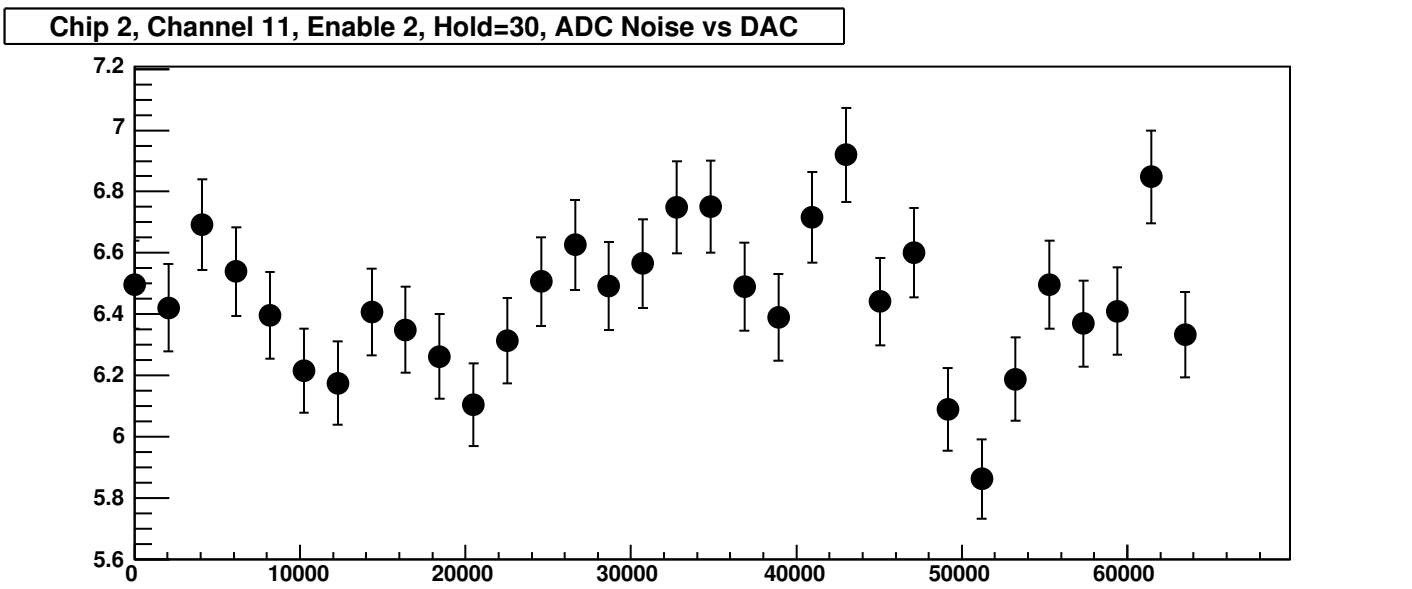

Chip 2, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

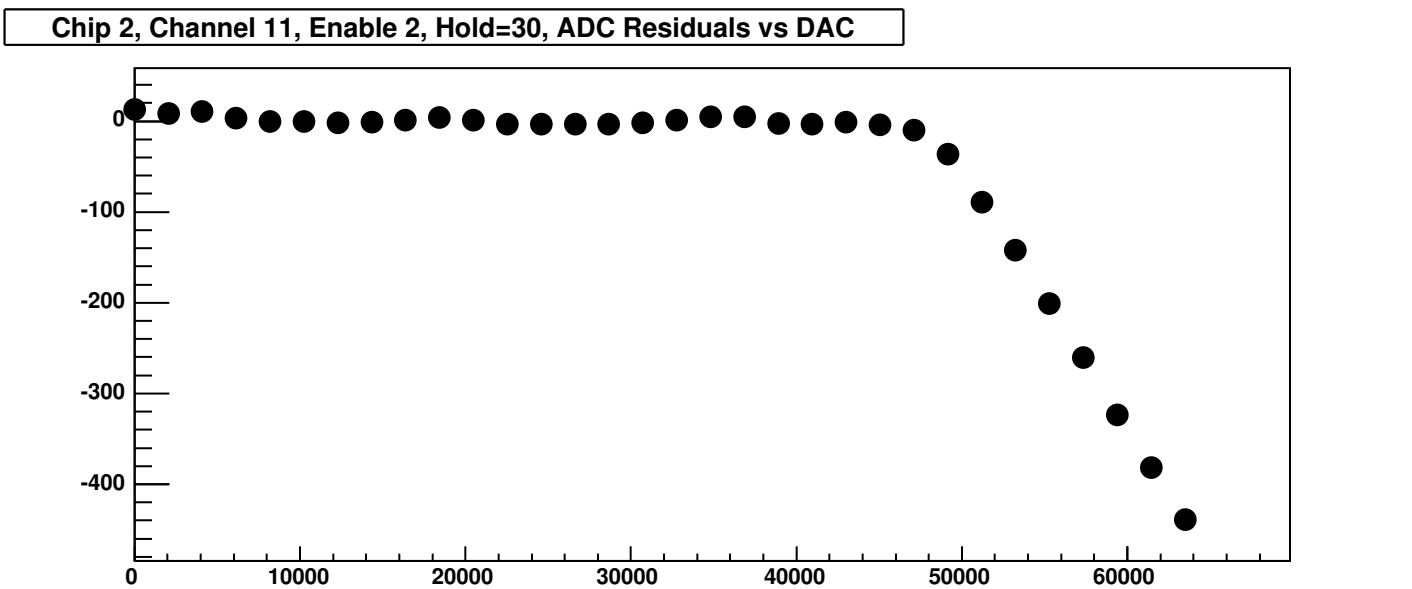

Chip 2, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3492 / 15
p0 832.1 ± 0.1168
p1 0.0002632 ± 4.766e-06
850
800
750
700
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000

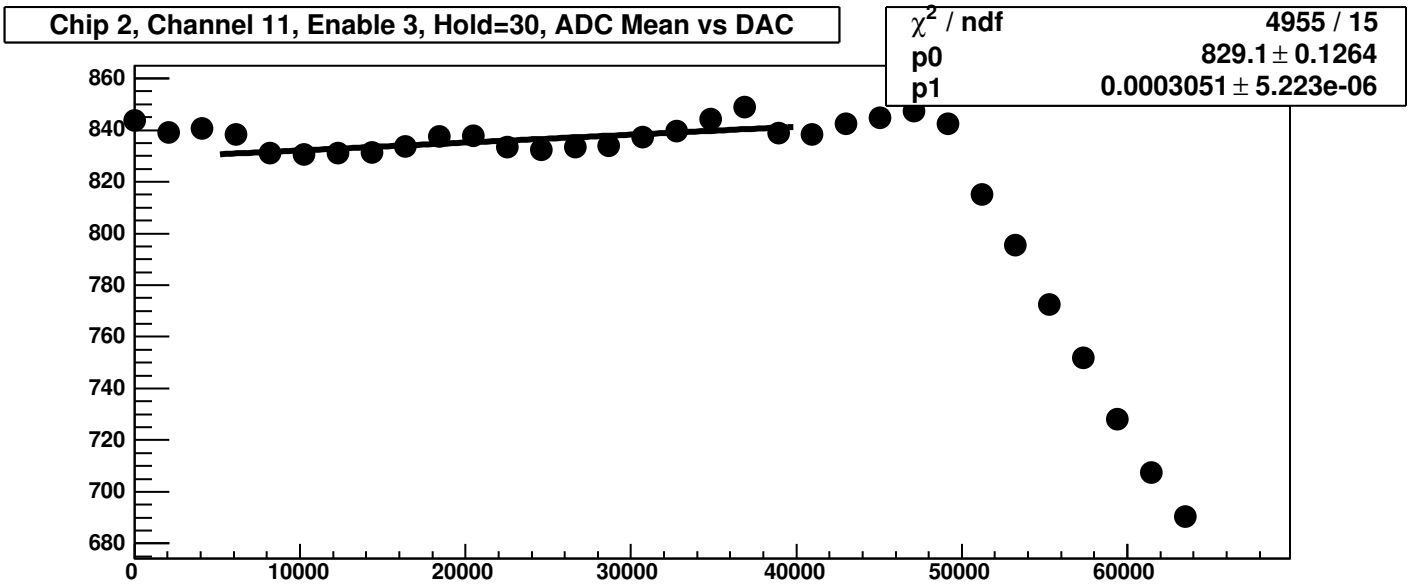

Chip 2, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

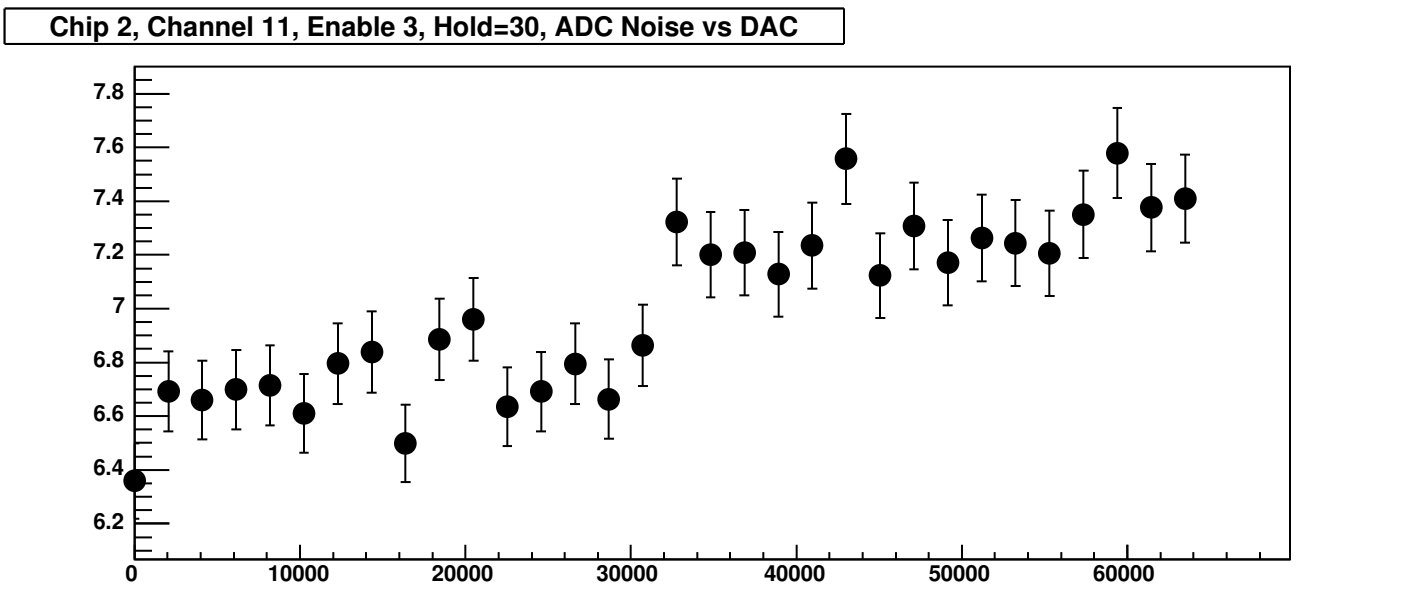

Chip 2, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

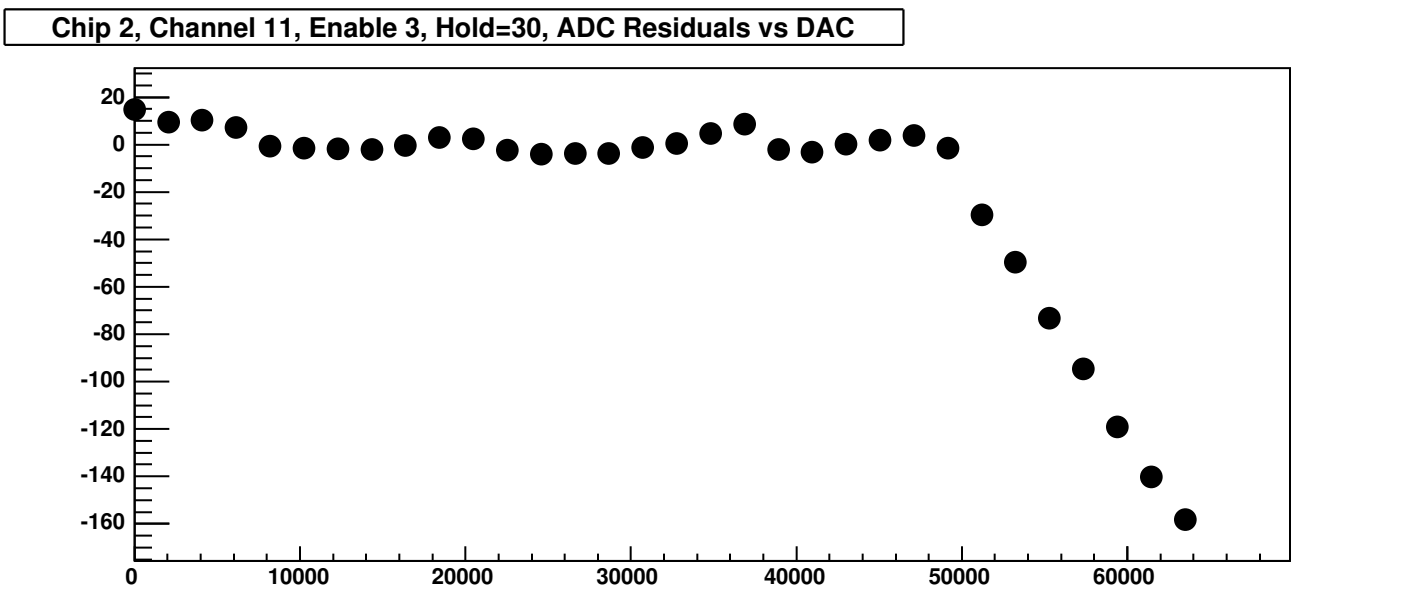

Chip 2, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3419 / 15
p0 831.6 ± 0.1189
p1 -0.00081 ± 4.878e-06
800
700
600
500
400
300
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

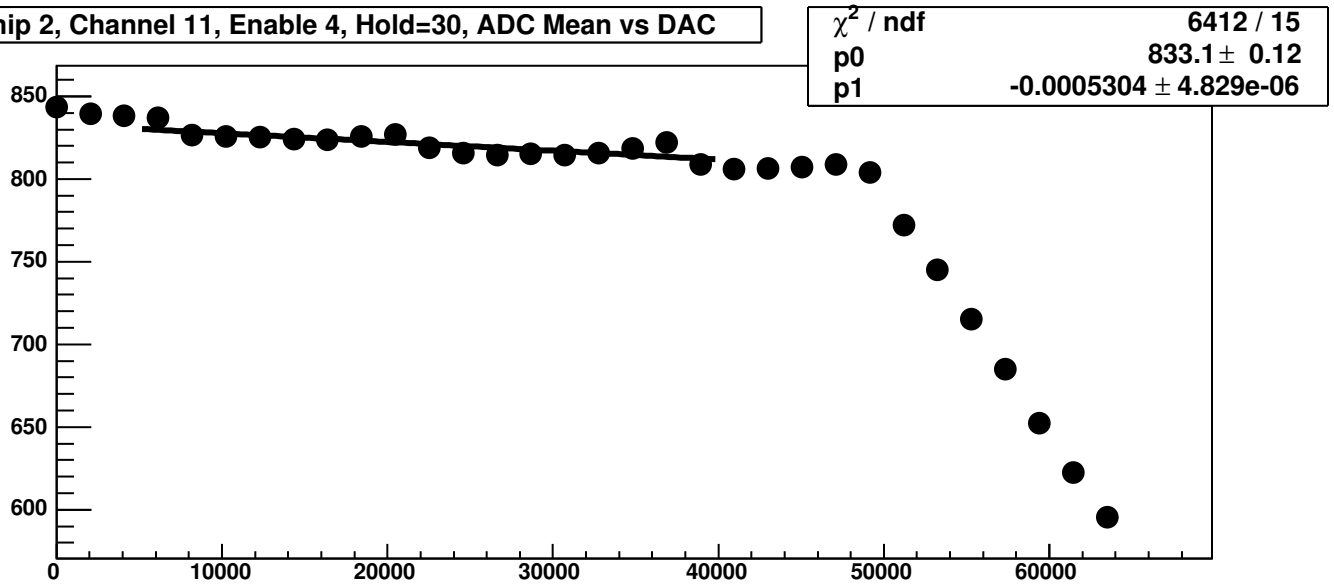

Chip 2, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4955 / 15
p0 829.1 ± 0.1264
p1 0.0003051 ± 5.223e-06
860
840
820
800
780
760
740
720
700
680
0
10000
20000
30000
40000
50000
60000

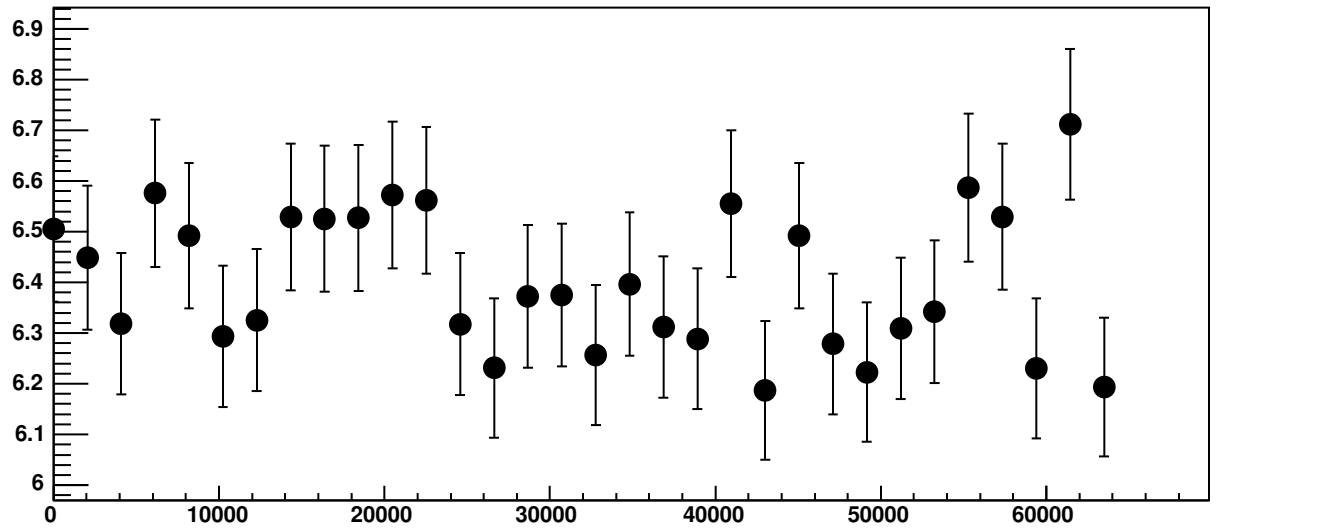

Chip 2, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

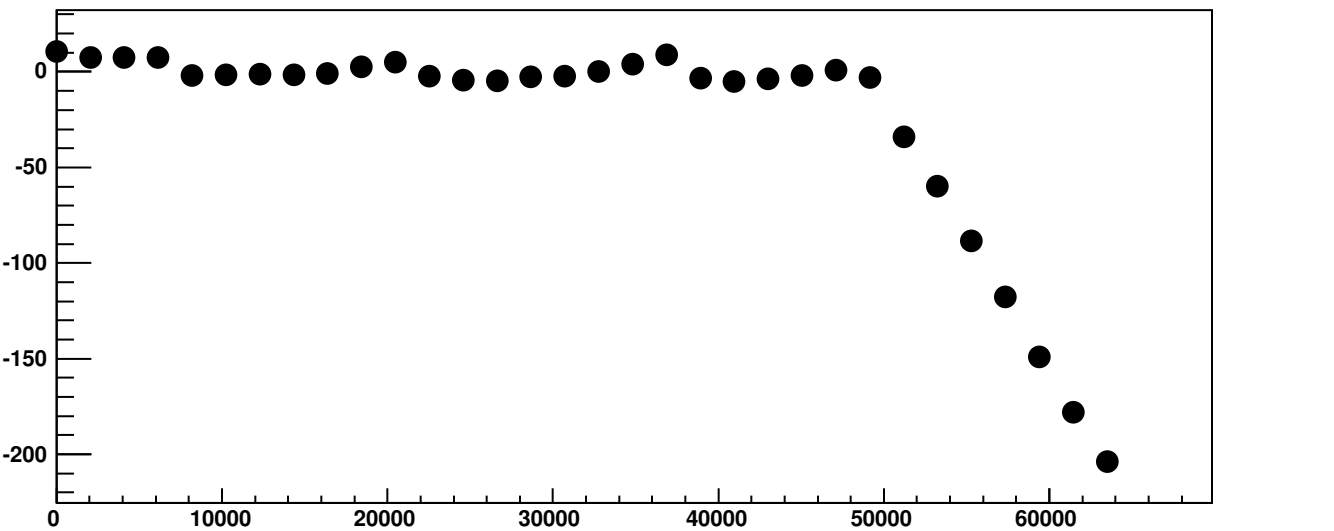
Chip 2, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

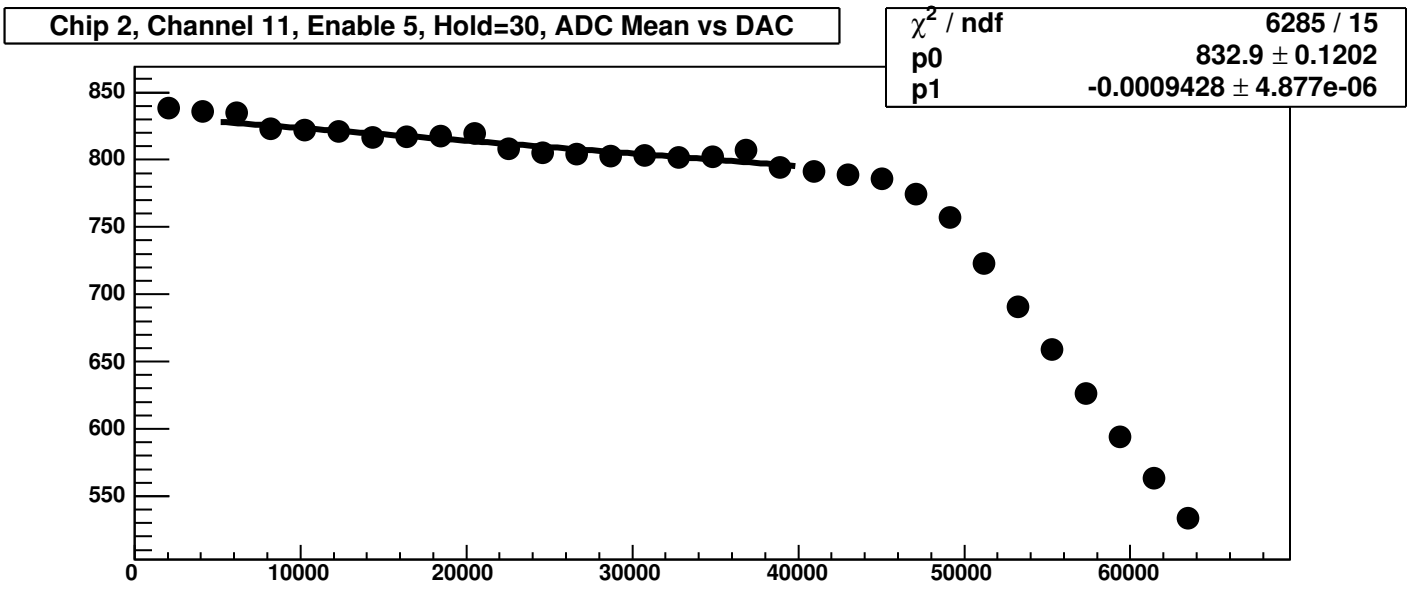


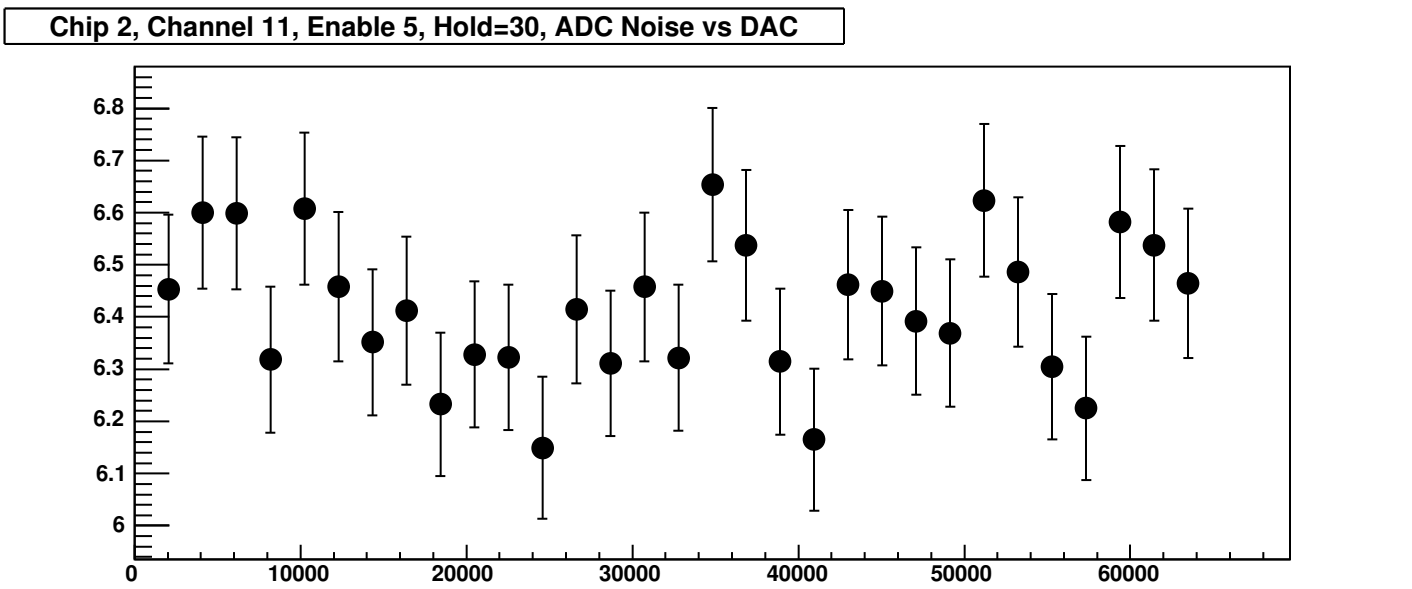
Chip 2, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC

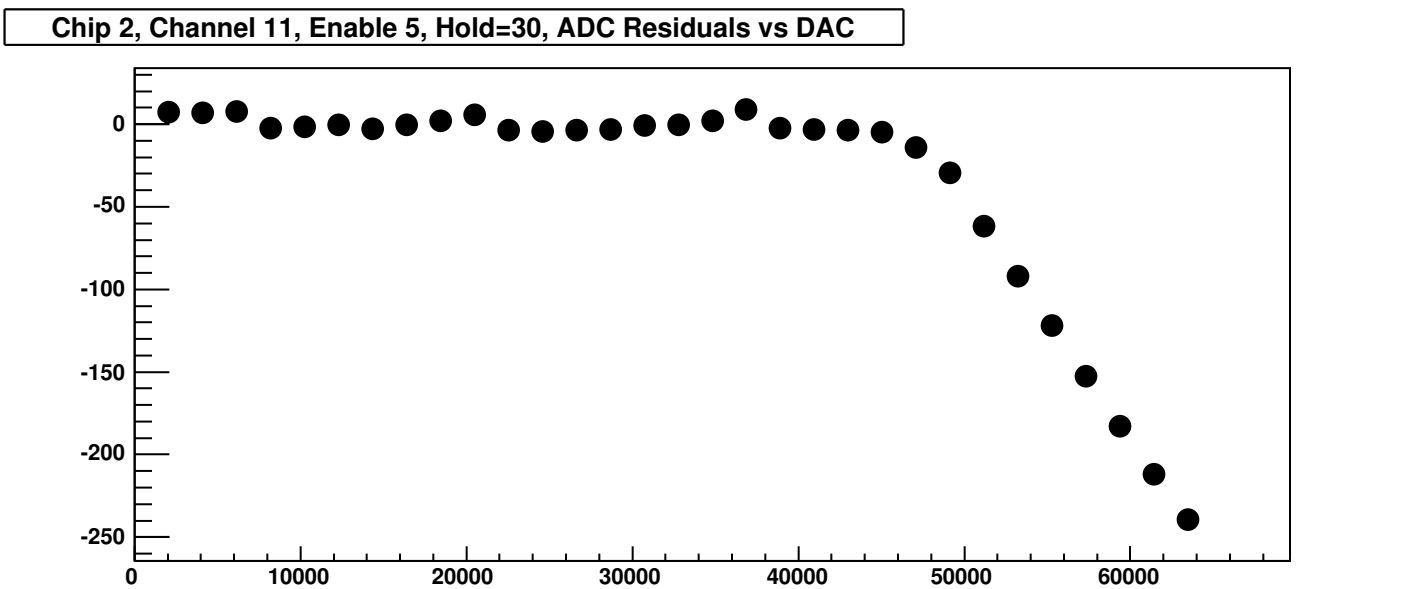


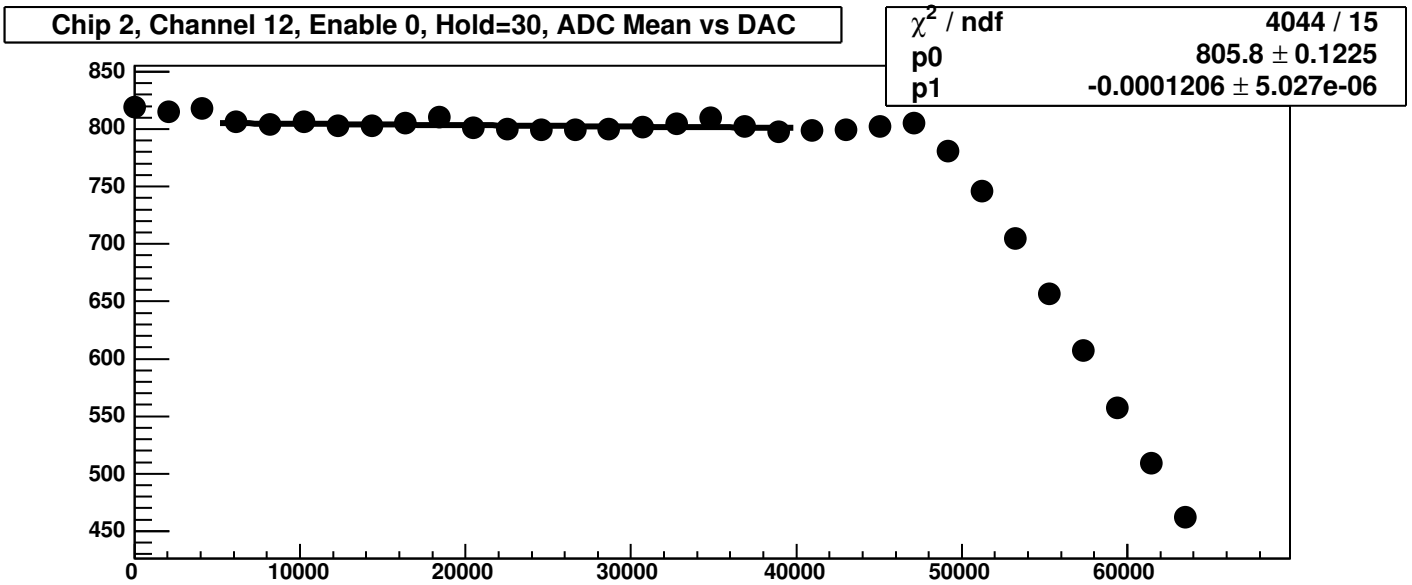
Chip 2, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC

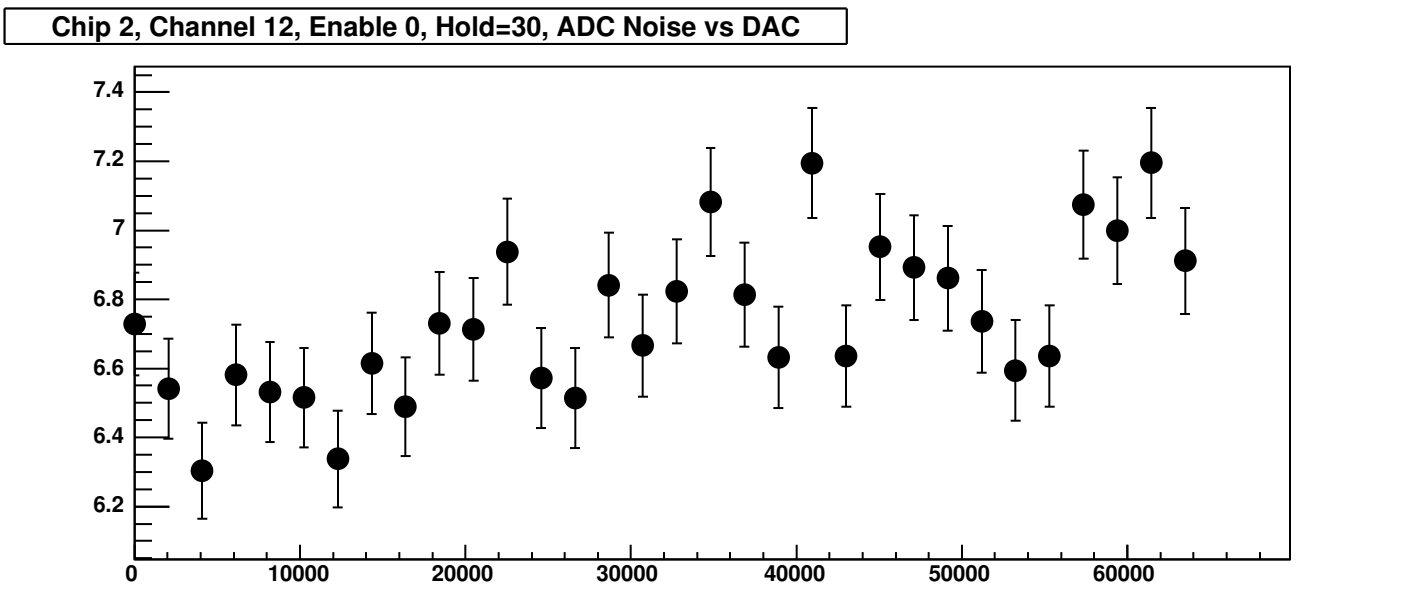


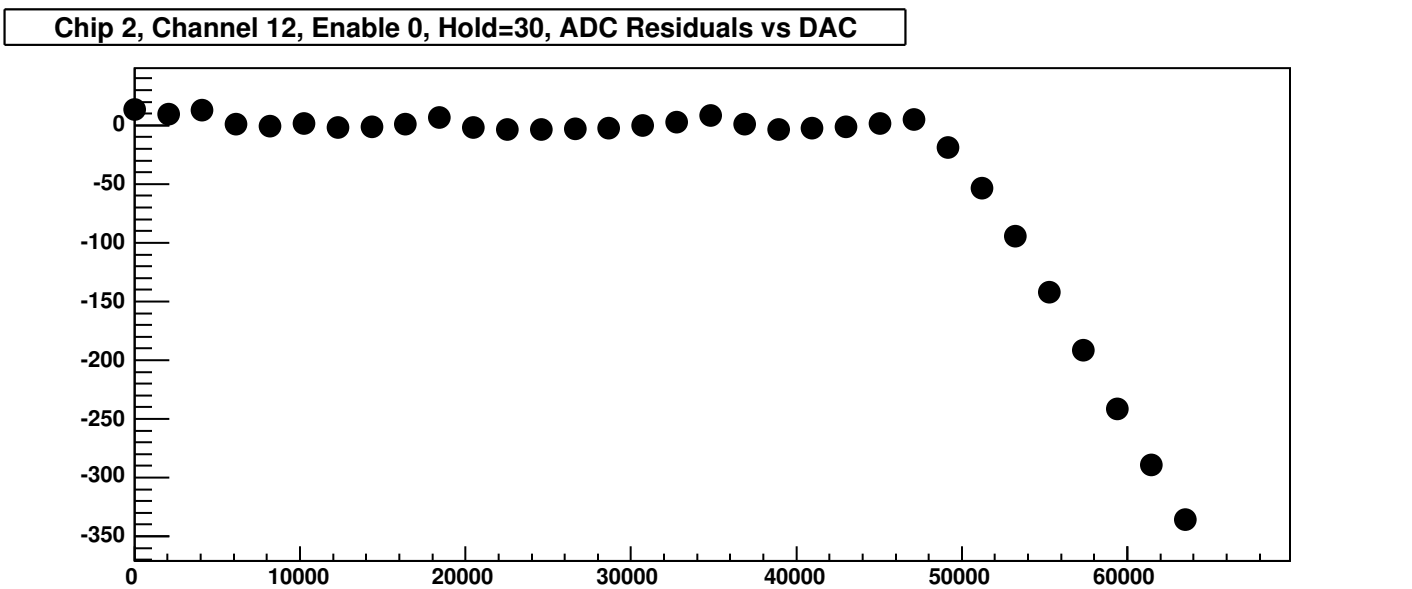

Chip 2, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6285 / 15
p0 832.9 ± 0.1202
p1 -0.0009428 ± 4.877e-06
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

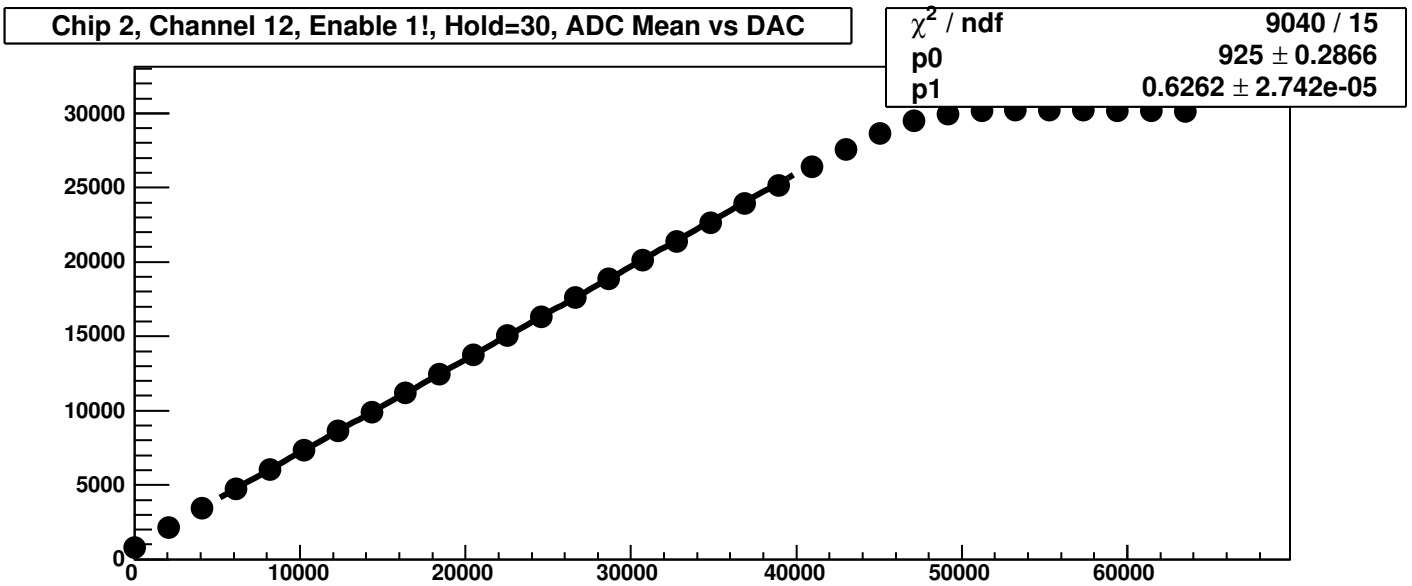

Chip 2, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.7
6.6
6.5
6.4
6.3
6.2
6.1
6
0
10000
20000
30000
40000
50000
60000

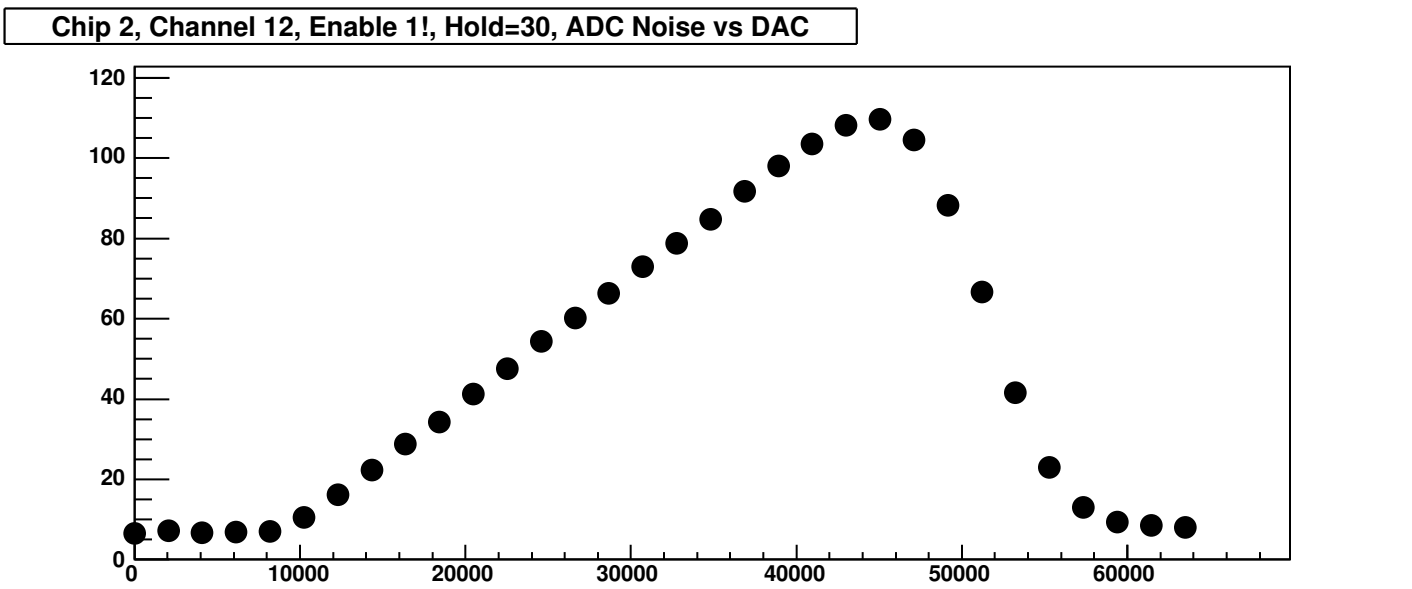

Chip 2, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

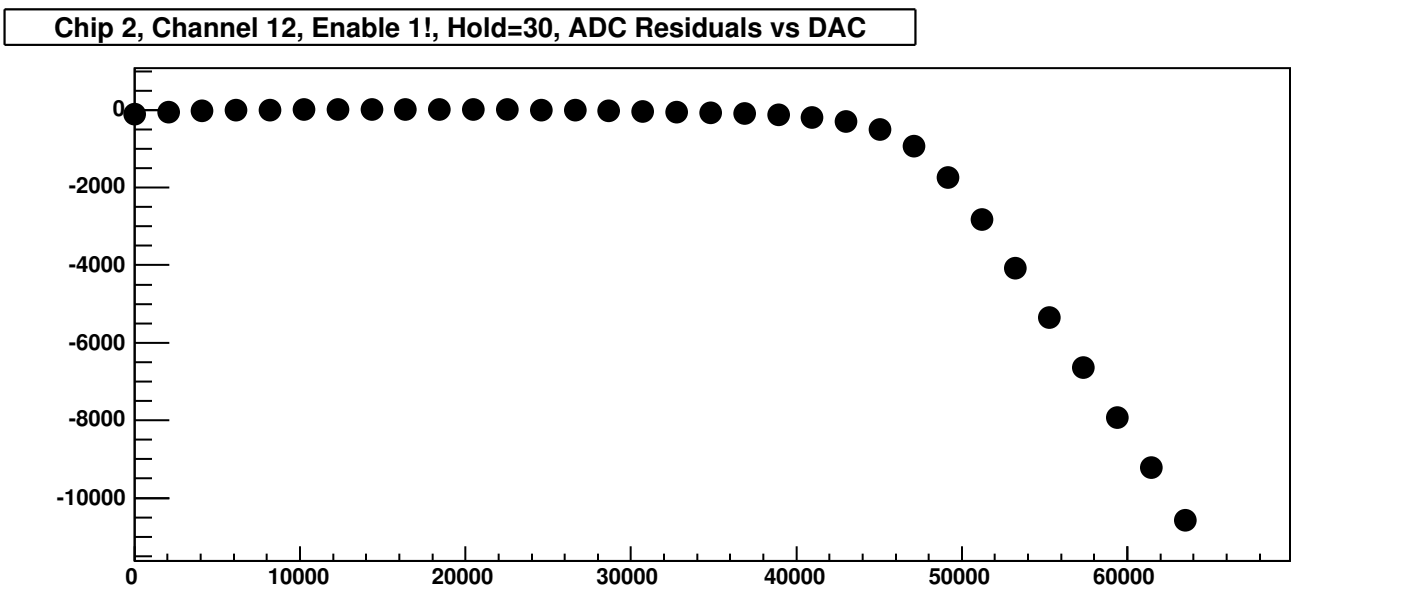

Chip 2, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4044 / 15
p0 805.8 ± 0.1225
p1 -0.0001206 ± 5.027e-06
850
800
750
700
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

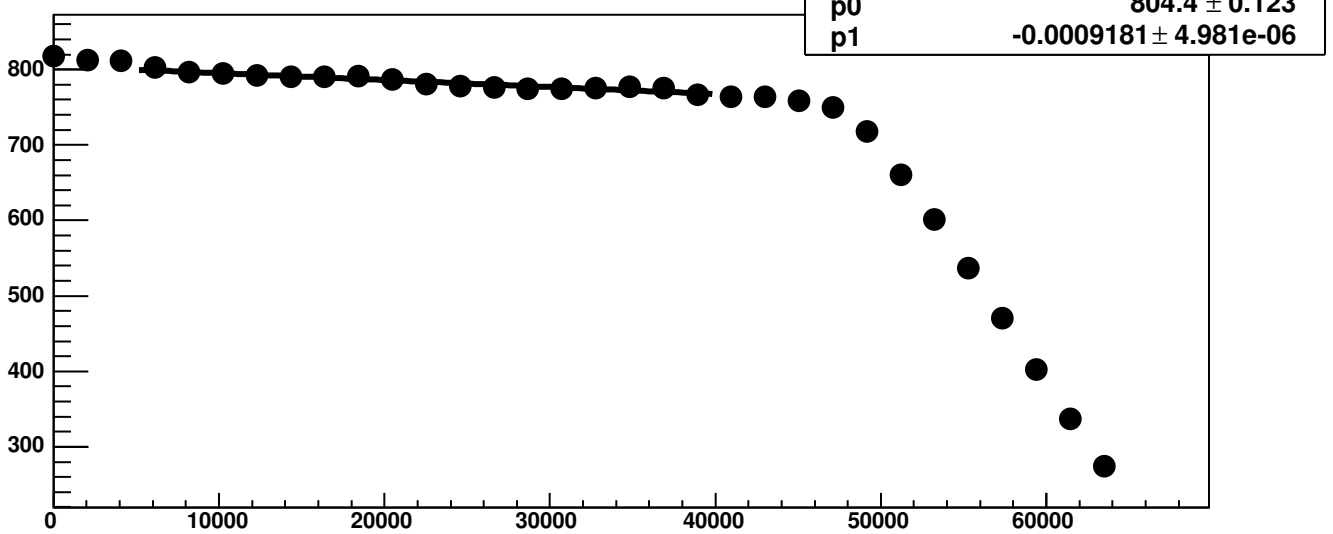

Chip 2, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 9040 / 15
p0 925 ± 0.2866
p1 0.6262 ± 2.742e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

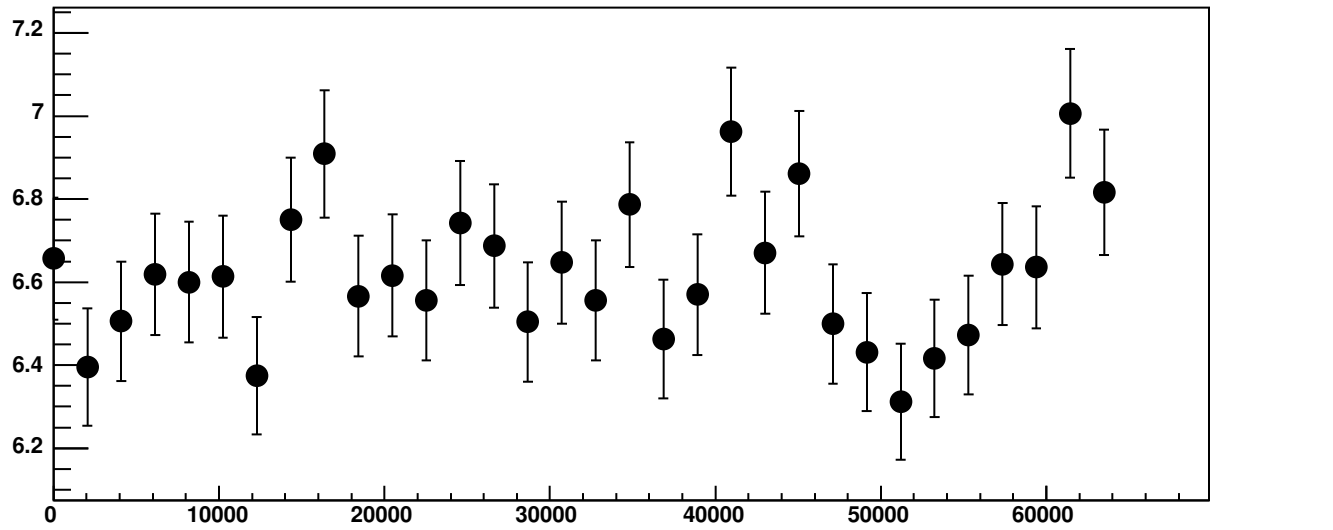

Chip 2, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

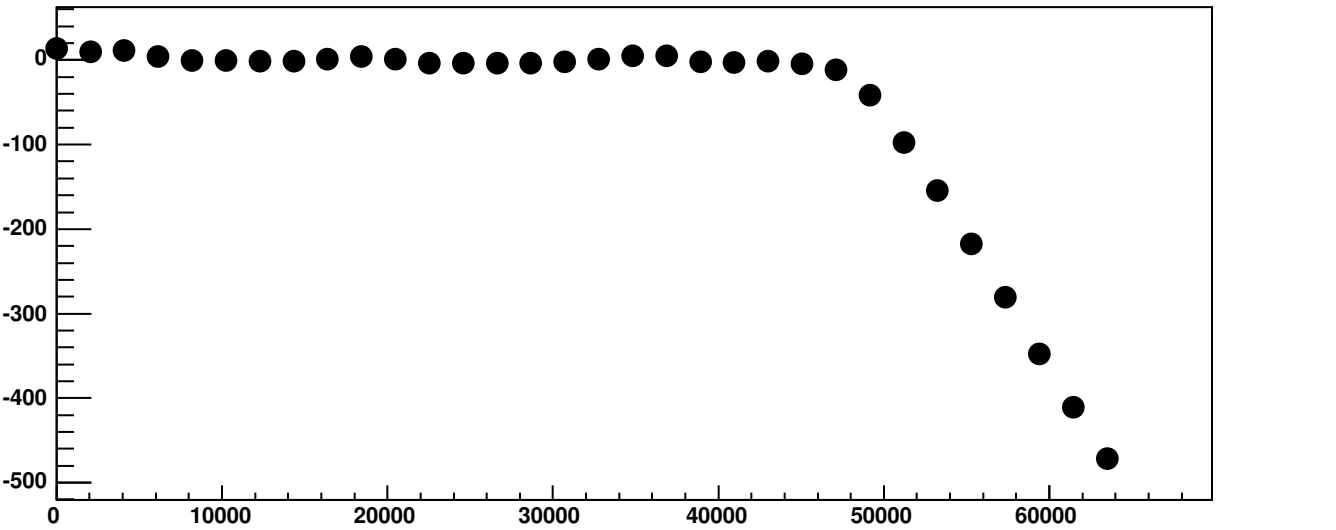
Chip 2, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC

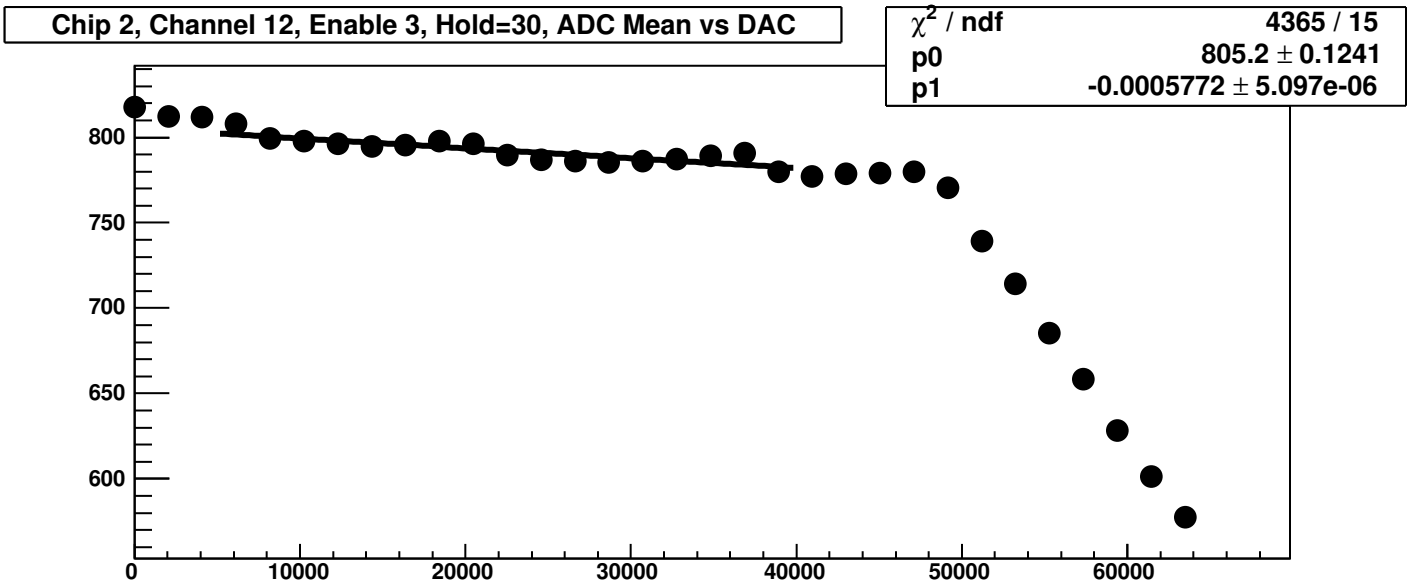


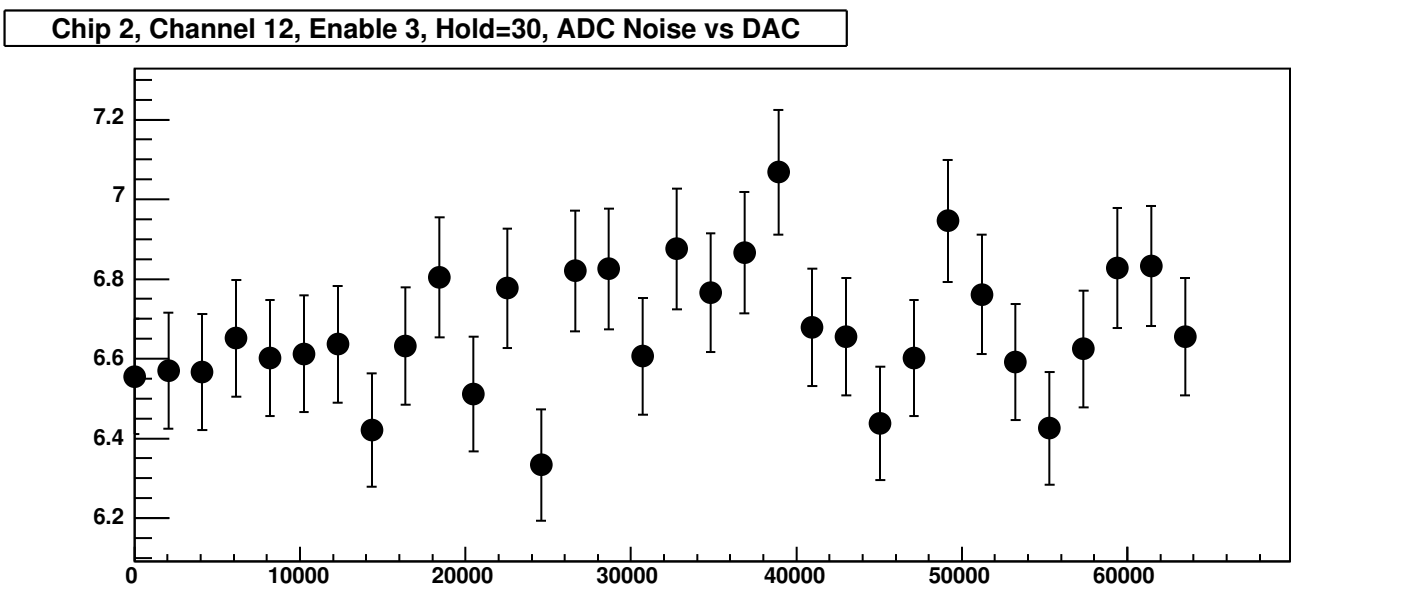
Chip 2, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC

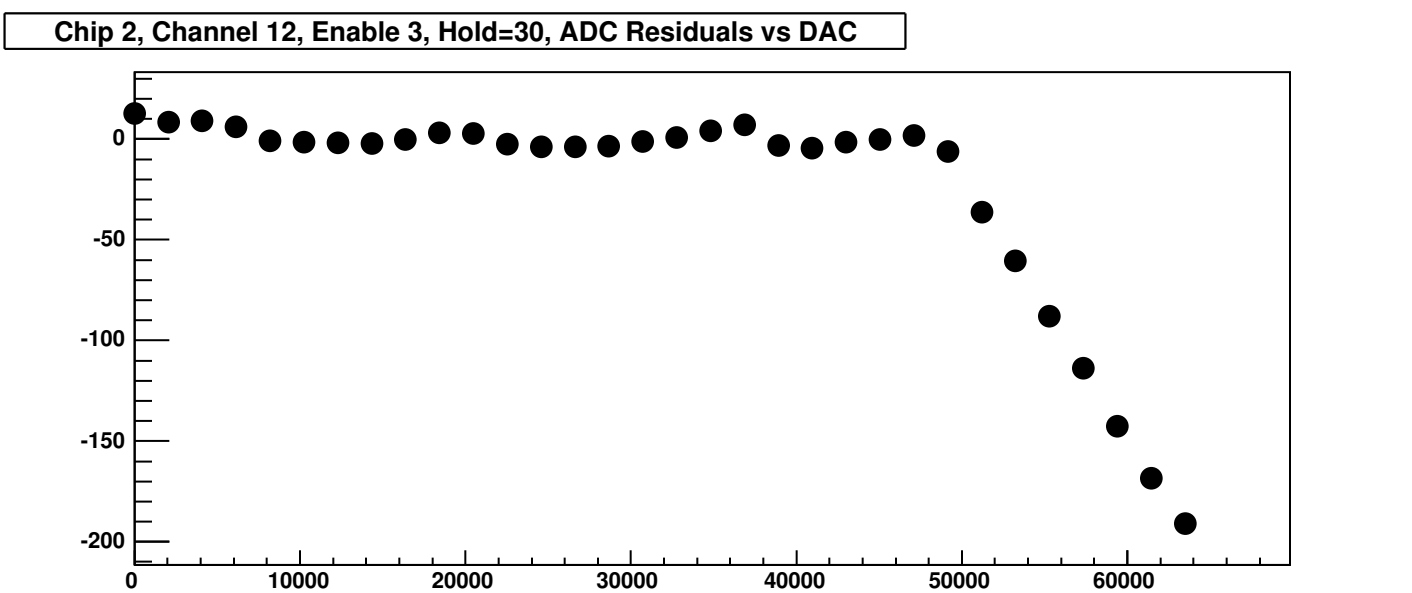


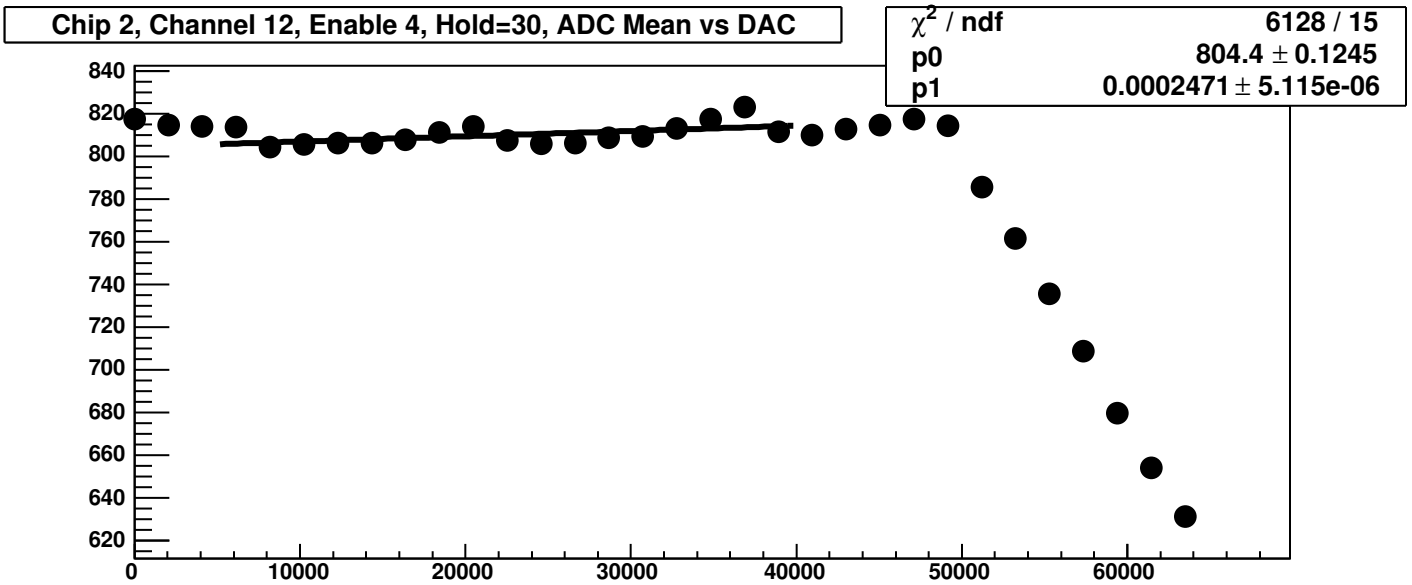
Chip 2, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC

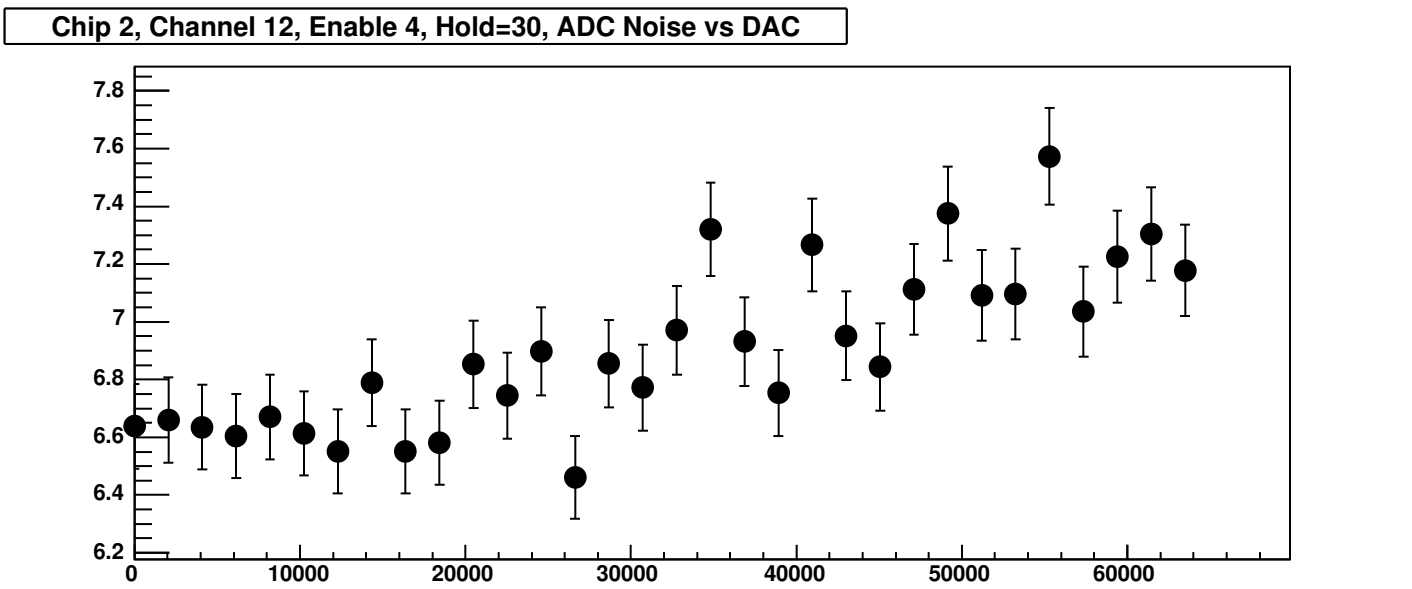


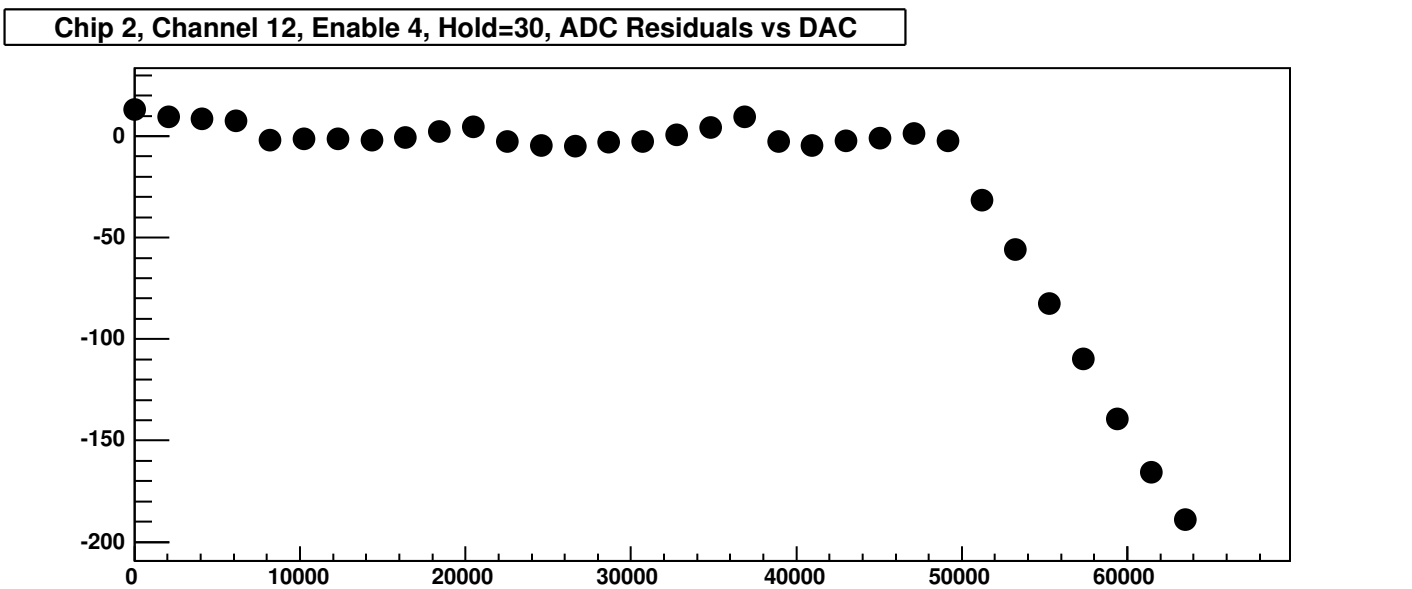

Chip 2, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4365 / 15
p0 805.2 ± 0.1241
p1 -0.0005772 ± 5.097e-06
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

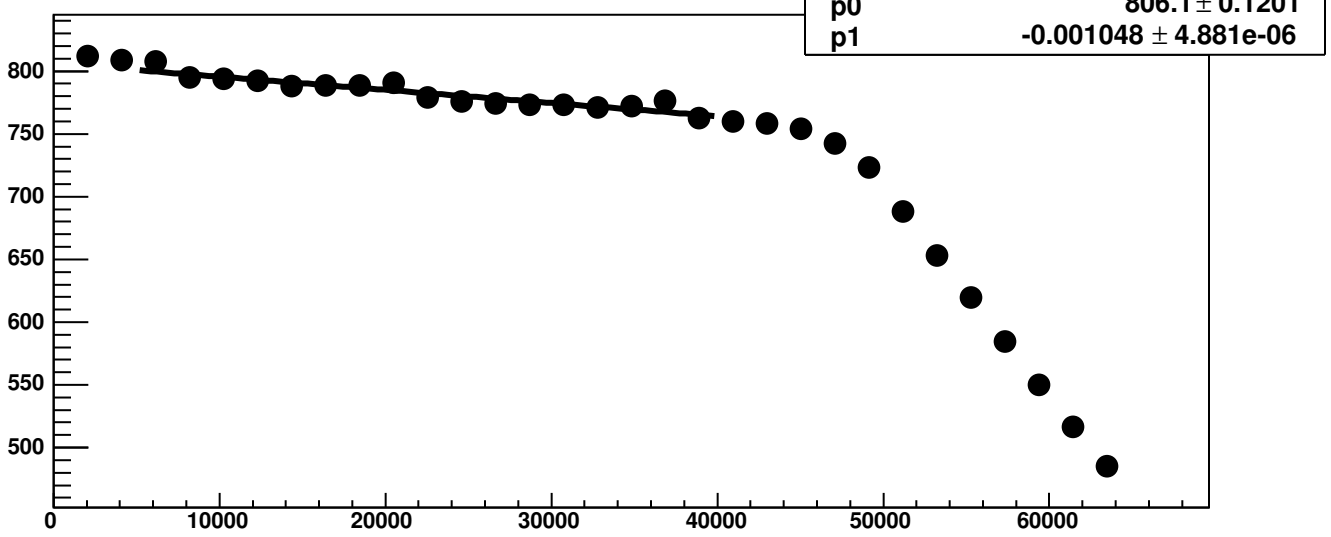

Chip 2, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6128 / 15
p0 804.4 ± 0.1245
p1 0.0002471 ± 5.115e-06
840
820
800
780
760
740
720
700
680
660
640
620
0
10000
20000
30000
40000
50000
60000

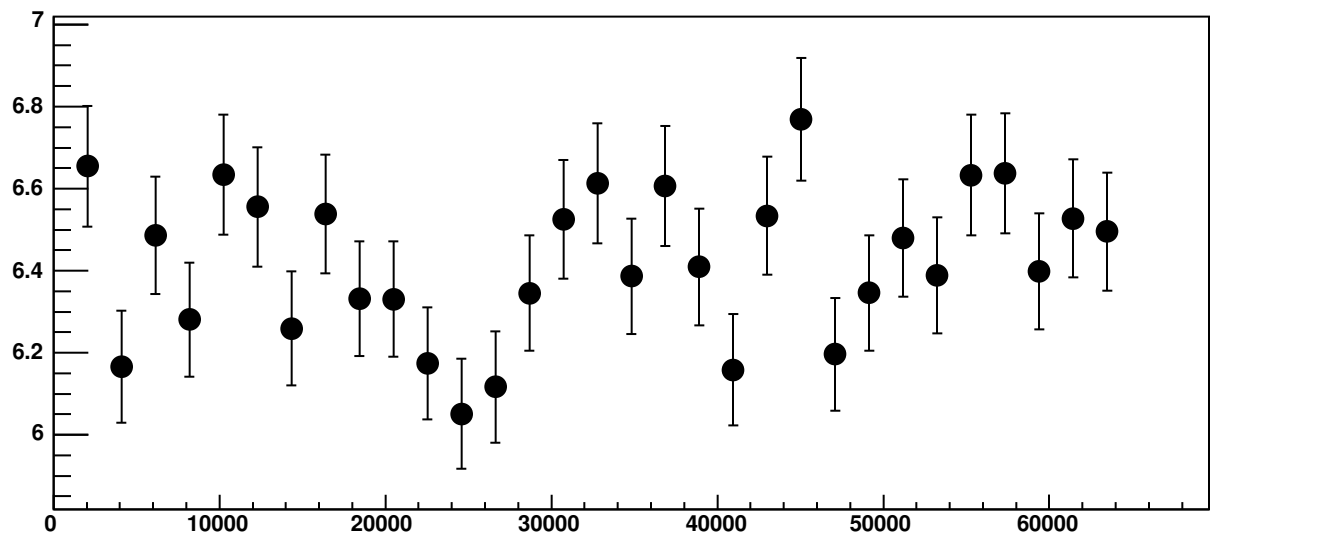

Chip 2, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

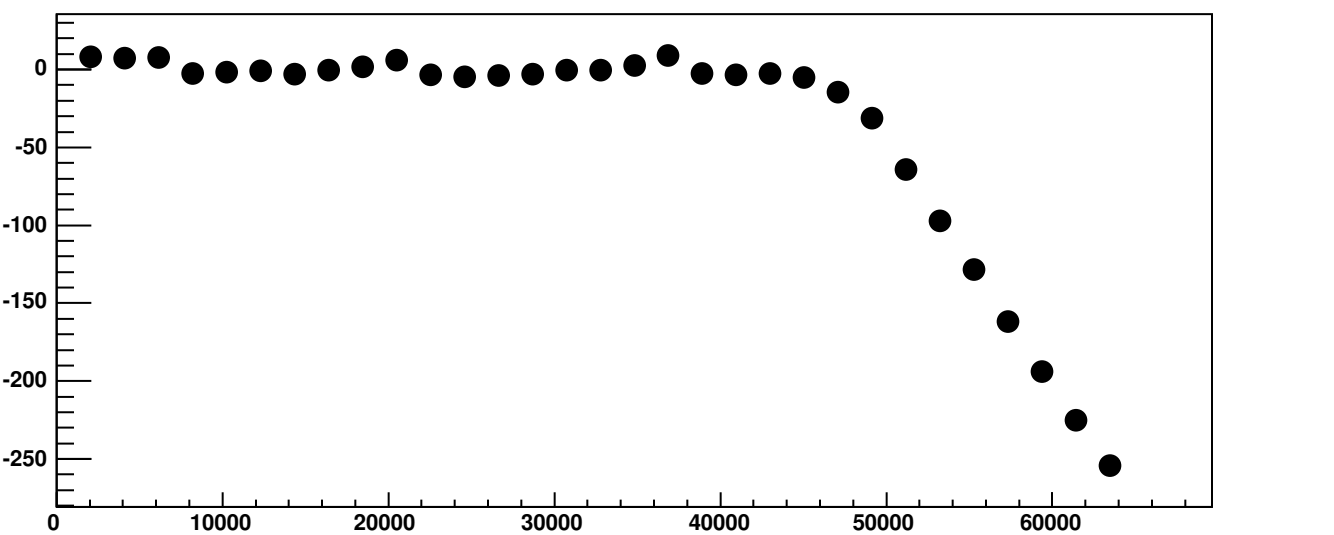
Chip 2, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC

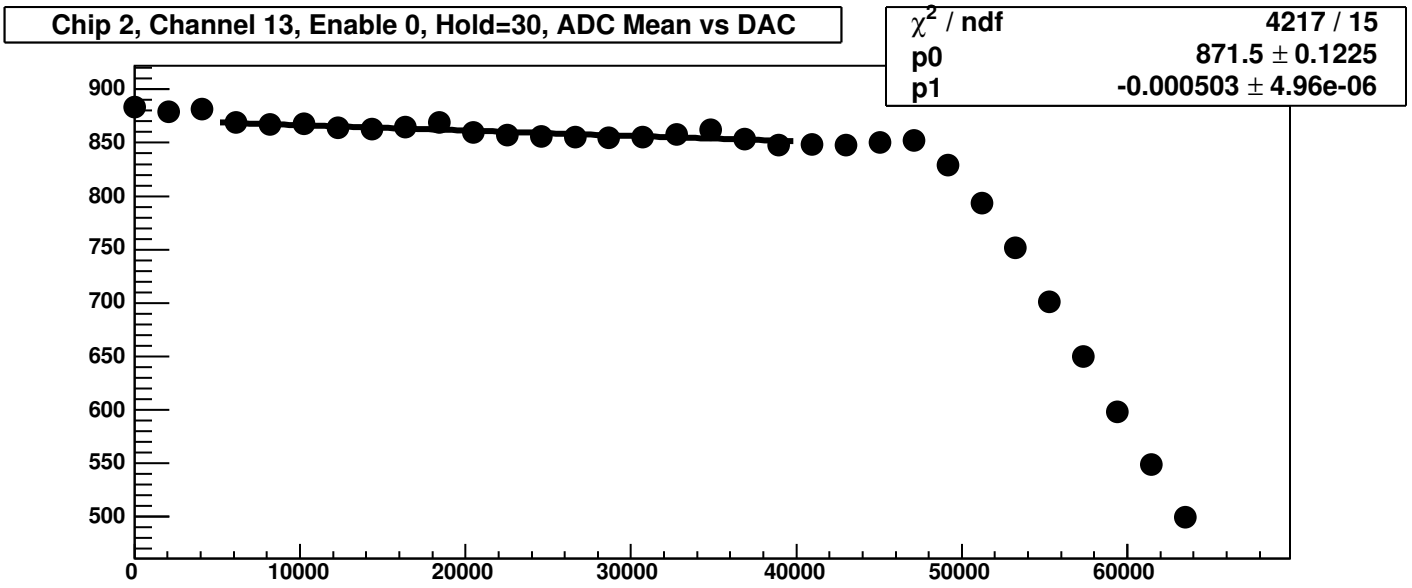


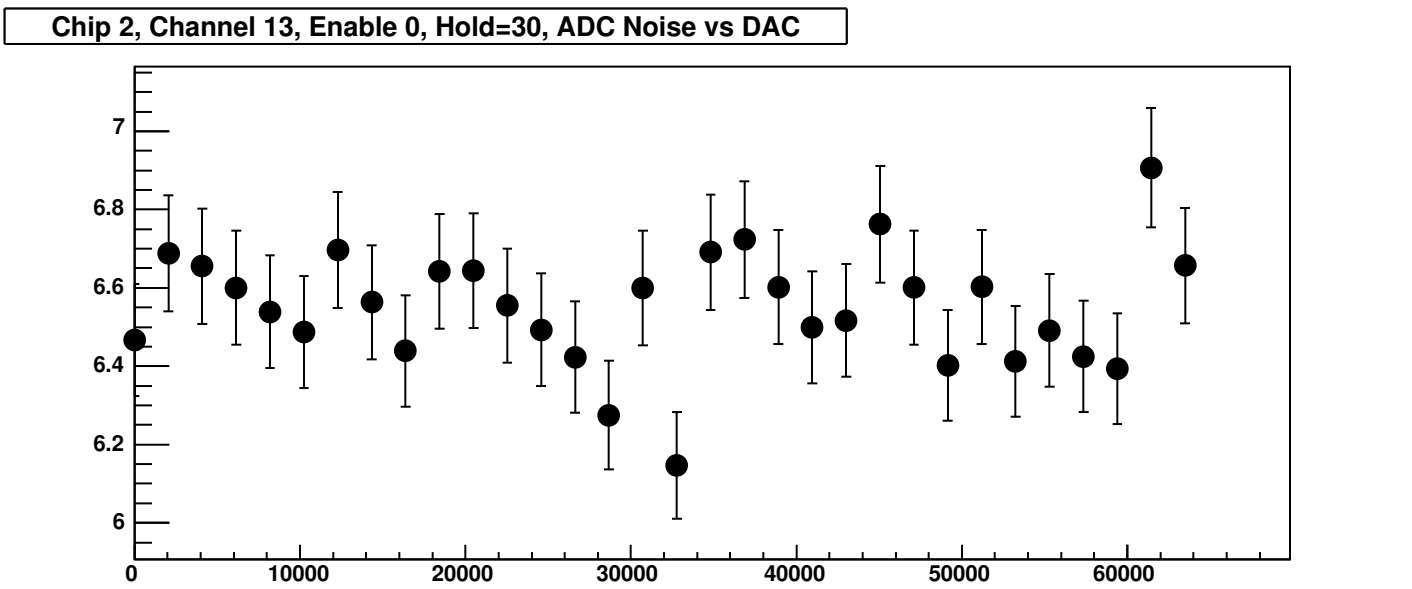
Chip 2, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC

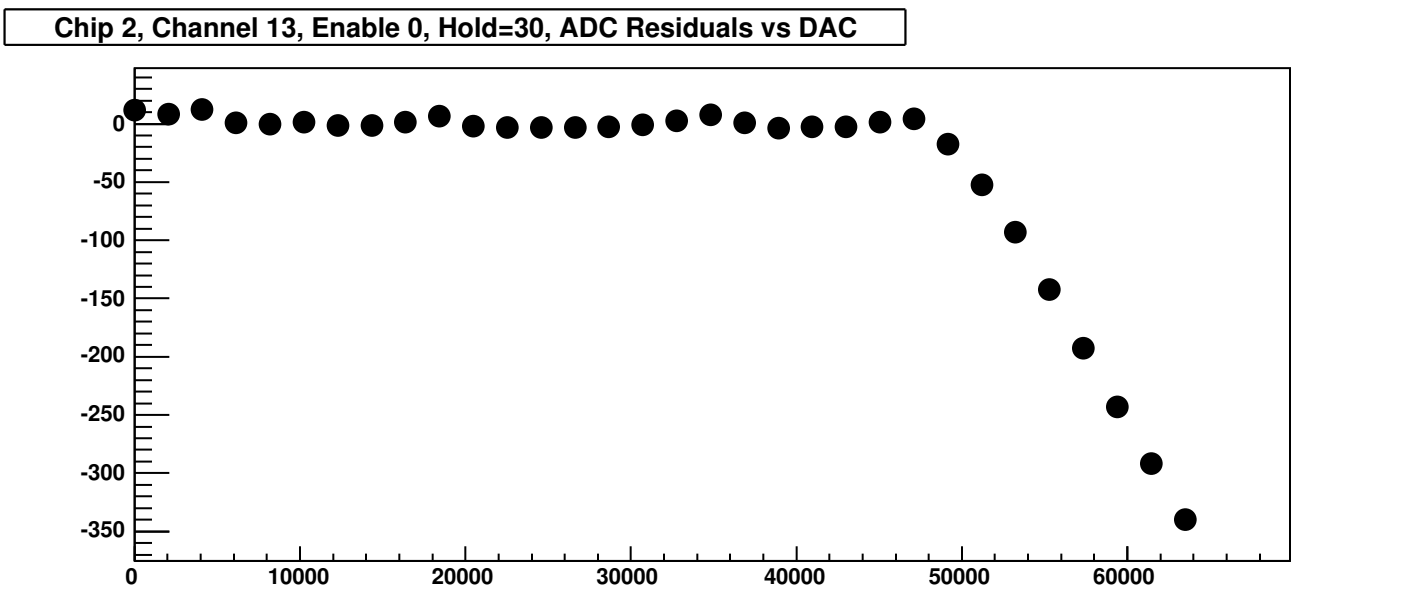


Chip 2, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

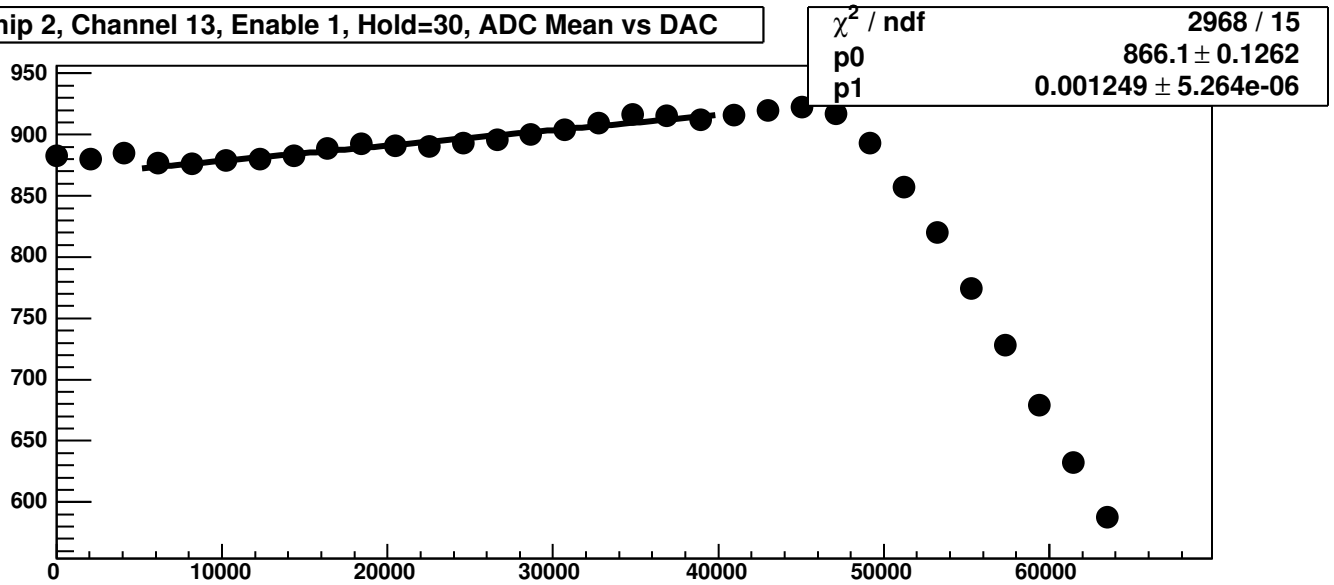



Chip 2, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4217 / 15
p0 871.5 ± 0.1225
p1 -0.000503 ± 4.96e-06
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

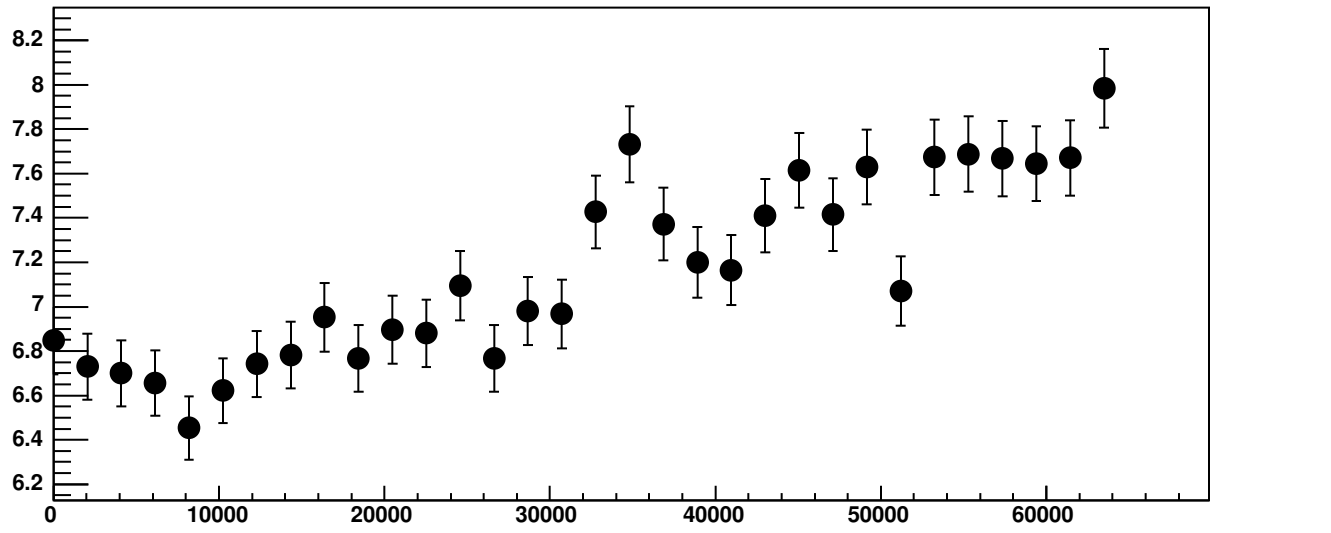

Chip 2, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

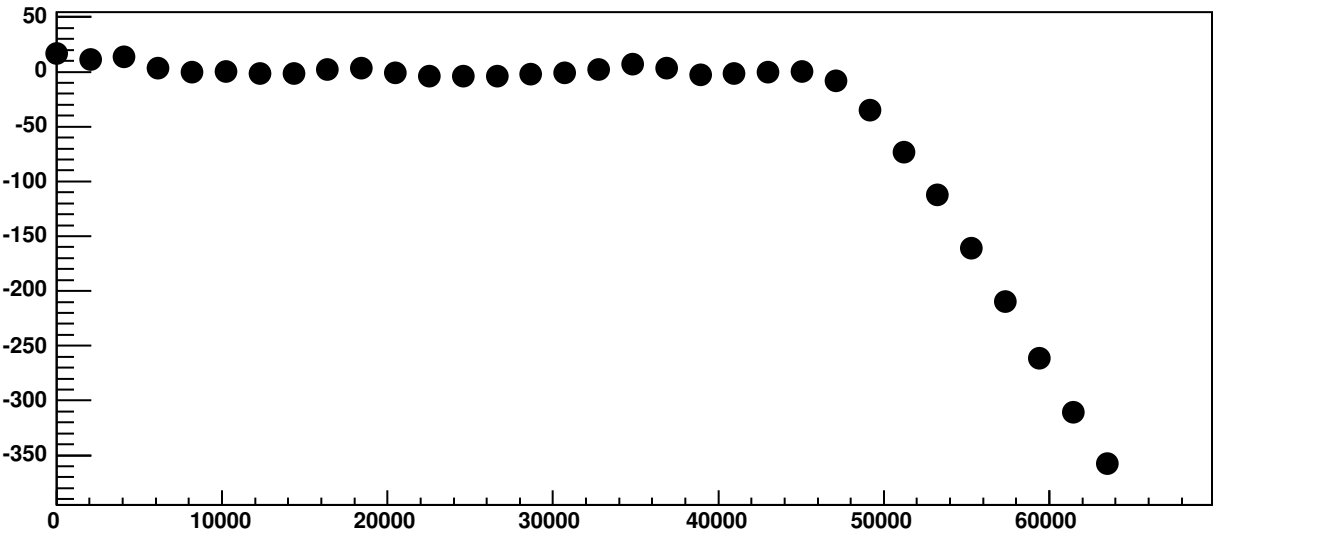
Chip 2, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC

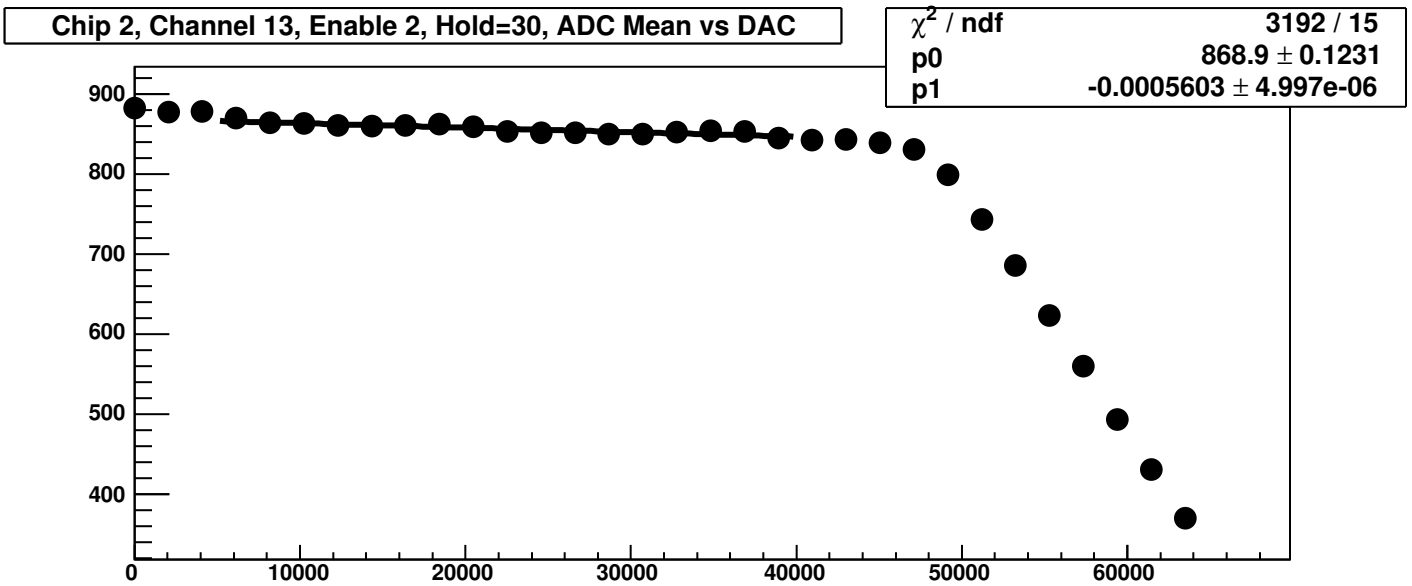


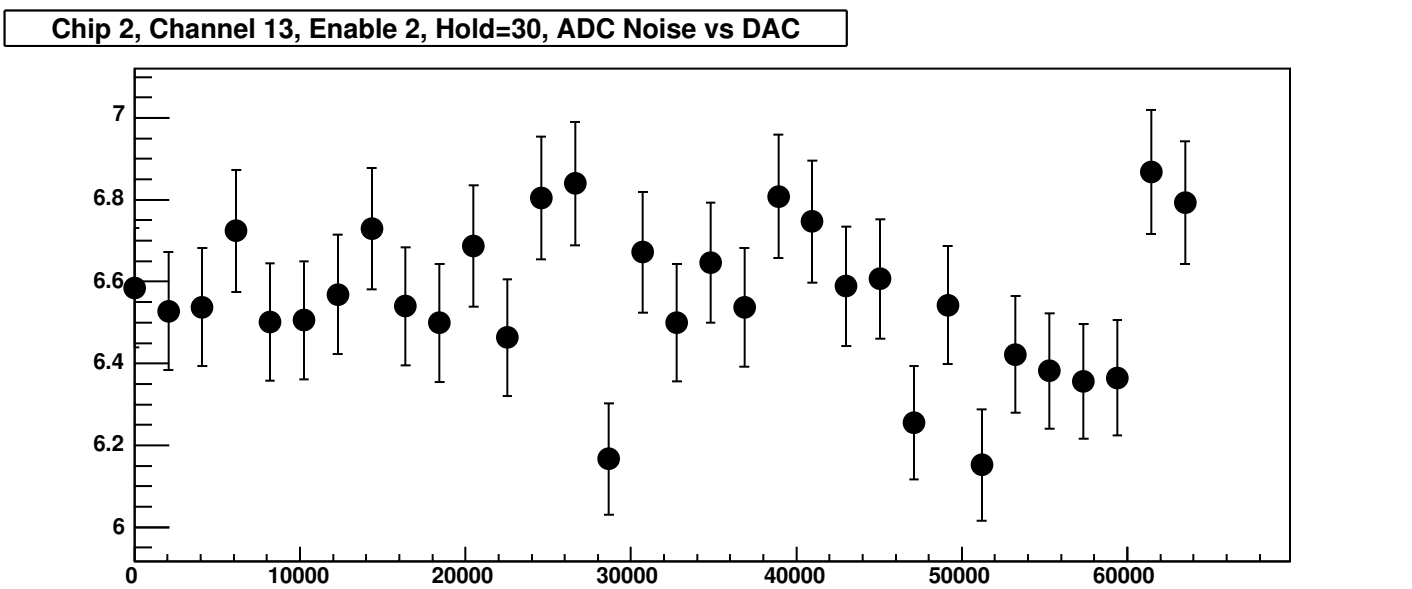
Chip 2, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC

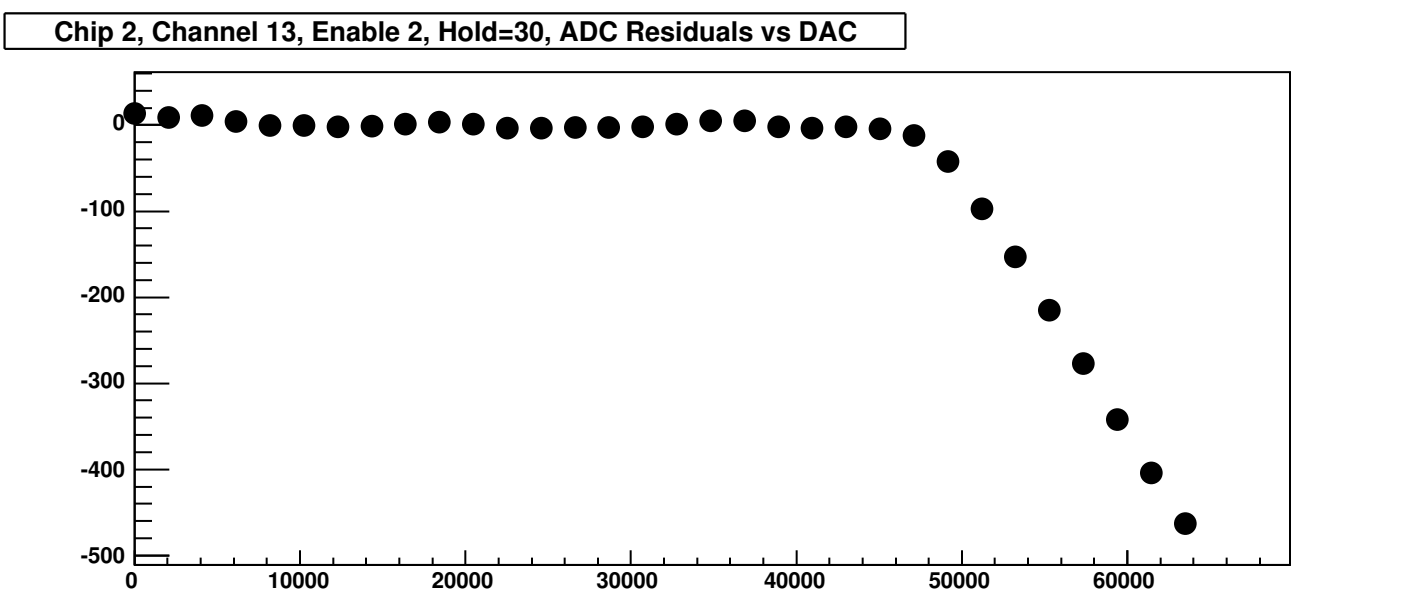


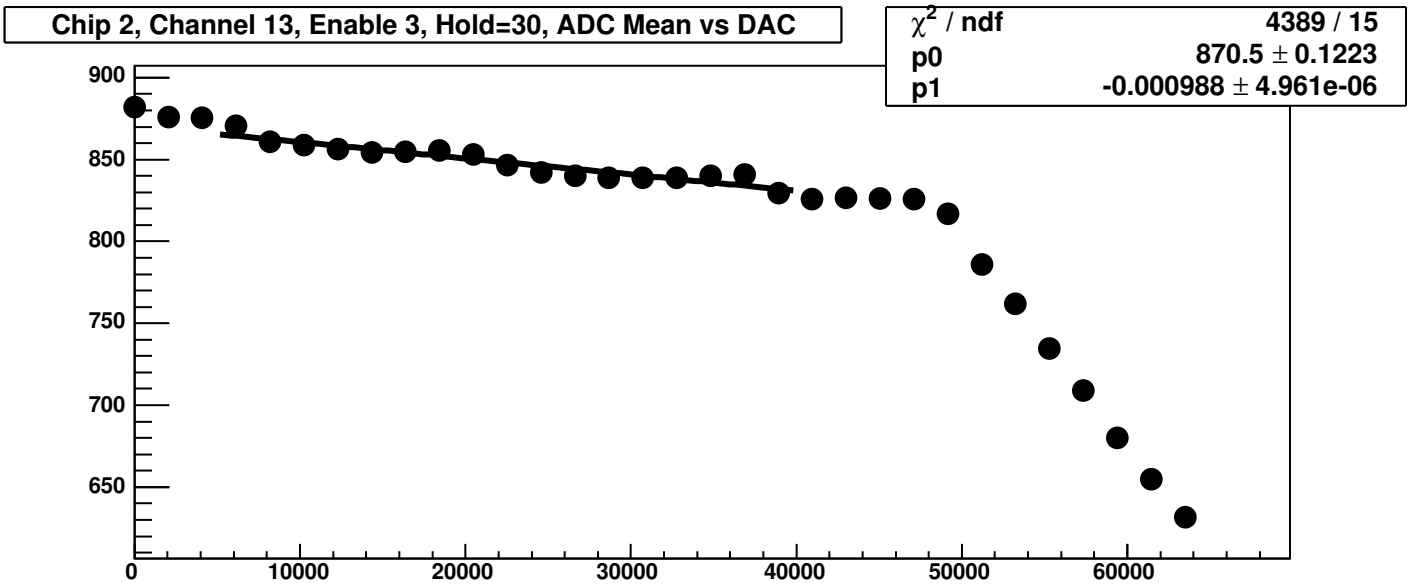
Chip 2, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC

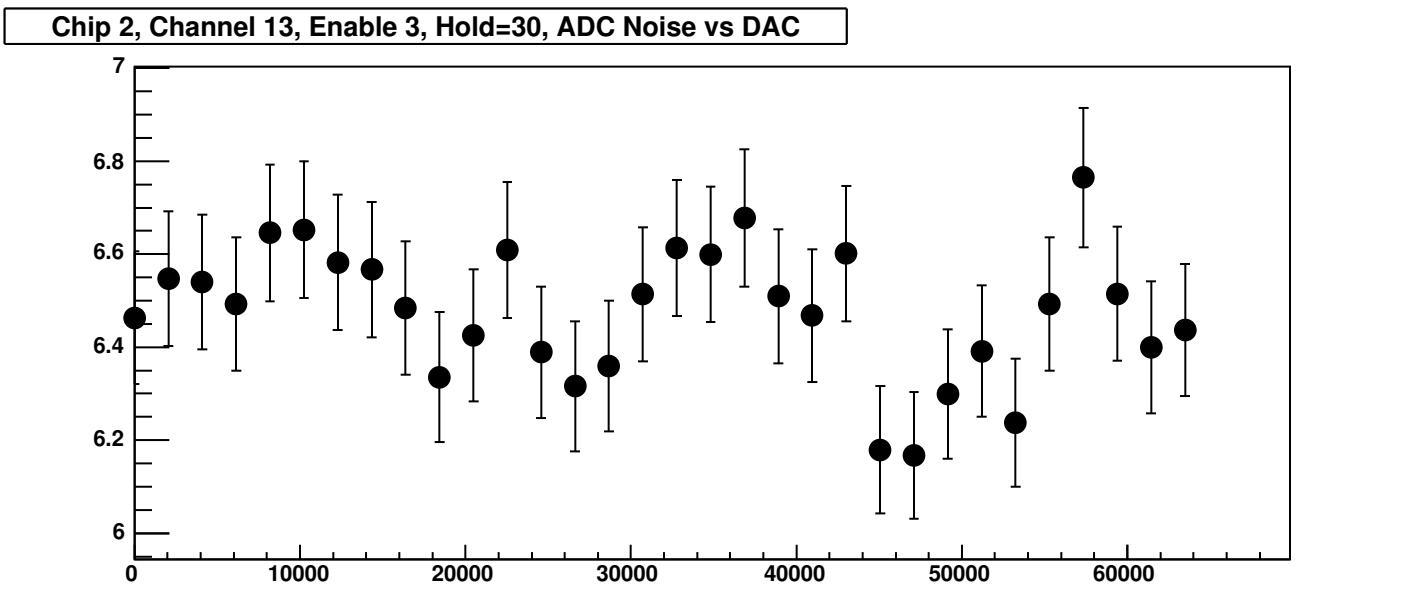


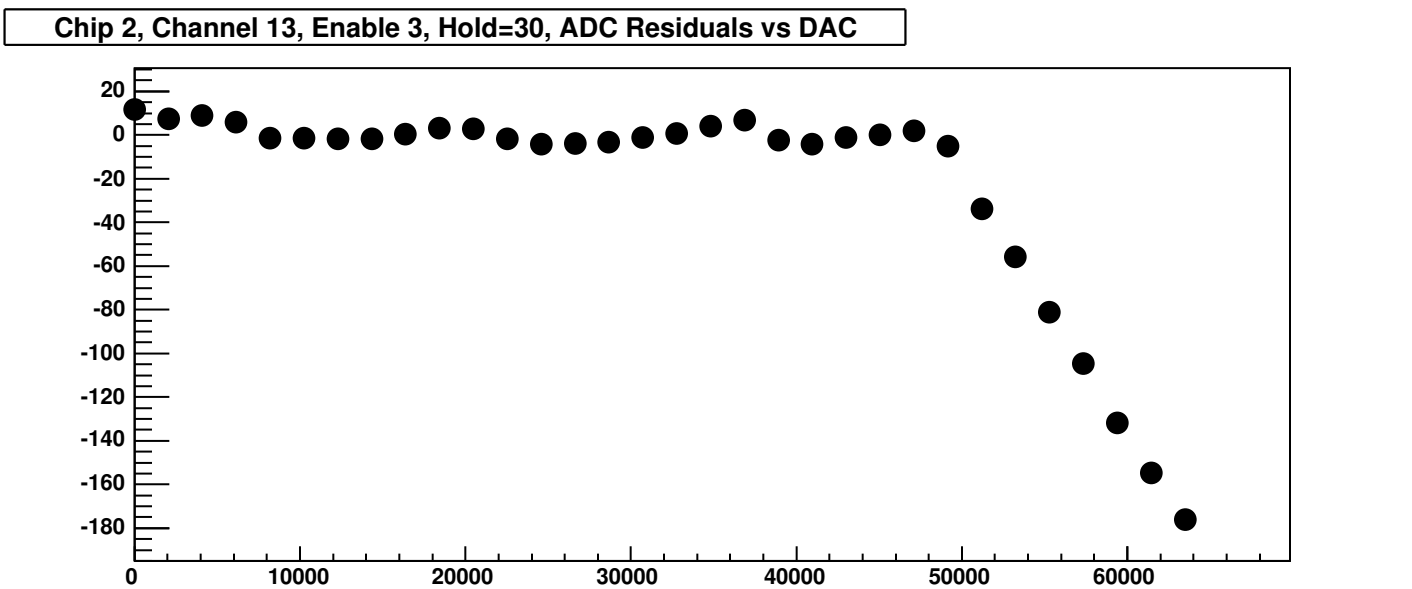

Chip 2, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3192 / 15
p0 868.9 ± 0.1231
p1 -0.0005603 ± 4.997e-06
900
800
700
600
500
400
0
10000
20000
30000
40000
50000
60000

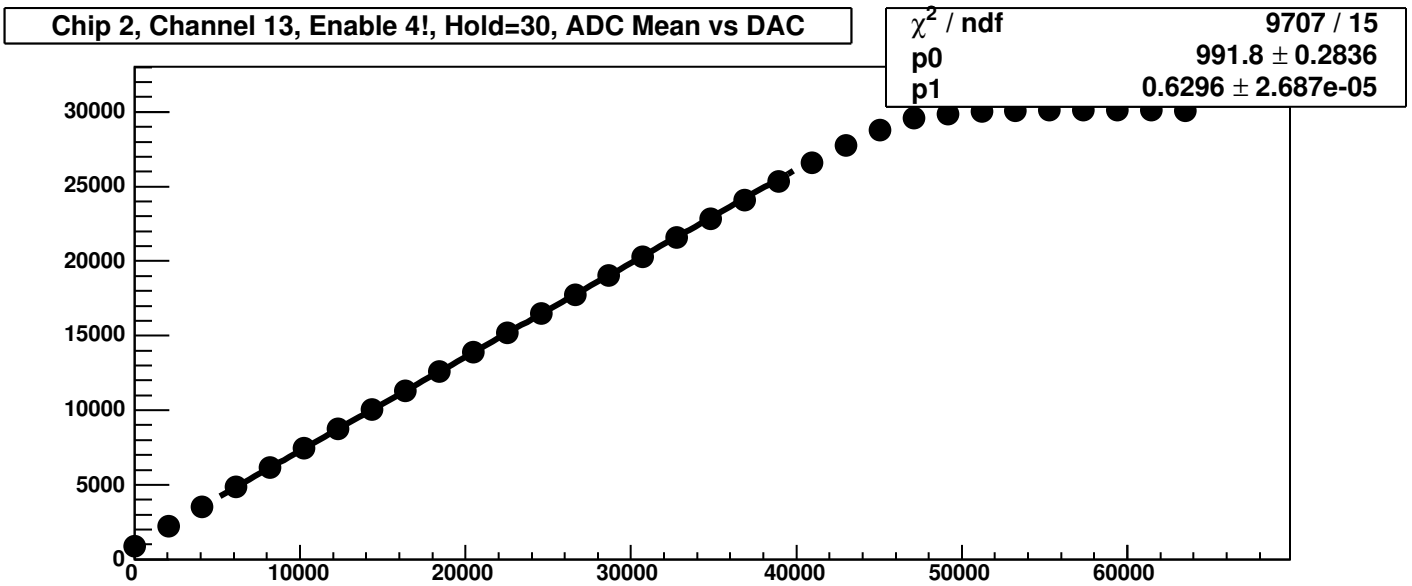

Chip 2, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

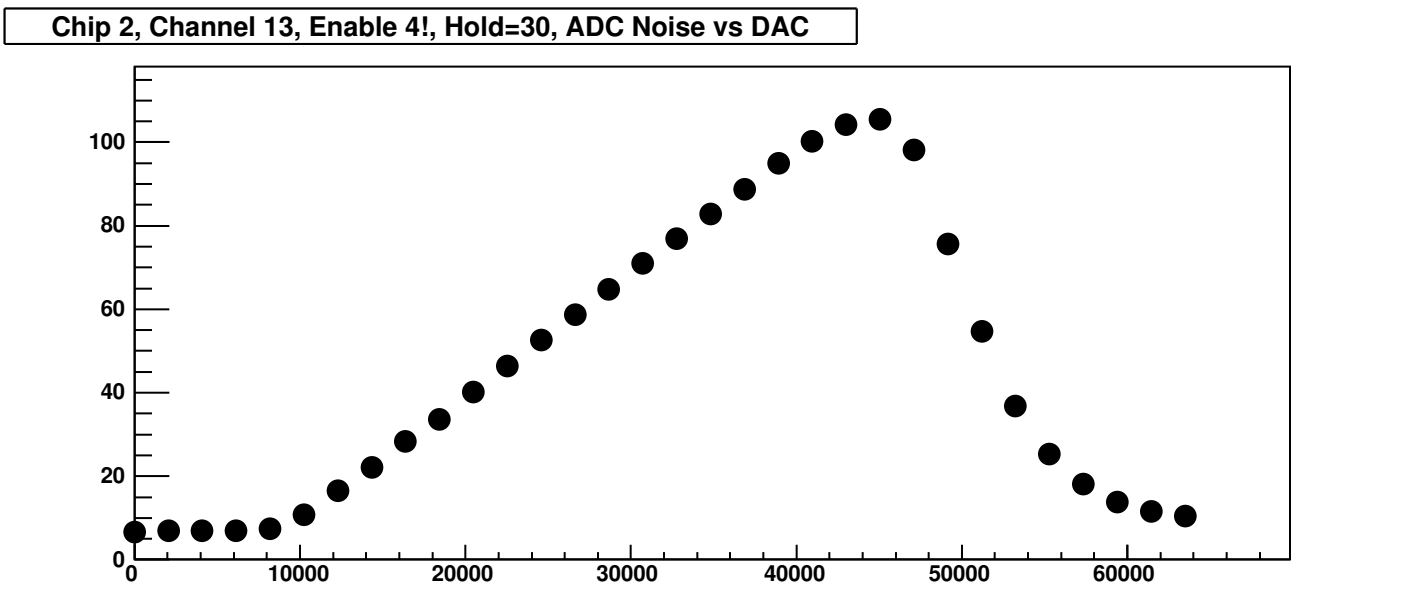

Chip 2, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

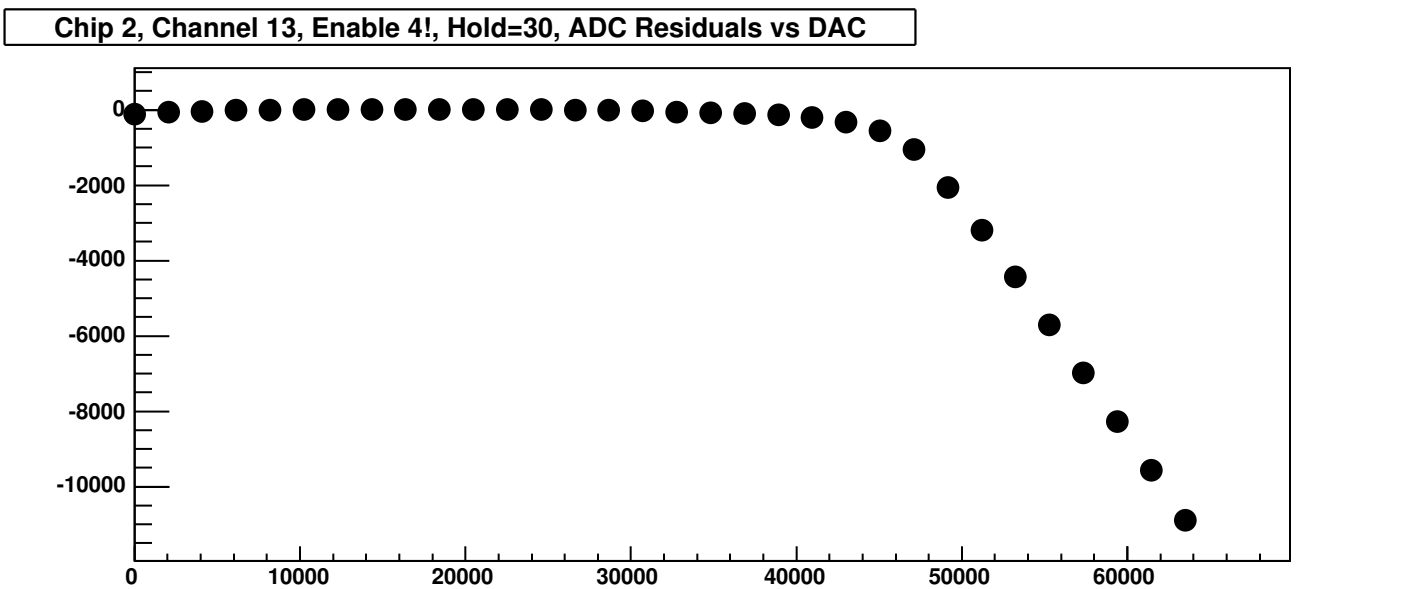

Chip 2, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4389 / 15
p0 870.5 ± 0.1223
p1 -0.000988 ± 4.961e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

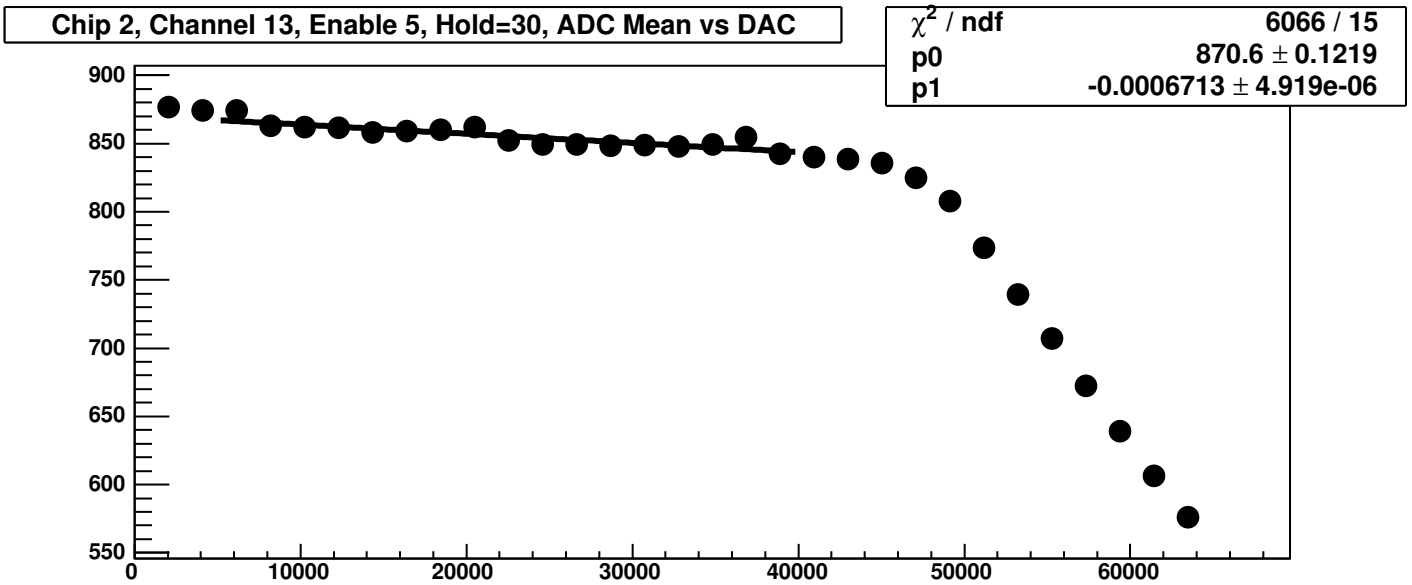

Chip 2, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

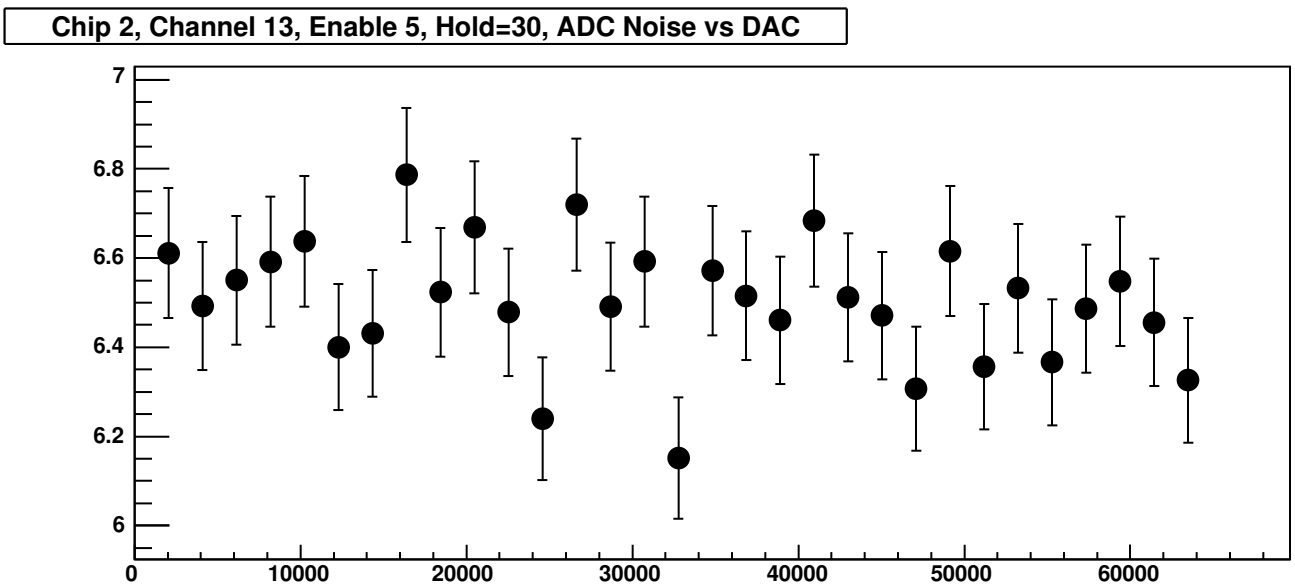

Chip 2, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

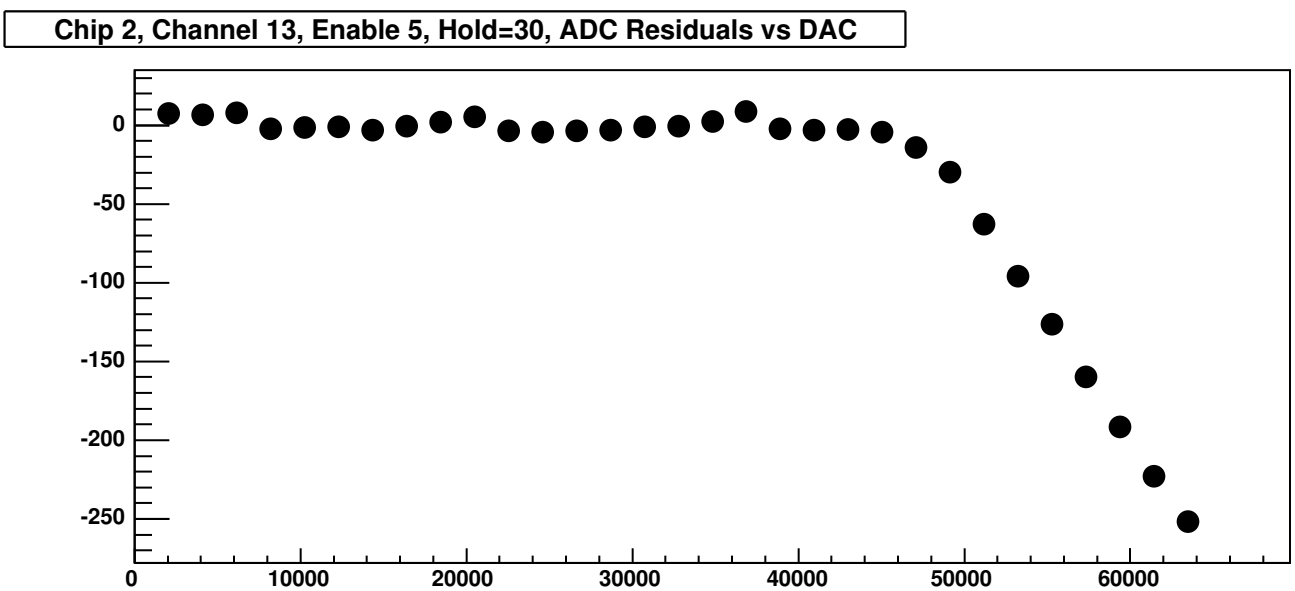

Chip 2, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 9707 / 15
p0 991.8 ± 0.2836
p1 0.6296 ± 2.687e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

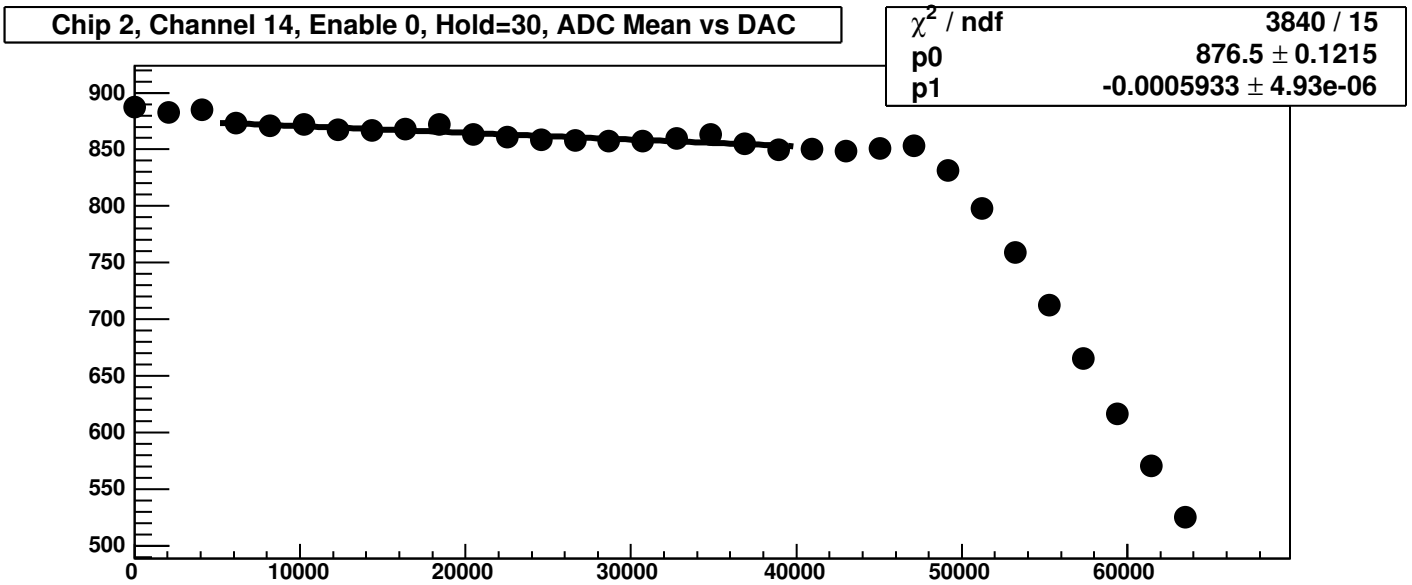

Chip 2, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

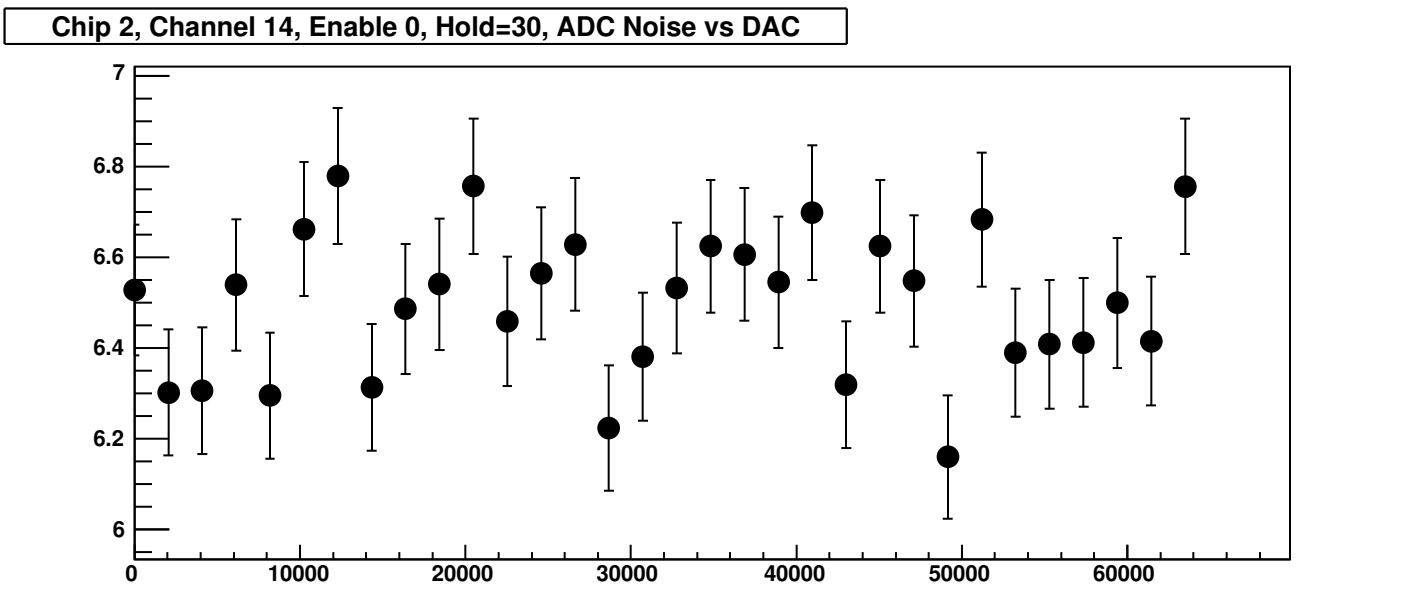

Chip 2, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

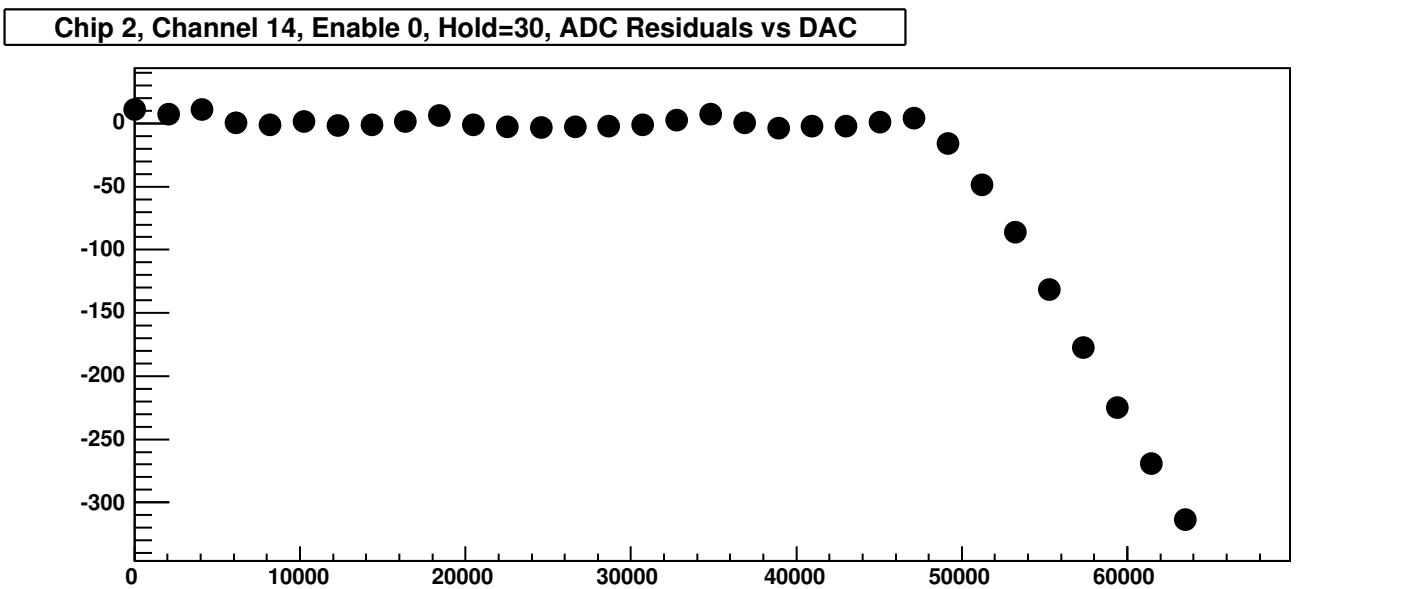

Chip 2, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6066 / 15
p0 870.6 ± 0.1219
p1 -0.0006713 ± 4.919e-06
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

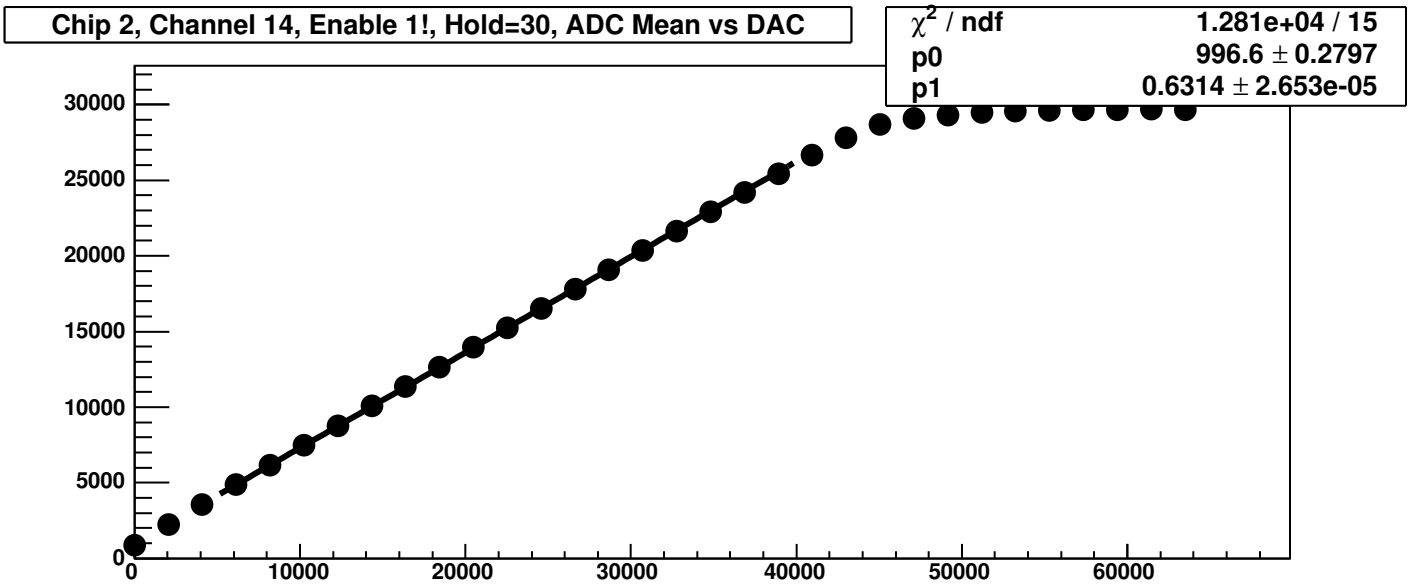

Chip 2, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

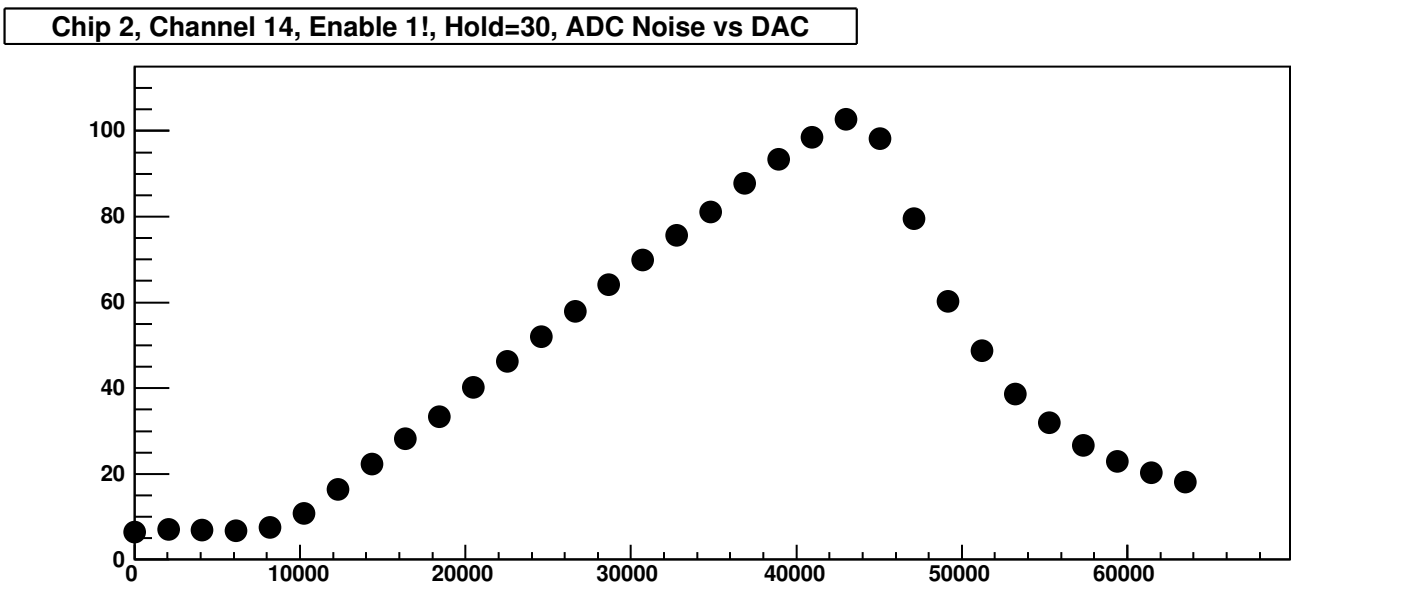

Chip 2, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

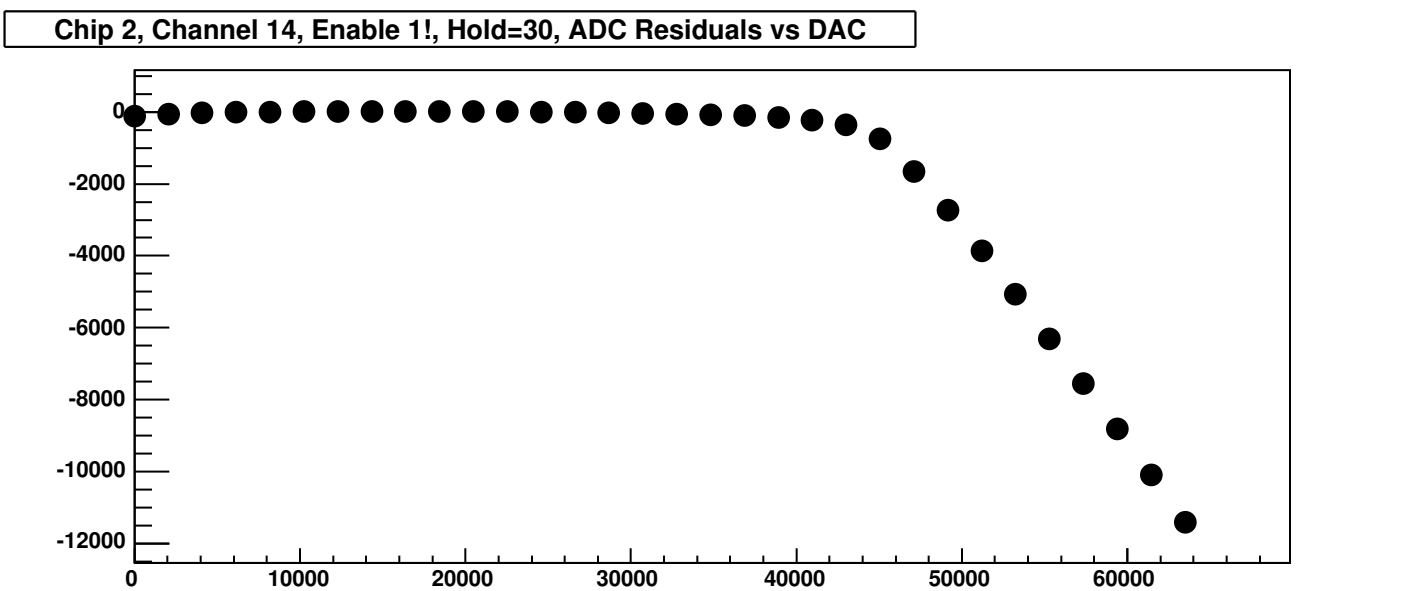

Chip 2, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3840 / 15
p0 876.5 ± 0.1215
p1 -0.0005933 ± 4.93e-06
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

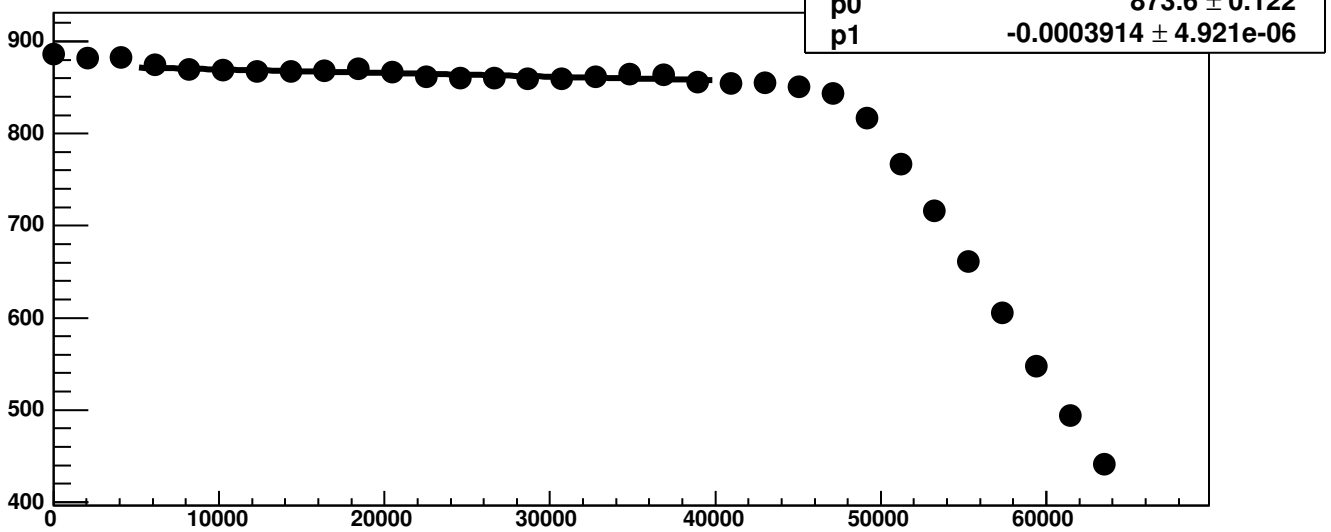

Chip 2, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.281e+04 / 15
p0 996.6 ± 0.2797
p1 0.6314 ± 2.653e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

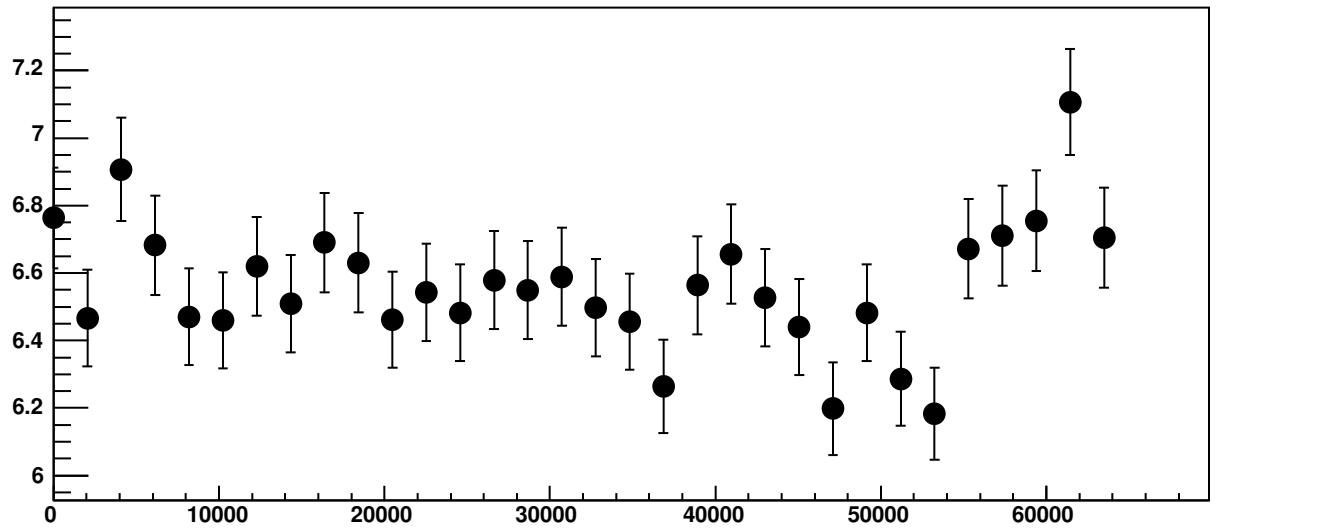

Chip 2, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

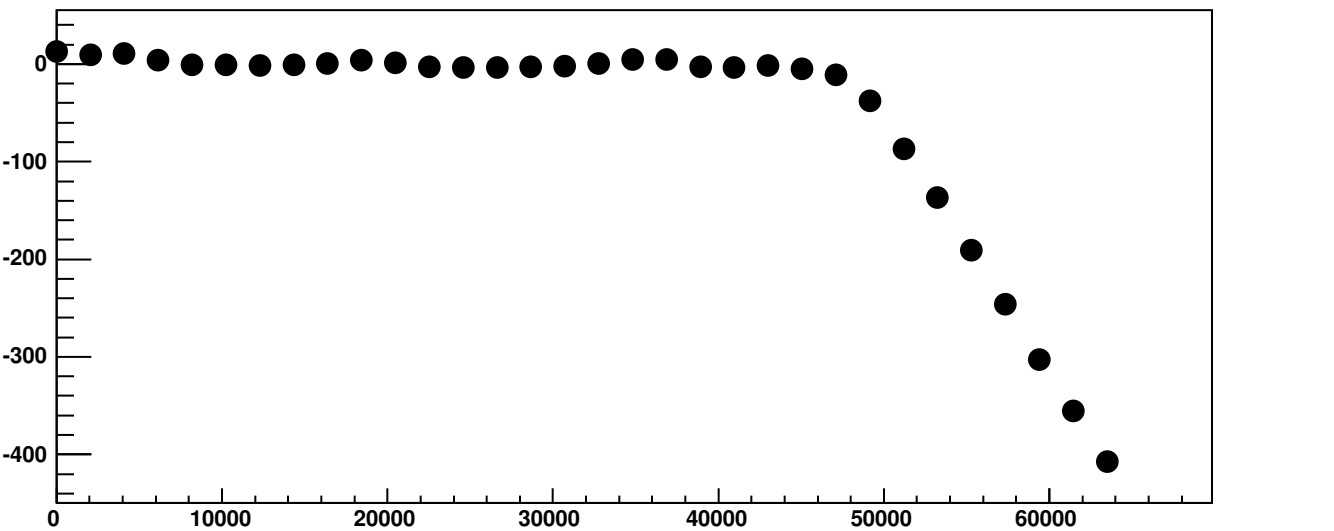
Chip 2, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC

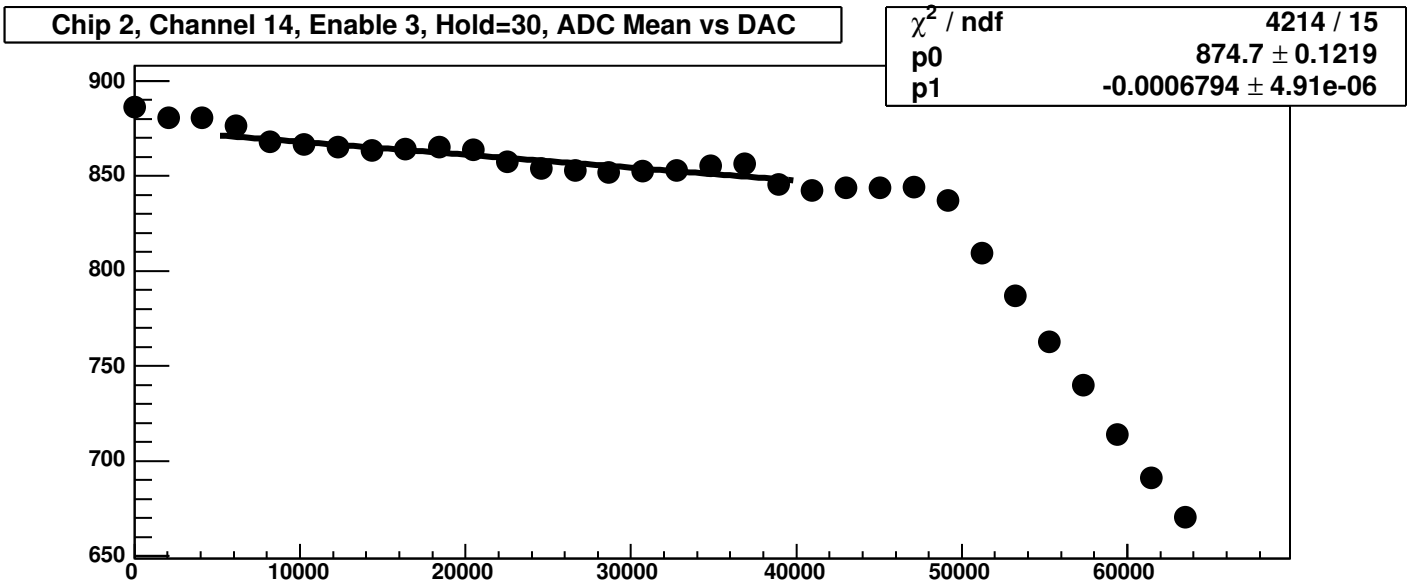


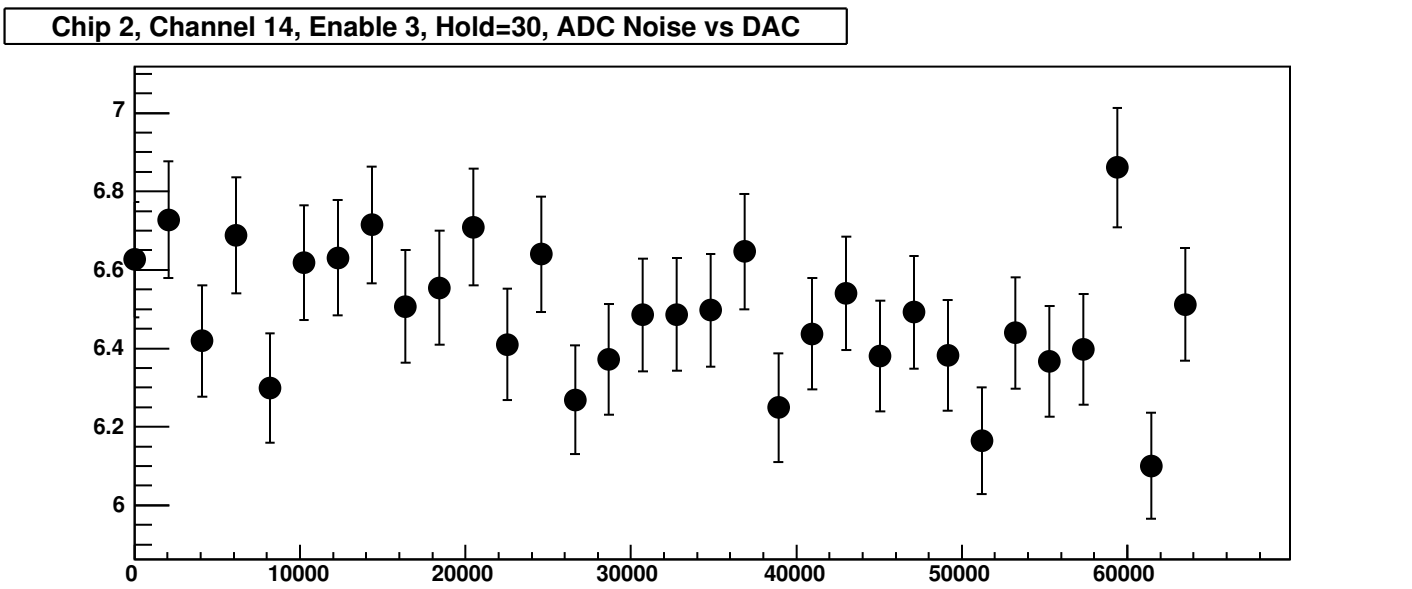
Chip 2, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC

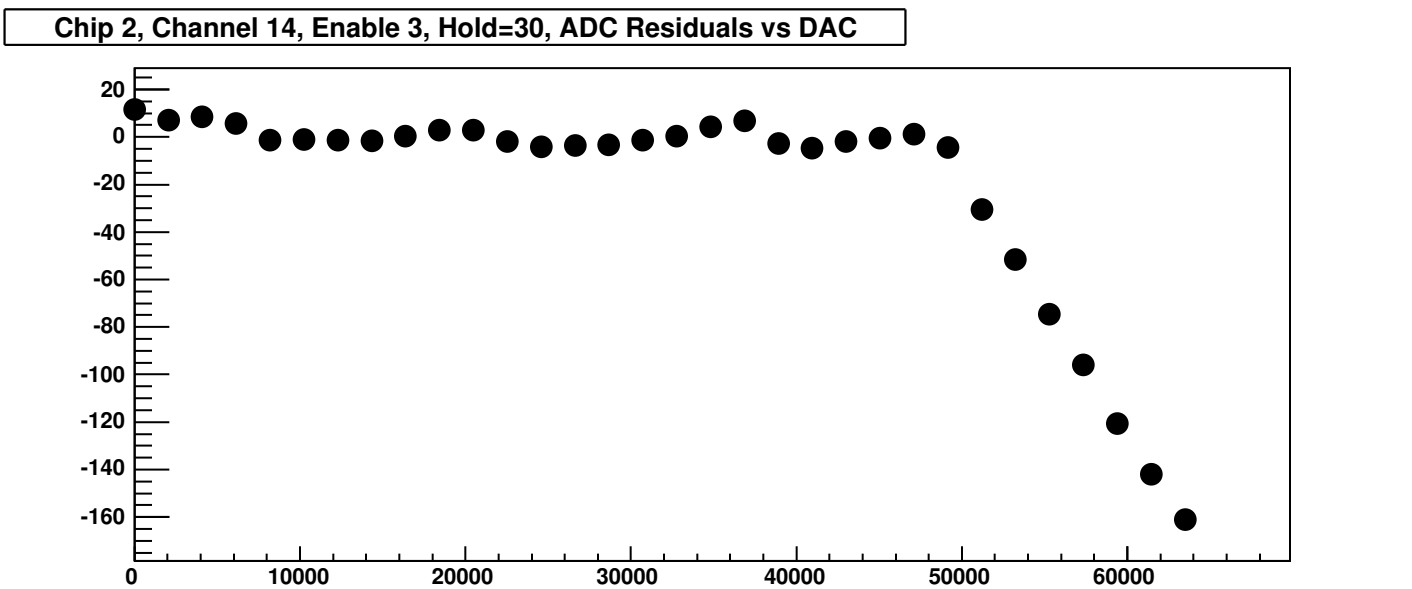


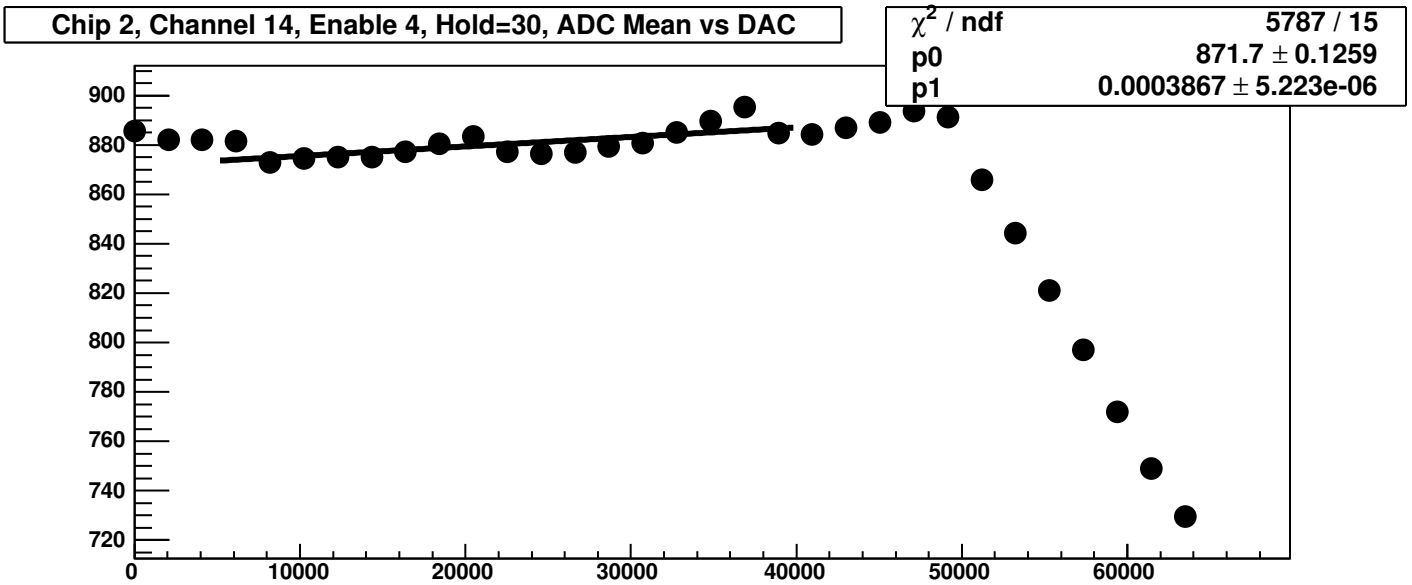
Chip 2, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

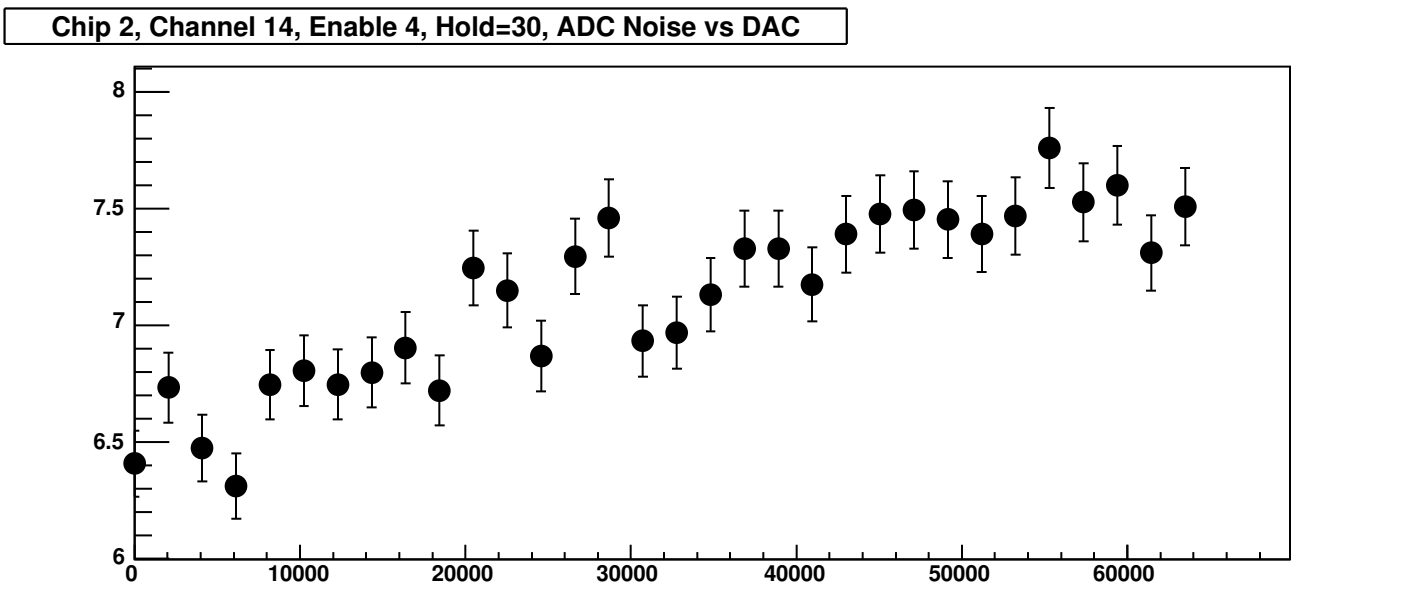


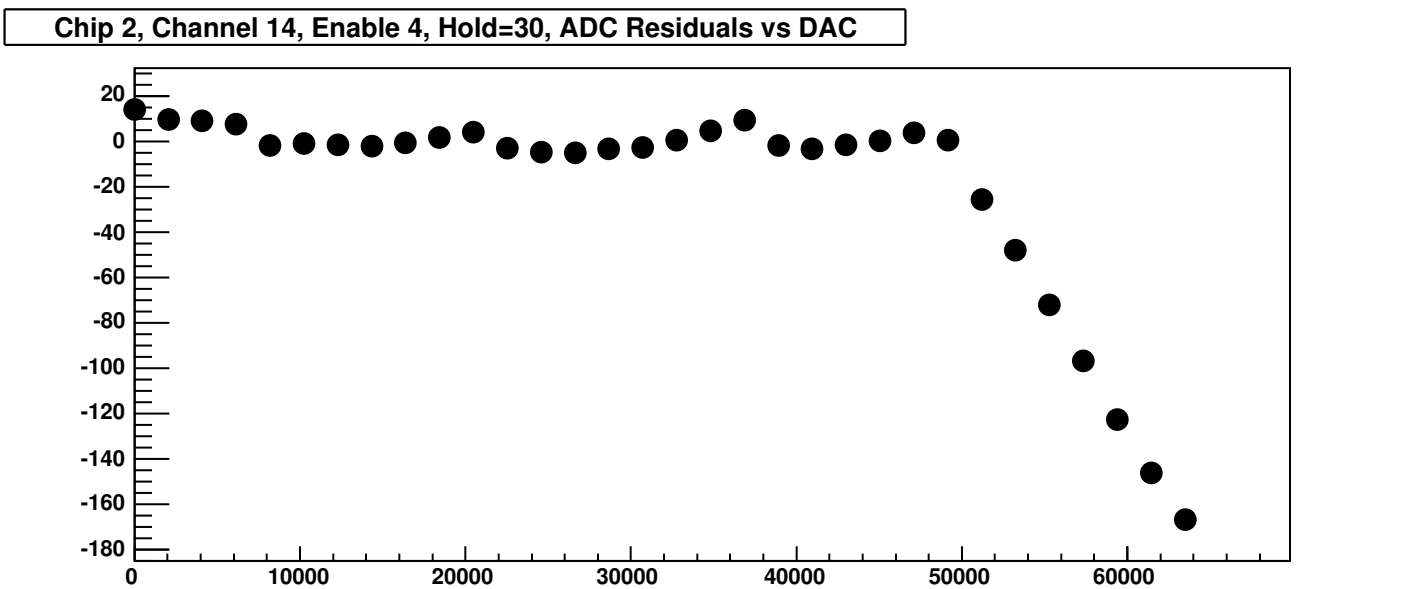

Chip 2, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4214 / 15
p0 874.7 ± 0.1219
p1 -0.0006794 ± 4.91e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

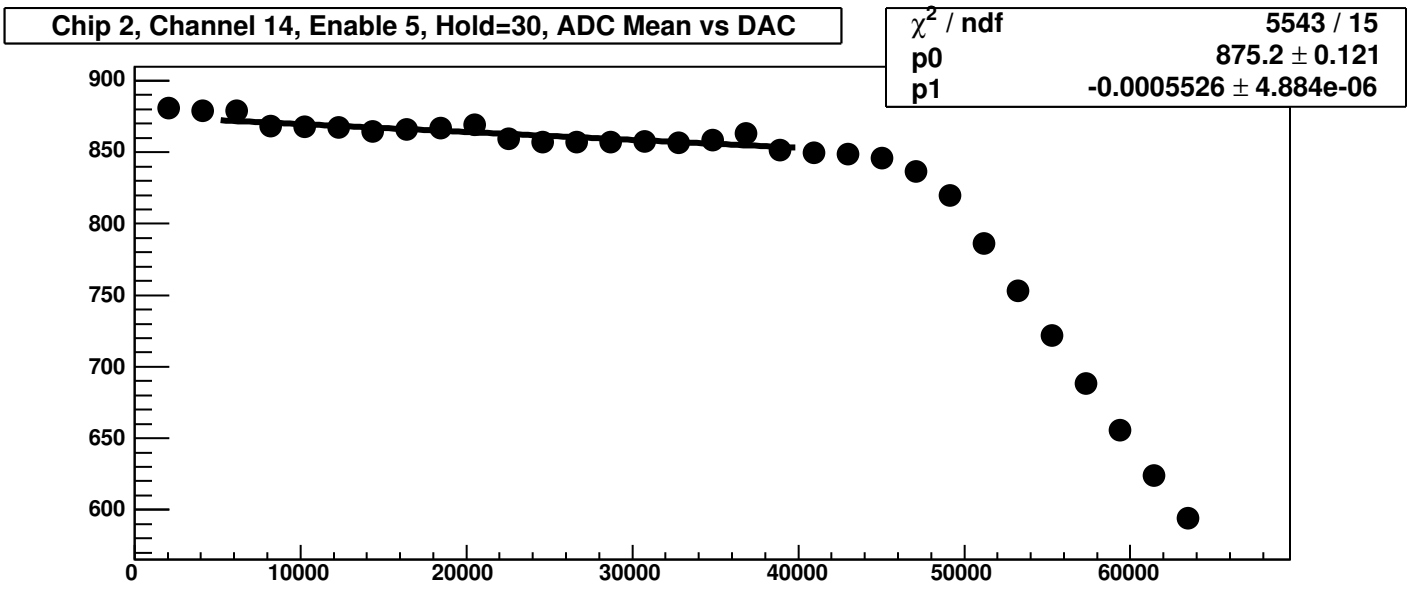

Chip 2, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

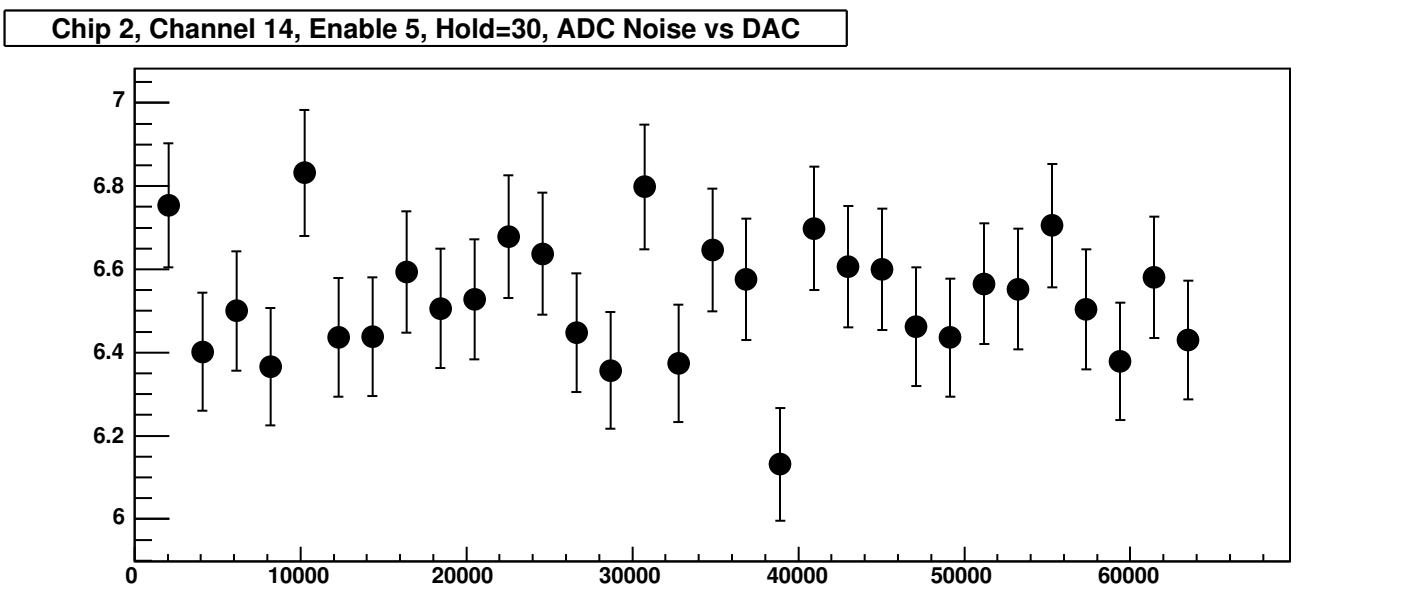

Chip 2, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

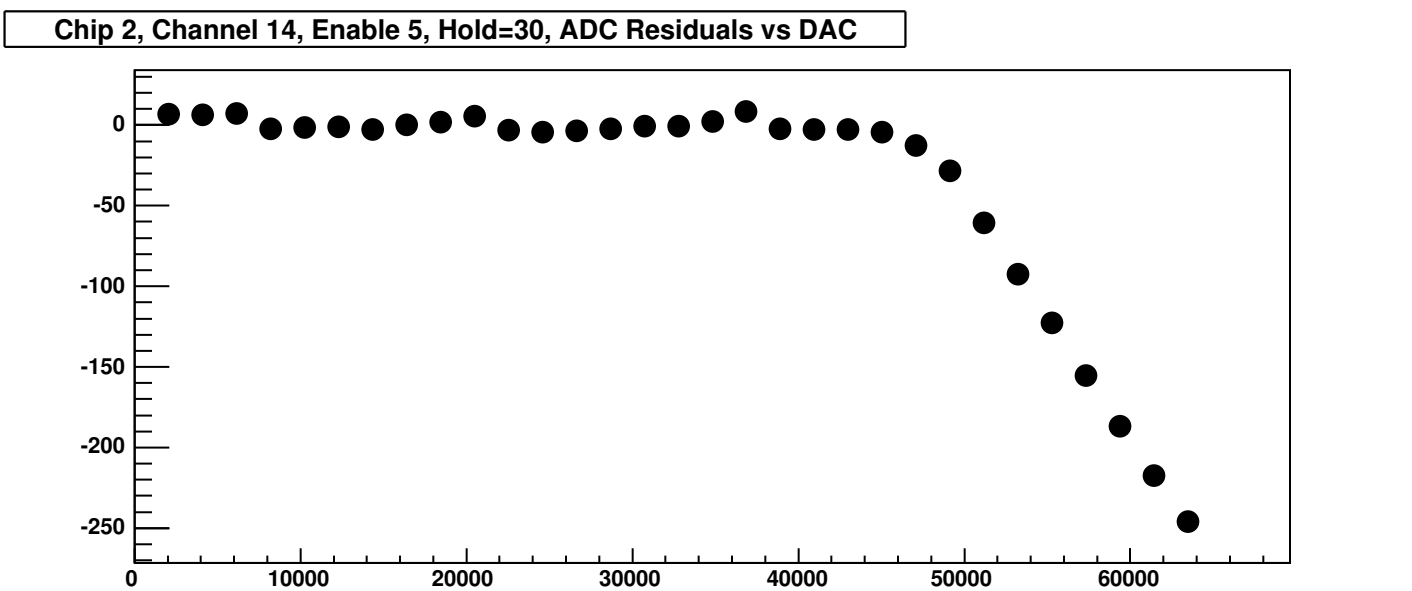

Chip 2, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5787 / 15
p0 871.7 ± 0.1259
p1 0.0003867 ± 5.223e-06
900
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

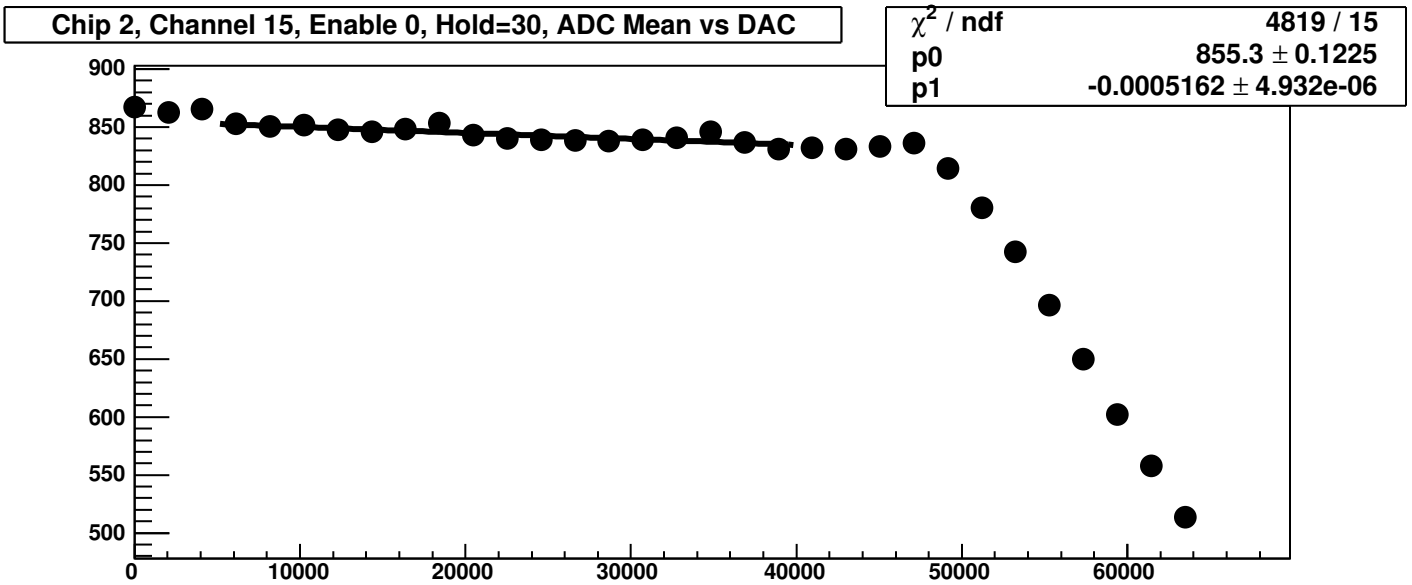

Chip 2, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC
8
7.5
7
6.5
6
0
10000
20000
30000
40000
50000
60000

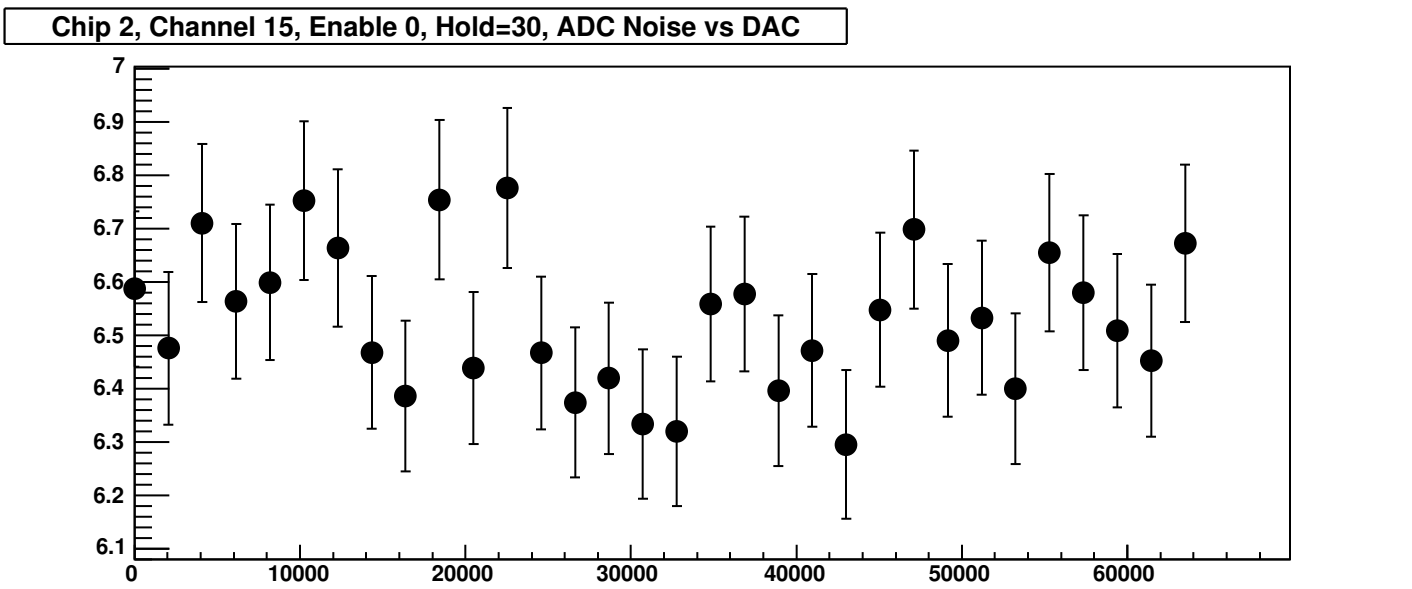

Chip 2, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

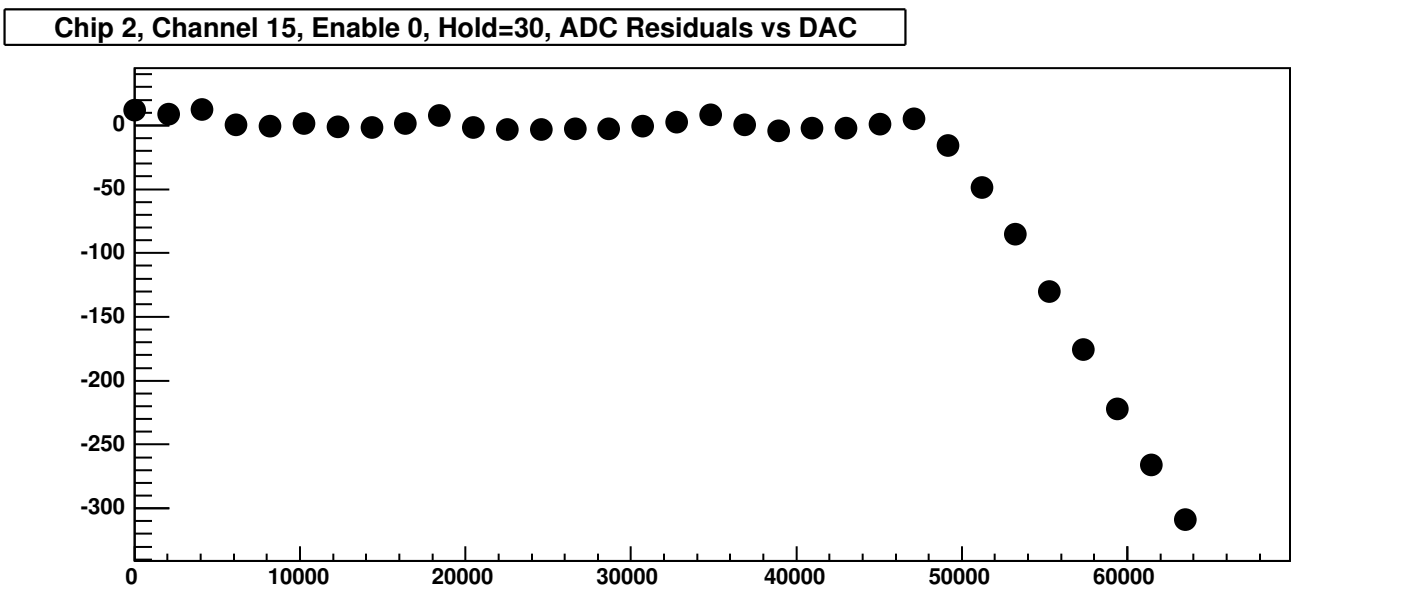

Chip 2, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5543 / 15
p0 875.2 ± 0.121
p1 -0.0005526 ± 4.884e-06
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

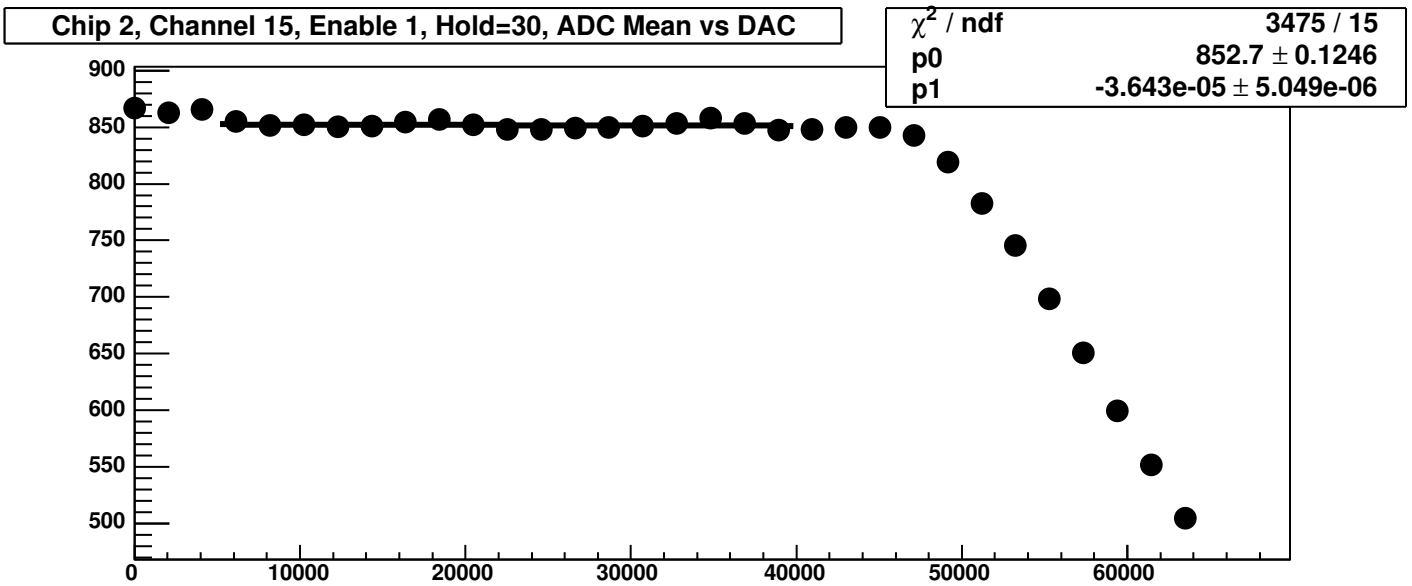

Chip 2, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

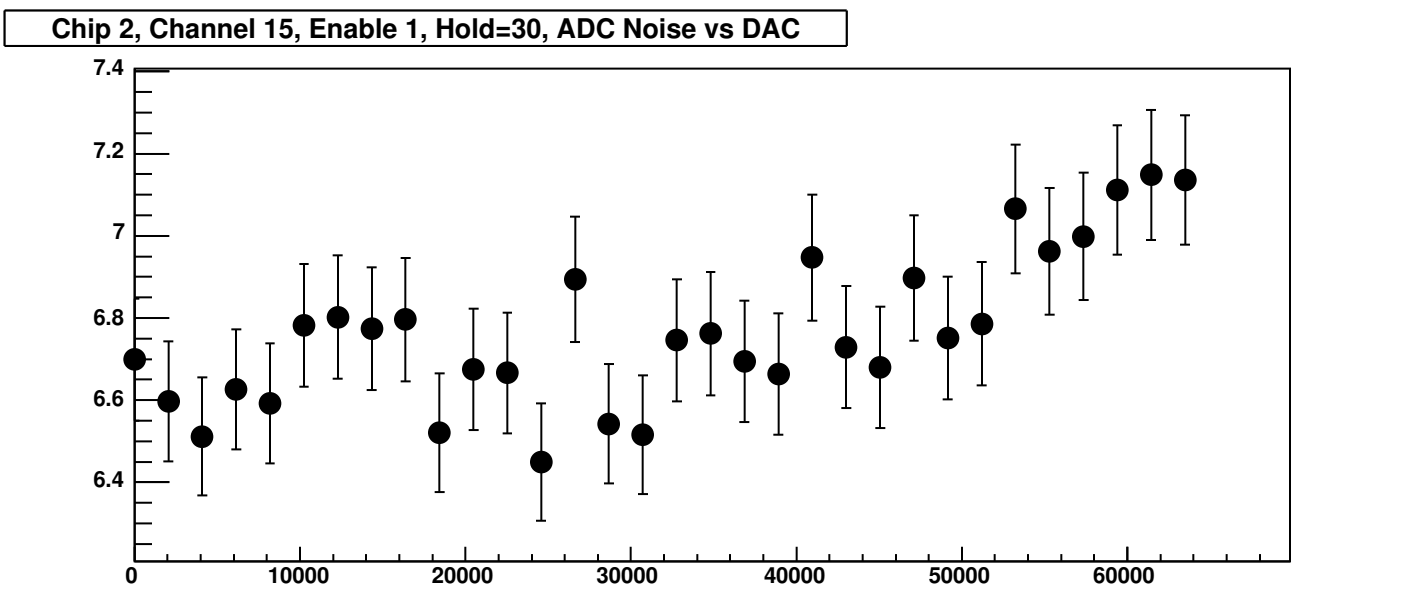

Chip 2, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

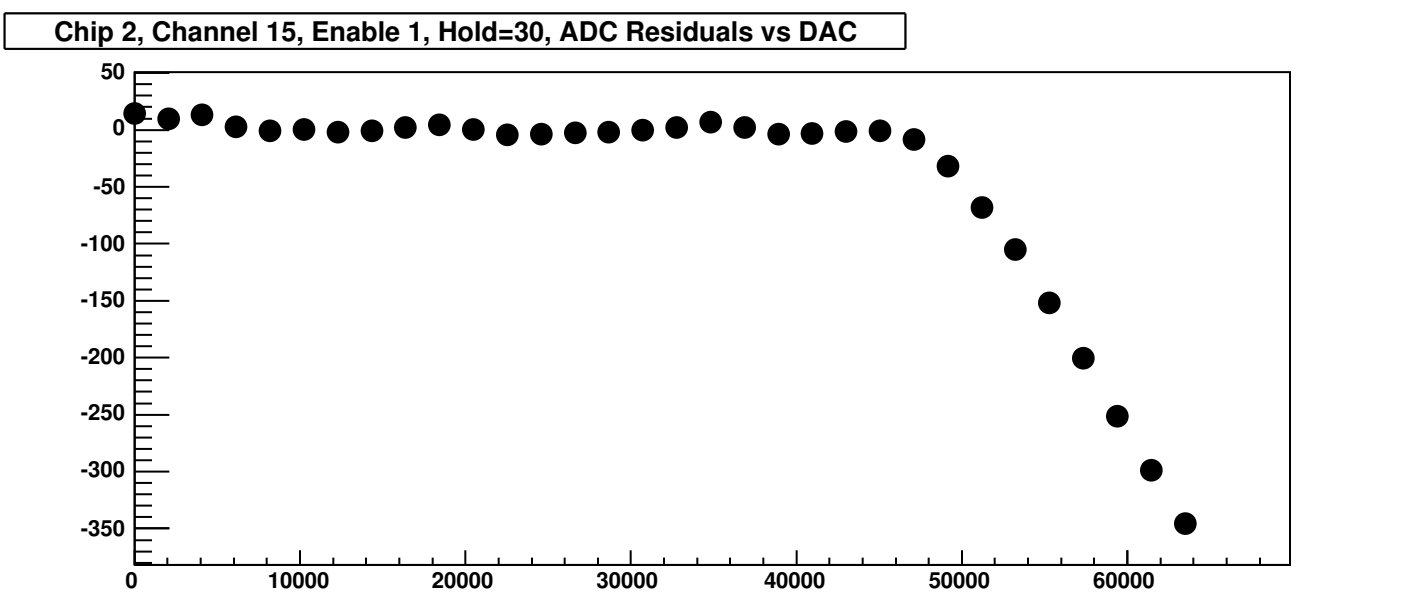

Chip 2, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4819 / 15
p0 855.3 ± 0.1225
p1 -0.0005162 ± 4.932e-06
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

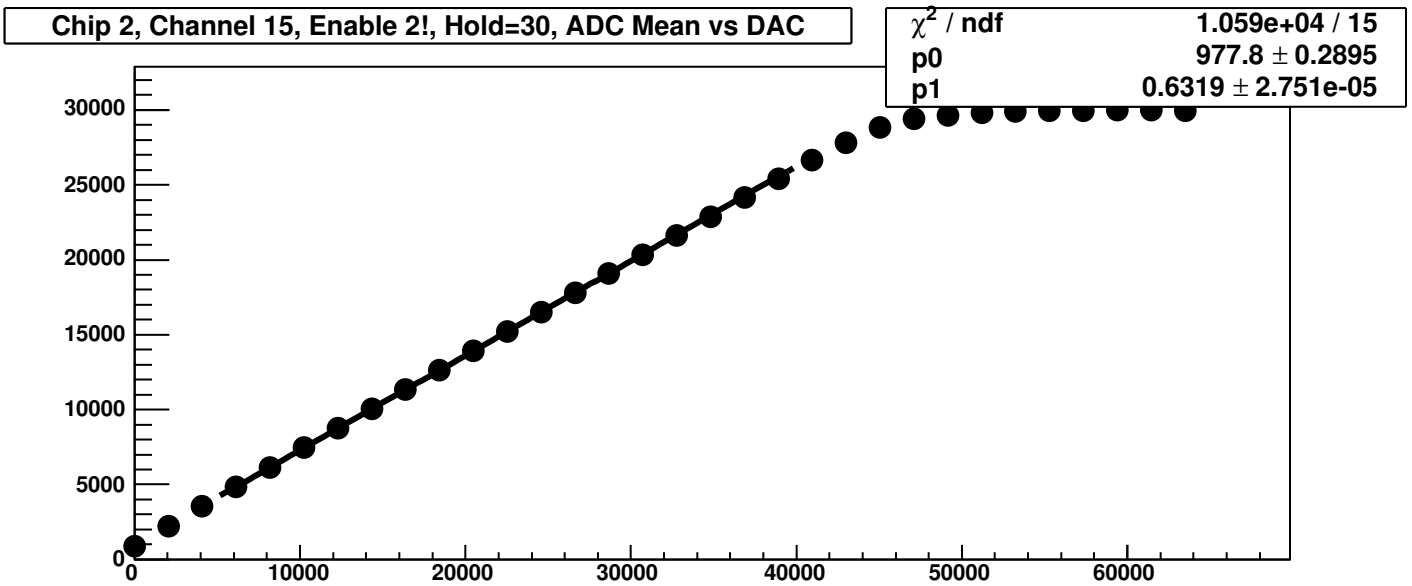

Chip 2, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
7
6.9
6.8
6.7
6.6
6.5
6.4
6.3
6.2
6.1
0
10000
20000
30000
40000
50000
60000

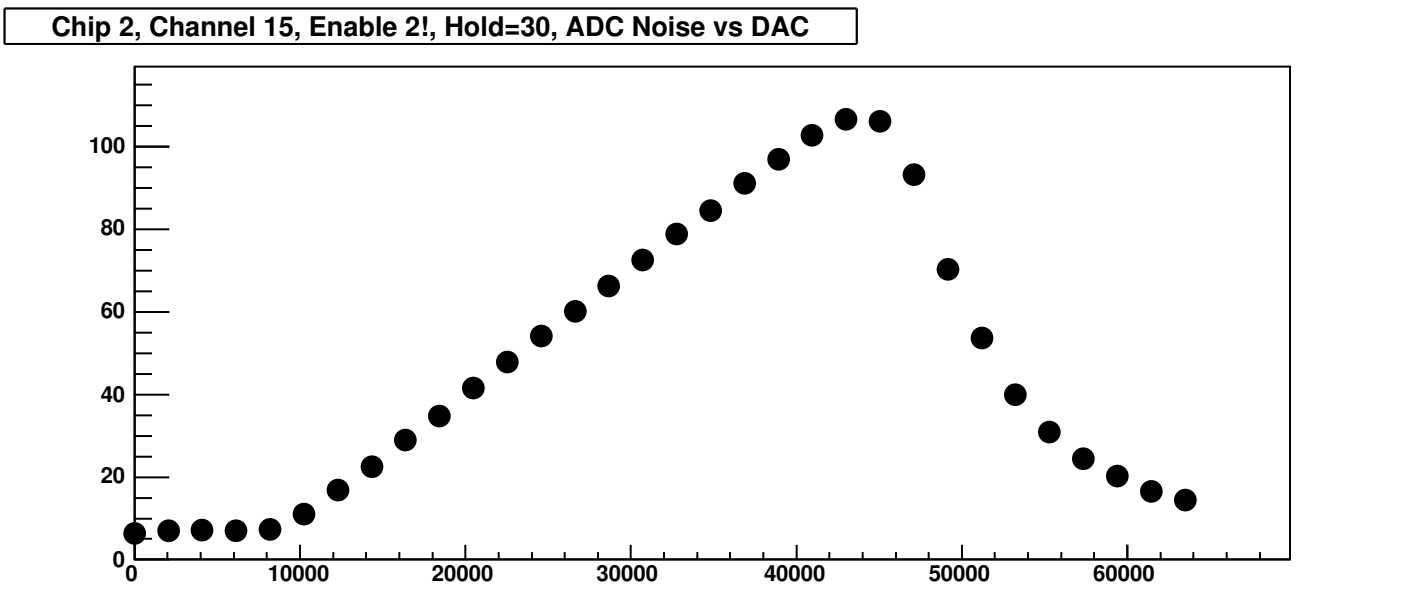

Chip 2, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

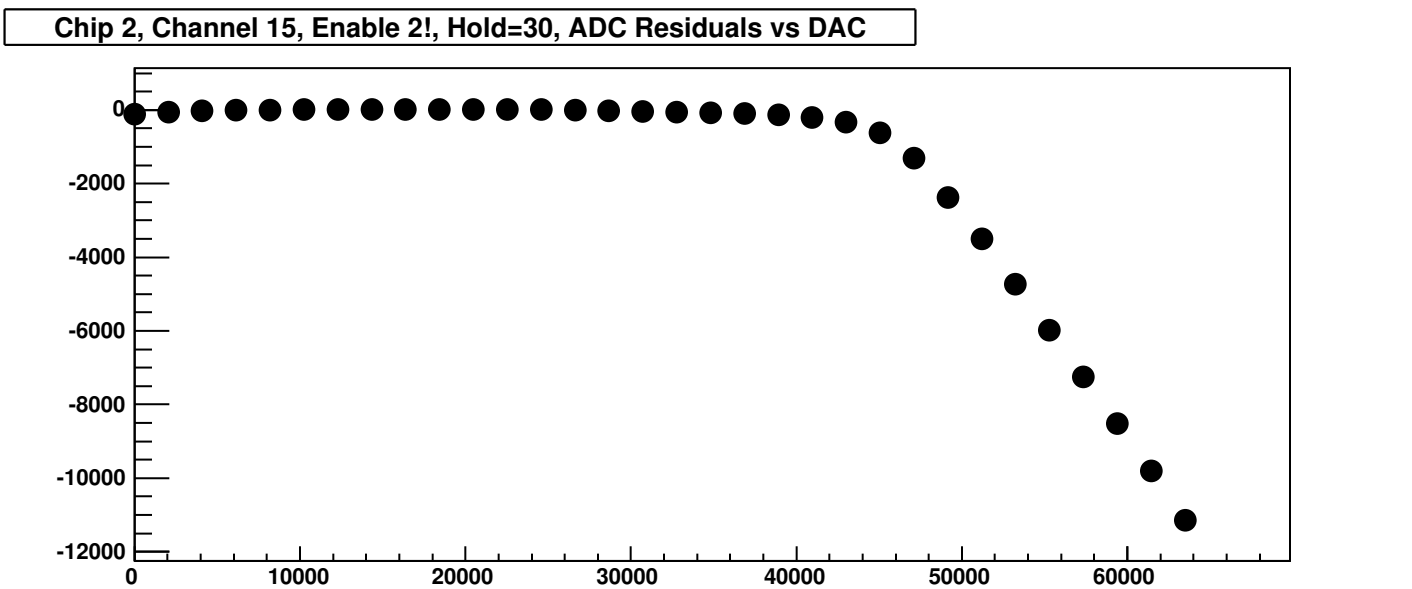

Chip 2, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3475 / 15
p0 852.7 ± 0.1246
p1 -3.643e-05 ± 5.049e-06
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

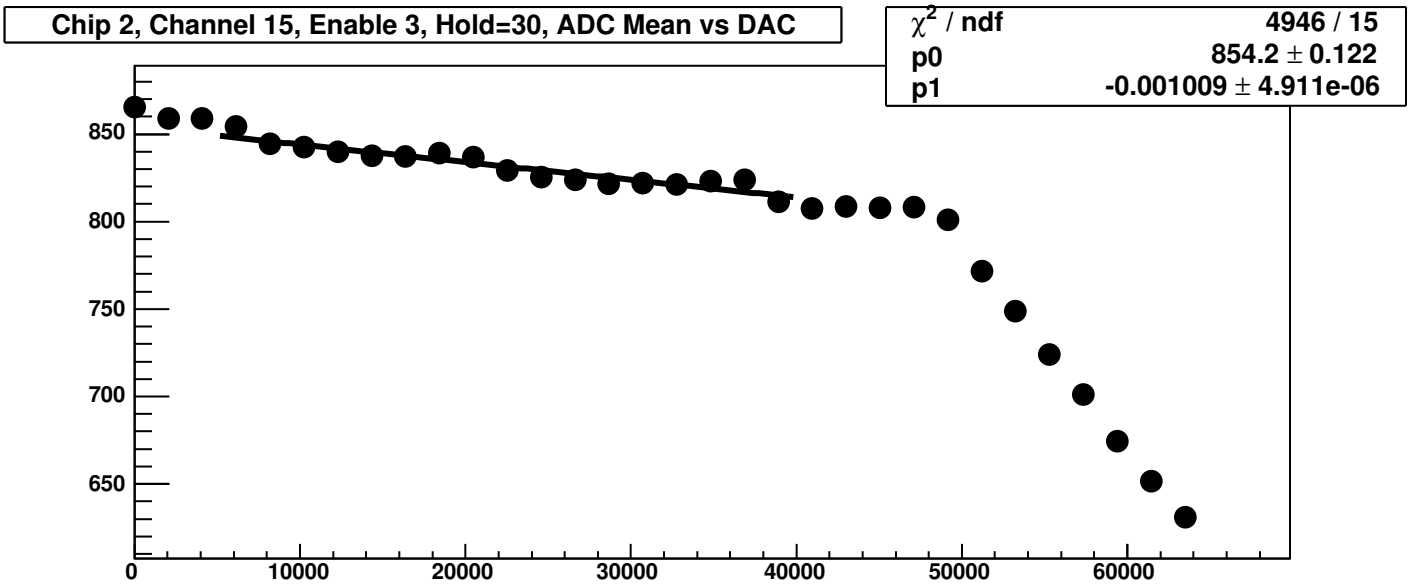

Chip 2, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

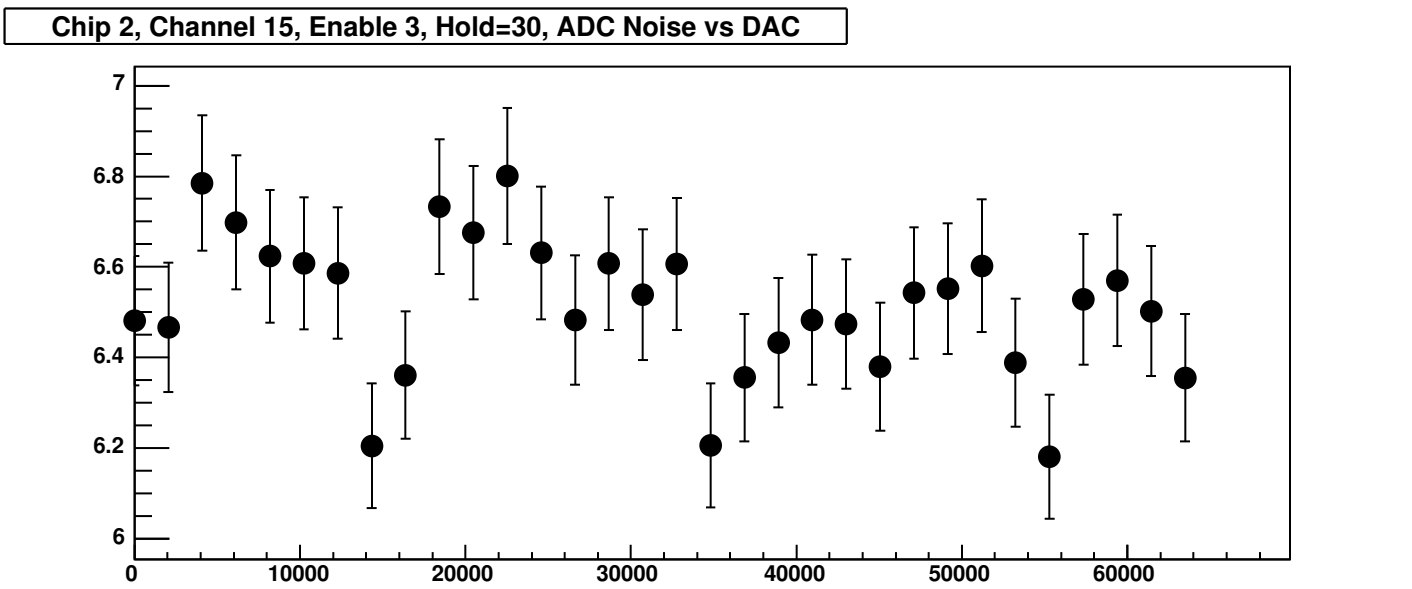

Chip 2, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

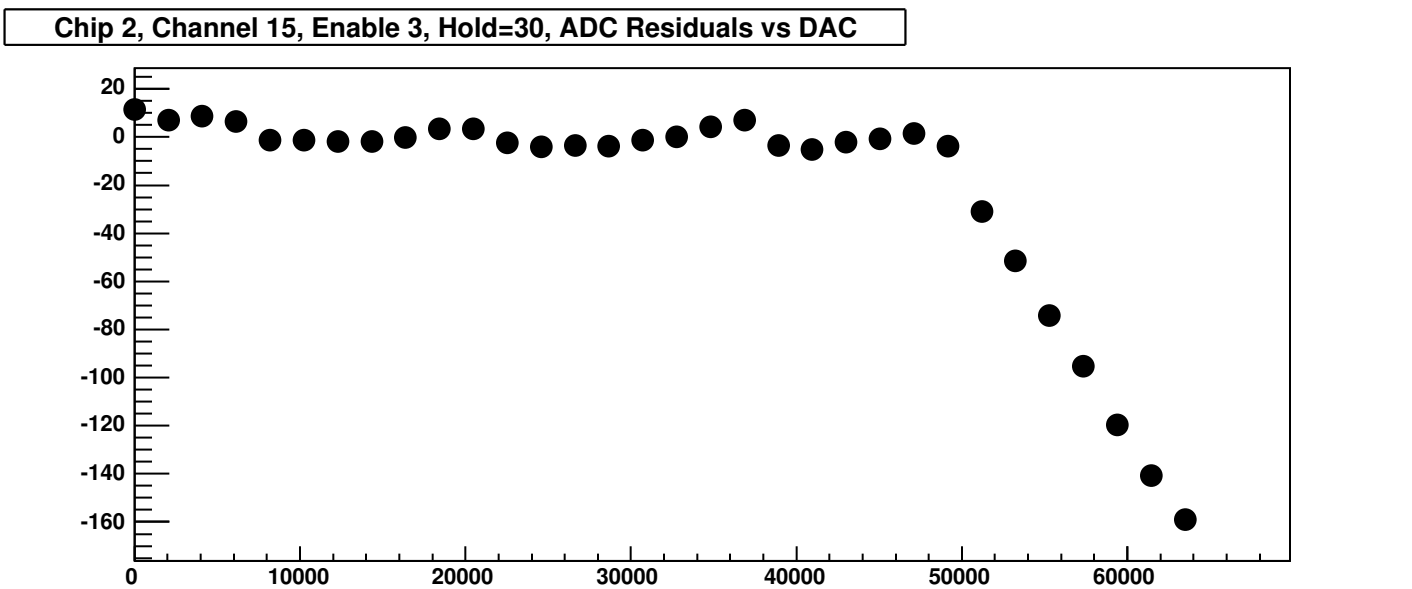

Chip 2, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.059e+04 / 15
p0 977.8 ± 0.2895
p1 0.6319 ± 2.751e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

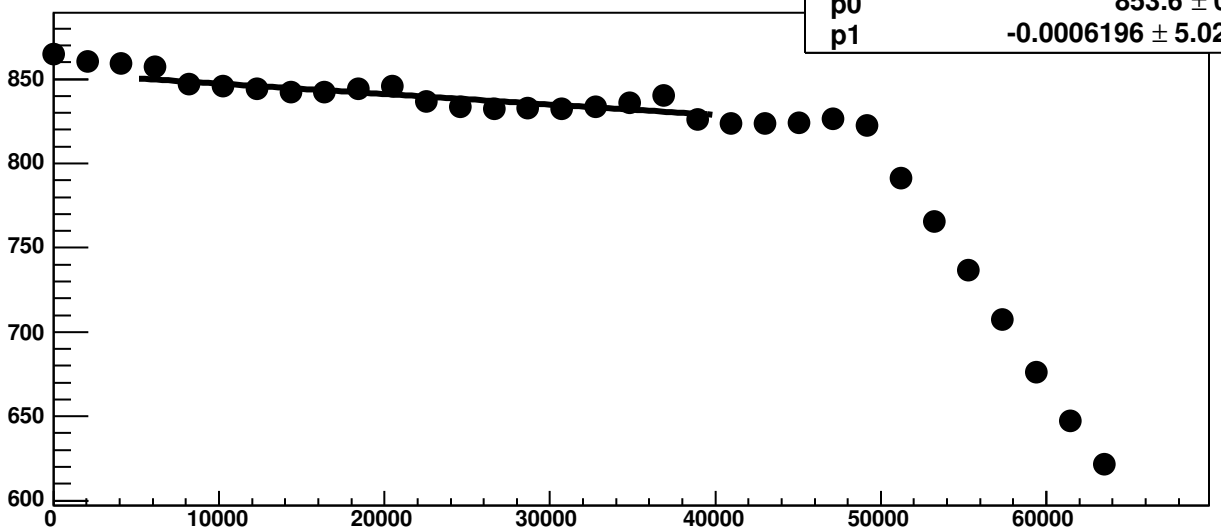

Chip 2, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4946 / 15
p0 854.2 ± 0.122
p1 -0.001009 ± 4.911e-06
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

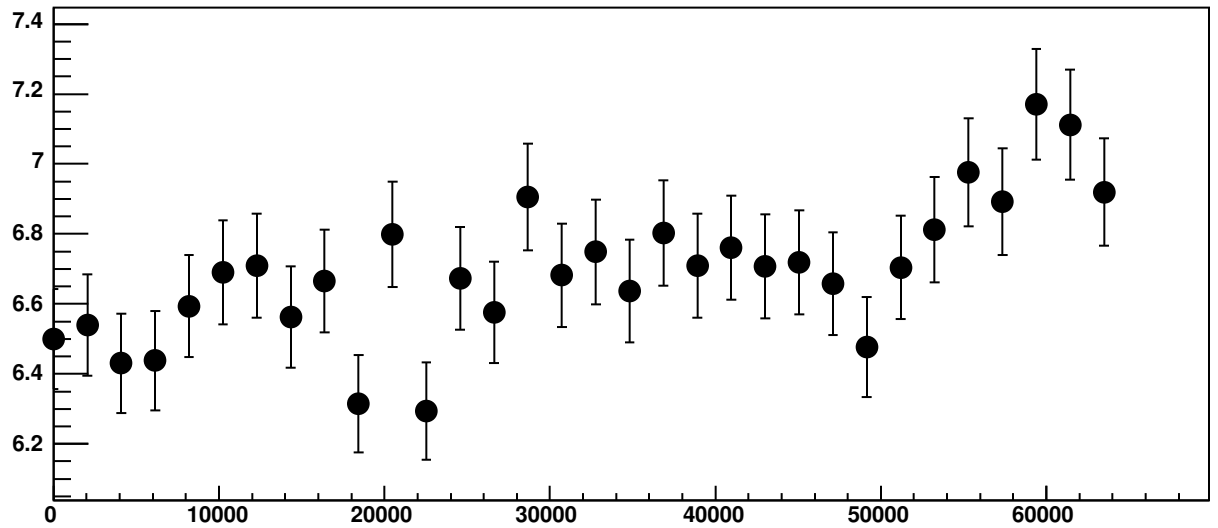

Chip 2, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

Chip 2, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

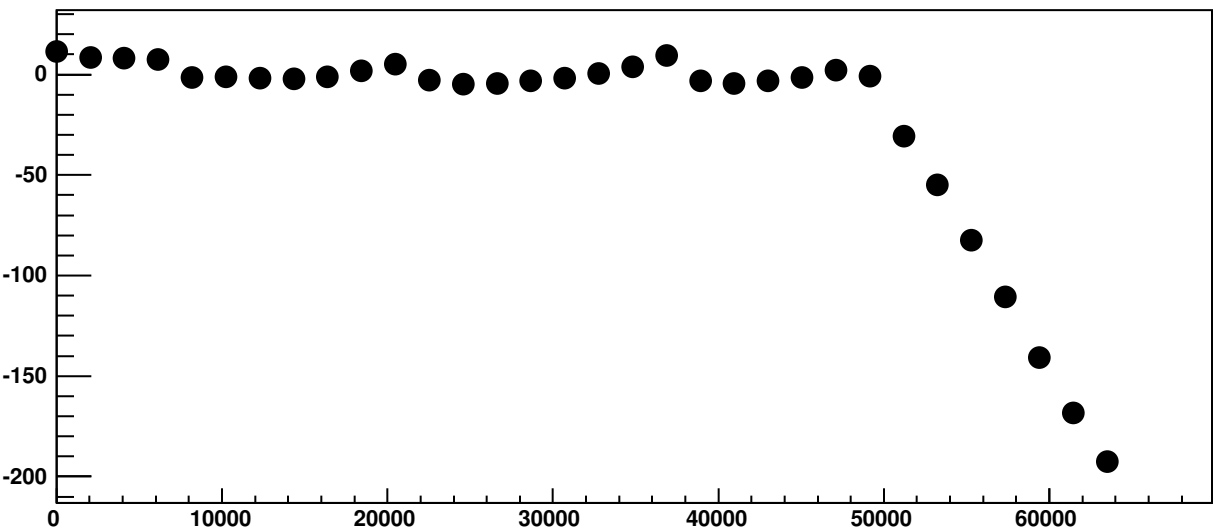
χ^2 / ndf	6461 / 15
p0	853.6 ± 0.1229
p1	-0.0006196 ± 5.022e-06



Chip 2, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC

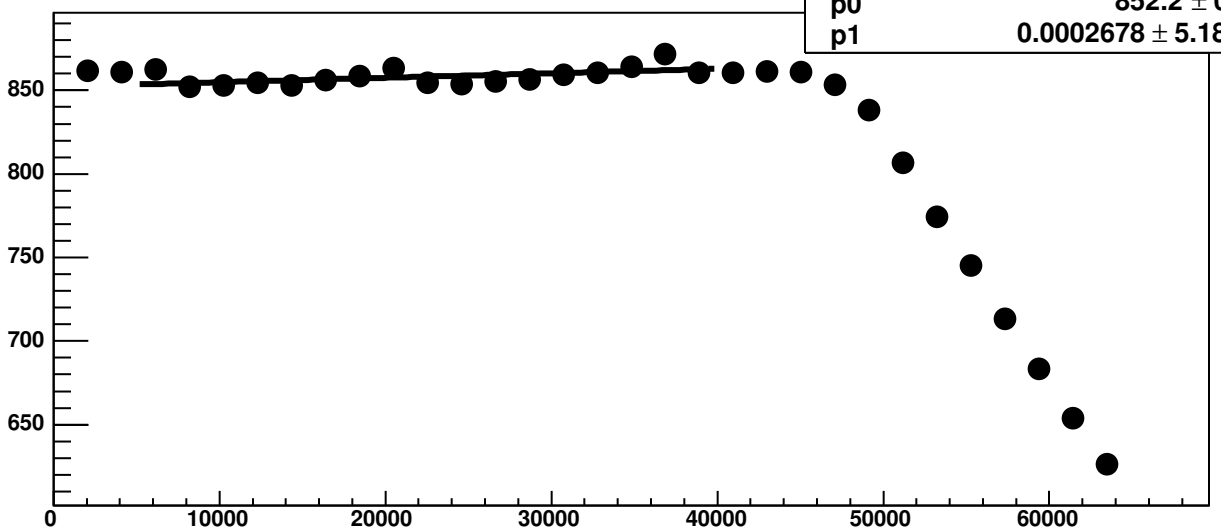


Chip 2, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

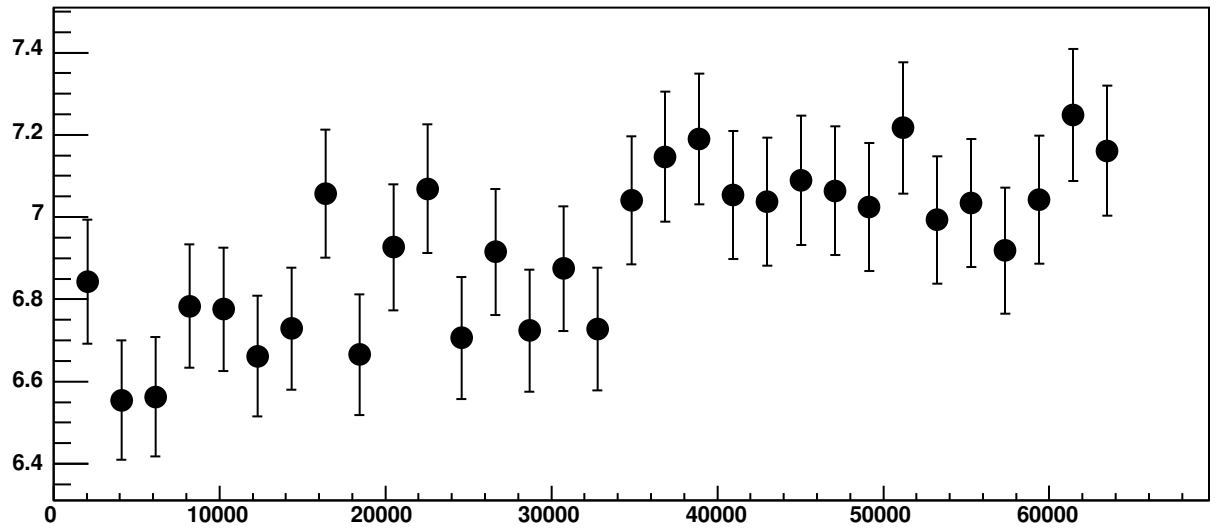


Chip 2, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC

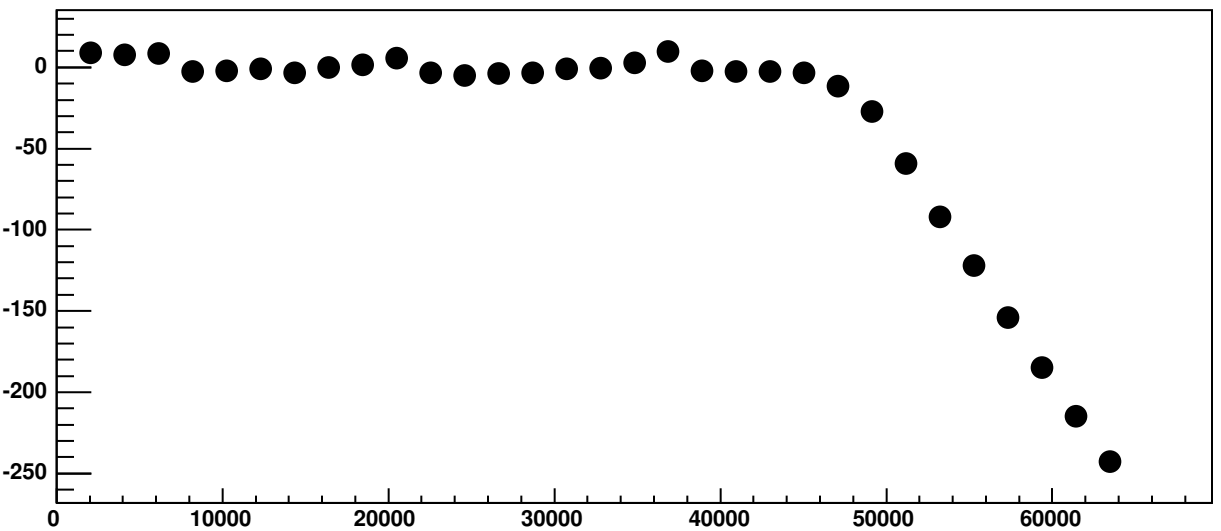
χ^2 / ndf	6459 / 15
p0	852.2 ± 0.1263
p1	0.0002678 ± 5.187e-06

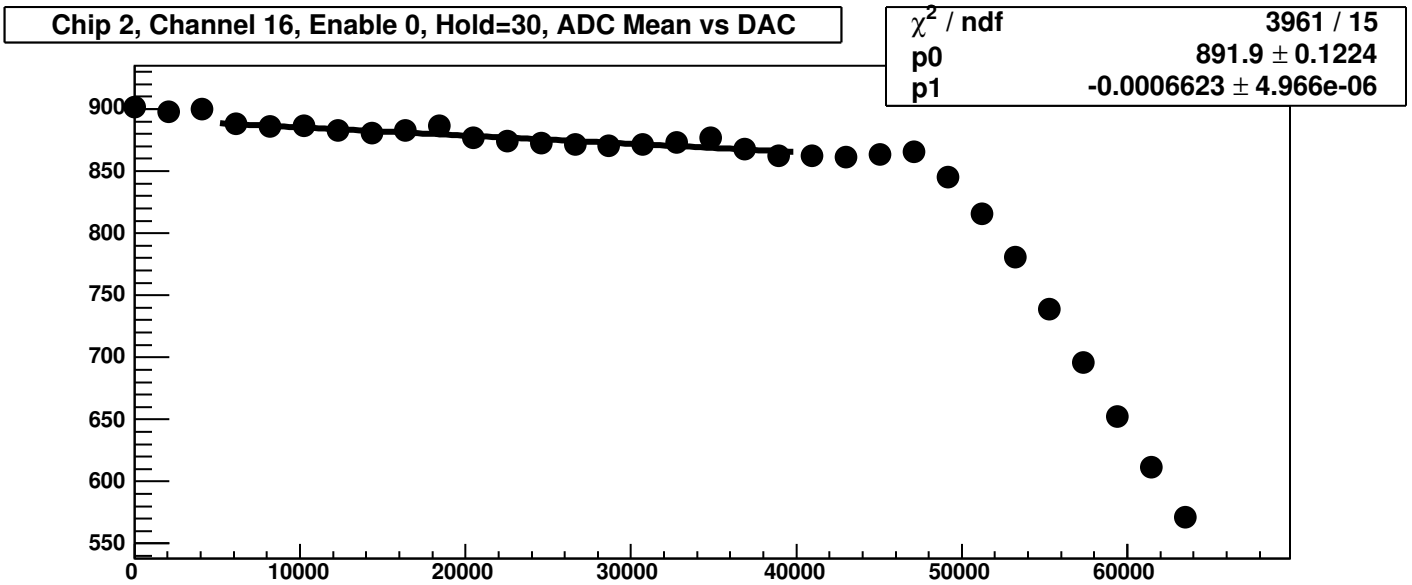


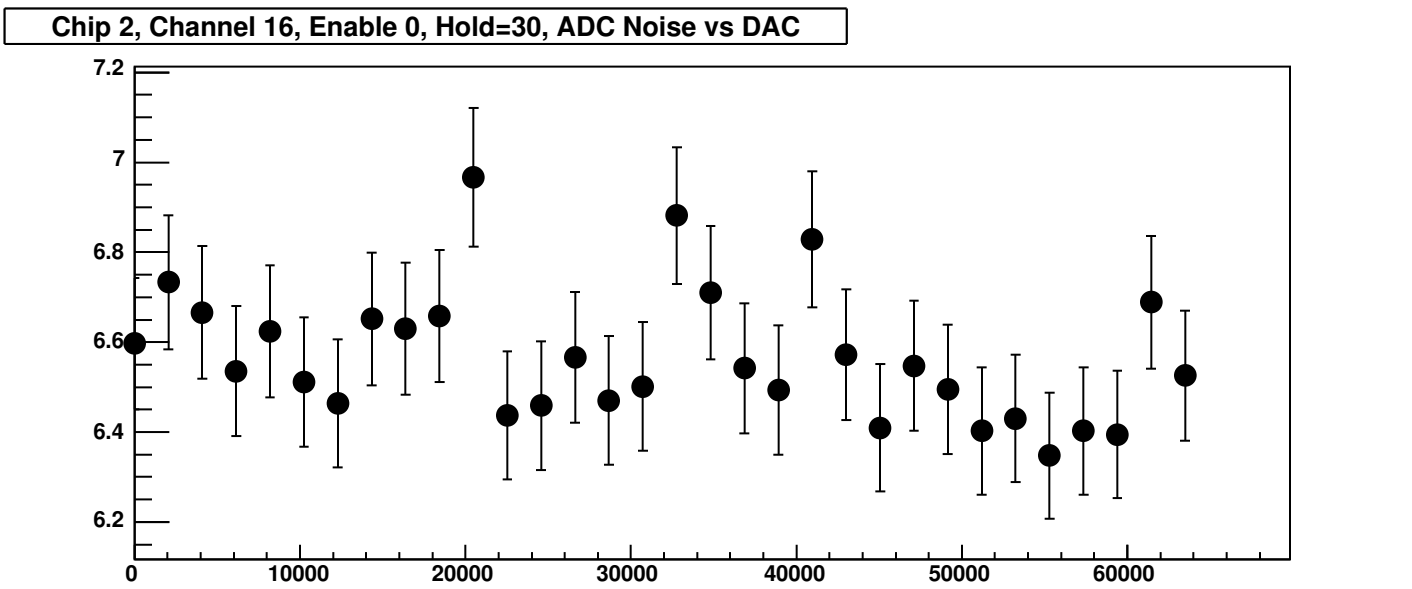
Chip 2, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC

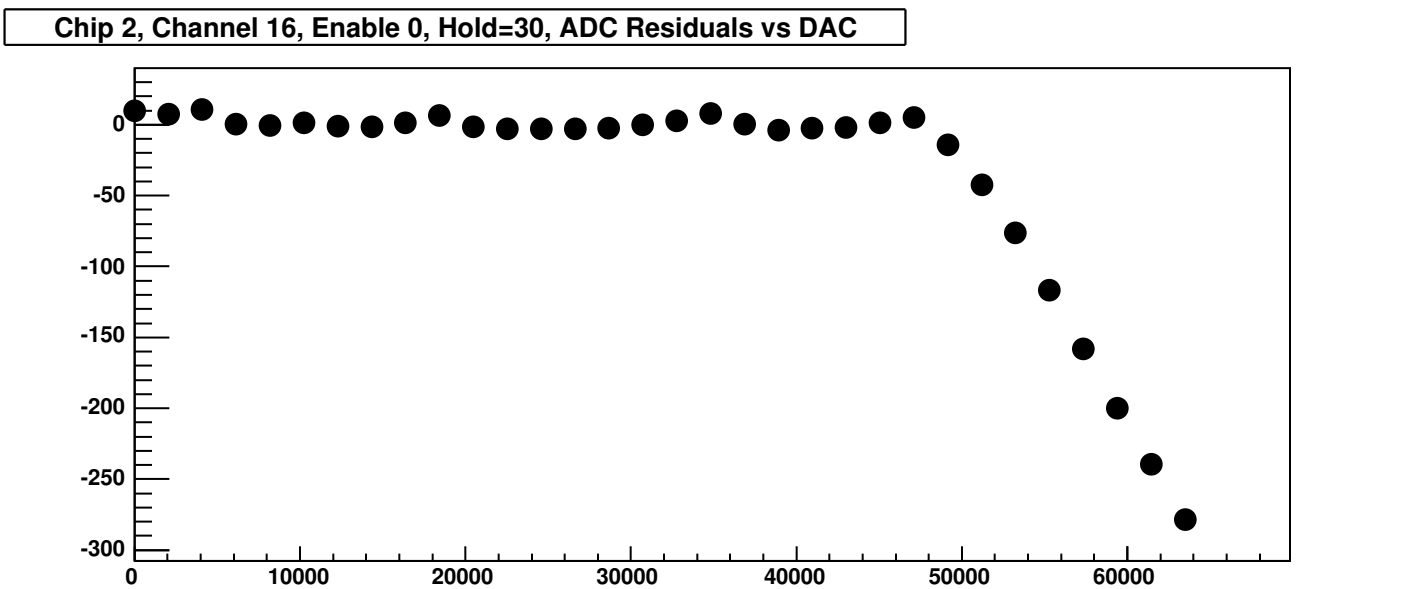


Chip 2, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC



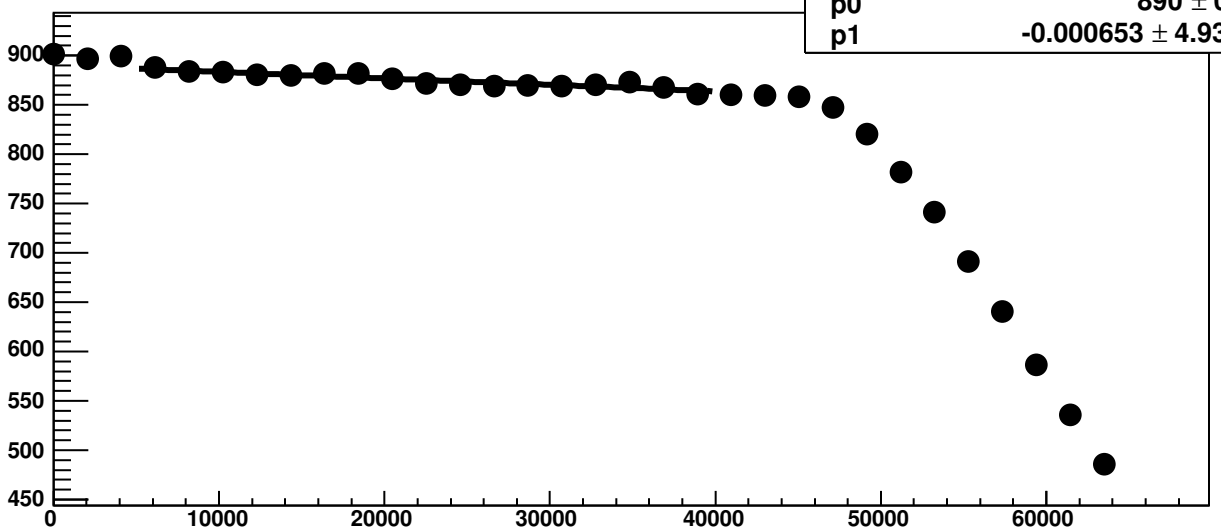

Chip 2, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3961 / 15
p0 891.9 ± 0.1224
p1 -0.0006623 ± 4.966e-06
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

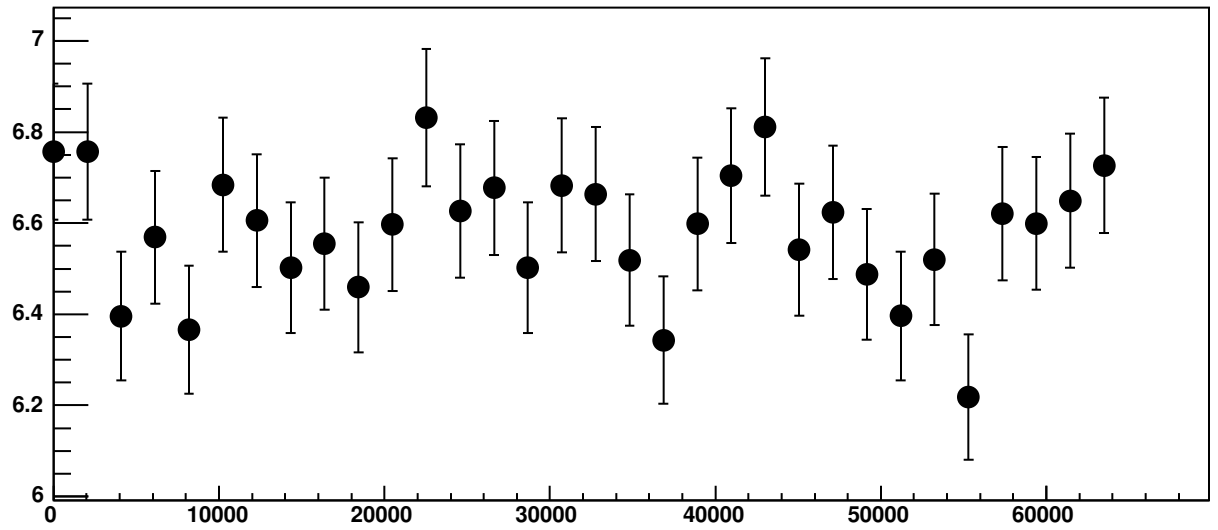

Chip 2, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

Chip 2, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC

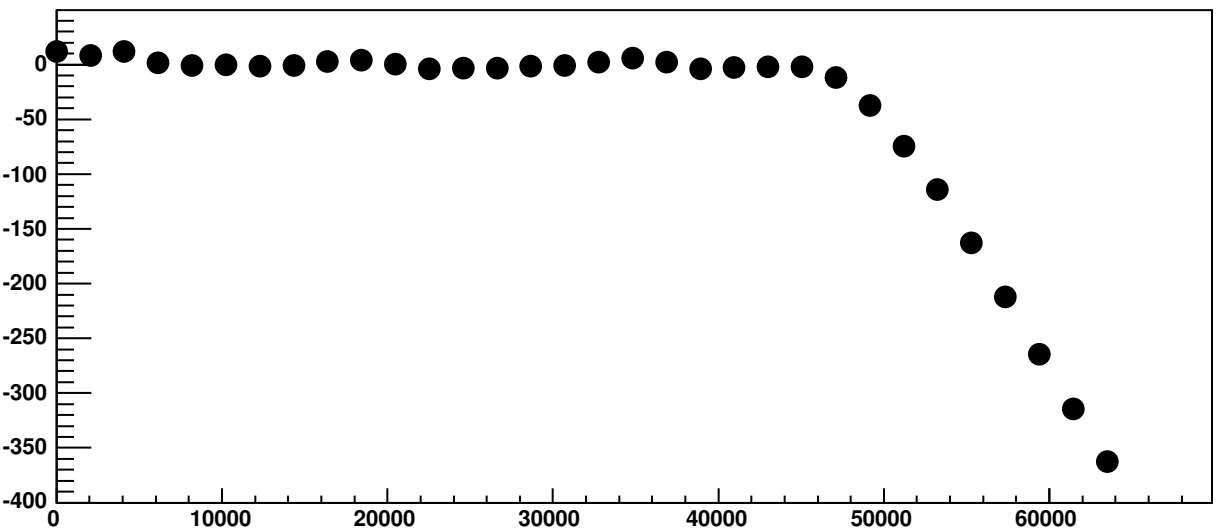
χ^2 / ndf	2890 / 15
p0	890 ± 0.1218
p1	-0.000653 ± 4.935e-06

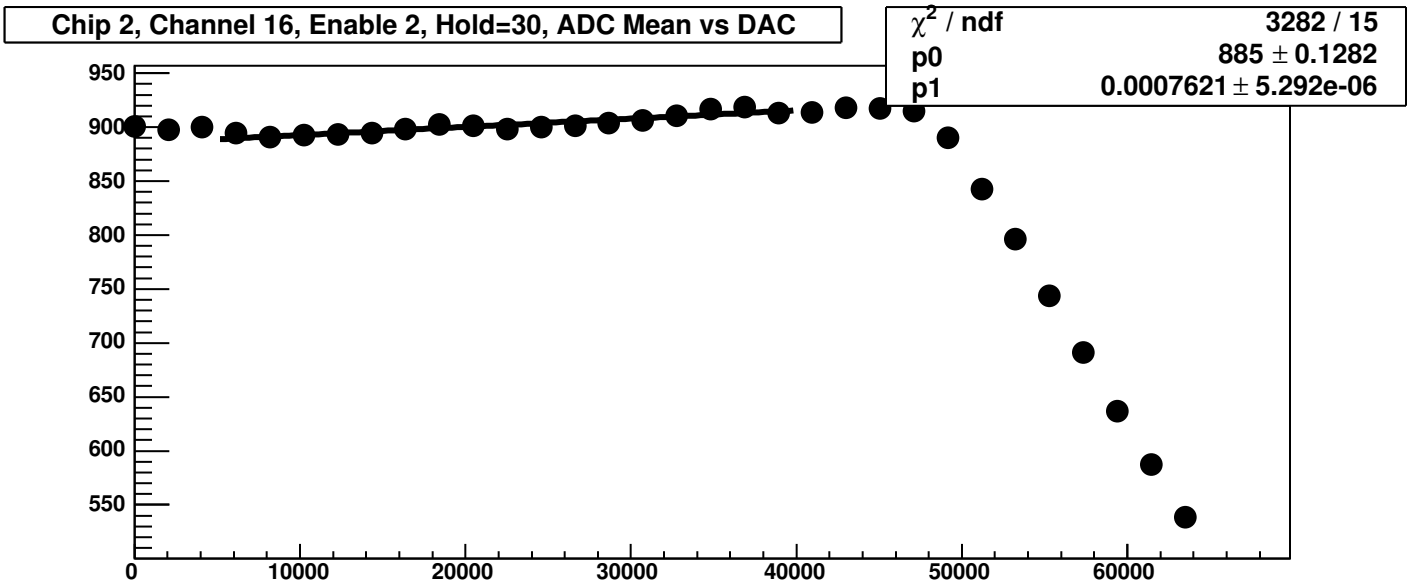


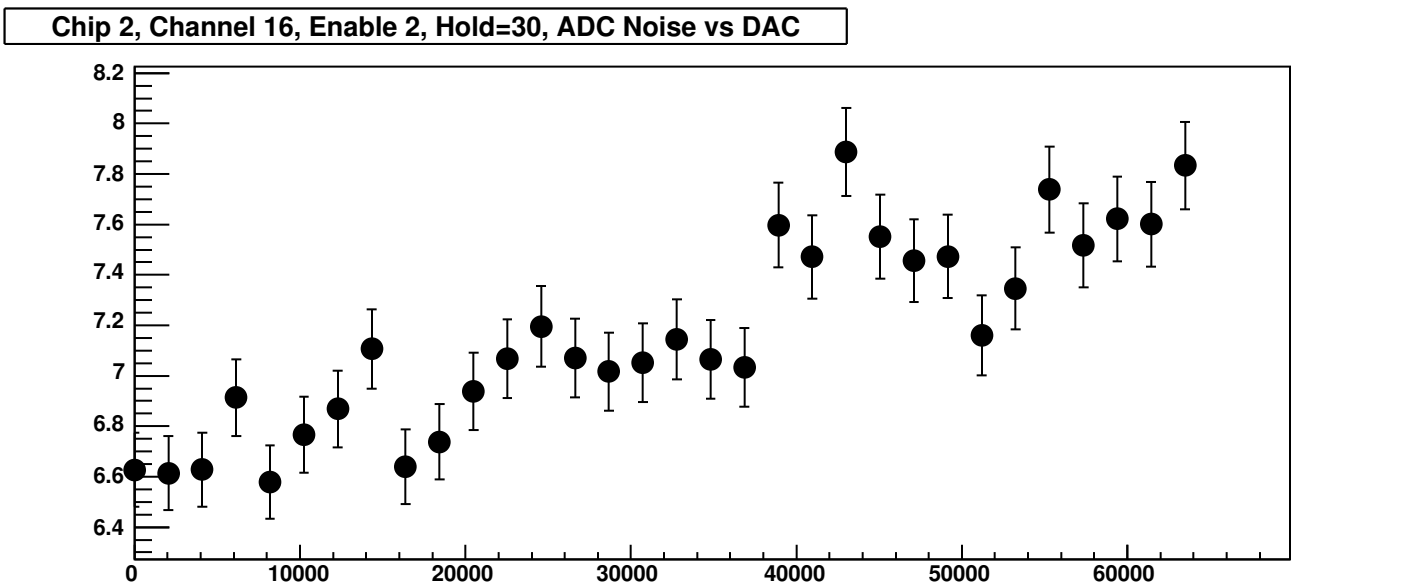
Chip 2, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC

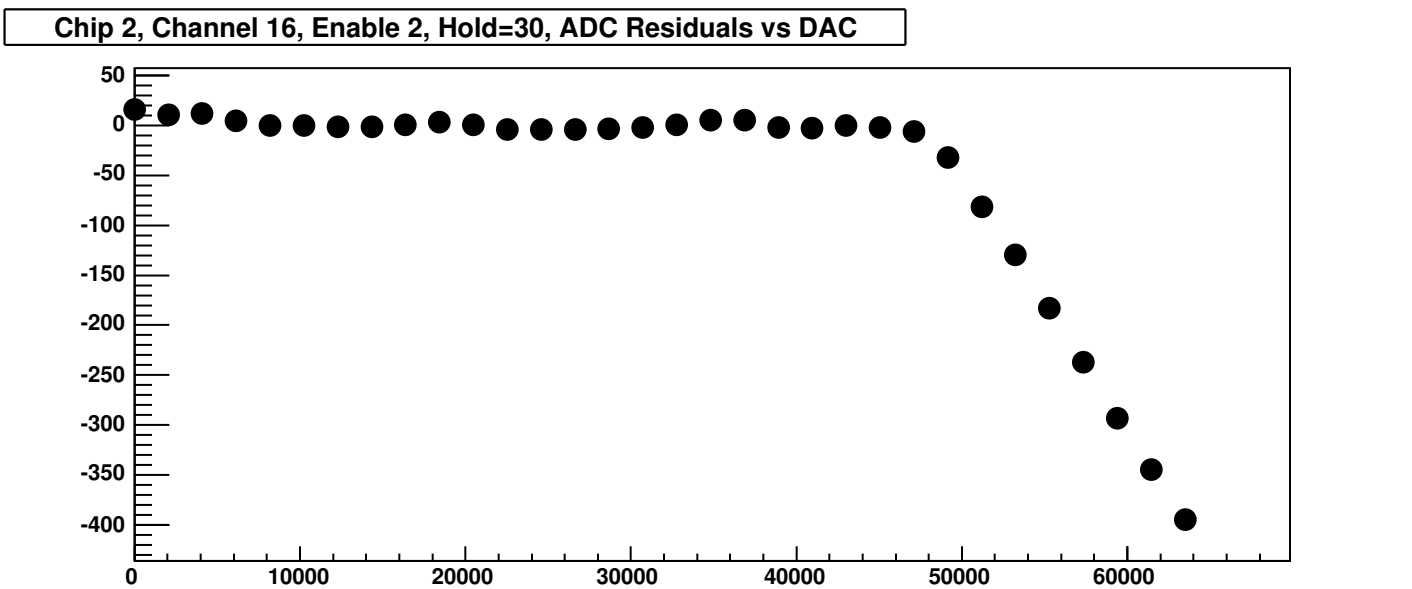


Chip 2, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC



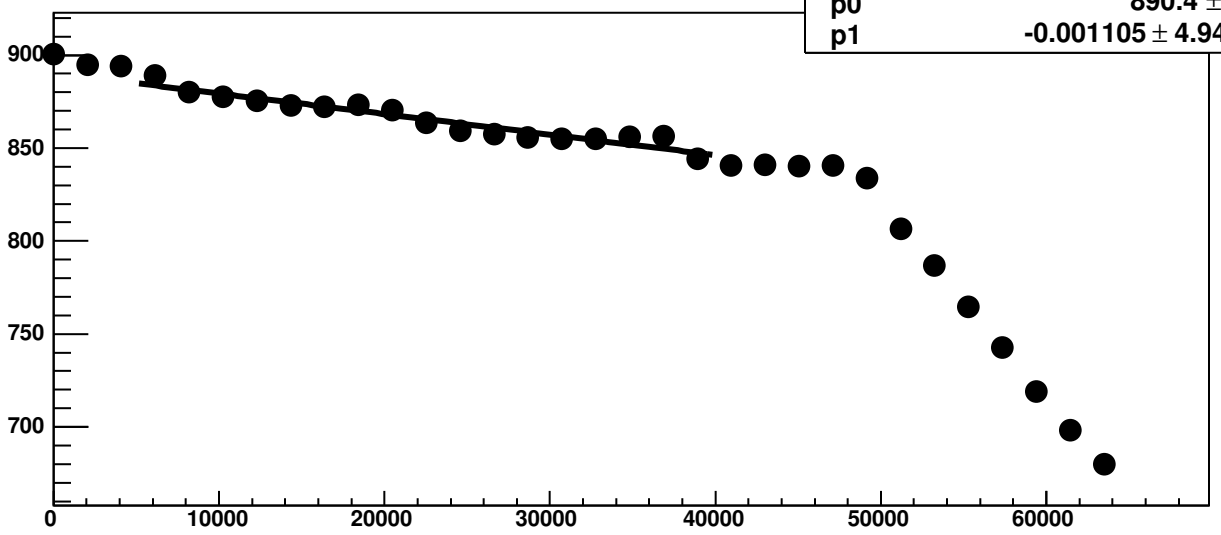

Chip 2, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3282 / 15
p0 885 ± 0.1282
p1 0.0007621 ± 5.292e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC
8.2
8
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

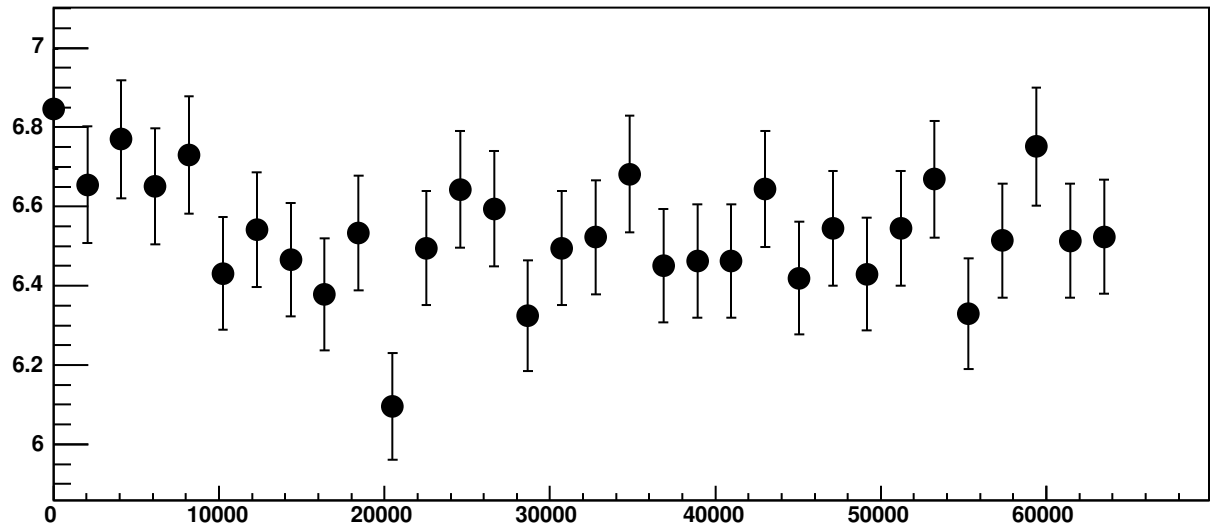

Chip 2, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

Chip 2, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

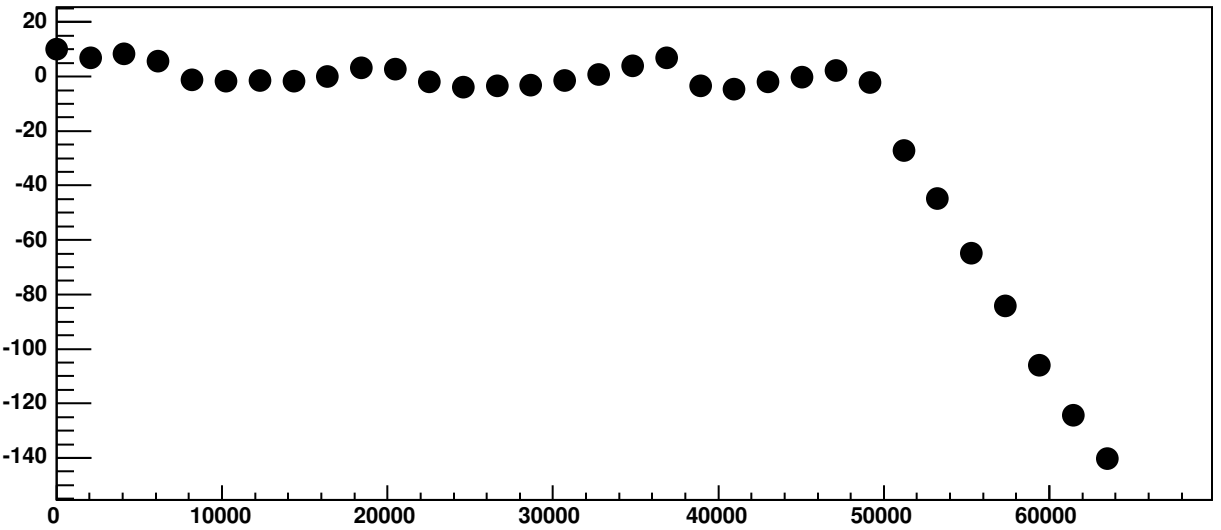
χ^2 / ndf	4223 / 15
p0	890.4 ± 0.122
p1	-0.001105 ± 4.943e-06

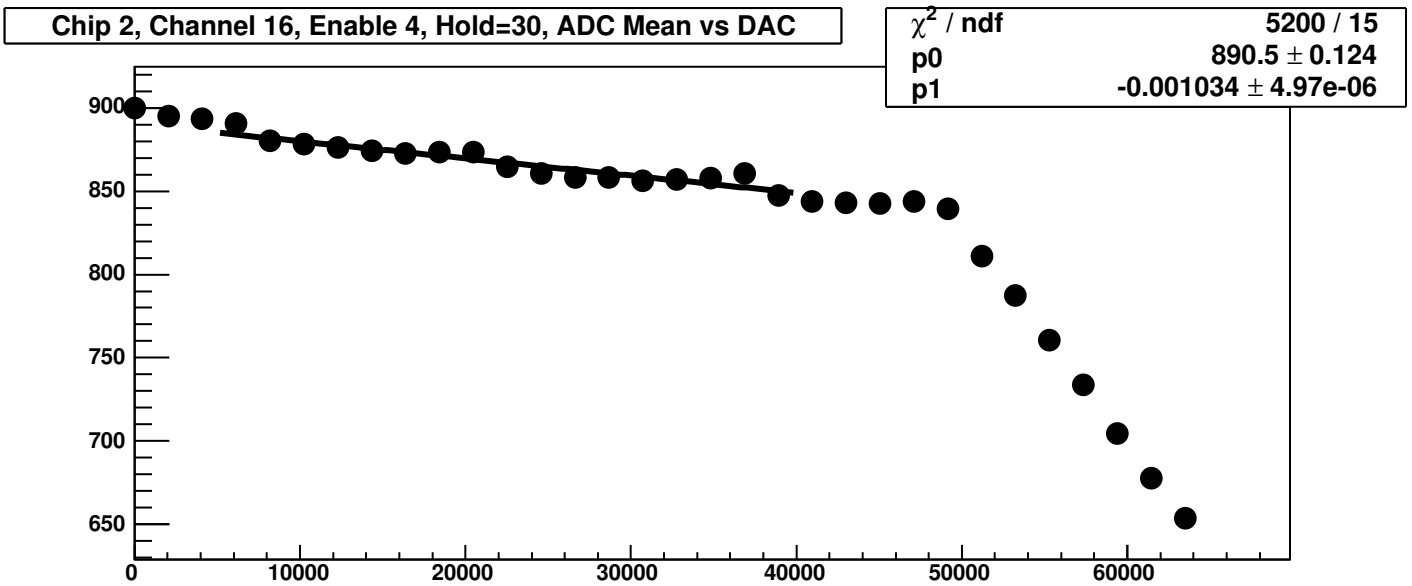


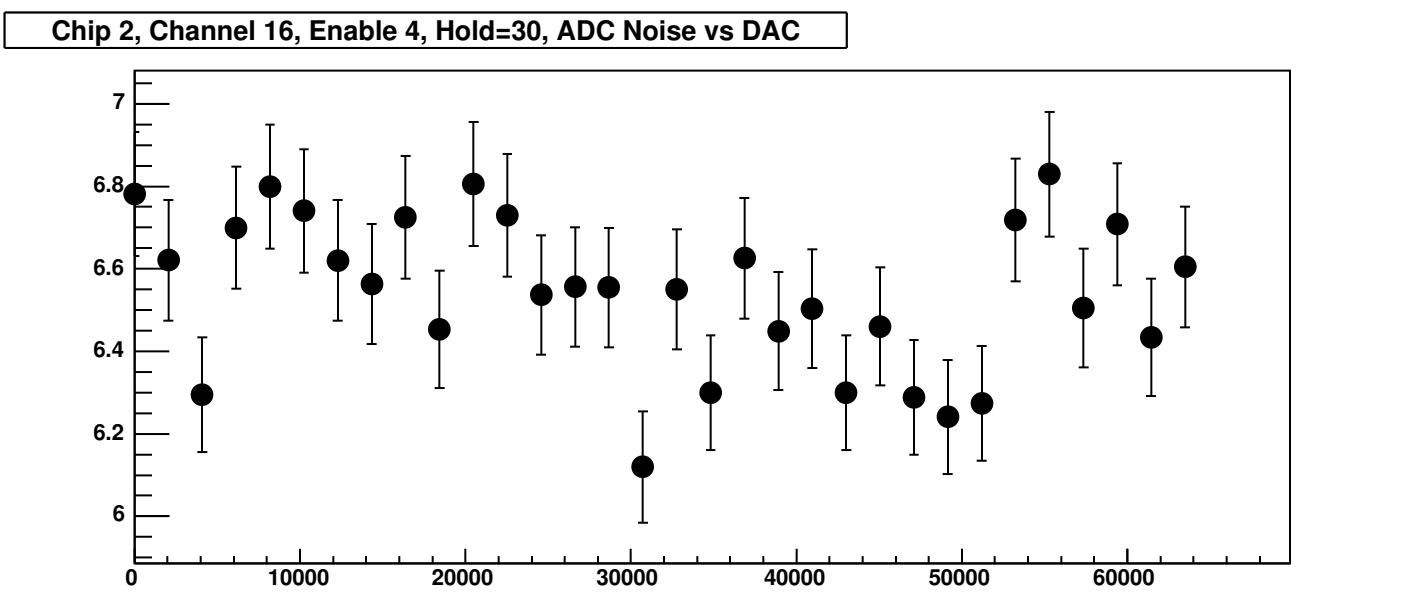
Chip 2, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC

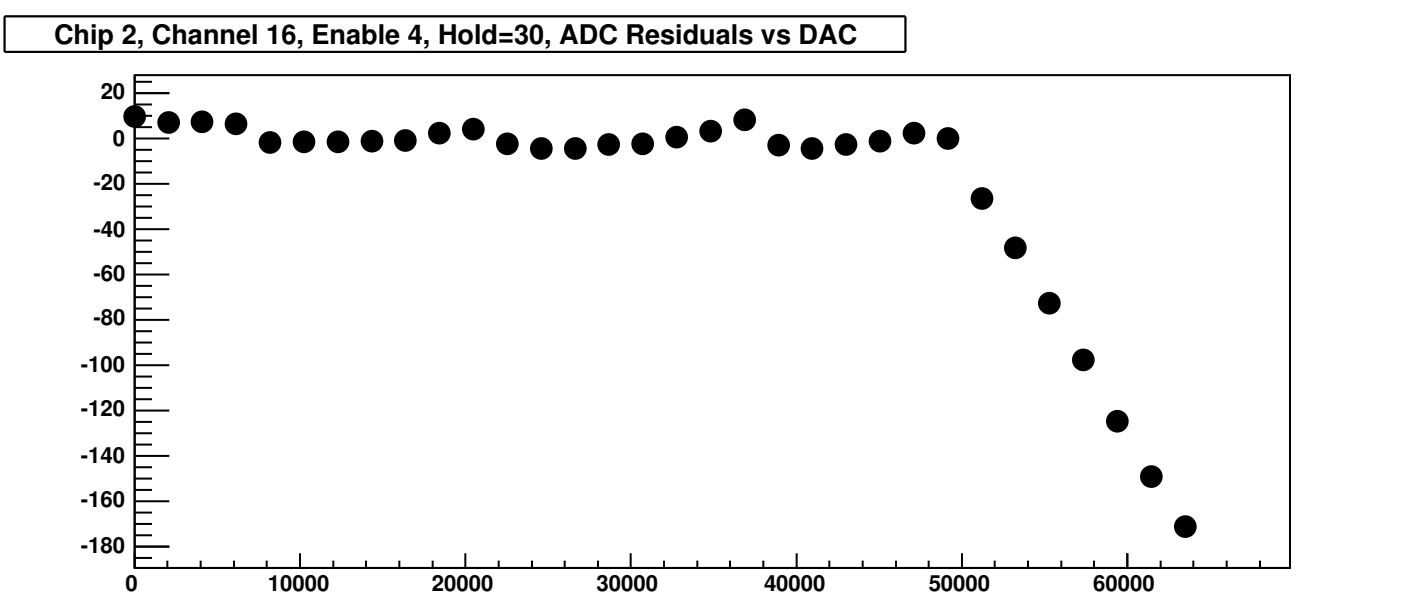


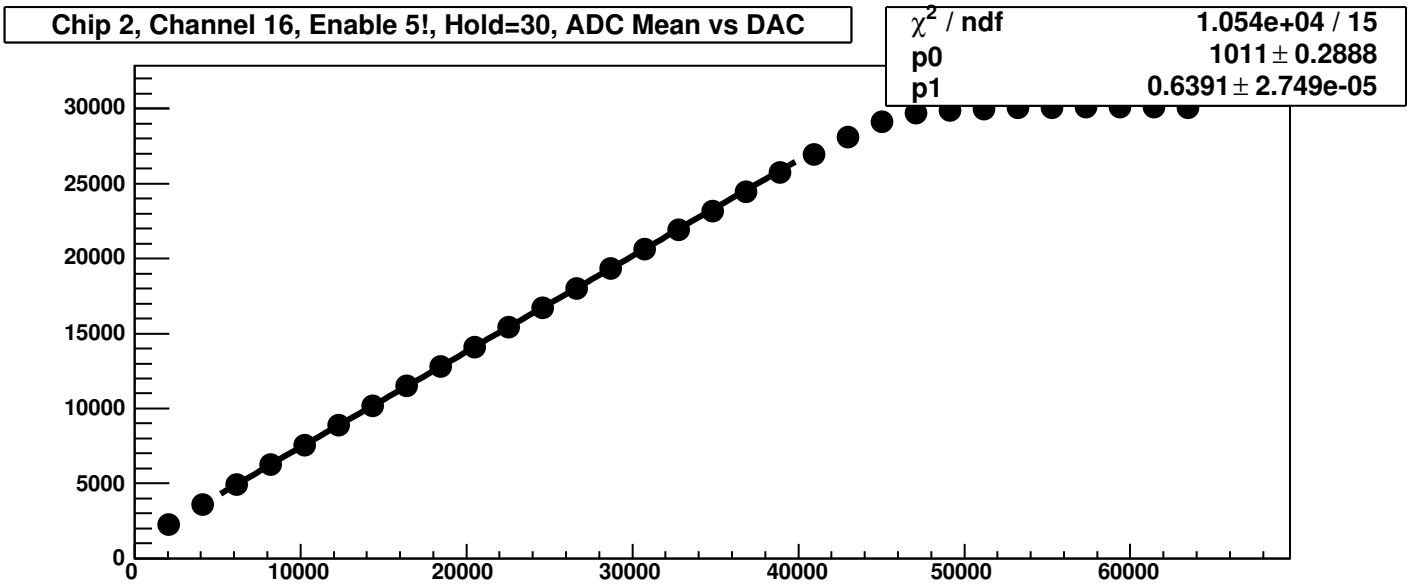
Chip 2, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

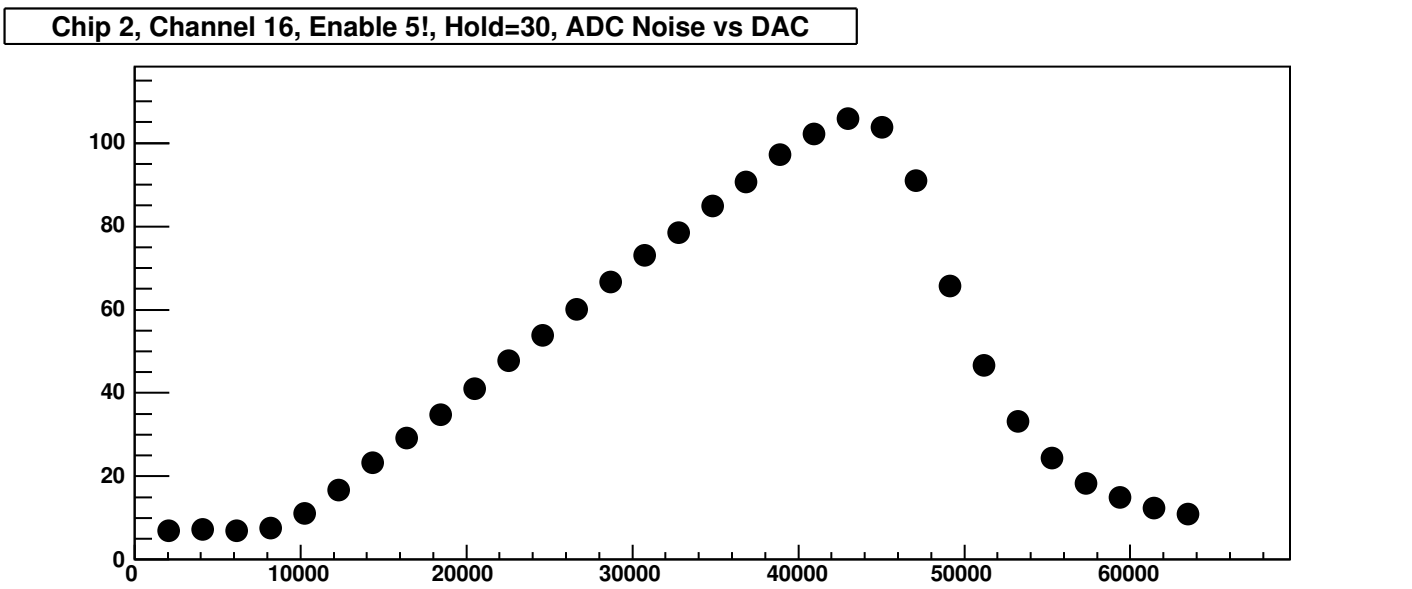


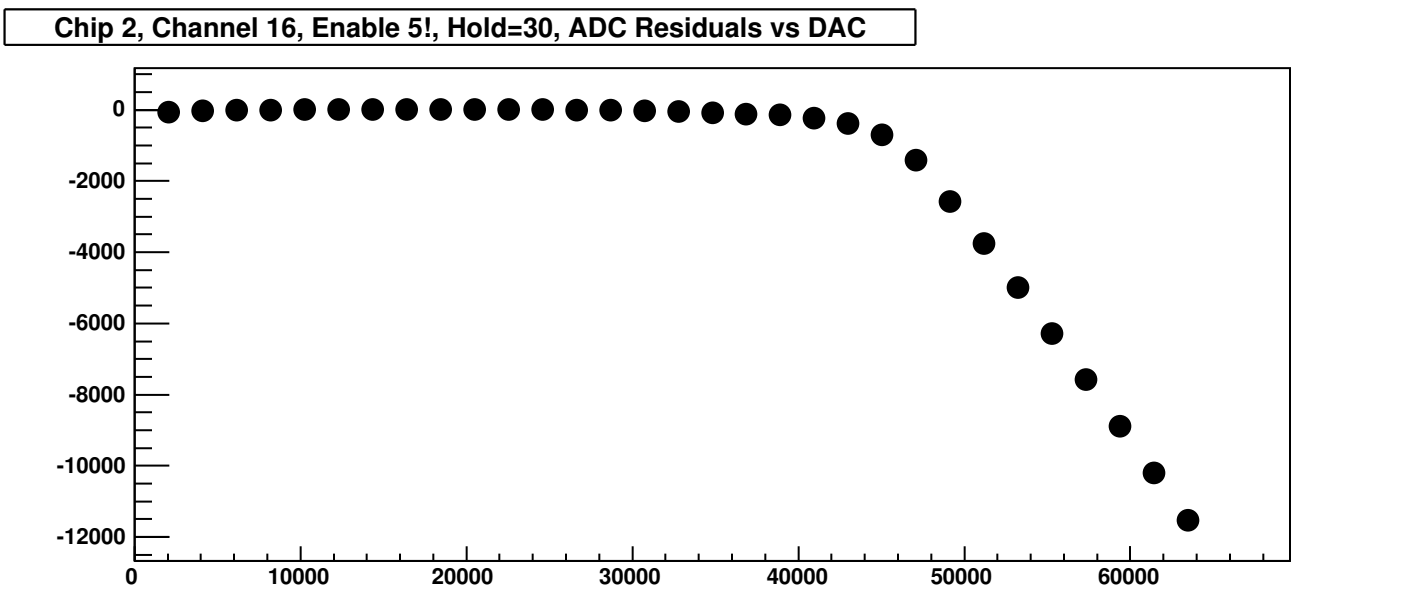

Chip 2, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5200 / 15
p0 890.5 ± 0.124
p1 -0.001034 ± 4.97e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

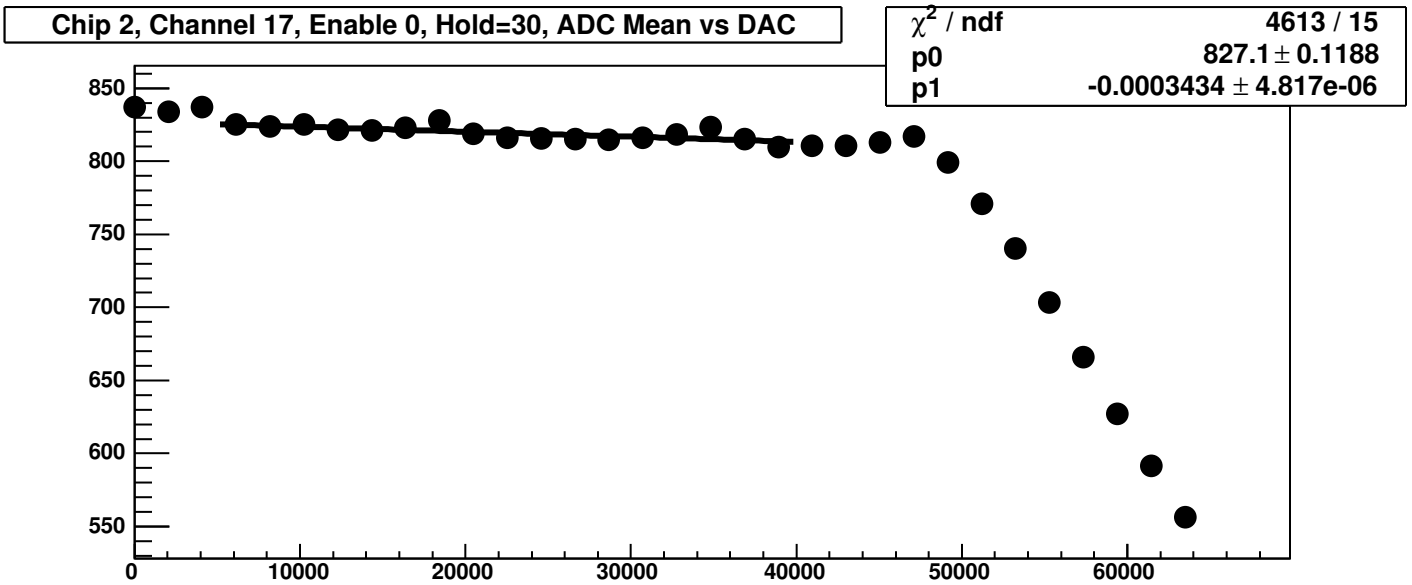

Chip 2, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

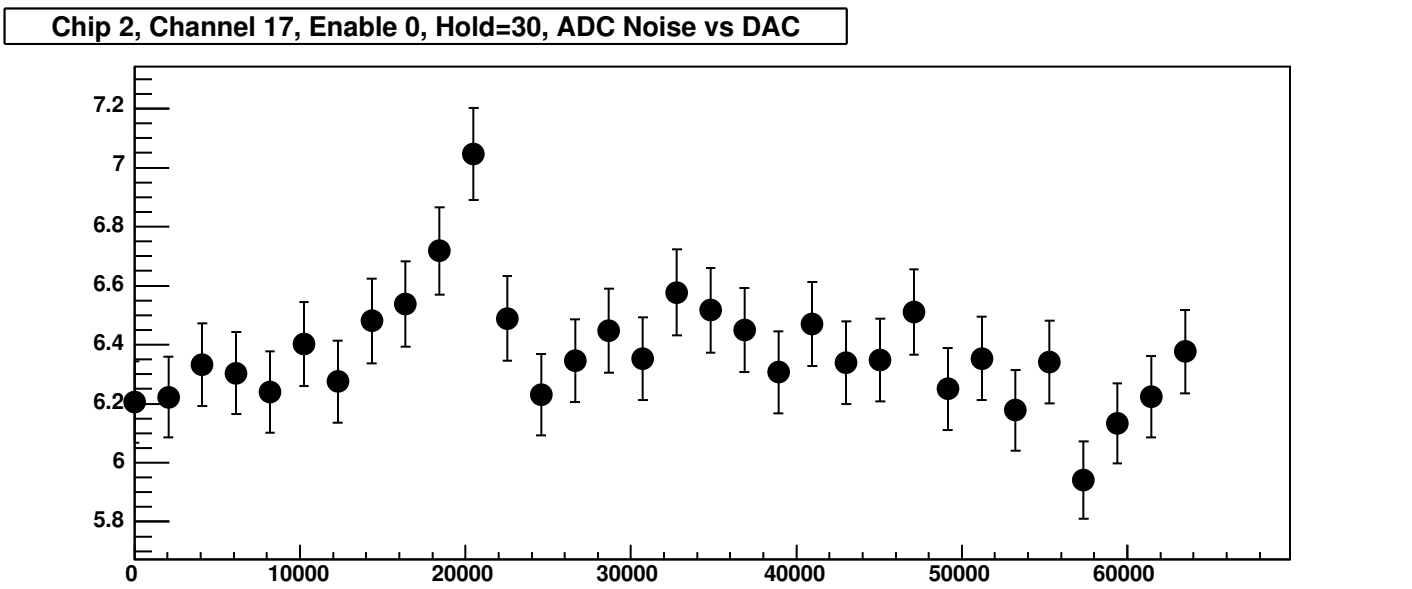

Chip 2, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

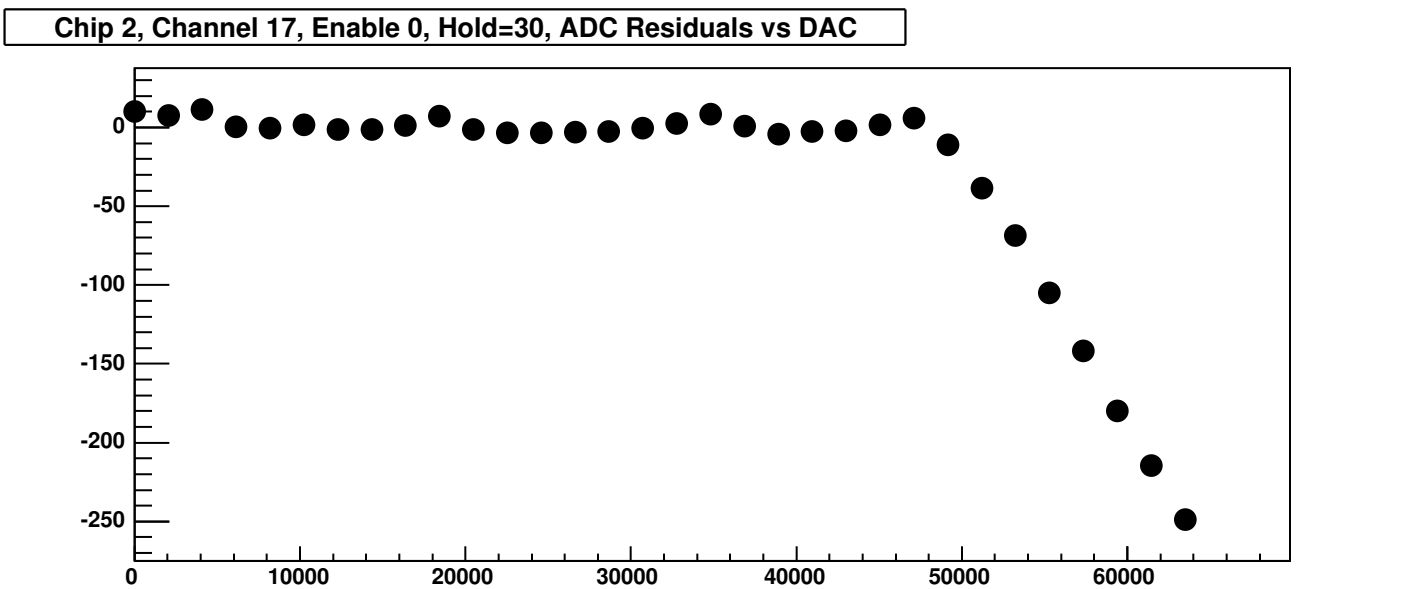

Chip 2, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.054e+04 / 15
p0 1011 ± 0.2888
p1 0.6391 ± 2.749e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

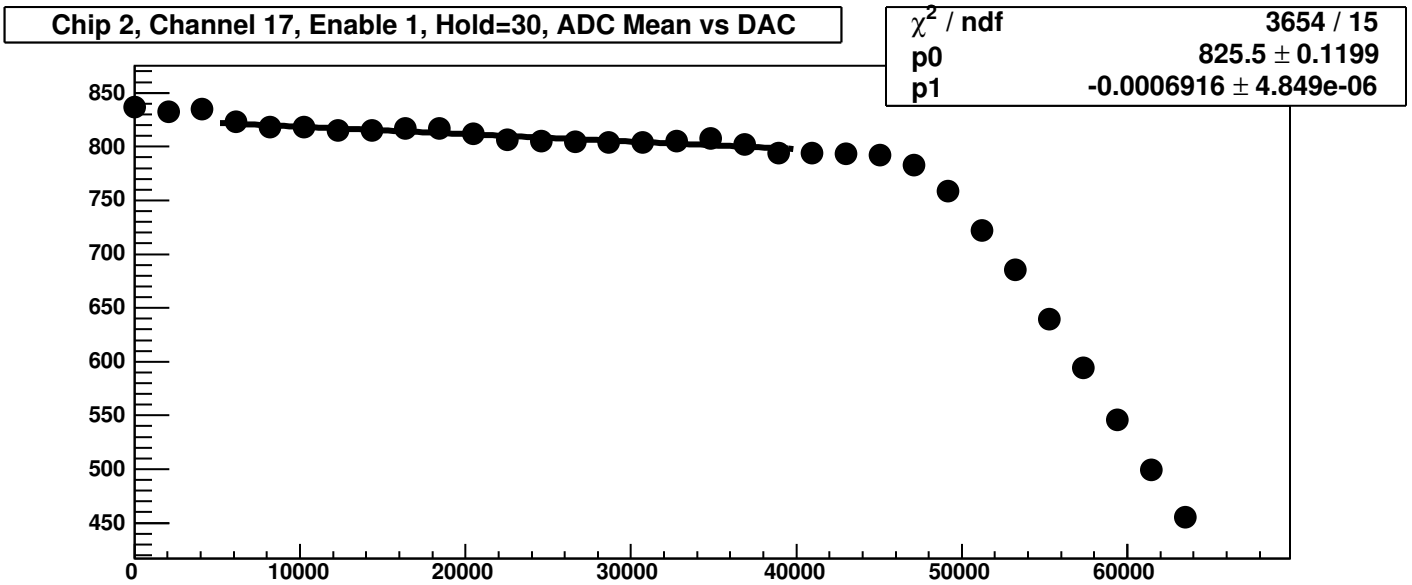

Chip 2, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

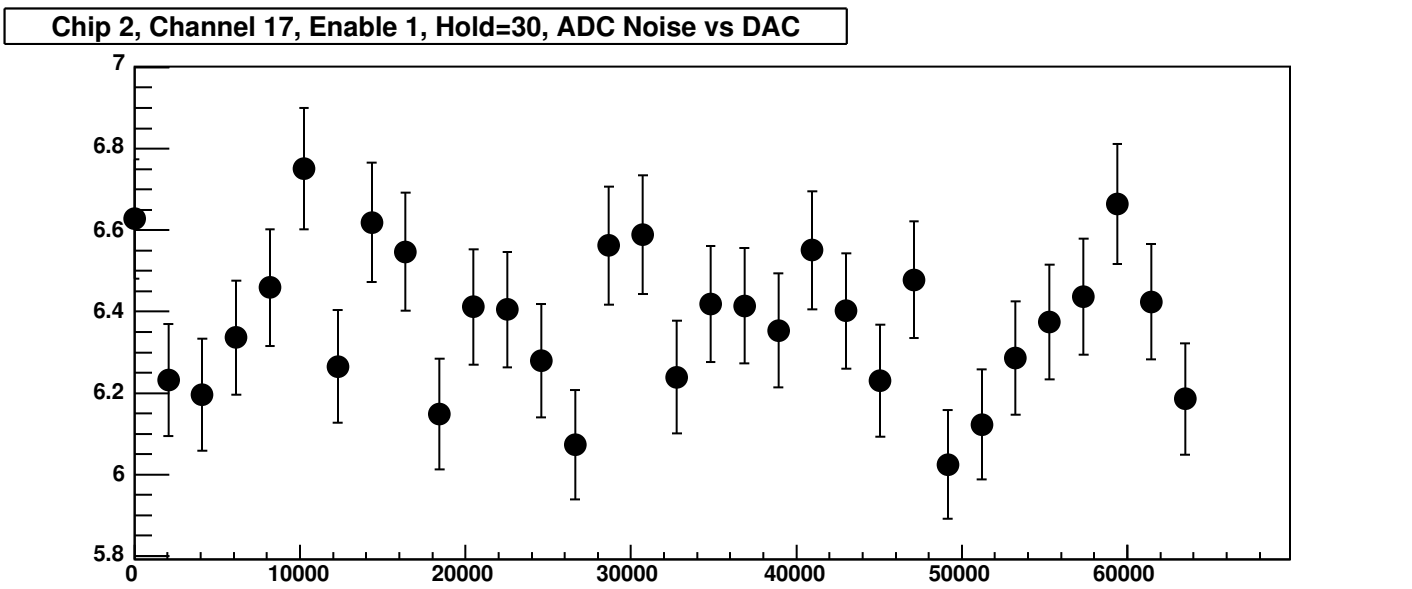

Chip 2, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

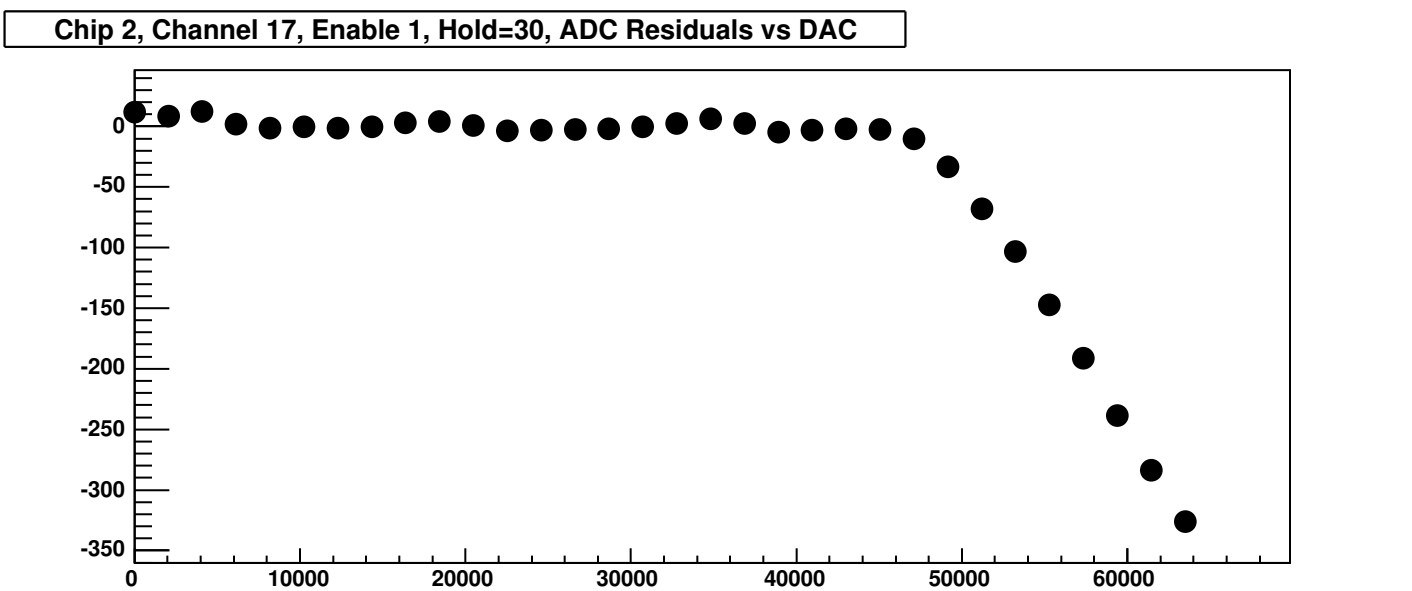

Chip 2, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4613 / 15
p0 827.1 ± 0.1188
p1 -0.0003434 ± 4.817e-06
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

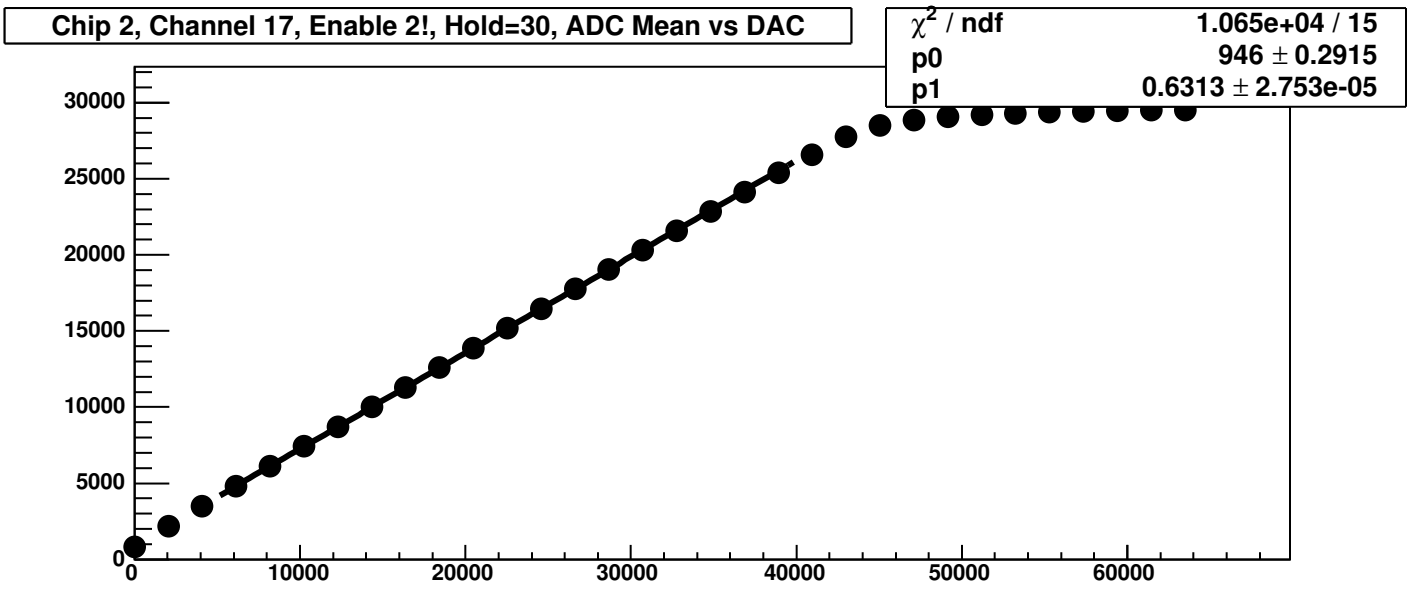

Chip 2, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

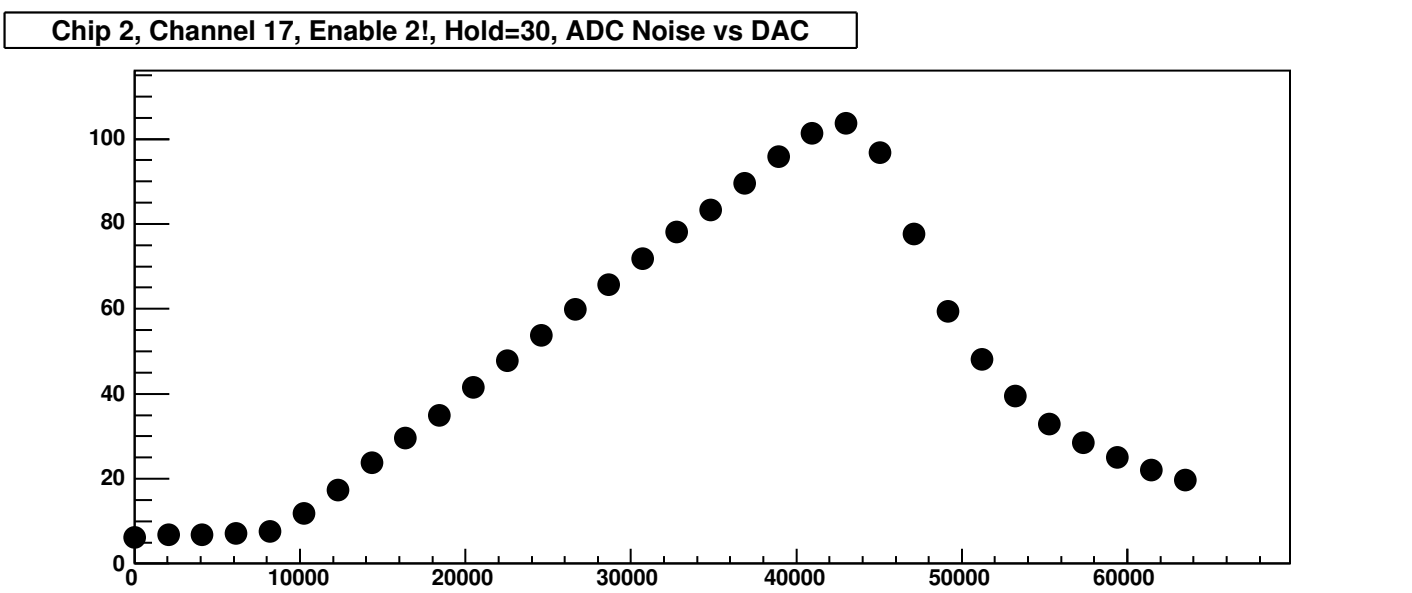

Chip 2, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

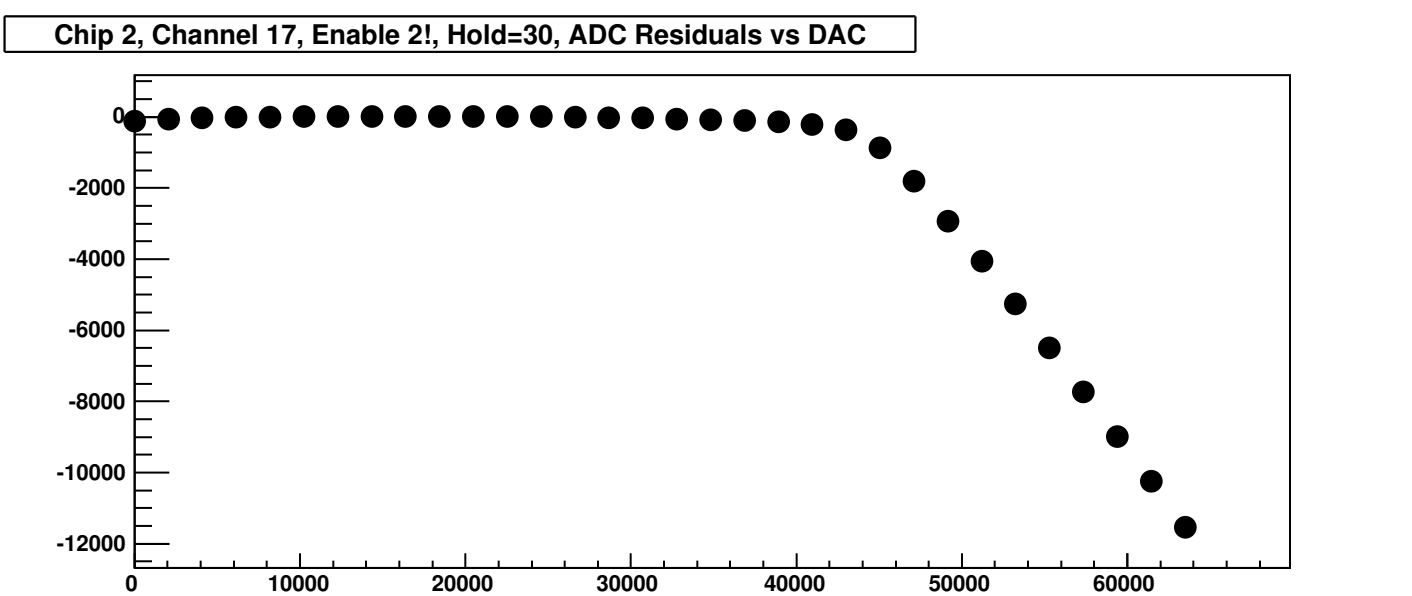

Chip 2, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3654 / 15
p0 825.5 ± 0.1199
p1 -0.0006916 ± 4.849e-06
850
800
750
700
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000

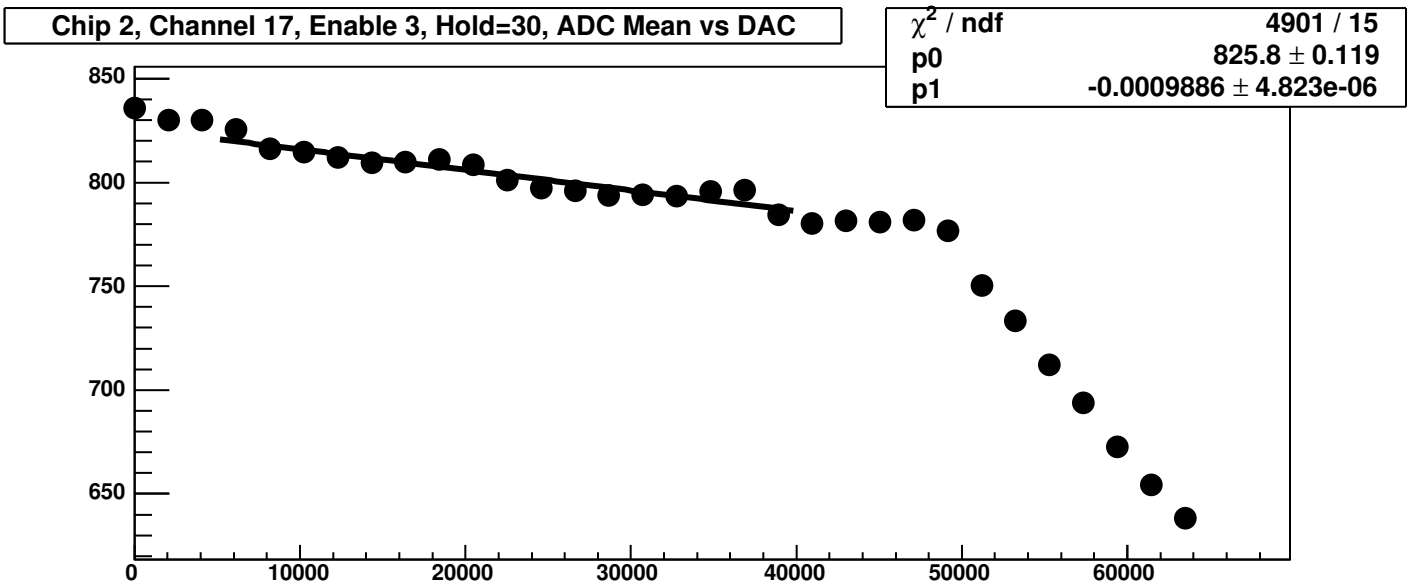

Chip 2, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

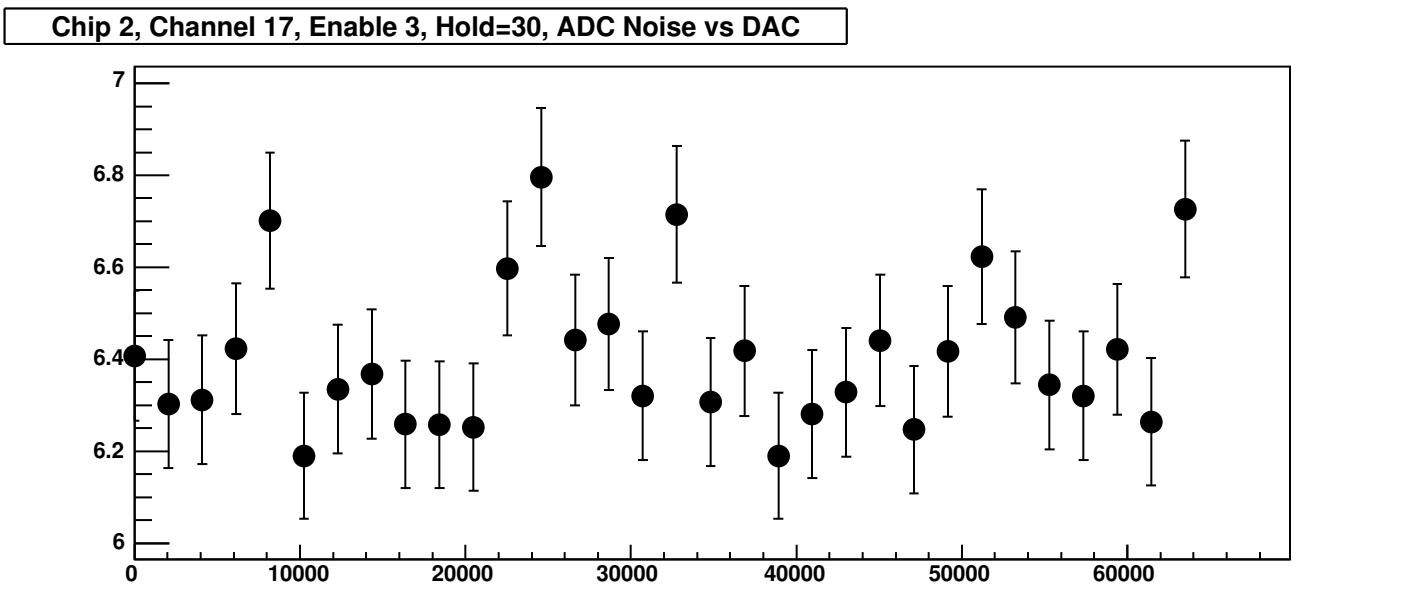

Chip 2, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

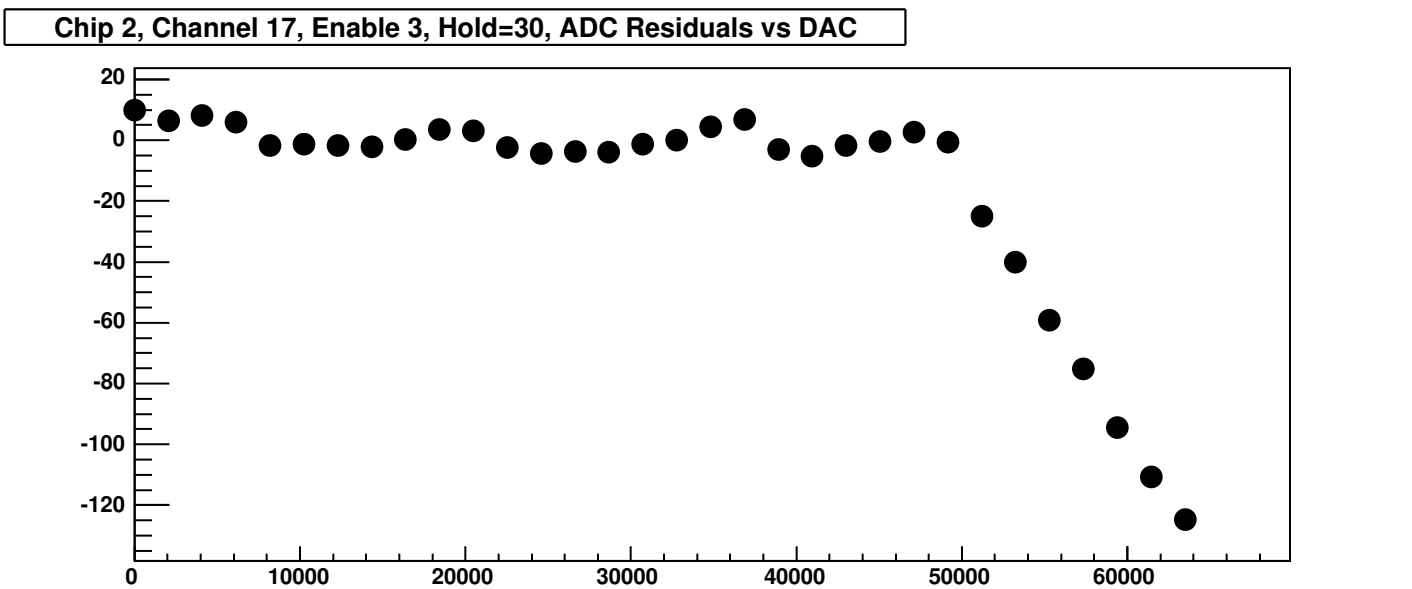

Chip 2, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.065e+04 / 15
p0 946 ± 0.2915
p1 0.6313 ± 2.753e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

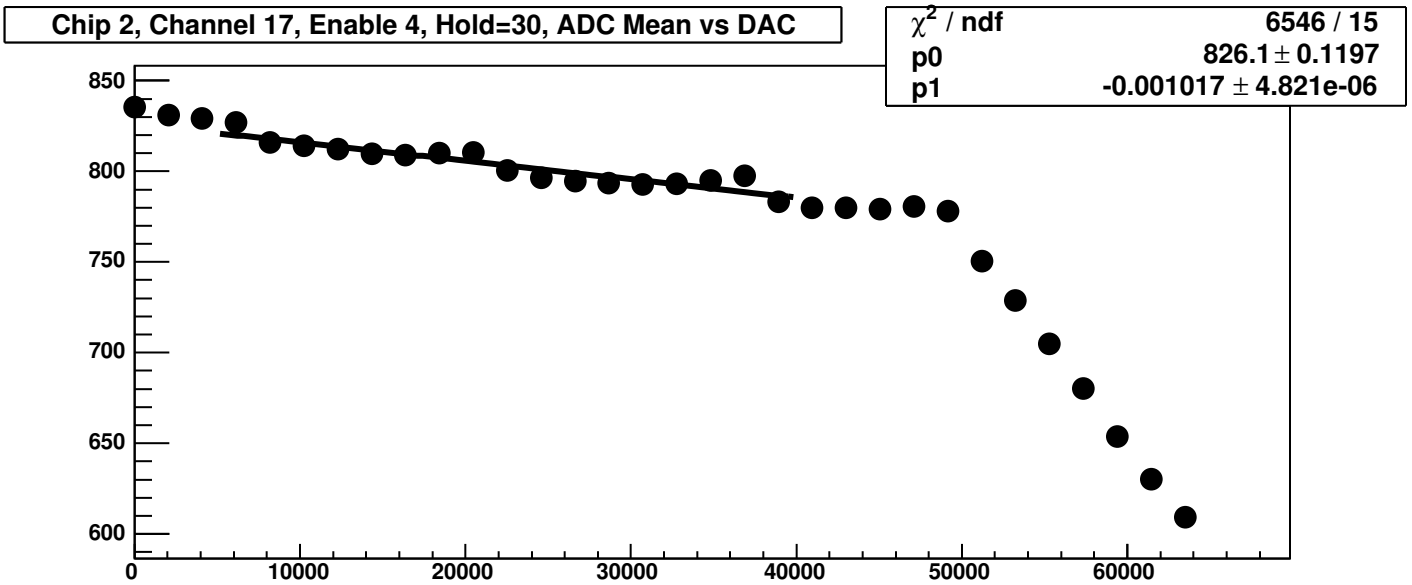

Chip 2, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

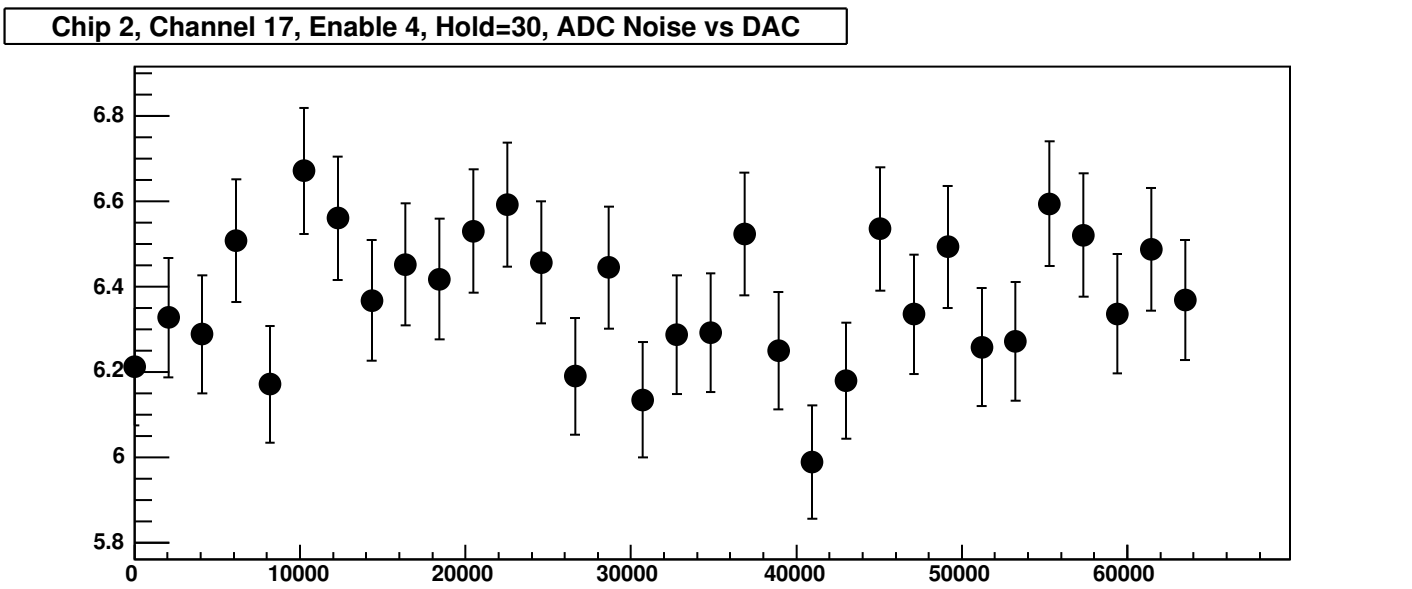

Chip 2, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

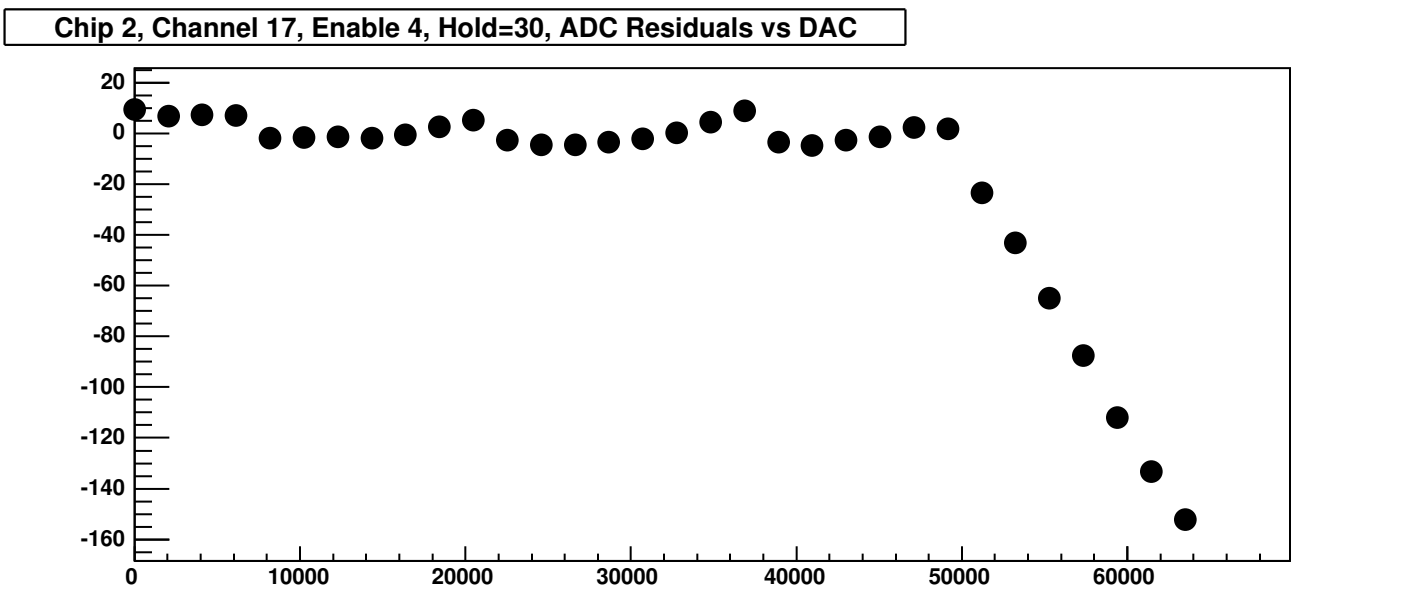

Chip 2, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4901 / 15
p0 825.8 ± 0.119
p1 -0.0009886 ± 4.823e-06
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

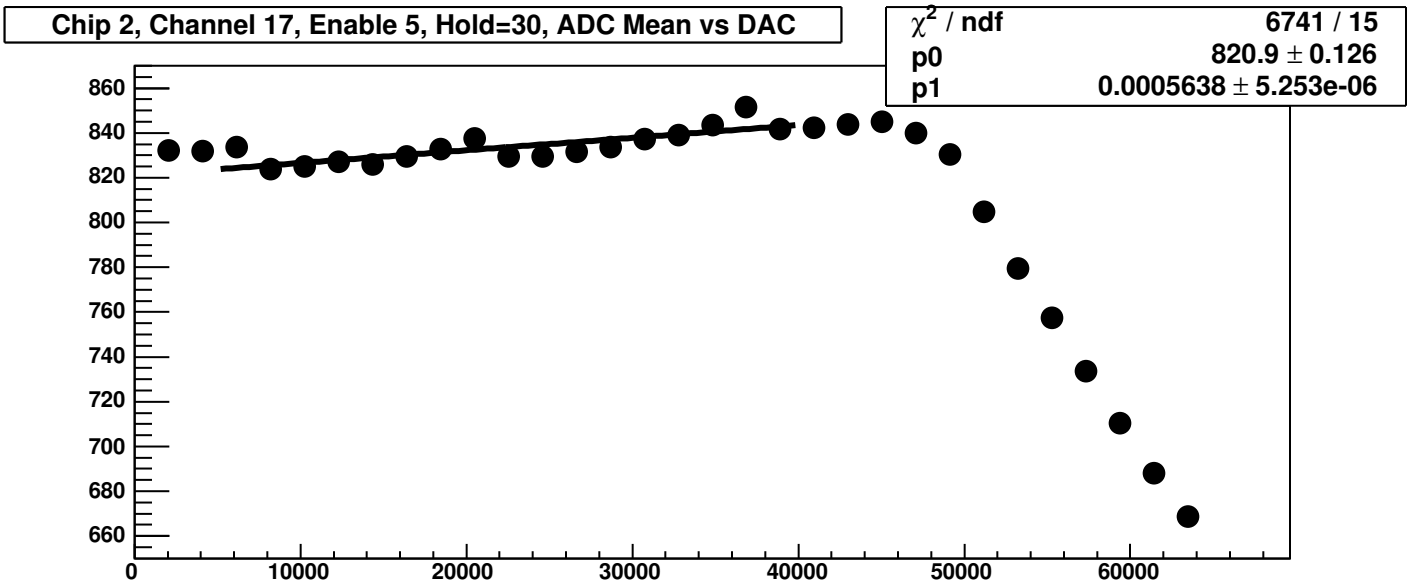

Chip 2, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

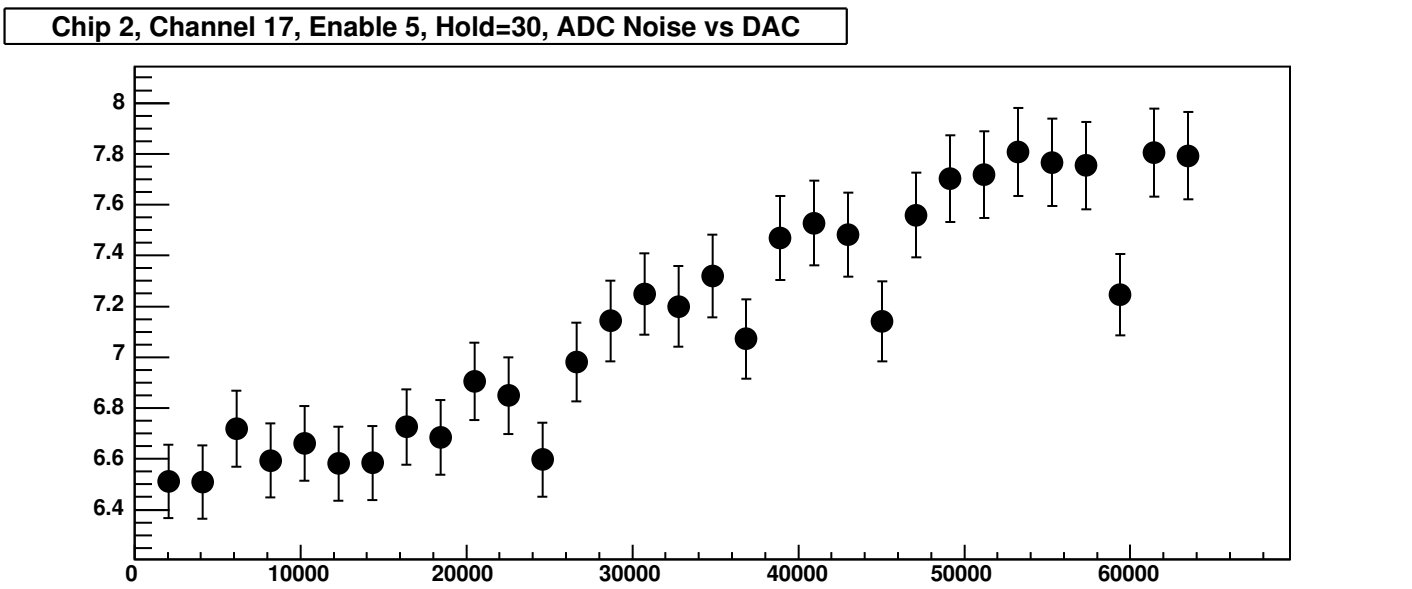

Chip 2, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

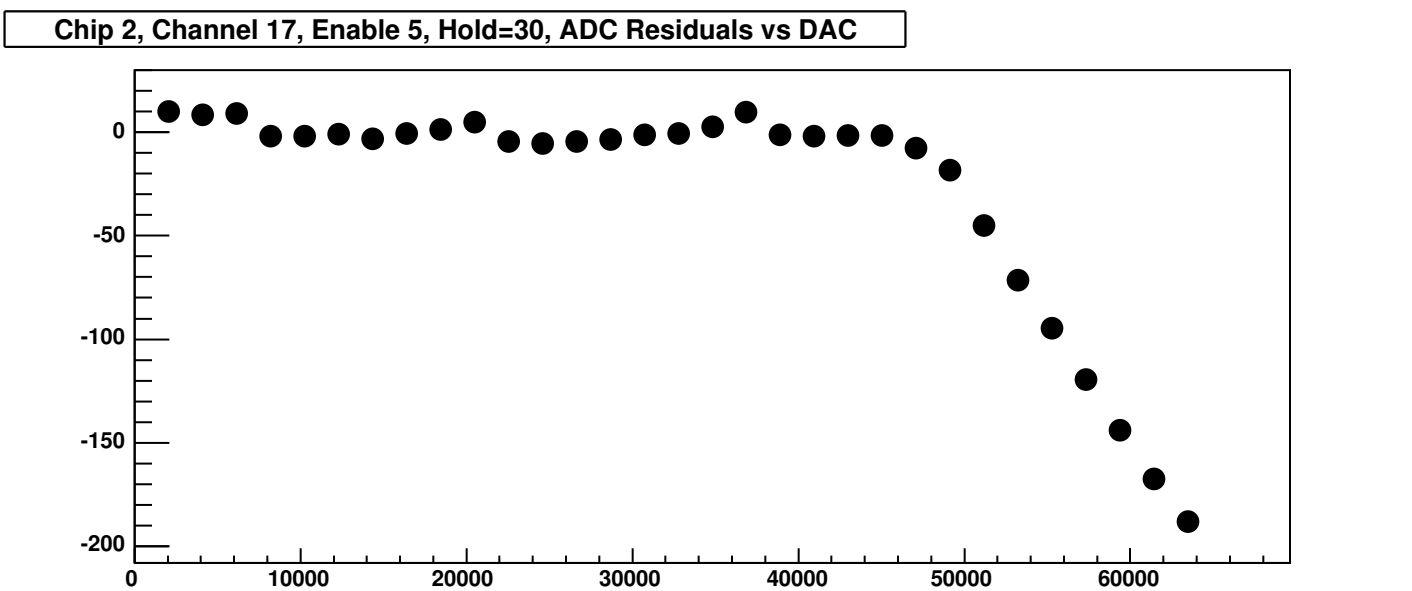

Chip 2, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6546 / 15
p0 826.1 ± 0.1197
p1 -0.001017 ± 4.821e-06
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

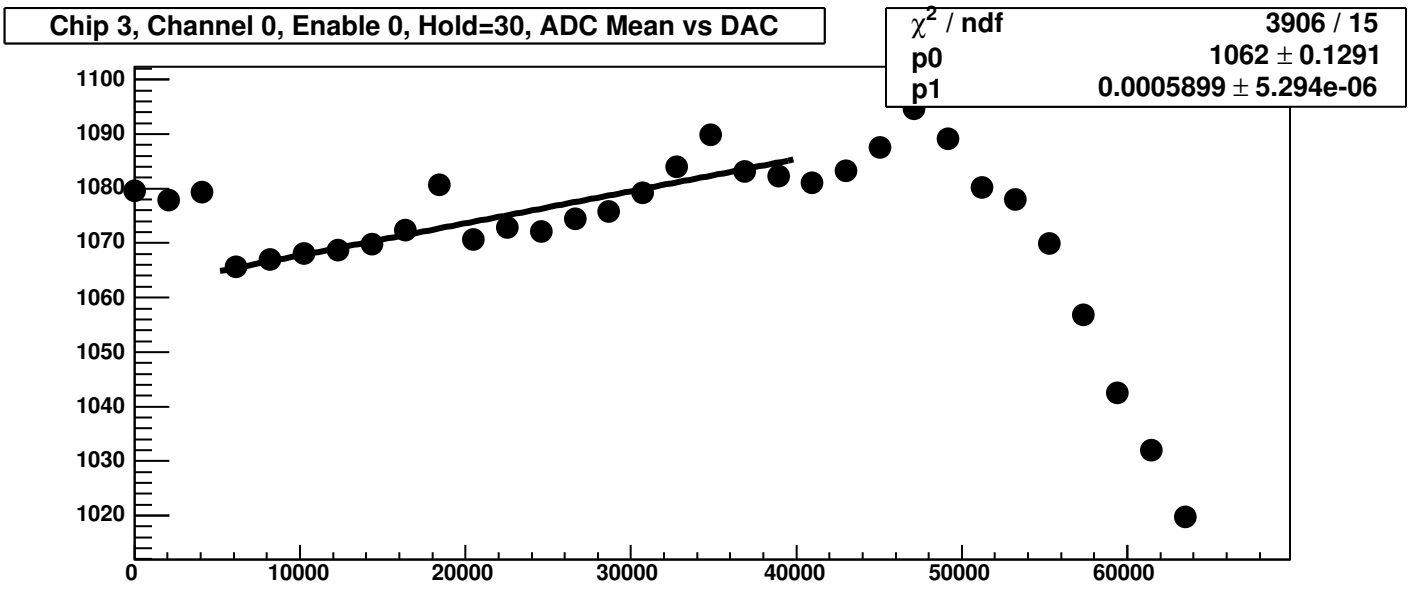

Chip 2, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

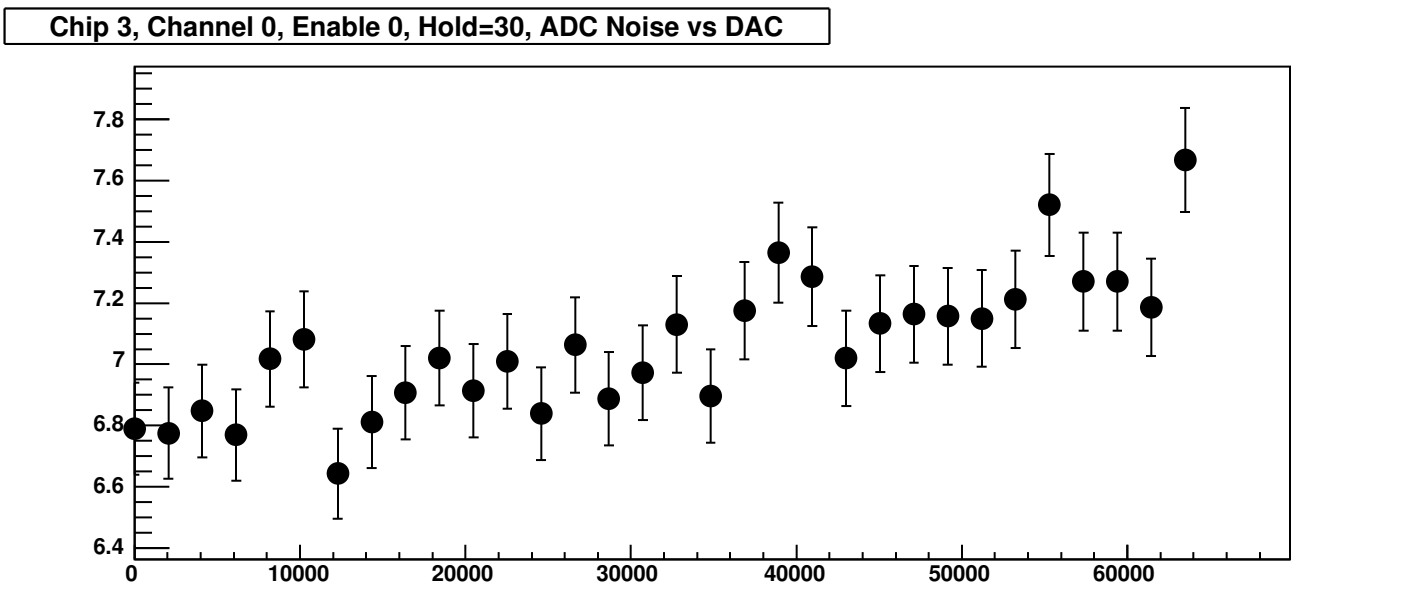

Chip 2, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

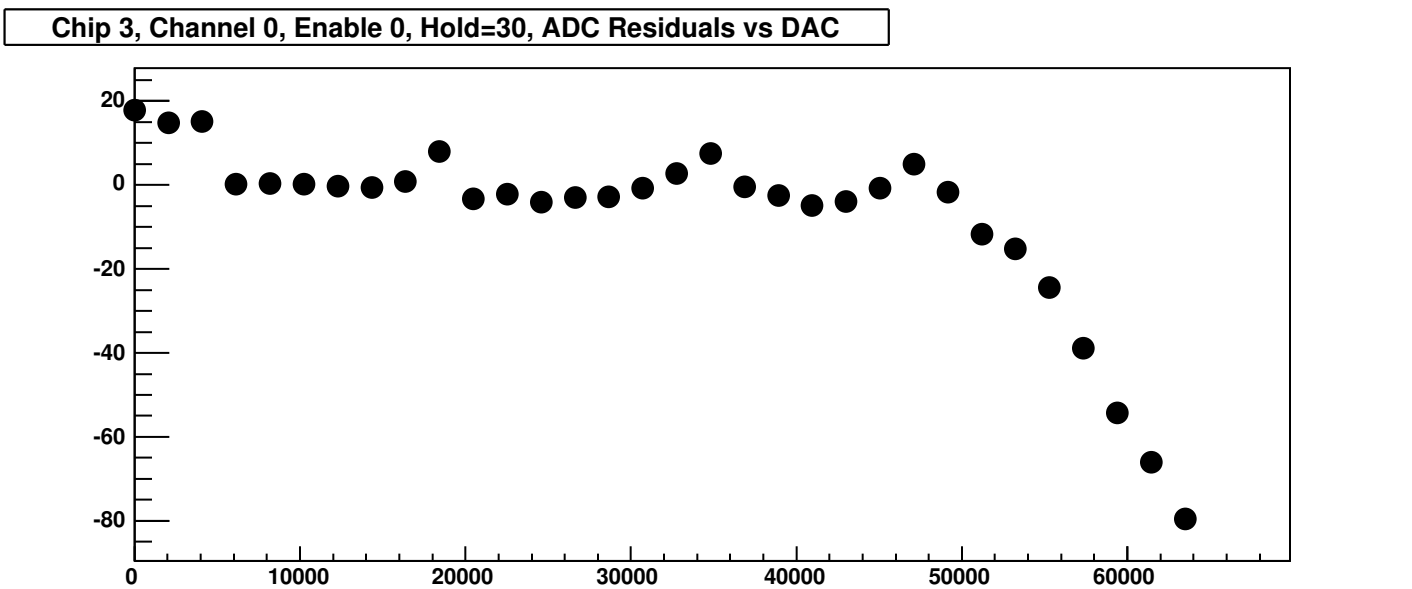

Chip 2, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6741 / 15
p0 820.9 ± 0.126
p1 0.0005638 ± 5.253e-06
860
840
820
800
780
760
740
720
700
680
660
0
10000
20000
30000
40000
50000
60000

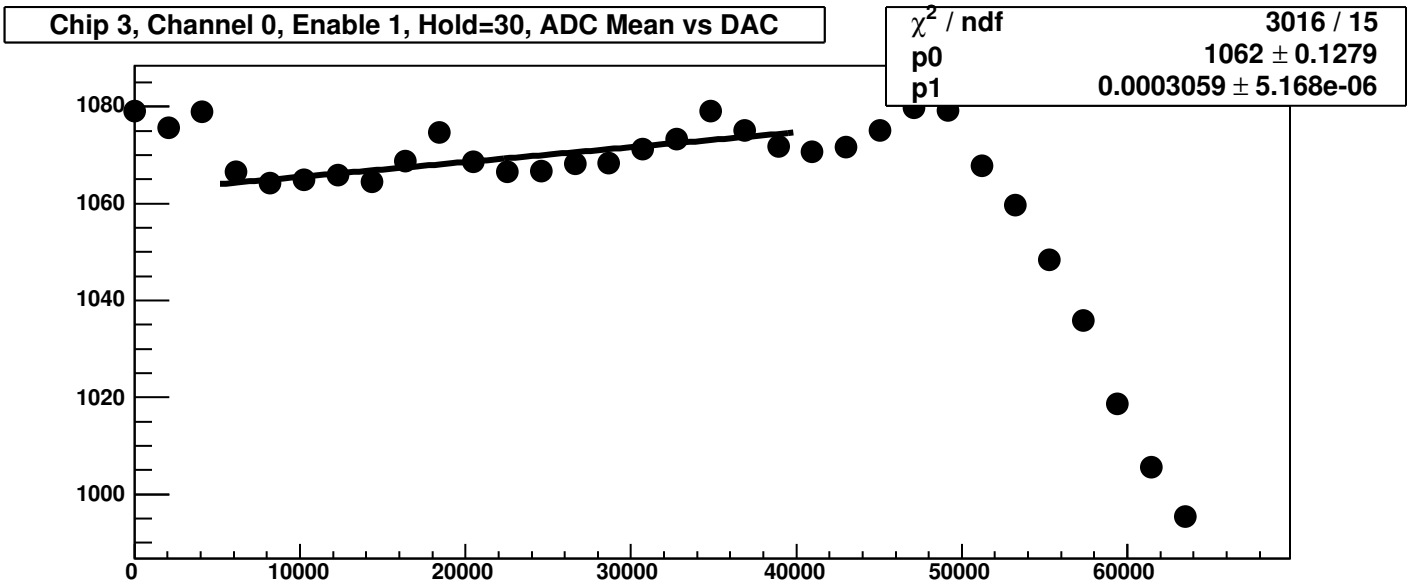

Chip 2, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC
8
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

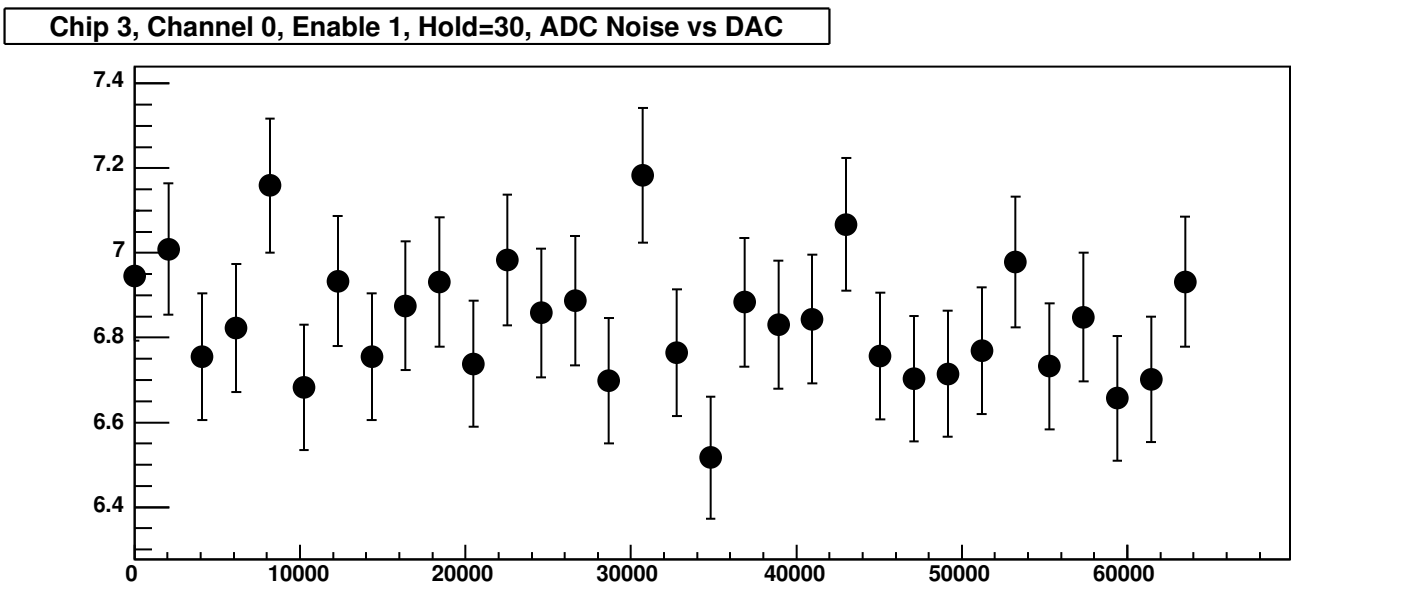

Chip 2, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

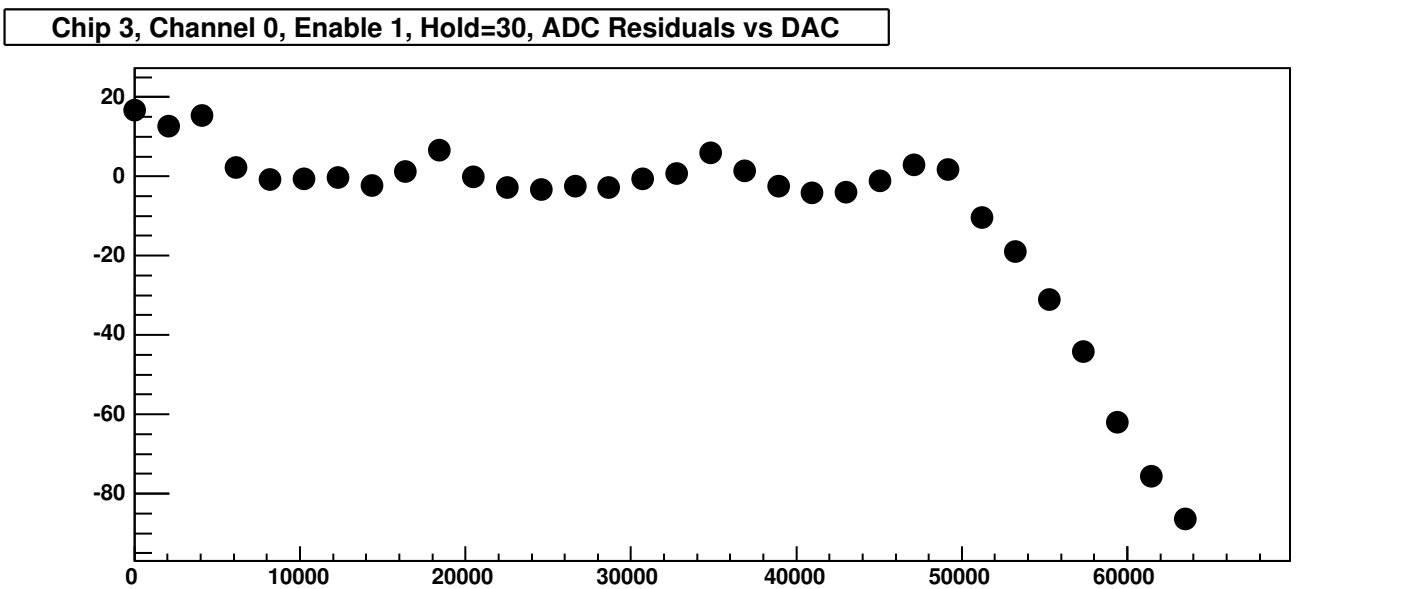

Chip 3, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3906 / 15
p0 1062 ± 0.1291
p1 0.0005899 ± 5.294e-06
1100
1090
1080
1070
1060
1050
1040
1030
1020
0
10000
20000
30000
40000
50000
60000

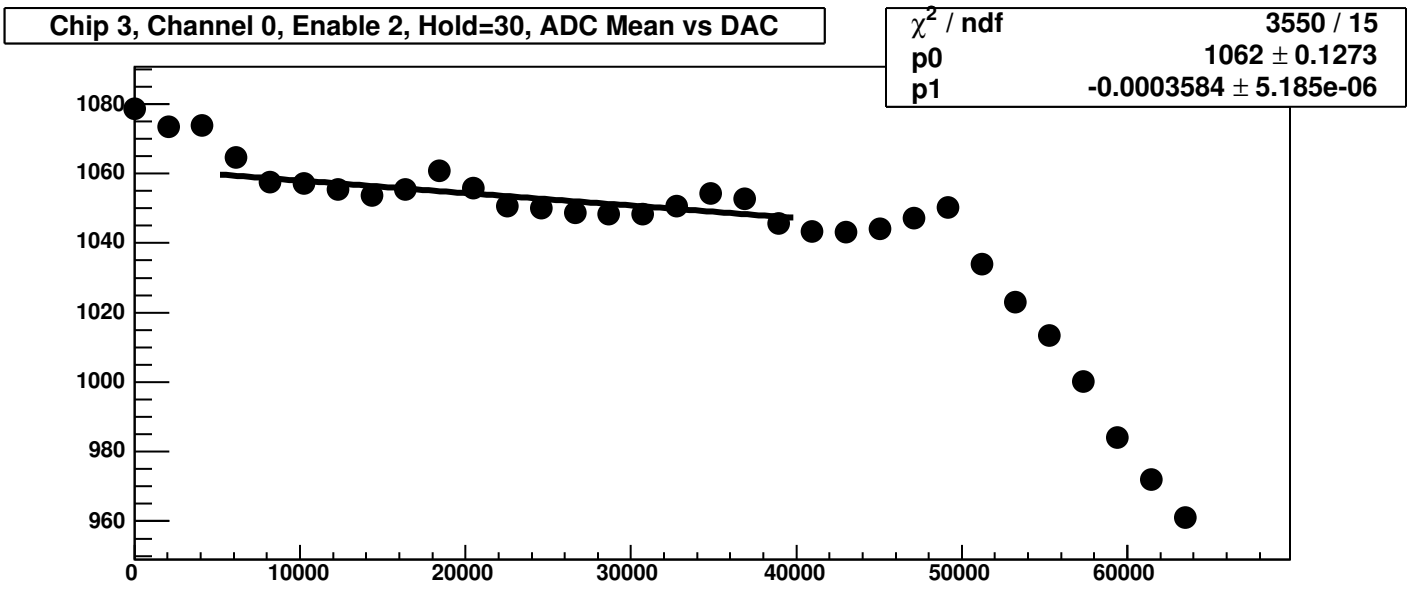

Chip 3, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

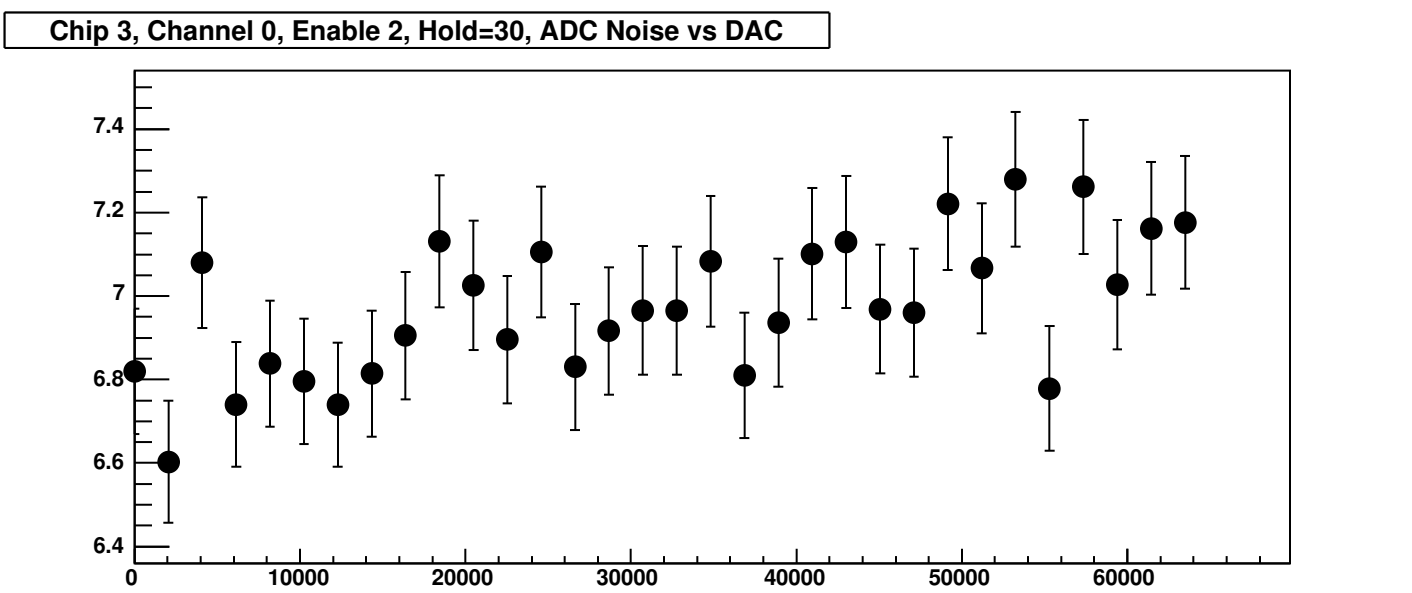

Chip 3, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

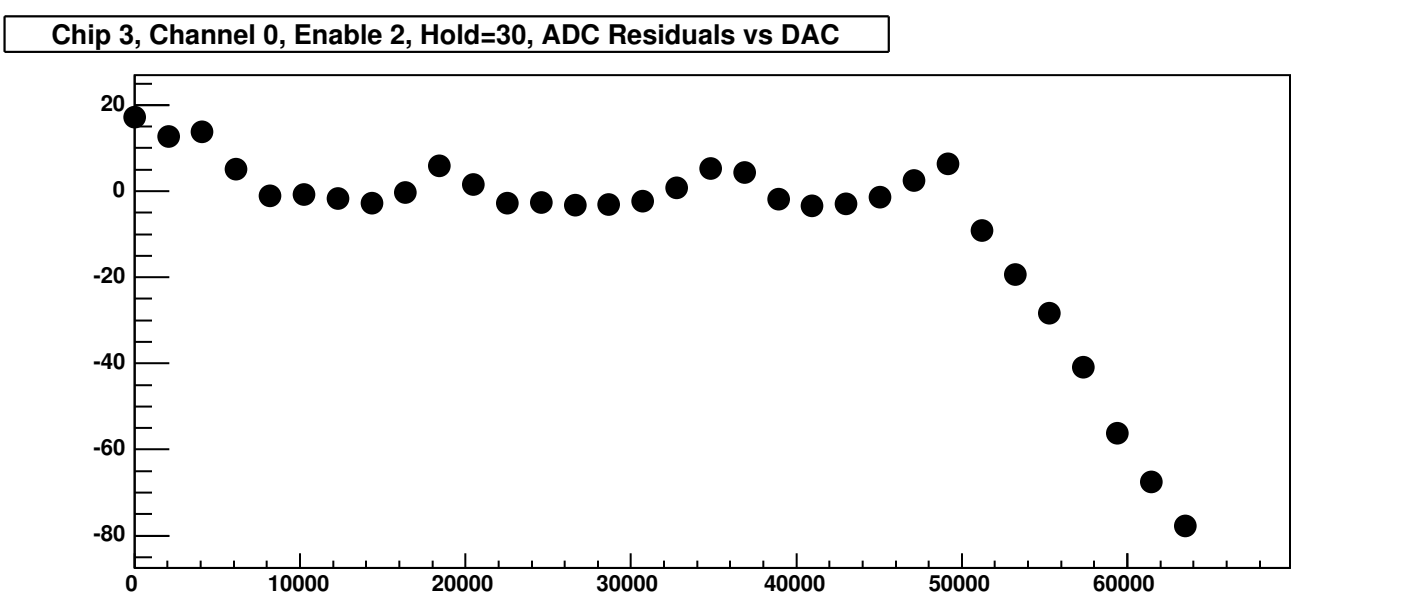

Chip 3, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3016 / 15
p0 1062 ± 0.1279
p1 0.0003059 ± 5.168e-06
1080
1060
1040
1020
1000
0
10000
20000
30000
40000
50000
60000

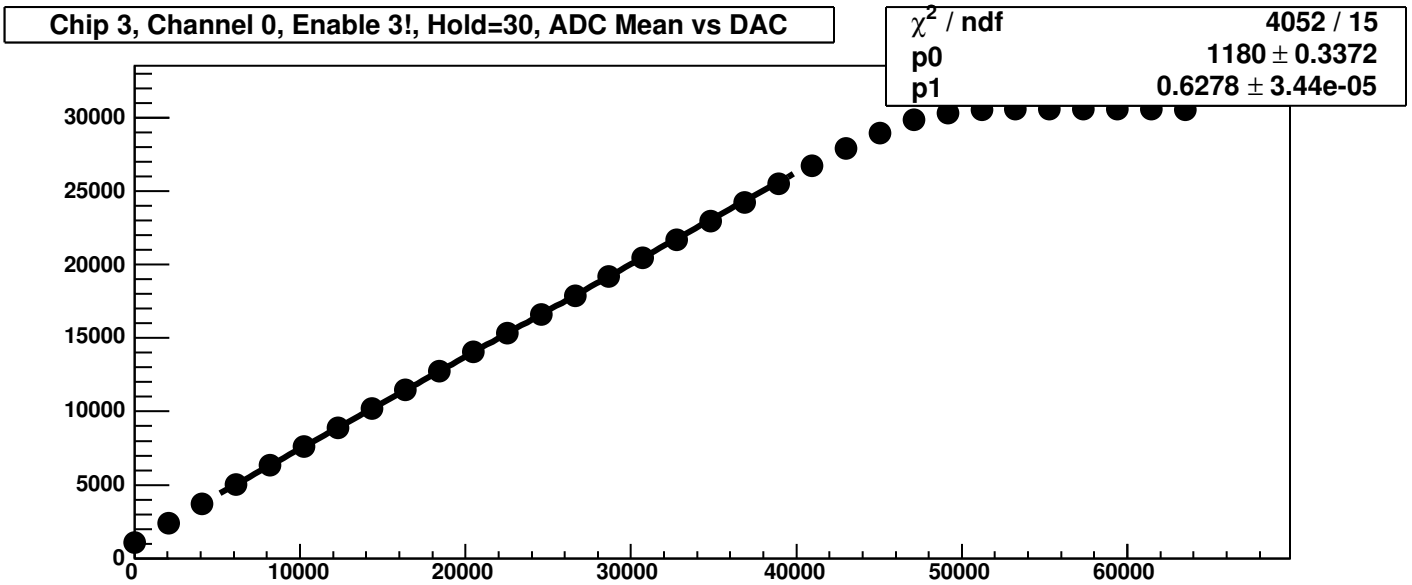

Chip 3, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

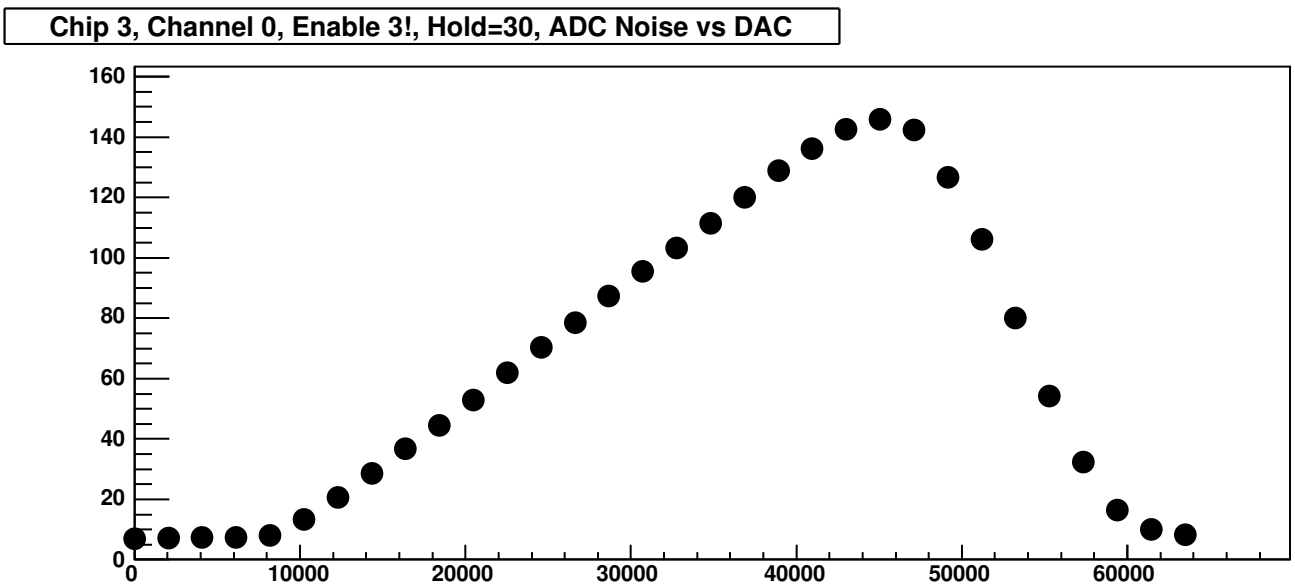

Chip 3, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

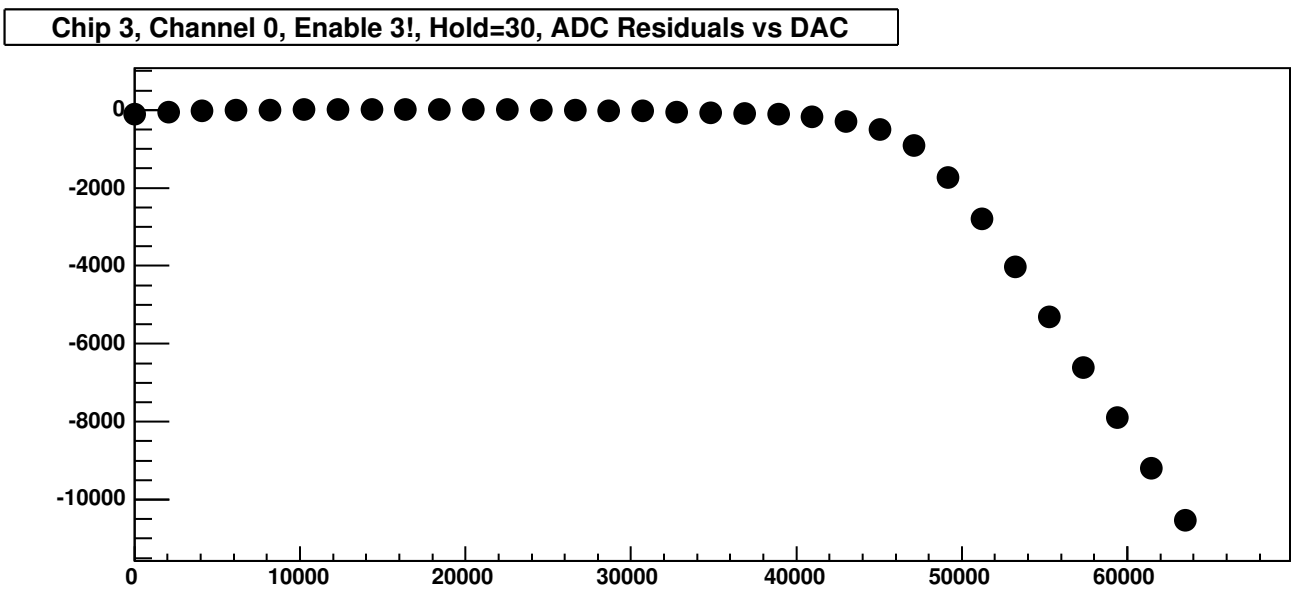

Chip 3, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3550 / 15
p0 1062 ± 0.1273
p1 -0.0003584 ± 5.185e-06
1080
1060
1040
1020
1000
980
960
0
10000
20000
30000
40000
50000
60000

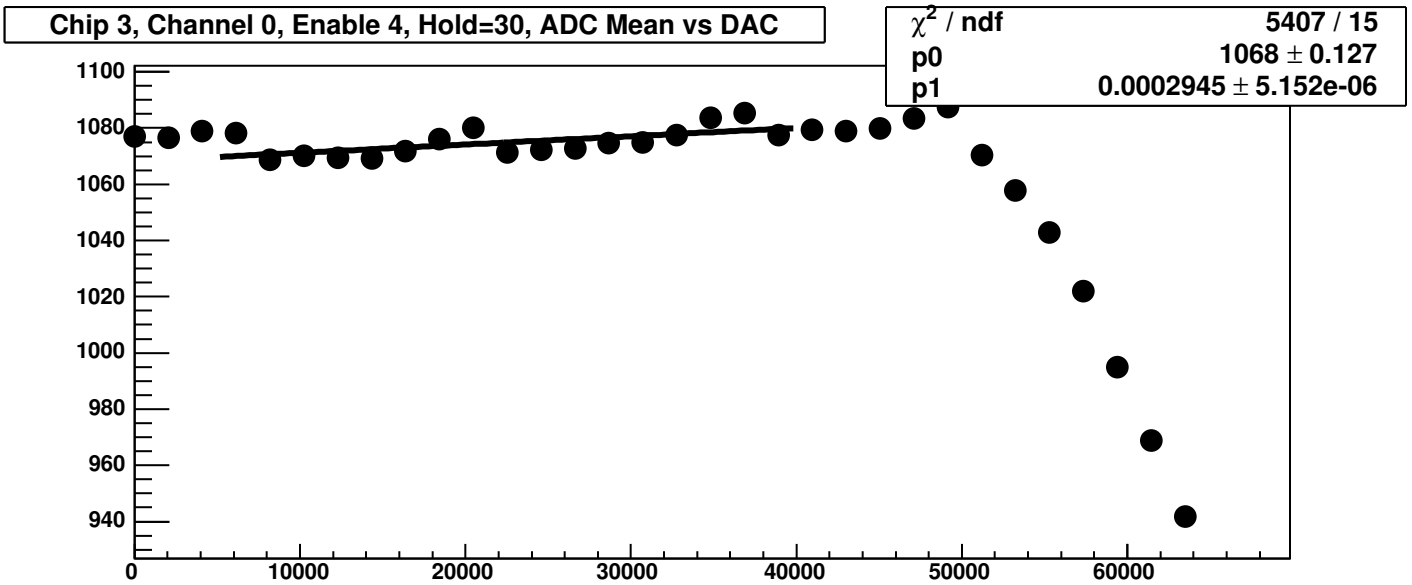

Chip 3, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

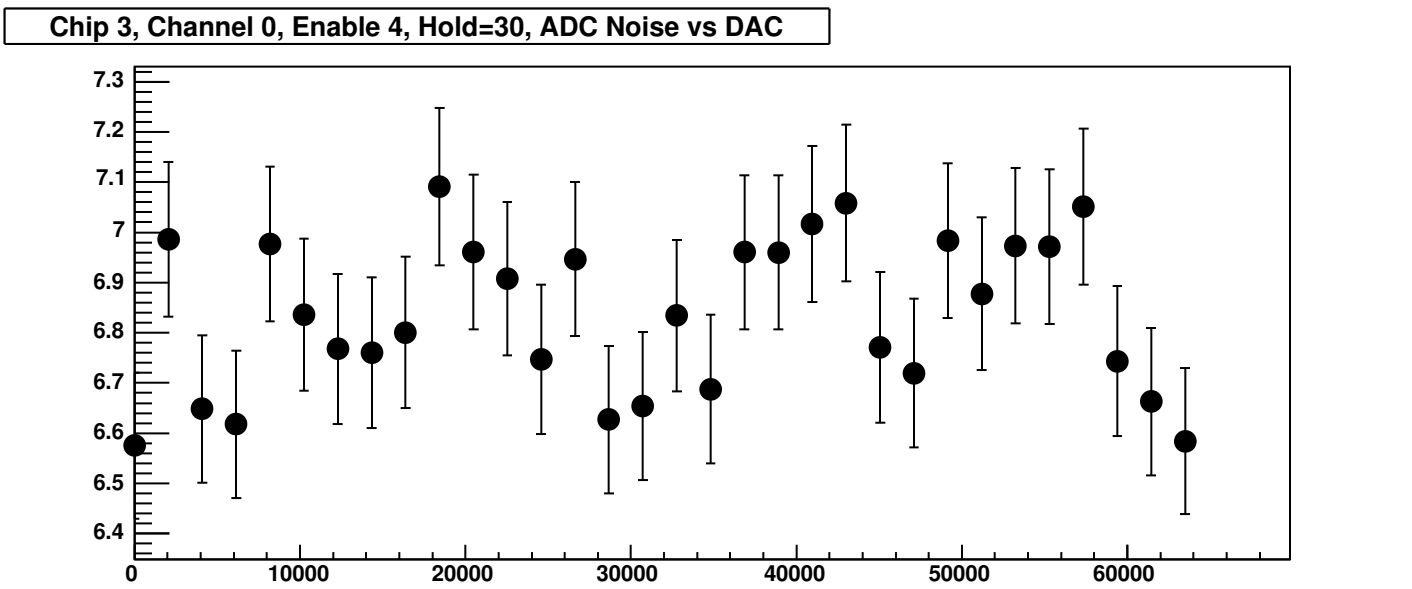

Chip 3, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

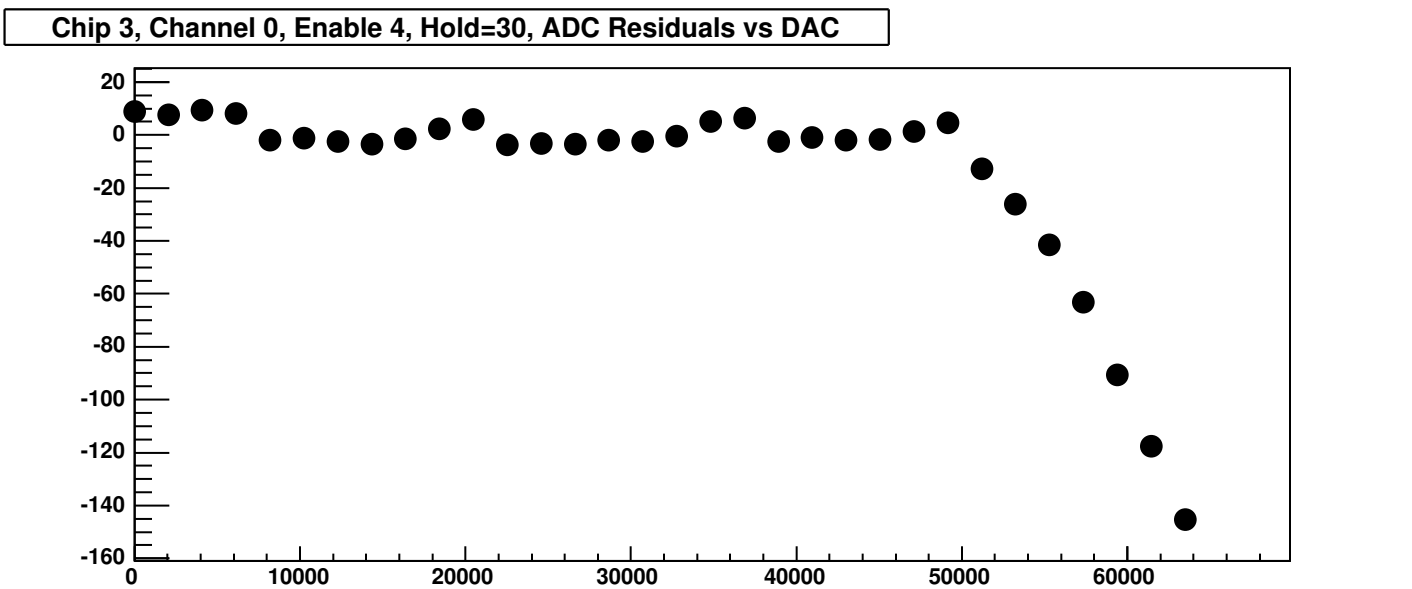

Chip 3, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 4052 / 15
p0 1180 ± 0.3372
p1 0.6278 ± 3.44e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

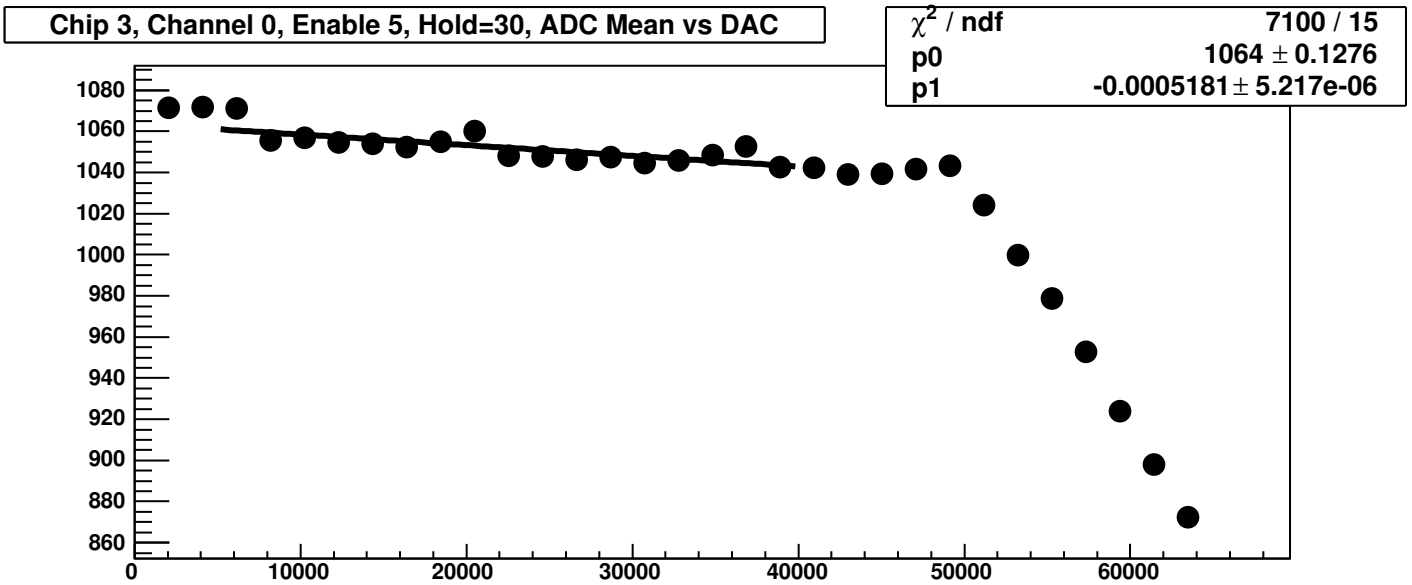

Chip 3, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

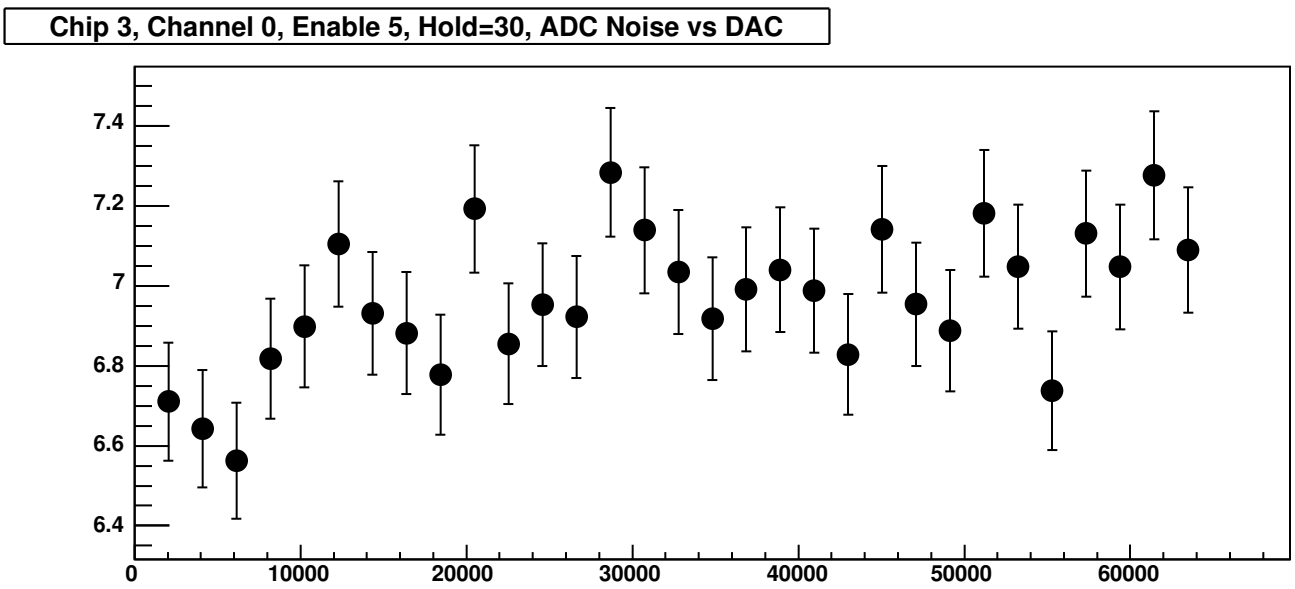

Chip 3, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

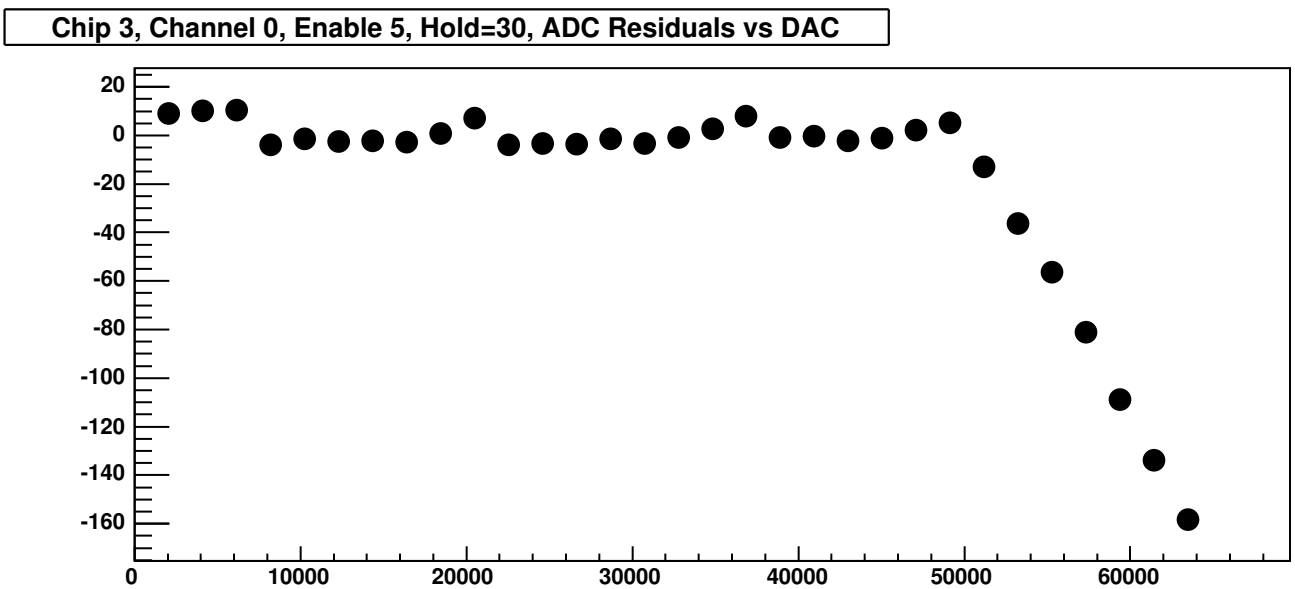

Chip 3, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5407 / 15
p0 1068 ± 0.127
p1 0.0002945 ± 5.152e-06
1100
1080
1060
1040
1020
1000
980
960
940
0
10000
20000
30000
40000
50000
60000

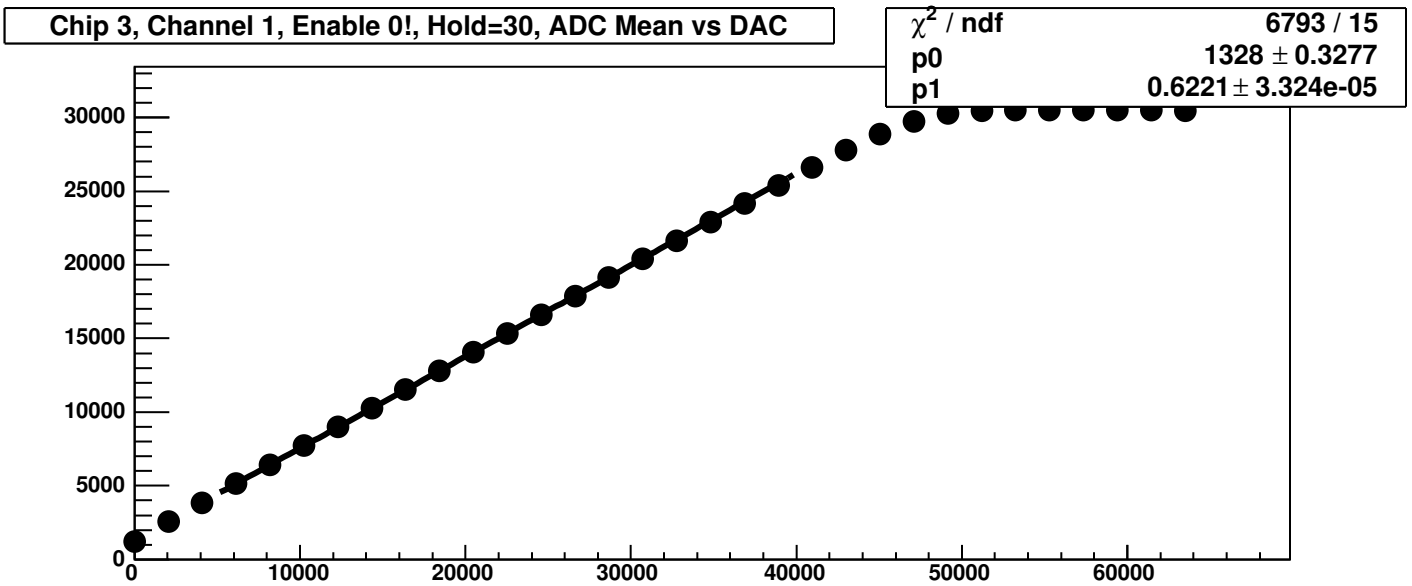

Chip 3, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
7.3
7.2
7.1
7
6.9
6.8
6.7
6.6
6.5
6.4
0
10000
20000
30000
40000
50000
60000

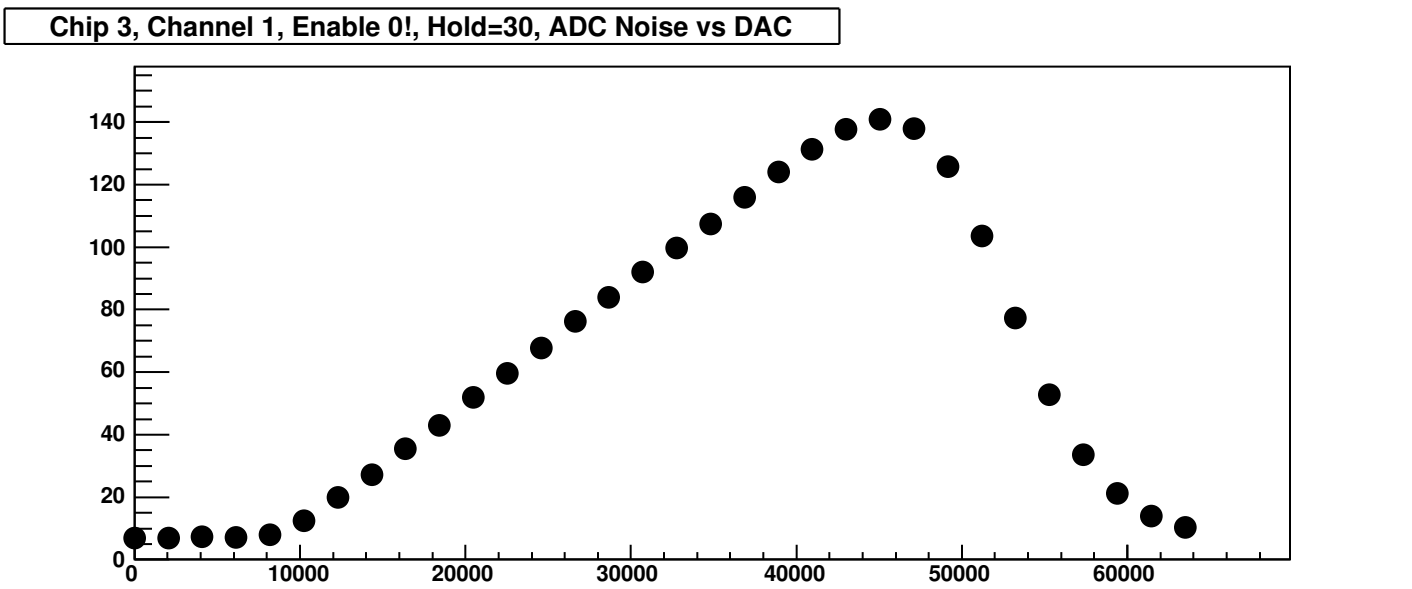

Chip 3, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

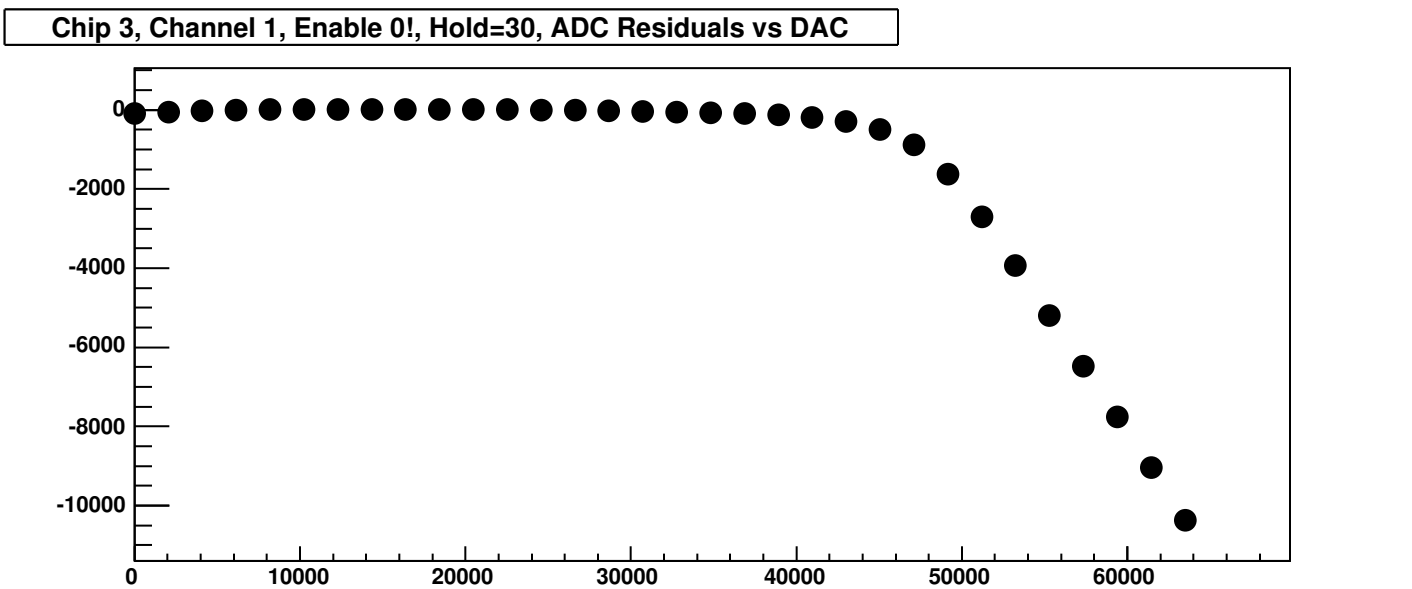

Chip 3, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7100 / 15
p0 1064 ± 0.1276
p1 -0.0005181 ± 5.217e-06
1080
1060
1040
1020
1000
980
960
940
920
900
880
860
0
10000
20000
30000
40000
50000
60000

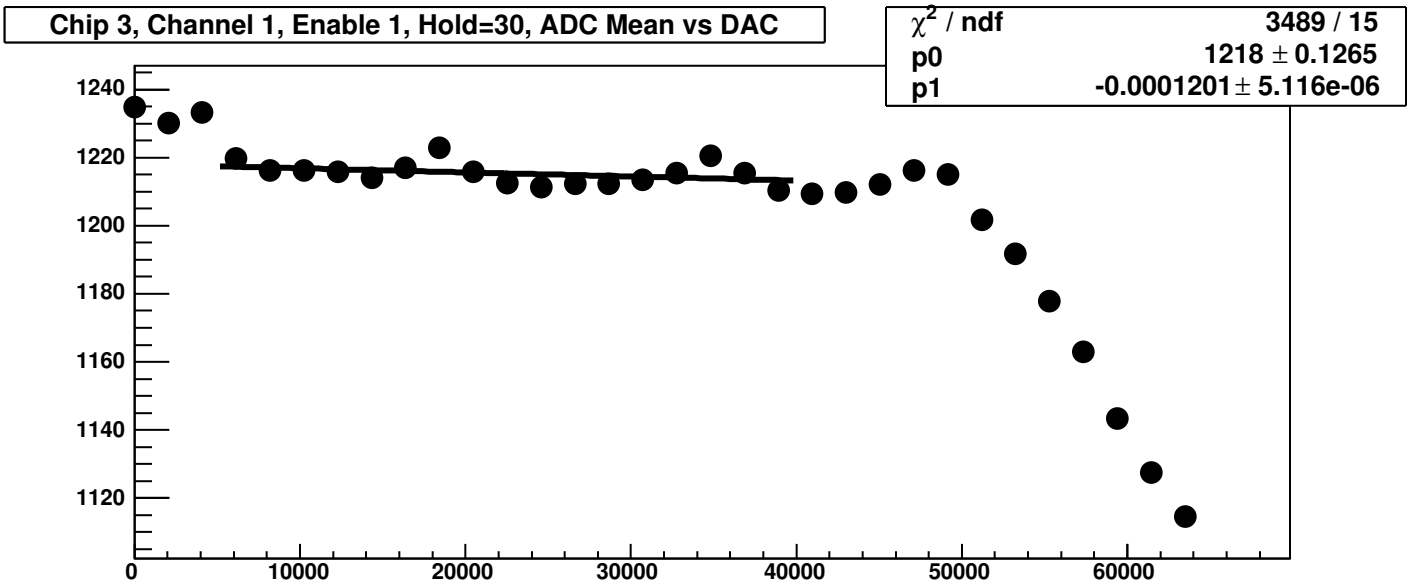

Chip 3, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

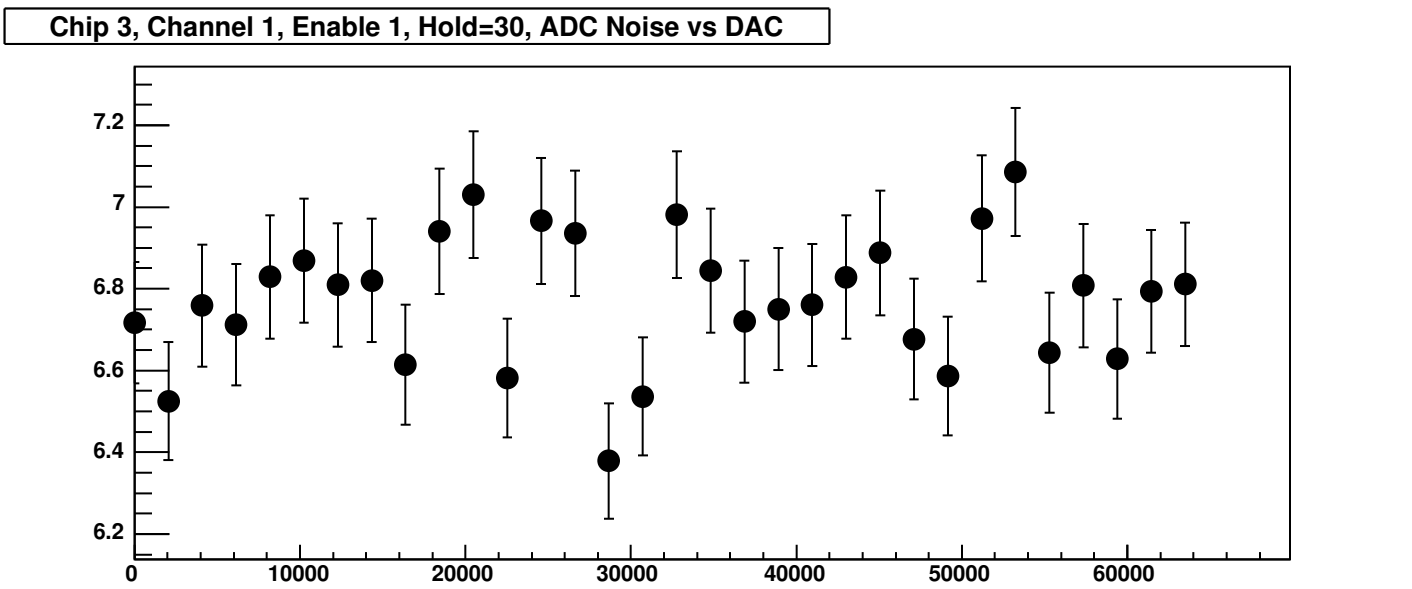

Chip 3, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

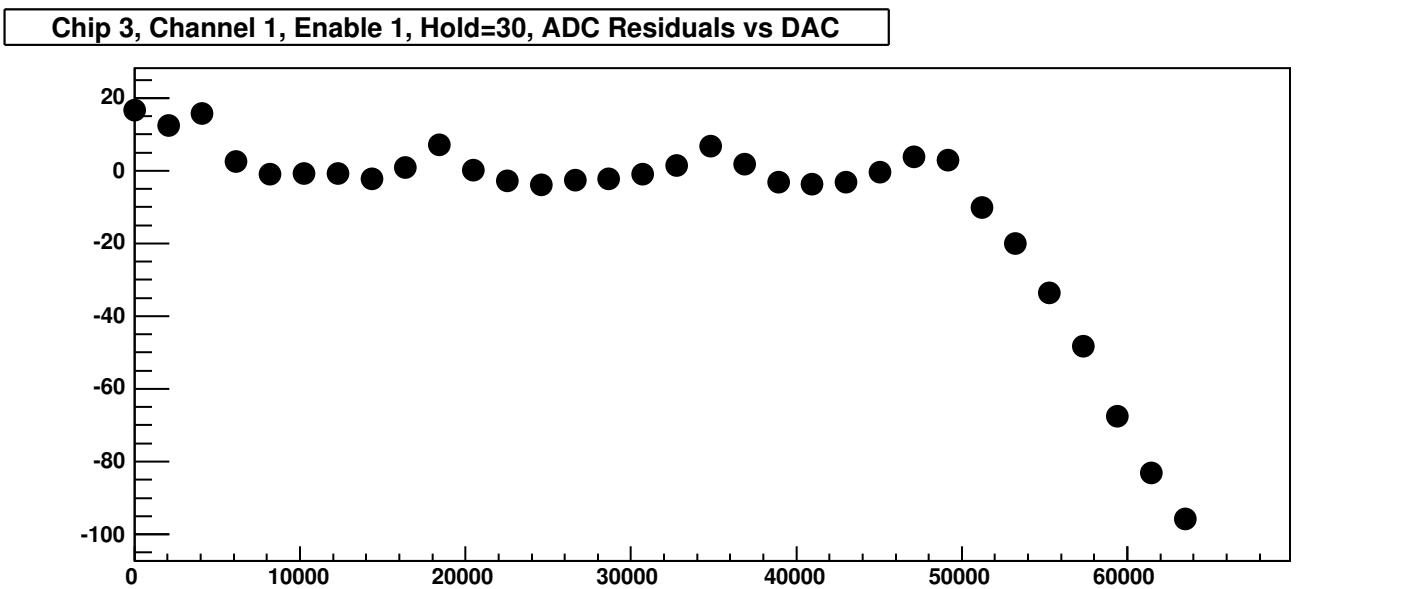

Chip 3, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 6793 / 15
p0 1328 ± 0.3277
p1 0.6221 ± 3.324e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

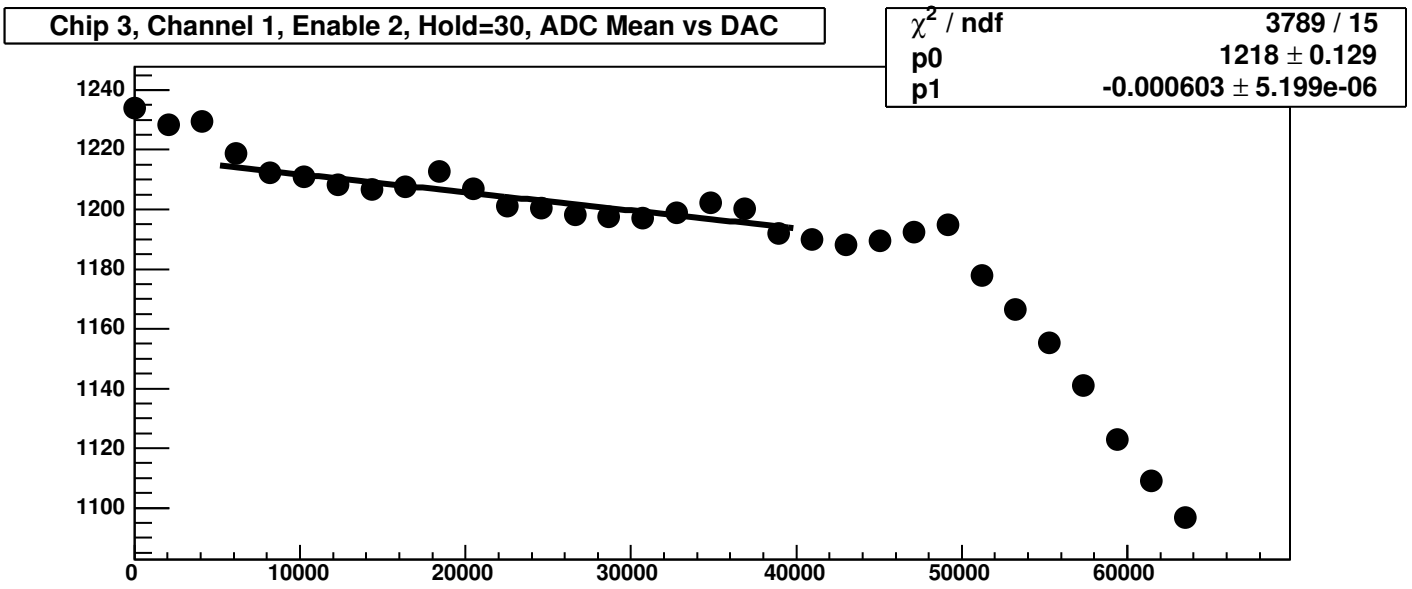

Chip 3, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

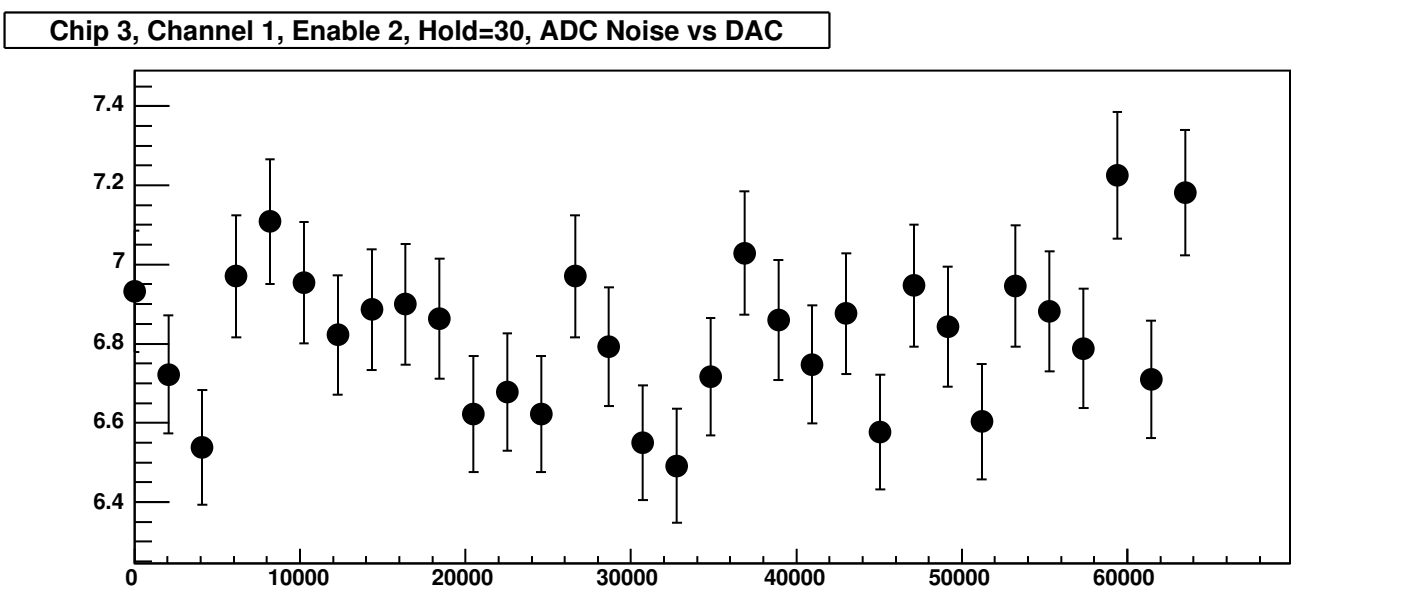

Chip 3, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

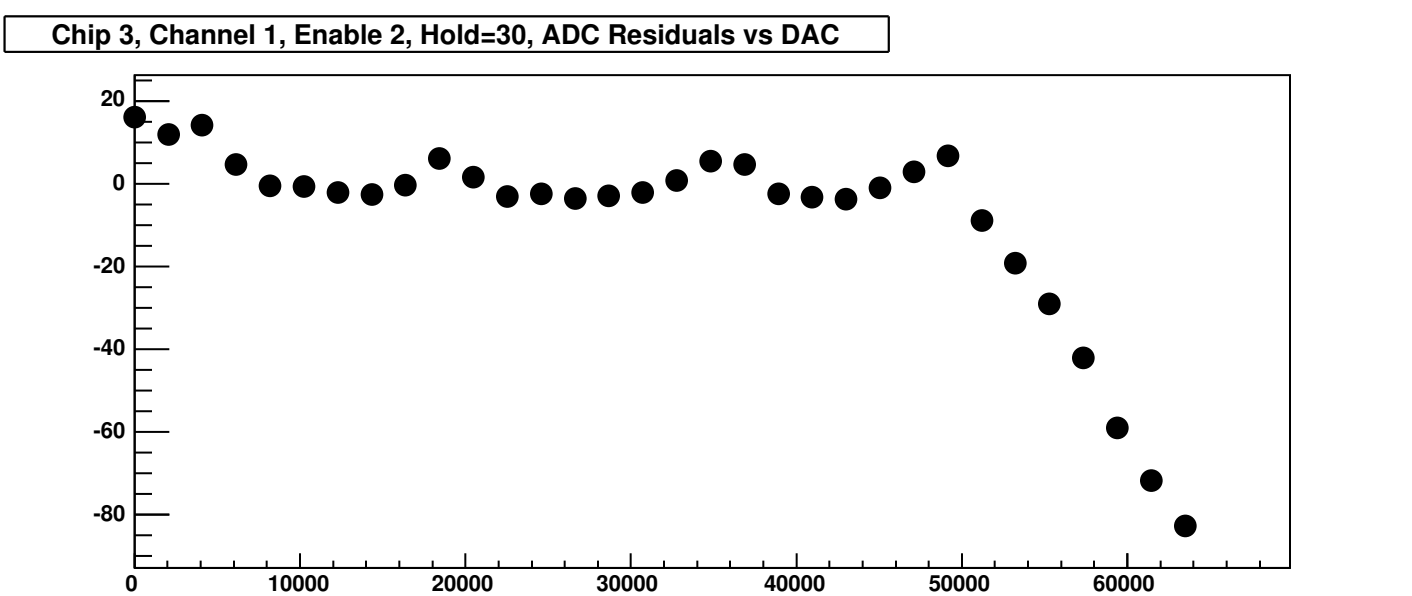

Chip 3, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3489 / 15
p0 1218 ± 0.1265
p1 -0.0001201 ± 5.116e-06
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000

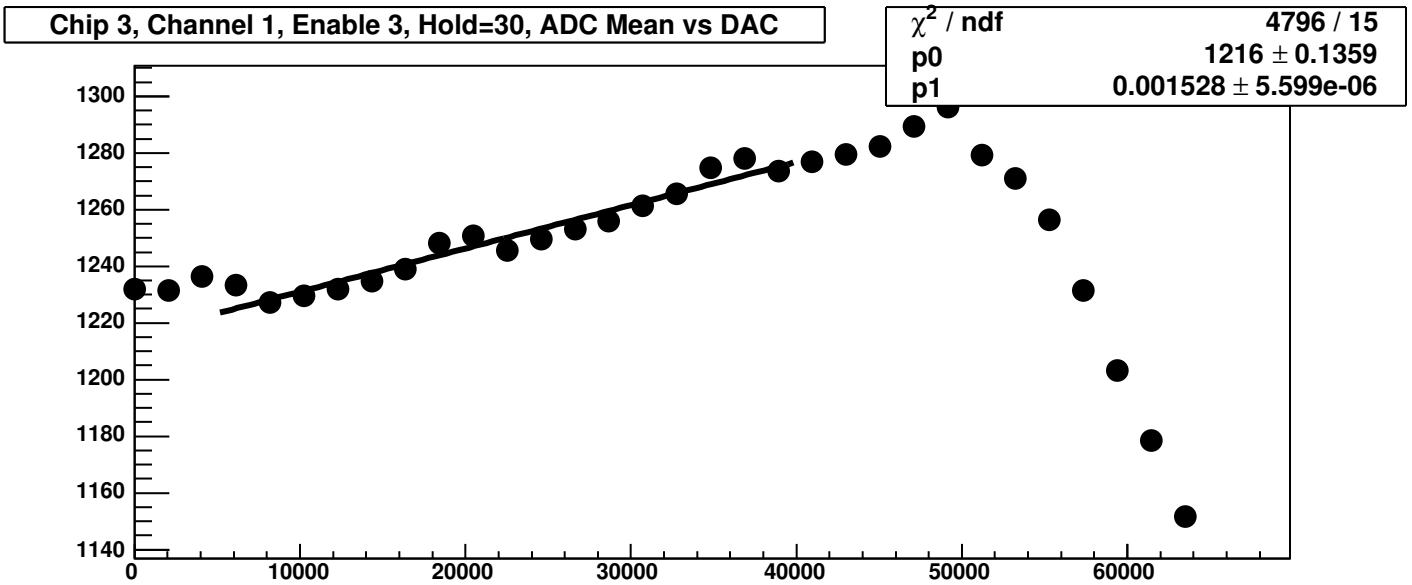

Chip 3, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

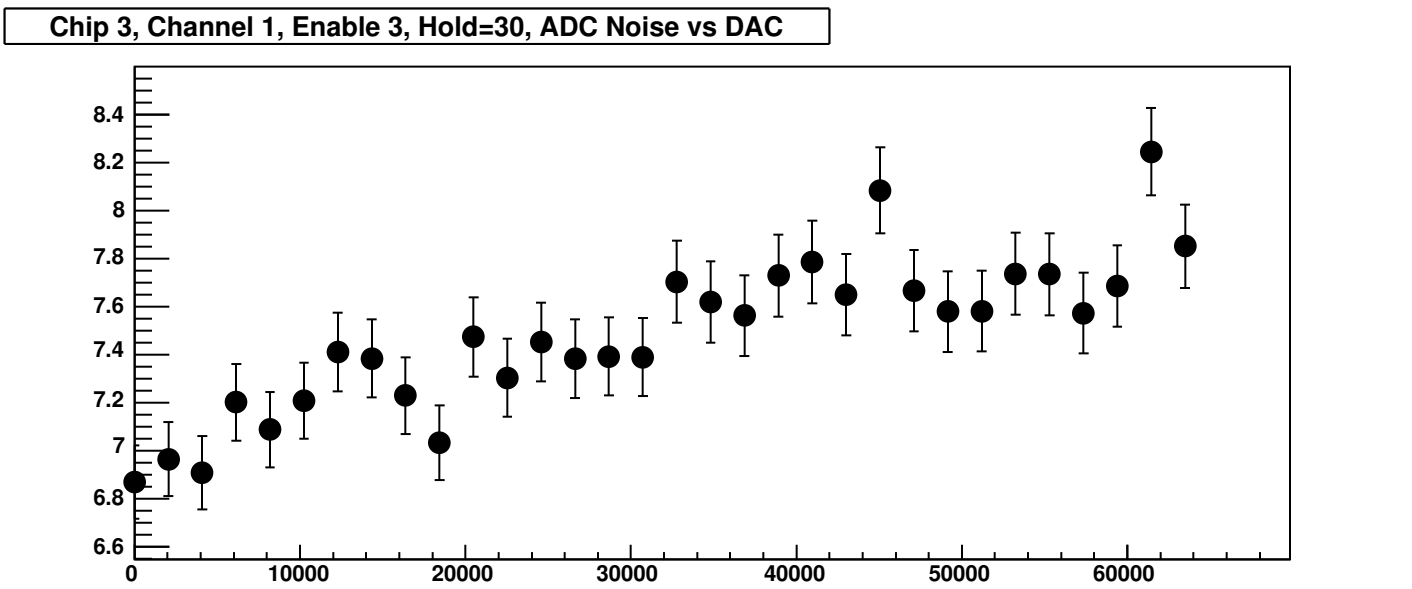

Chip 3, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

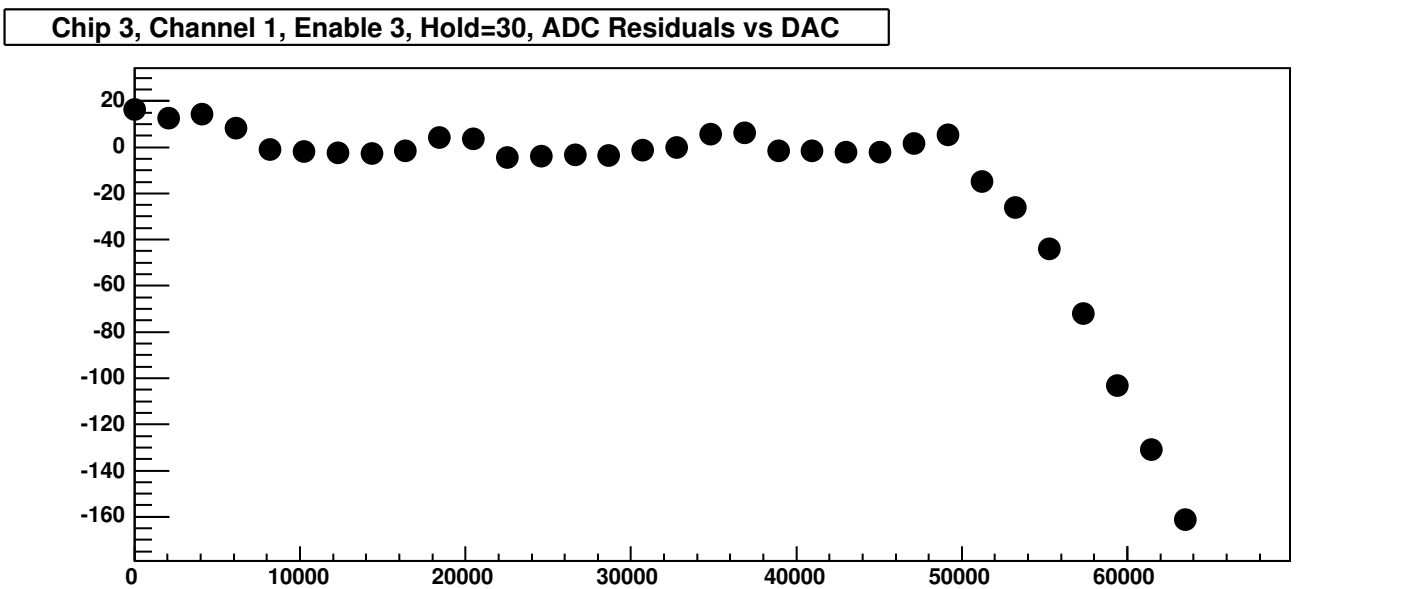

Chip 3, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3789 / 15
p0 1218 ± 0.129
p1 -0.000603 ± 5.199e-06
1240
1220
1200
1180
1160
1140
1120
1100
0
10000
20000
30000
40000
50000
60000

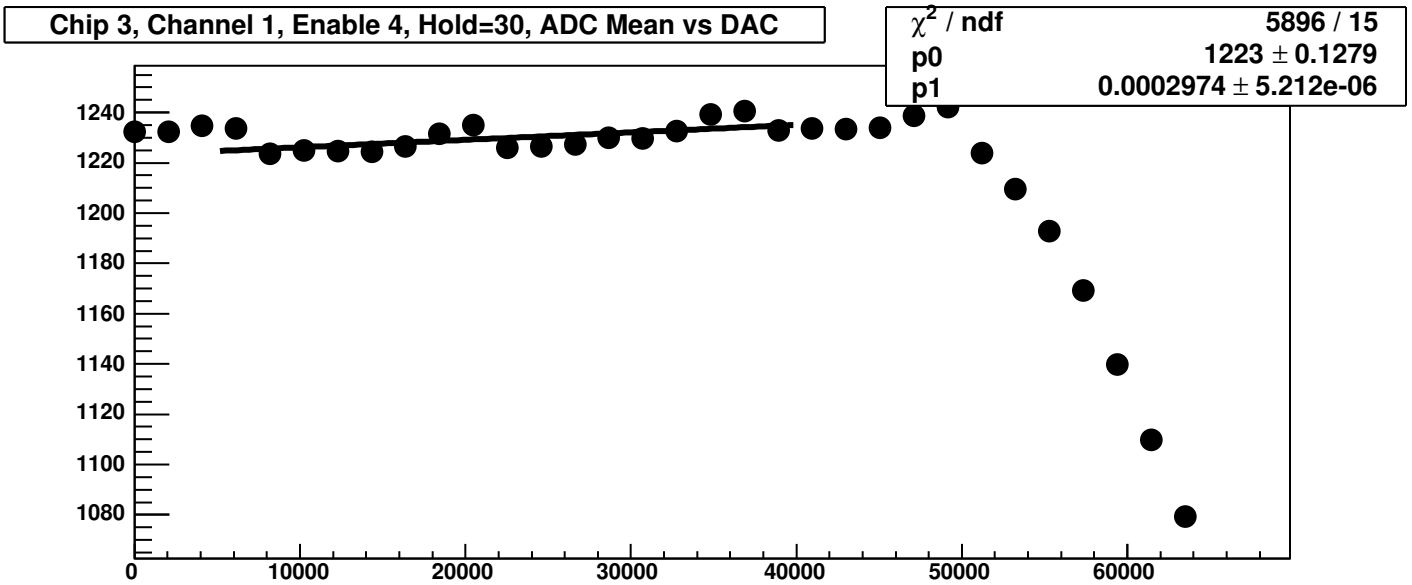

Chip 3, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

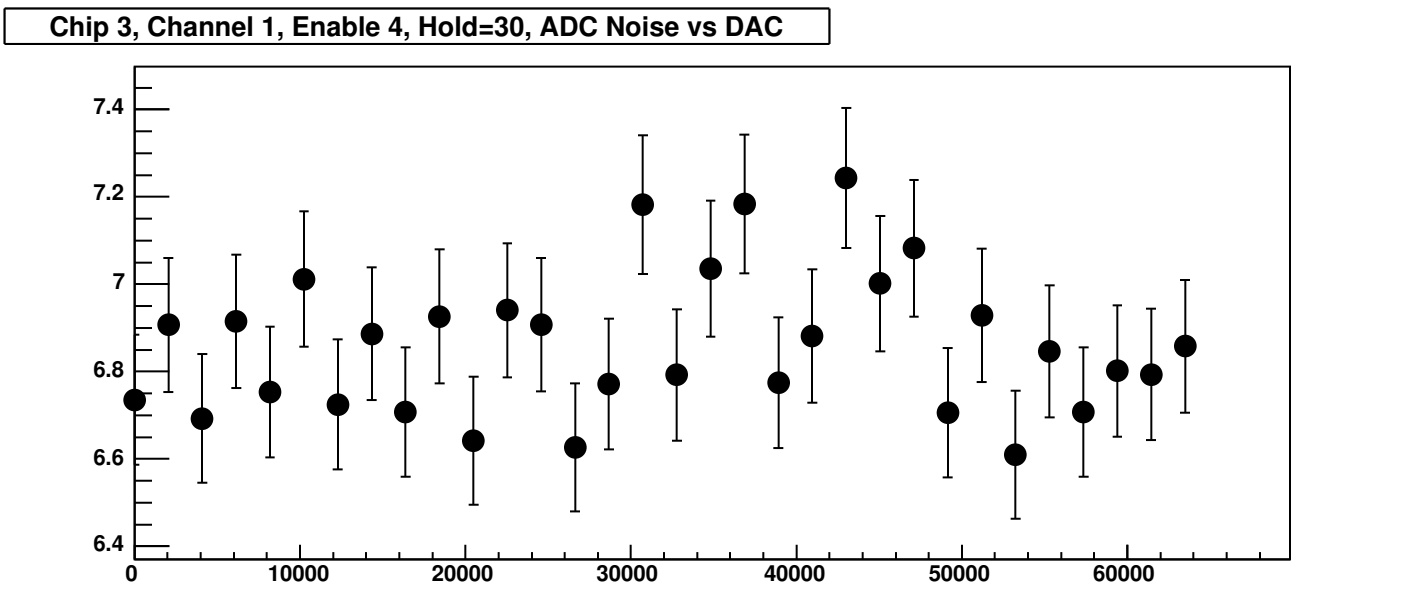

Chip 3, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

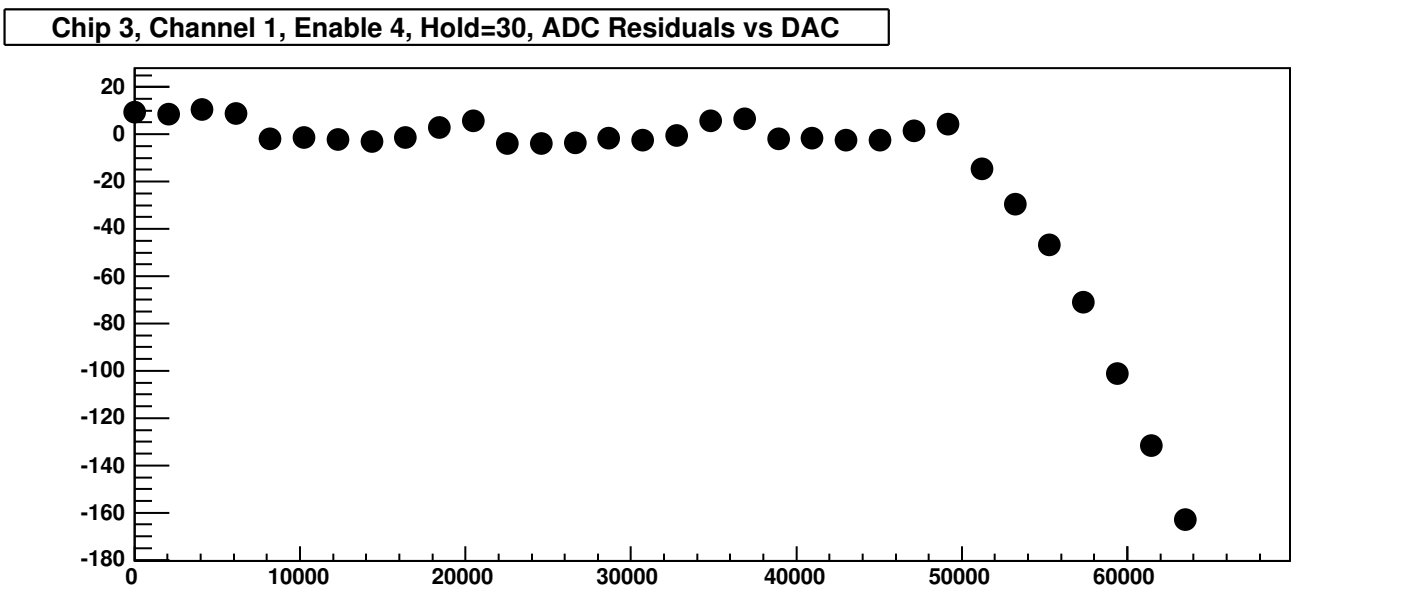

Chip 3, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4796 / 15
p0 1216 ± 0.1359
p1 0.001528 ± 5.599e-06
1300
1280
1260
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

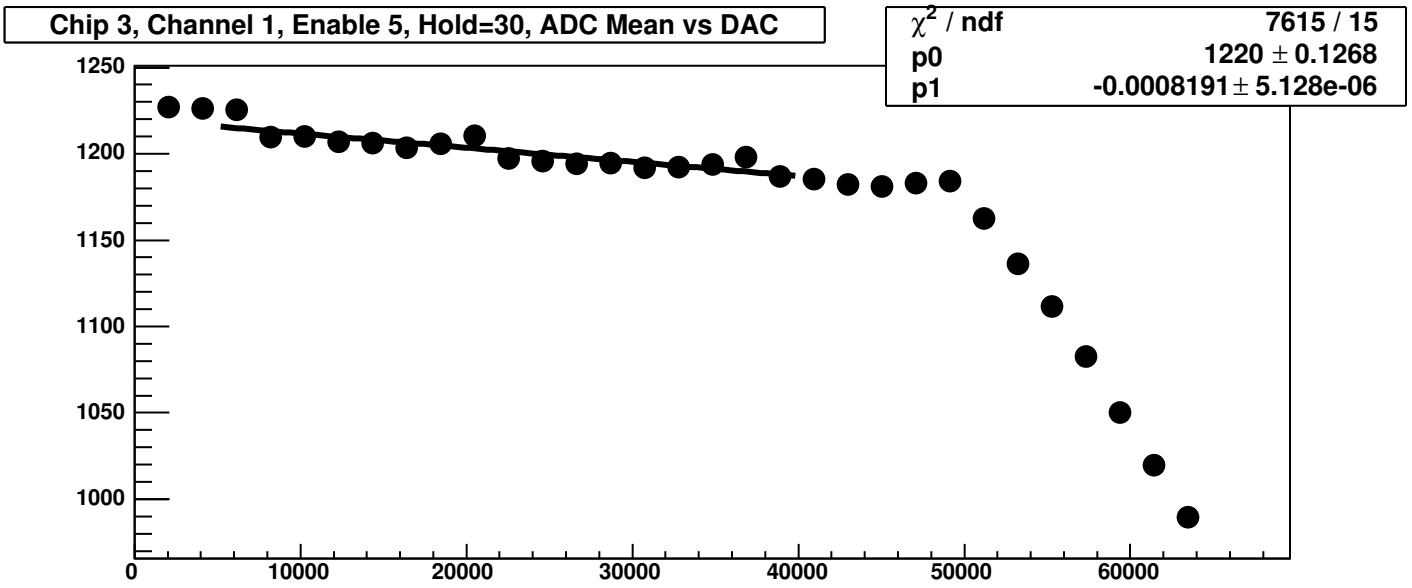

Chip 3, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC
8.4
8.2
8
7.8
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

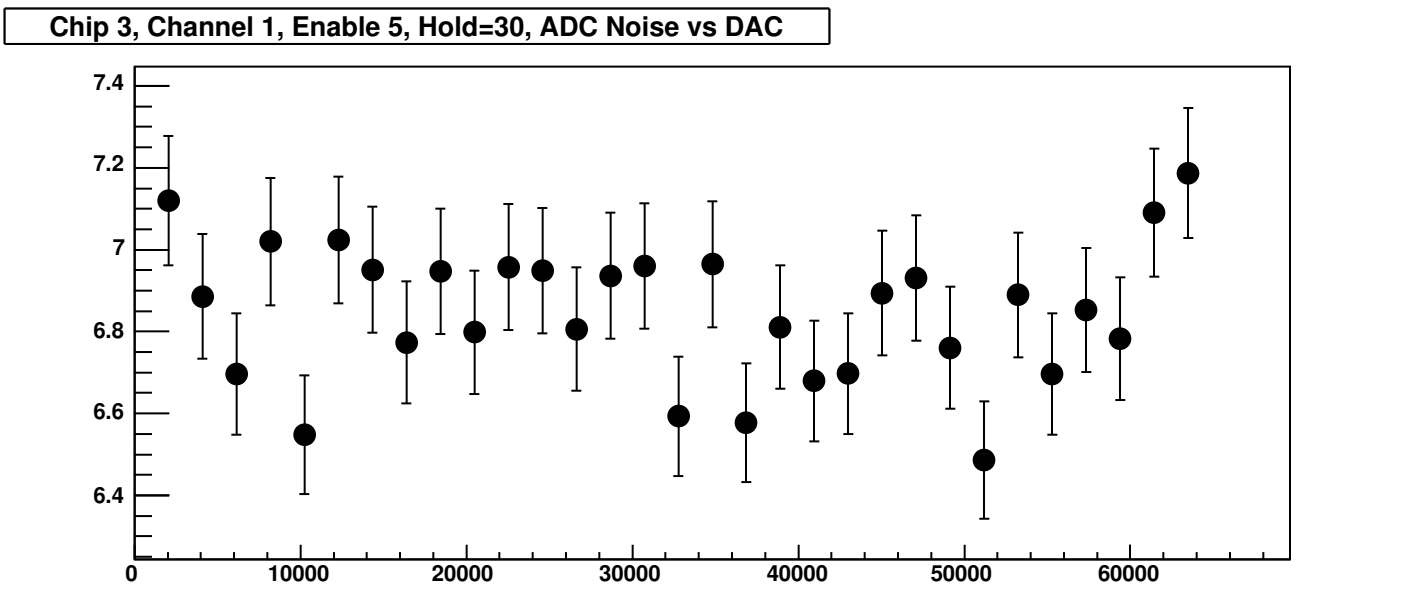

Chip 3, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

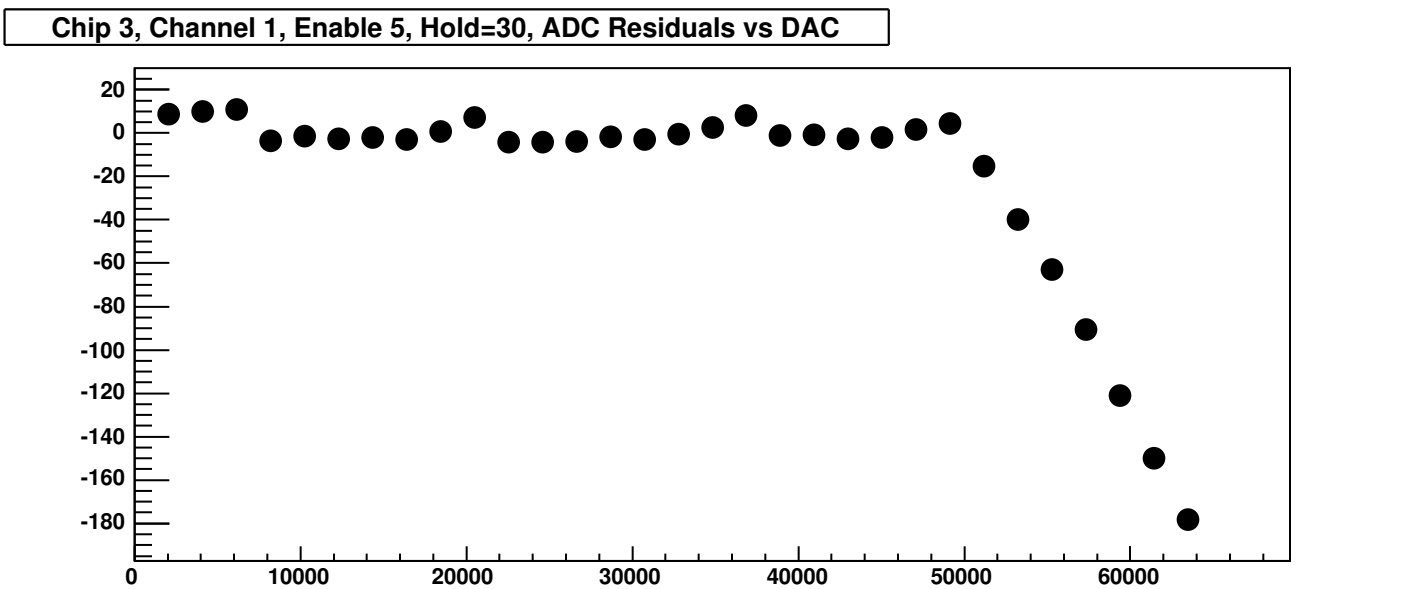

Chip 3, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5896 / 15
p0 1223 ± 0.1279
p1 0.0002974 ± 5.212e-06
1240
1220
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

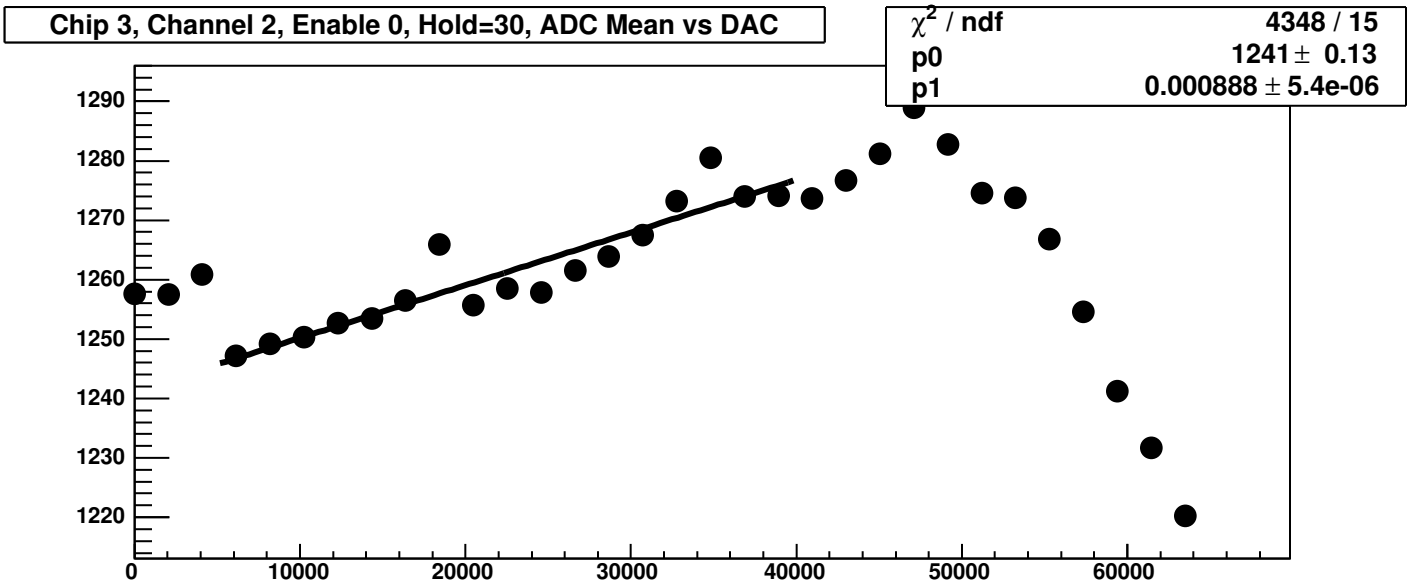

Chip 3, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

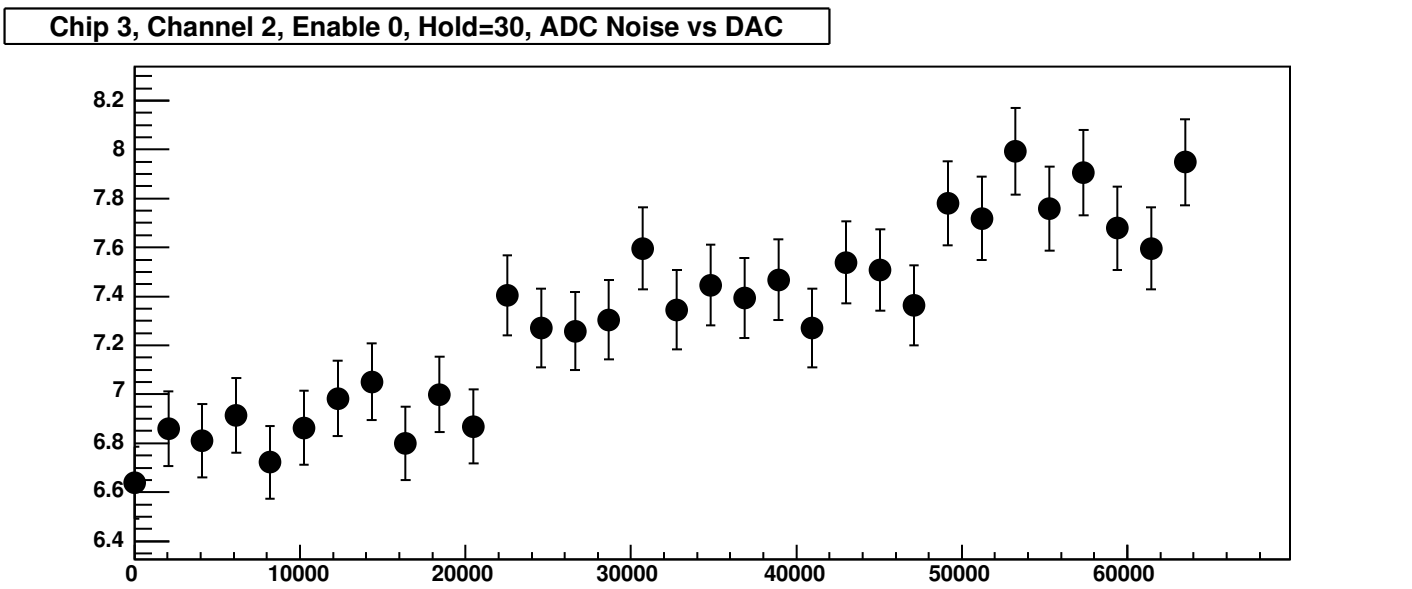

Chip 3, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

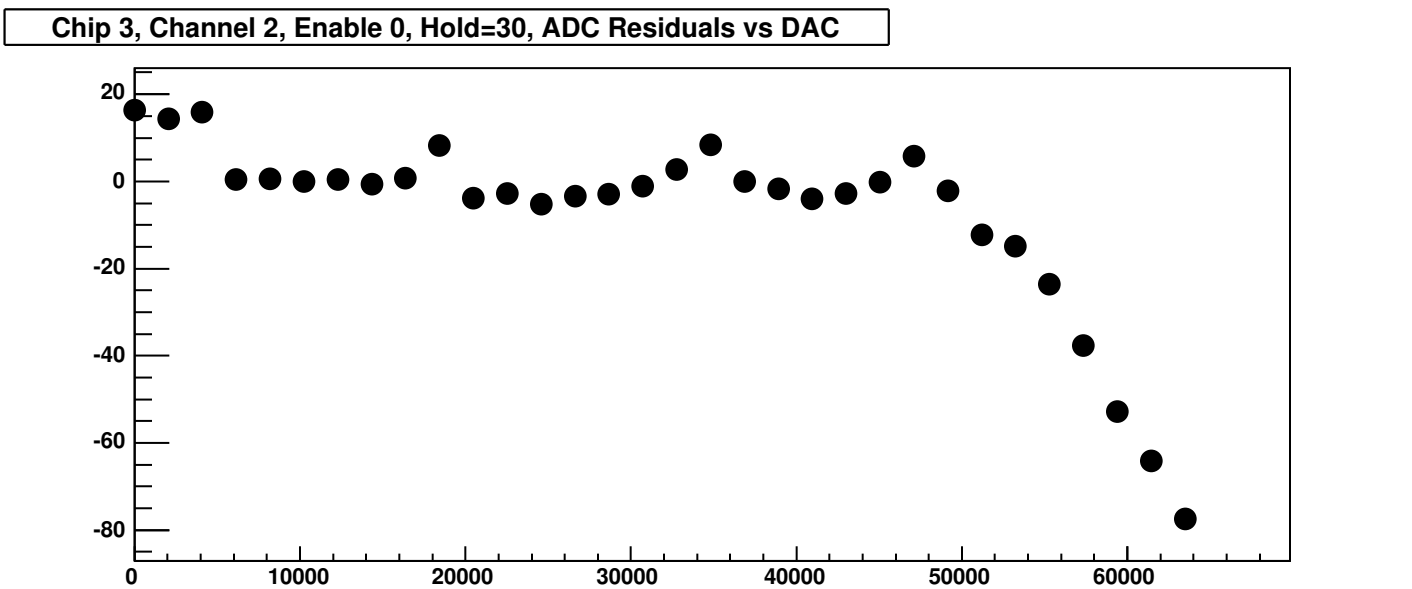

Chip 3, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7615 / 15
p0 1220 ± 0.1268
p1 -0.0008191 ± 5.128e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

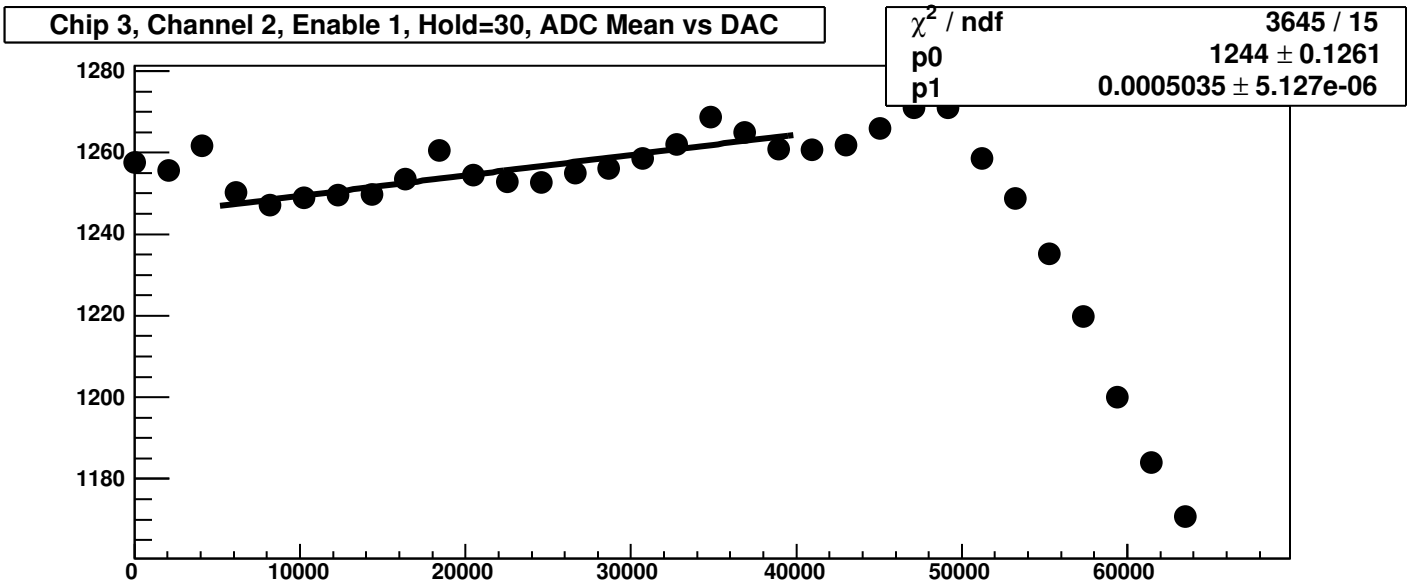

Chip 3, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

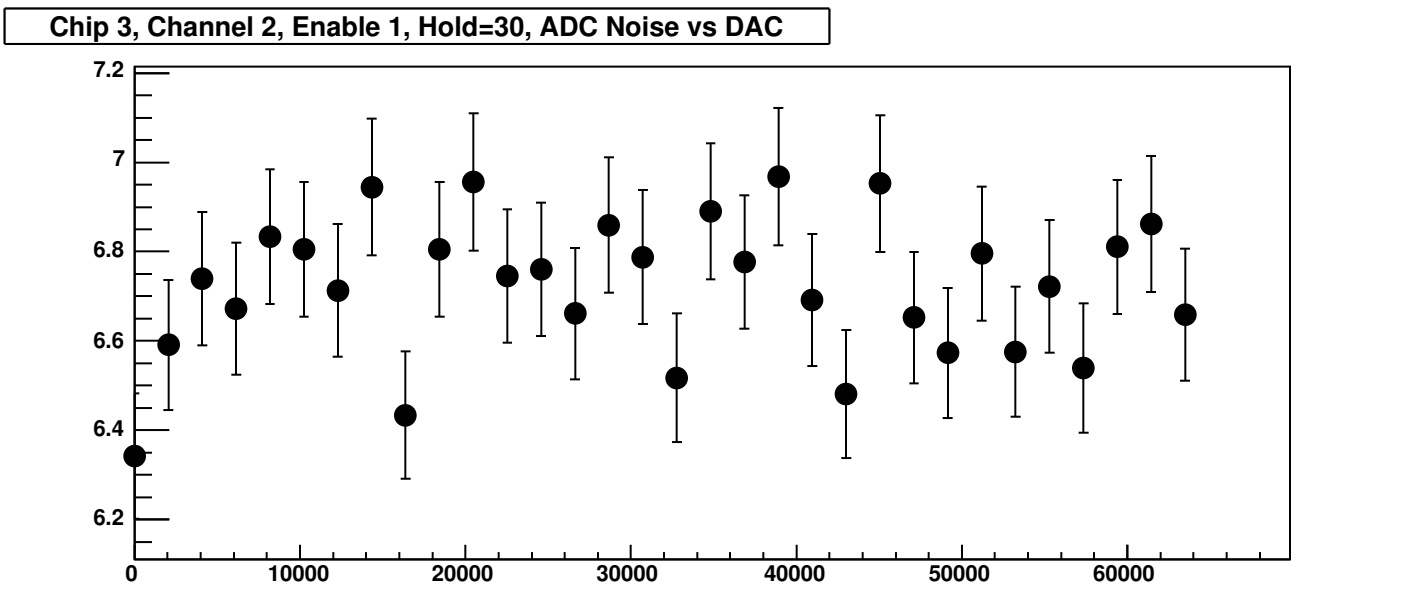

Chip 3, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

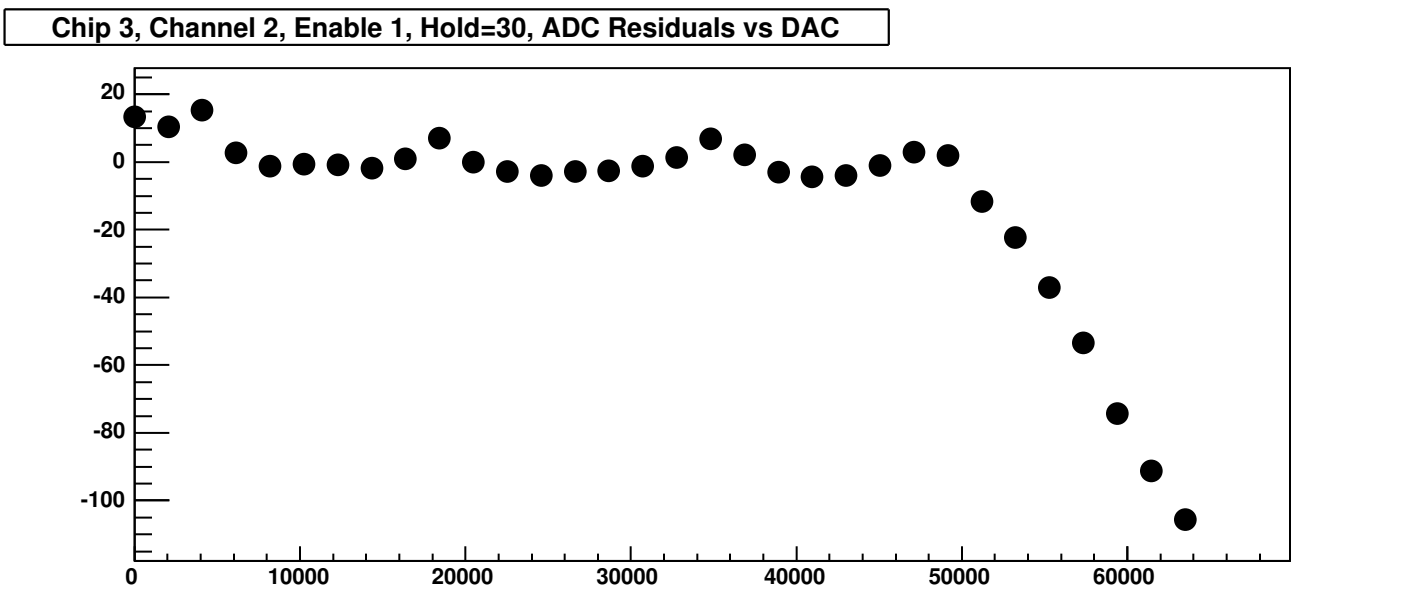

Chip 3, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4348 / 15
p0 1241 ± 0.13
p1 0.000888 ± 5.4e-06
1290
1280
1270
1260
1250
1240
1230
1220
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC
8.2
8
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

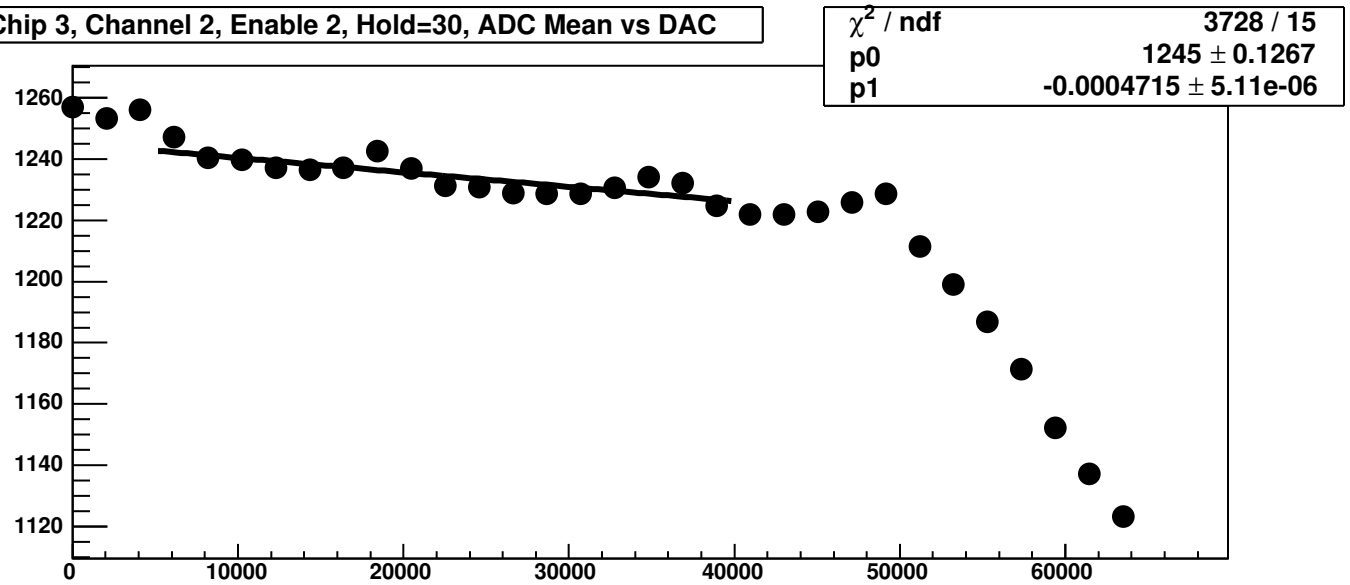

Chip 3, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3645 / 15
p0 1244 ± 0.1261
p1 0.0005035 ± 5.127e-06
1280
1260
1240
1220
1200
1180
0
10000
20000
30000
40000
50000
60000

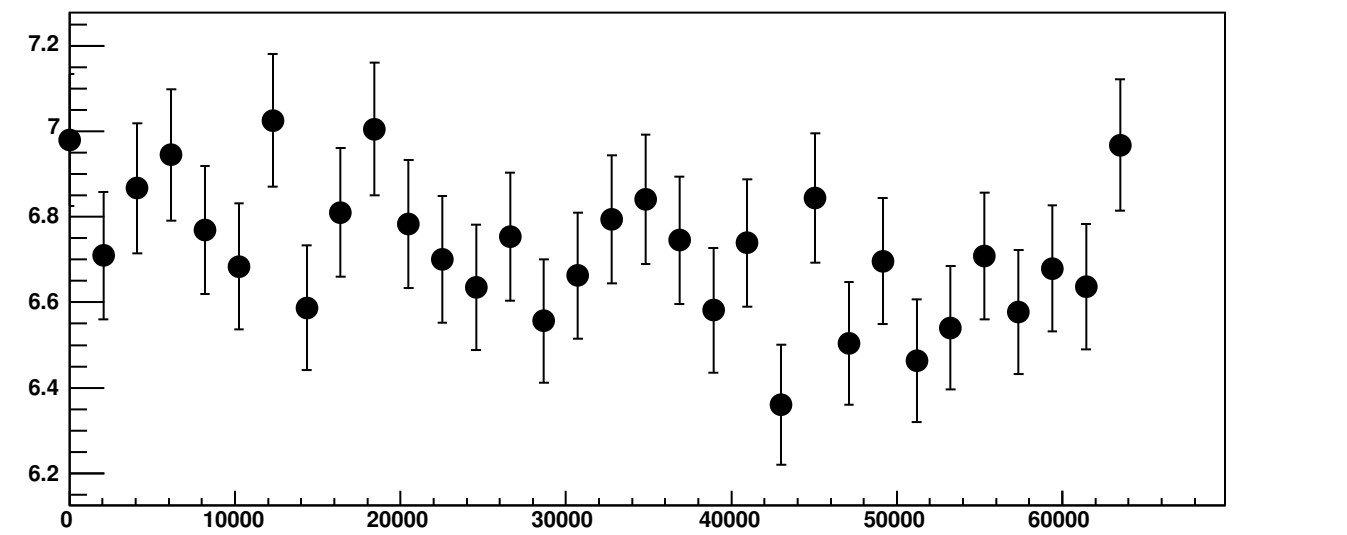

Chip 3, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

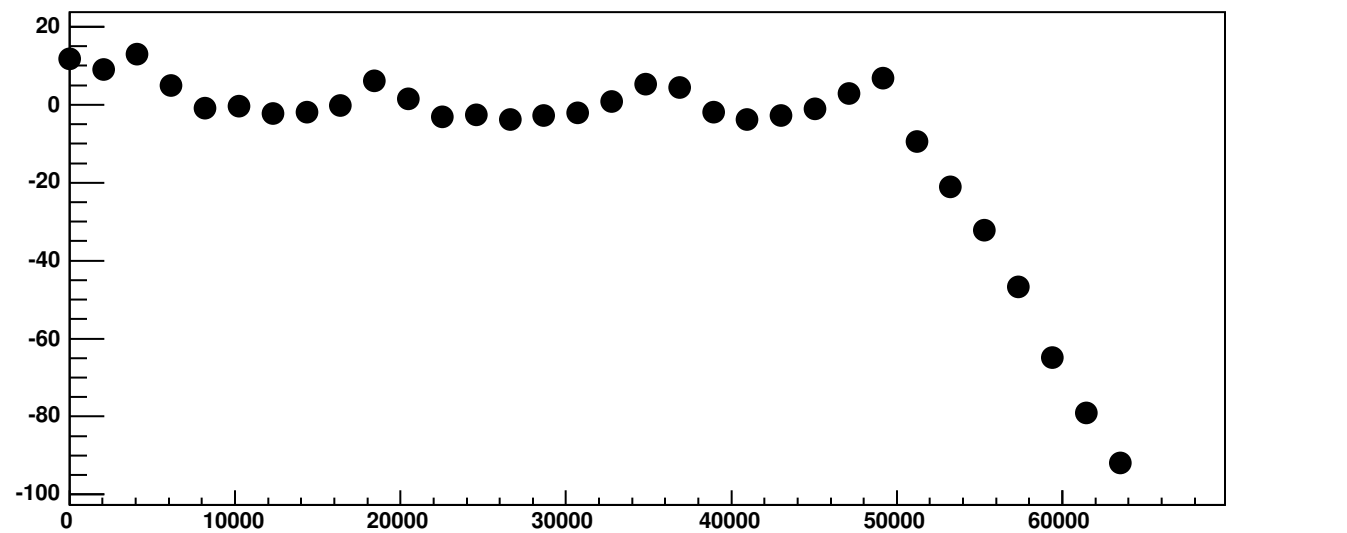
Chip 3, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC

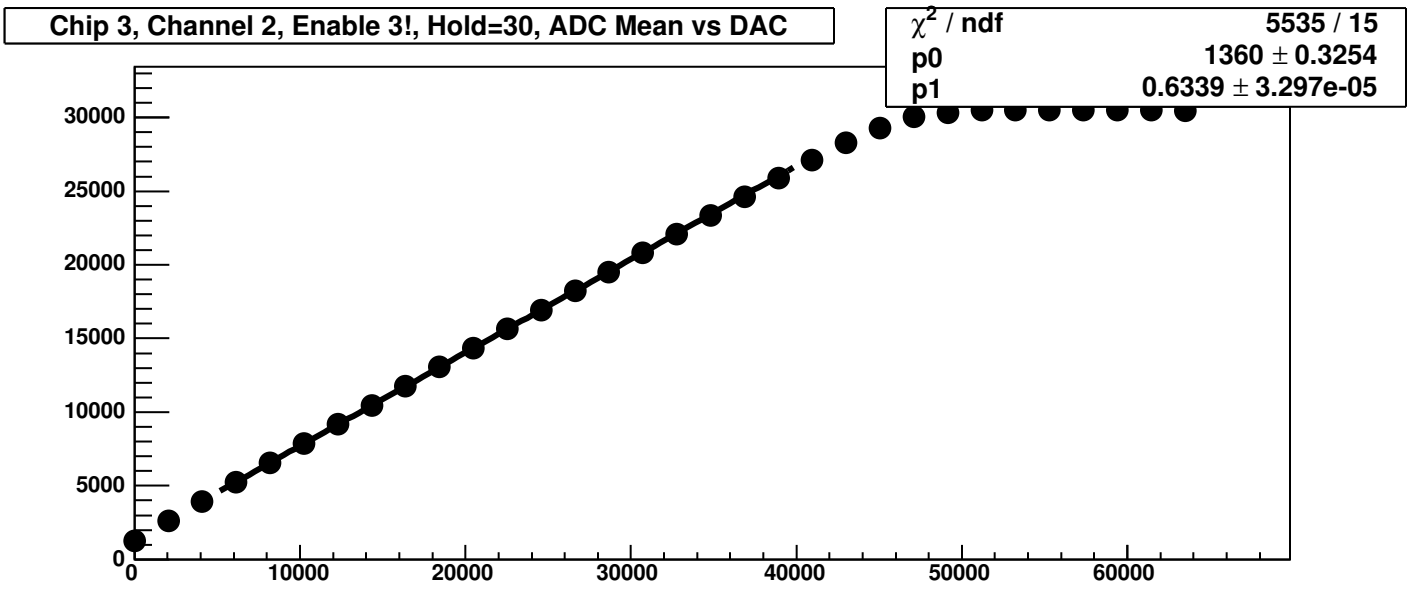


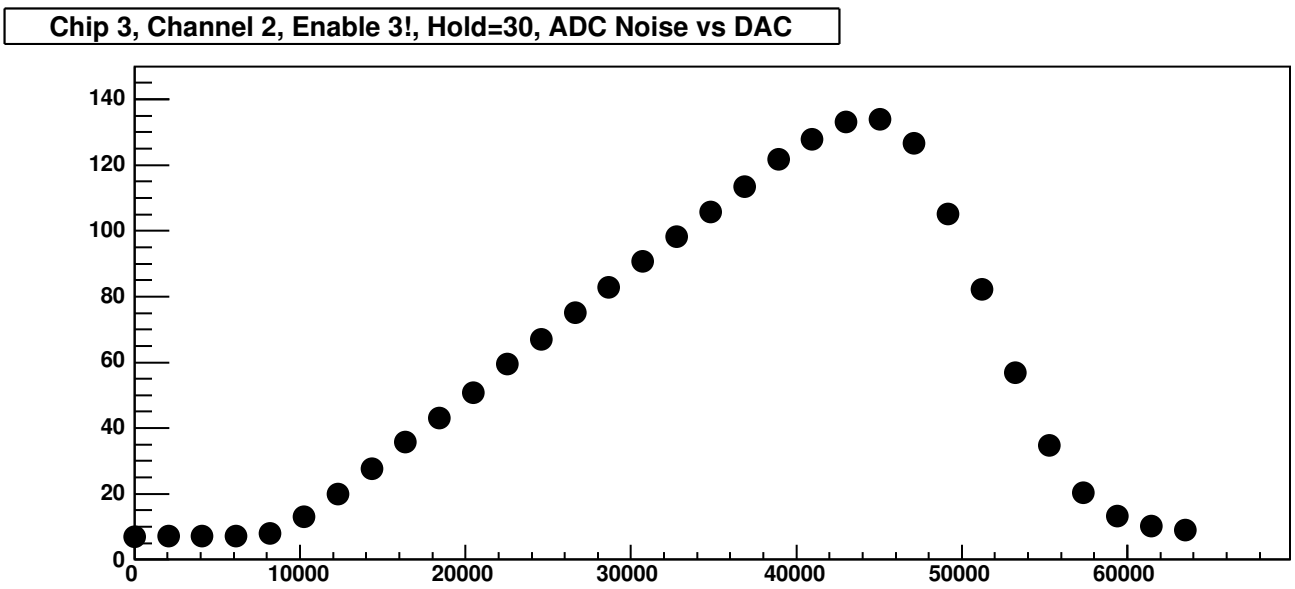
Chip 3, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC

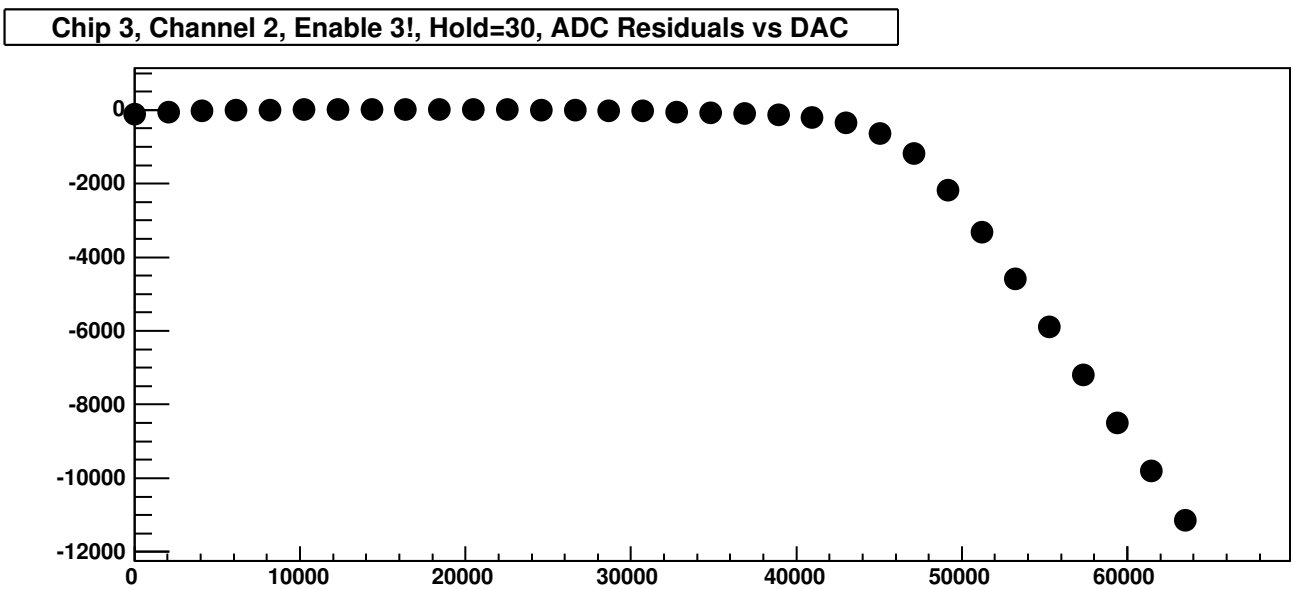


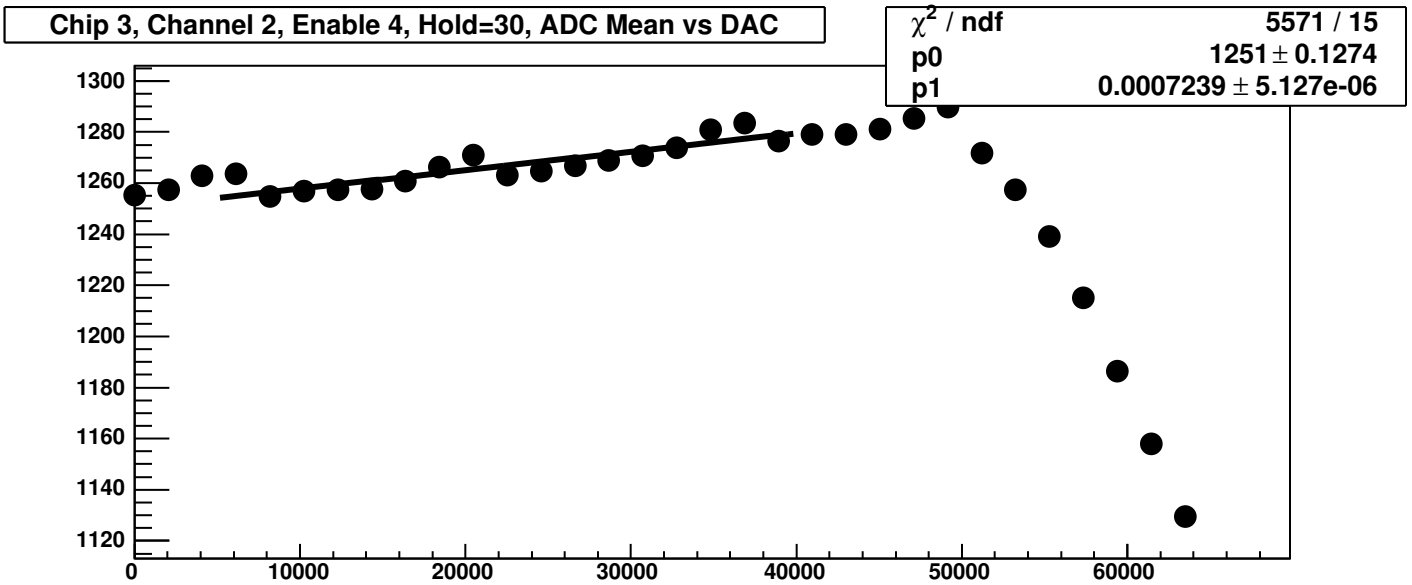
Chip 3, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC

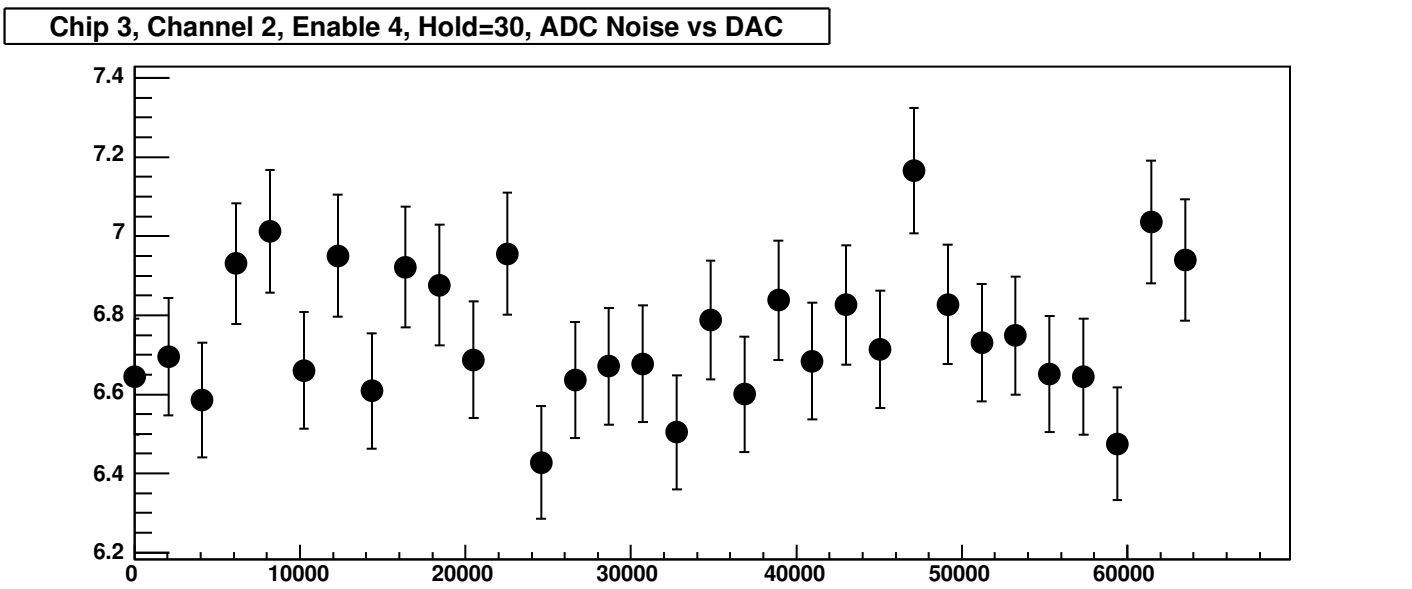


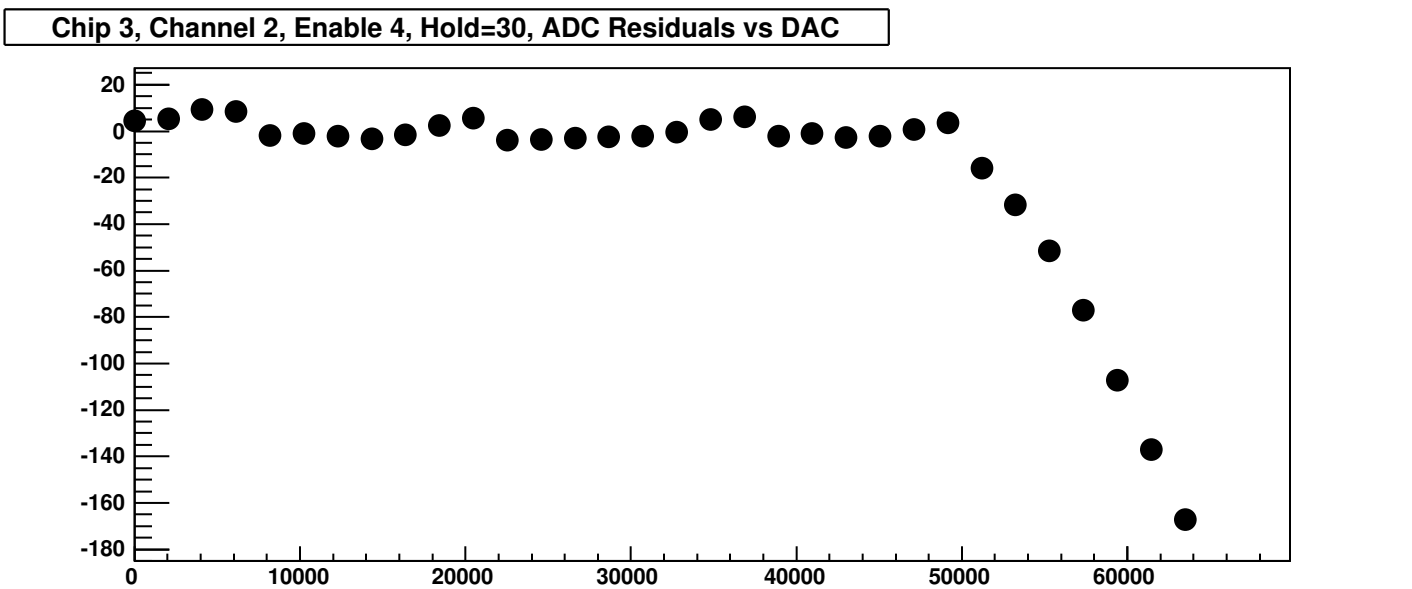

Chip 3, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 5535 / 15
p0 1360 ± 0.3254
p1 0.6339 ± 3.297e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

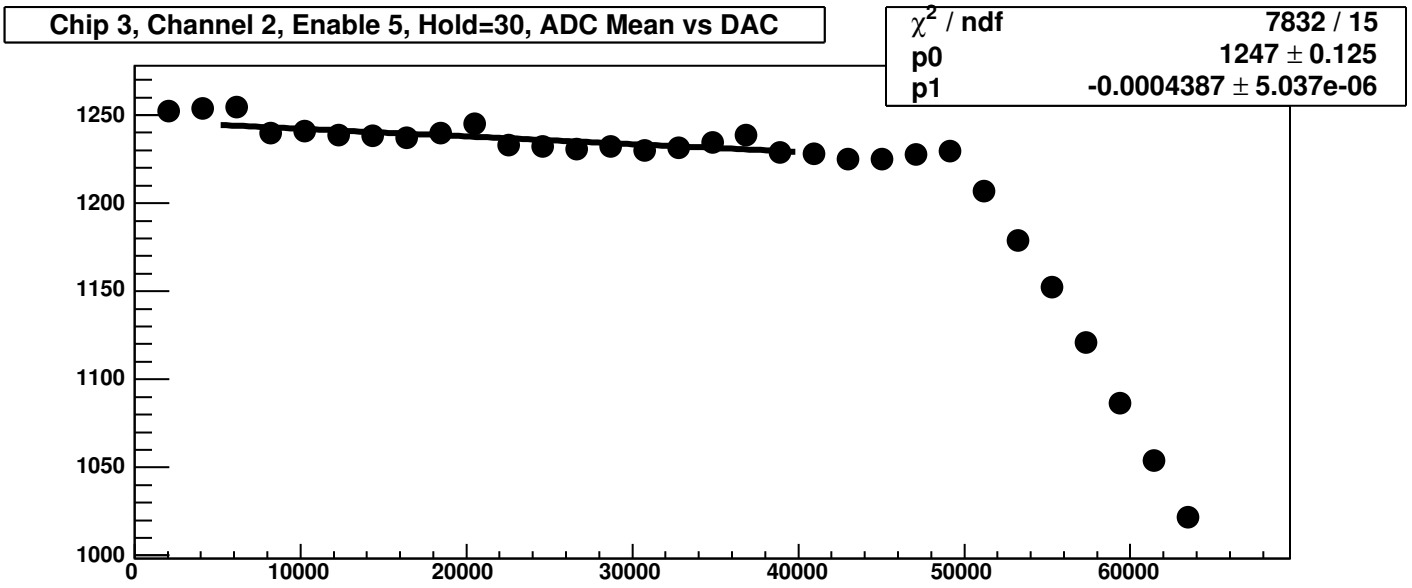

Chip 3, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

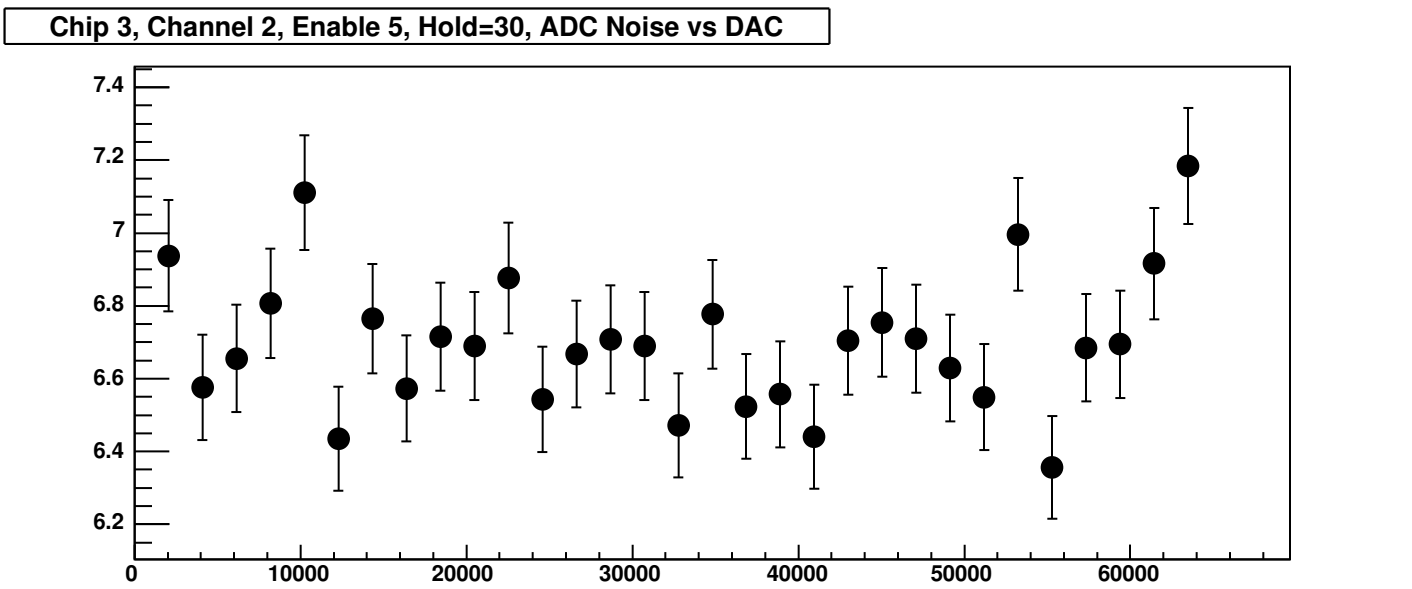

Chip 3, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

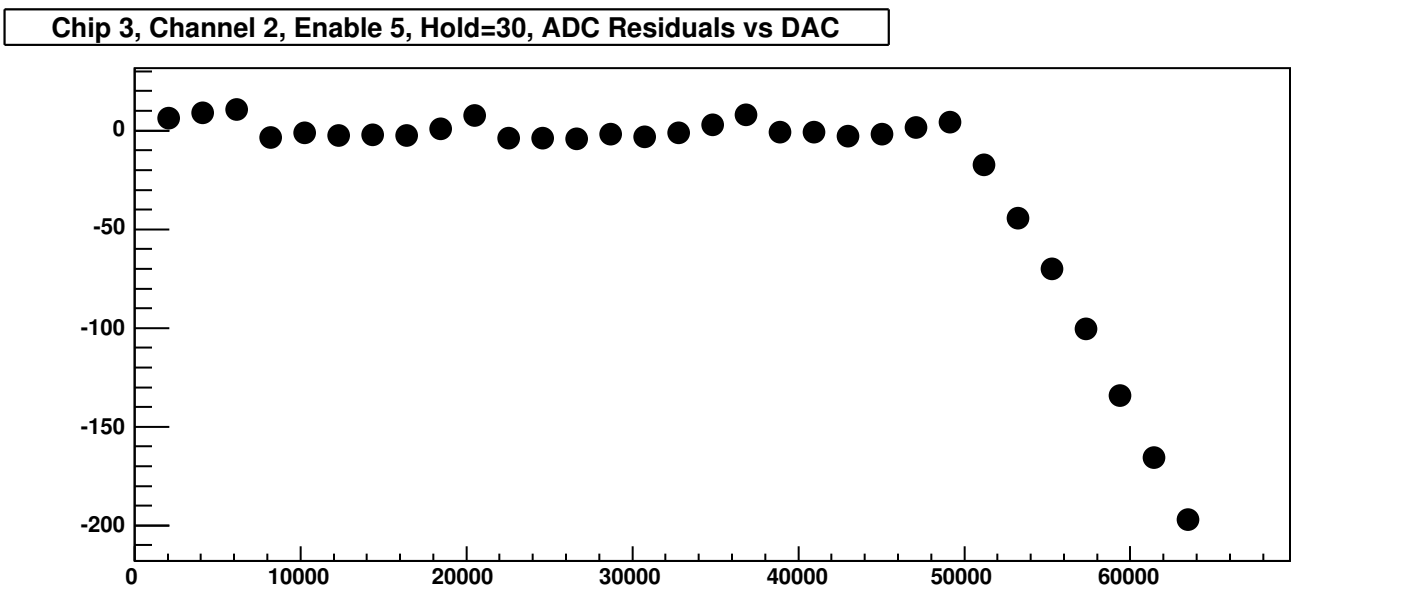

Chip 3, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5571 / 15
p0 1251 ± 0.1274
p1 0.0007239 ± 5.127e-06
1300
1280
1260
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000

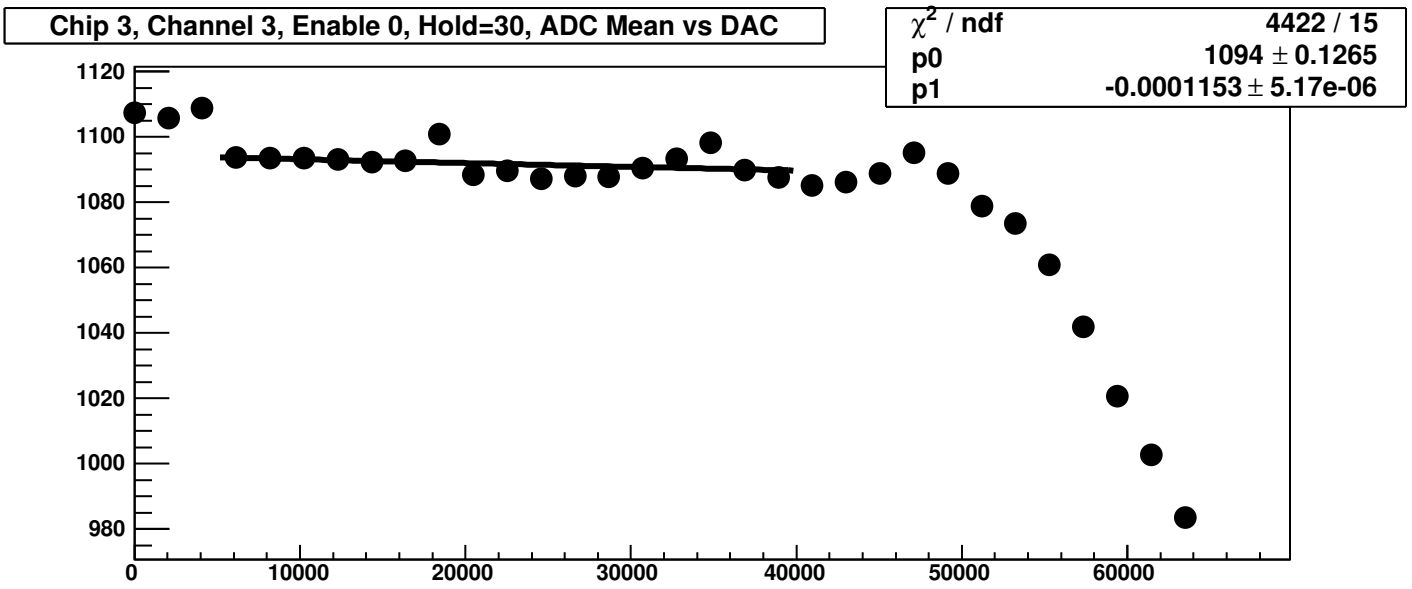

Chip 3, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

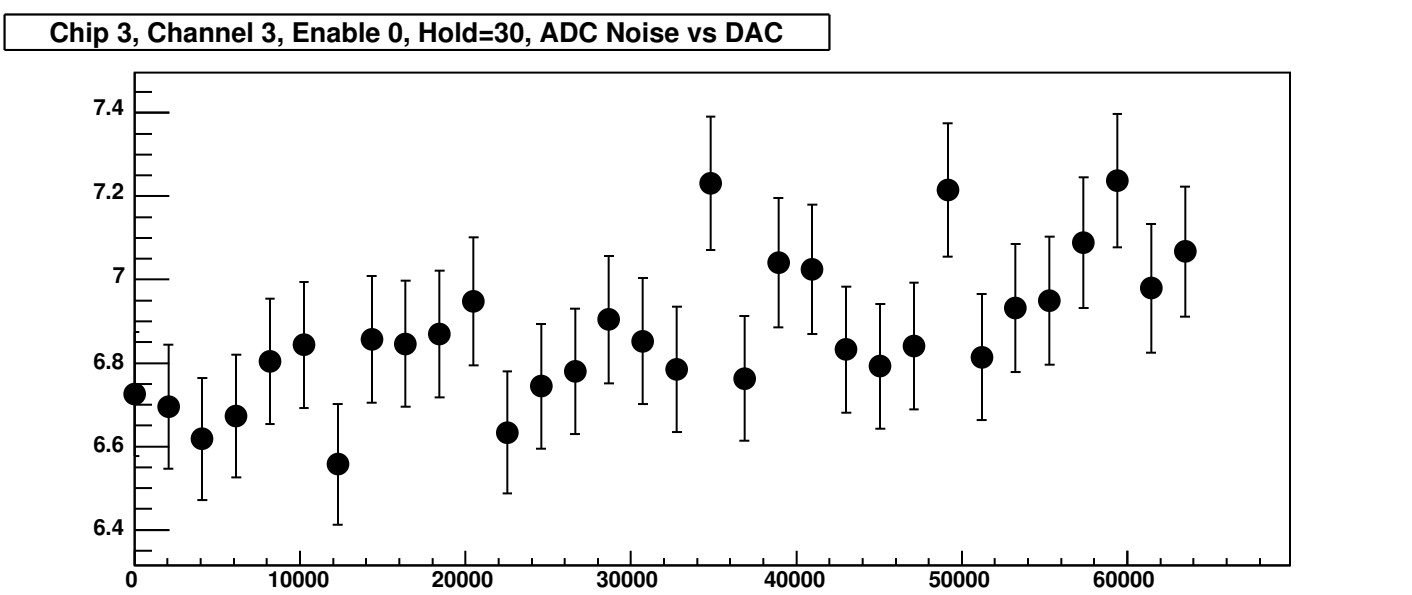

Chip 3, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

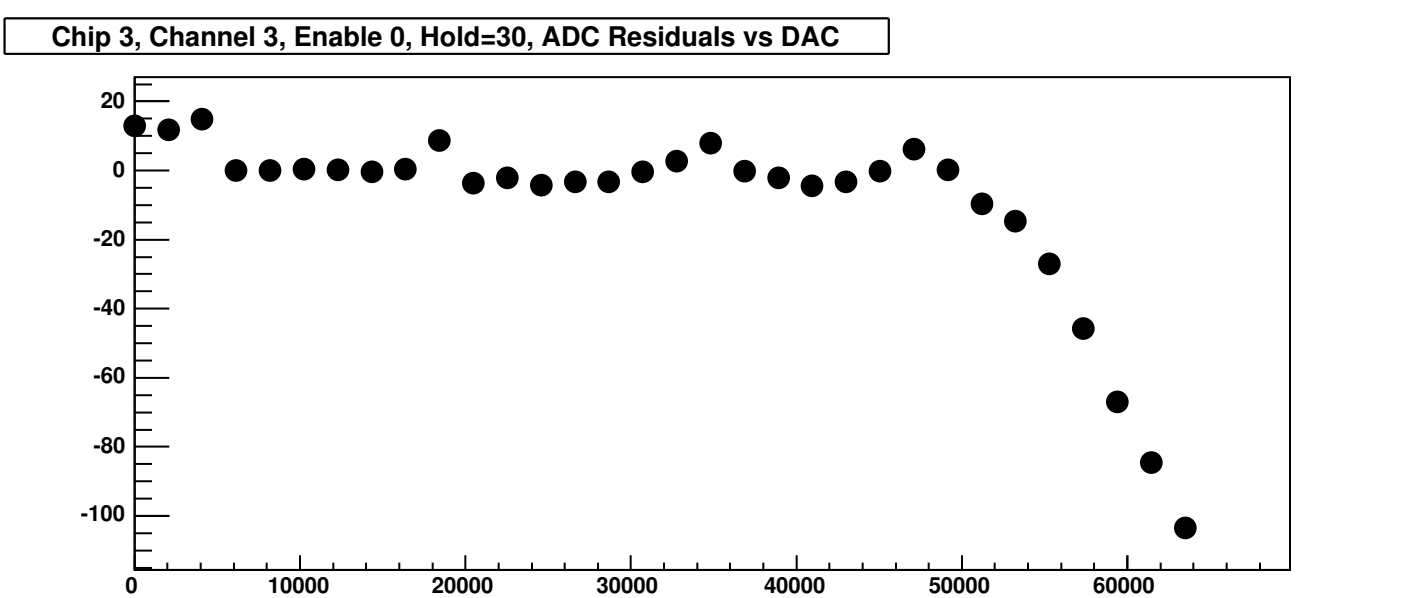

Chip 3, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7832 / 15
p0 1247 ± 0.125
p1 -0.0004387 ± 5.037e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

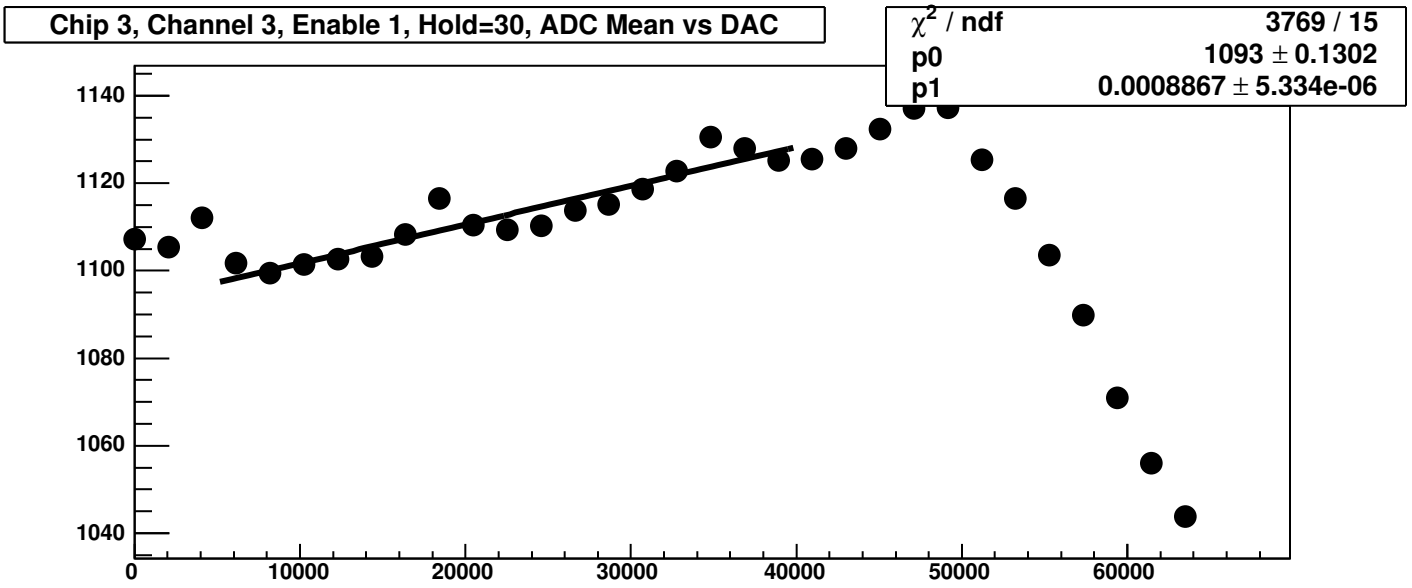

Chip 3, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

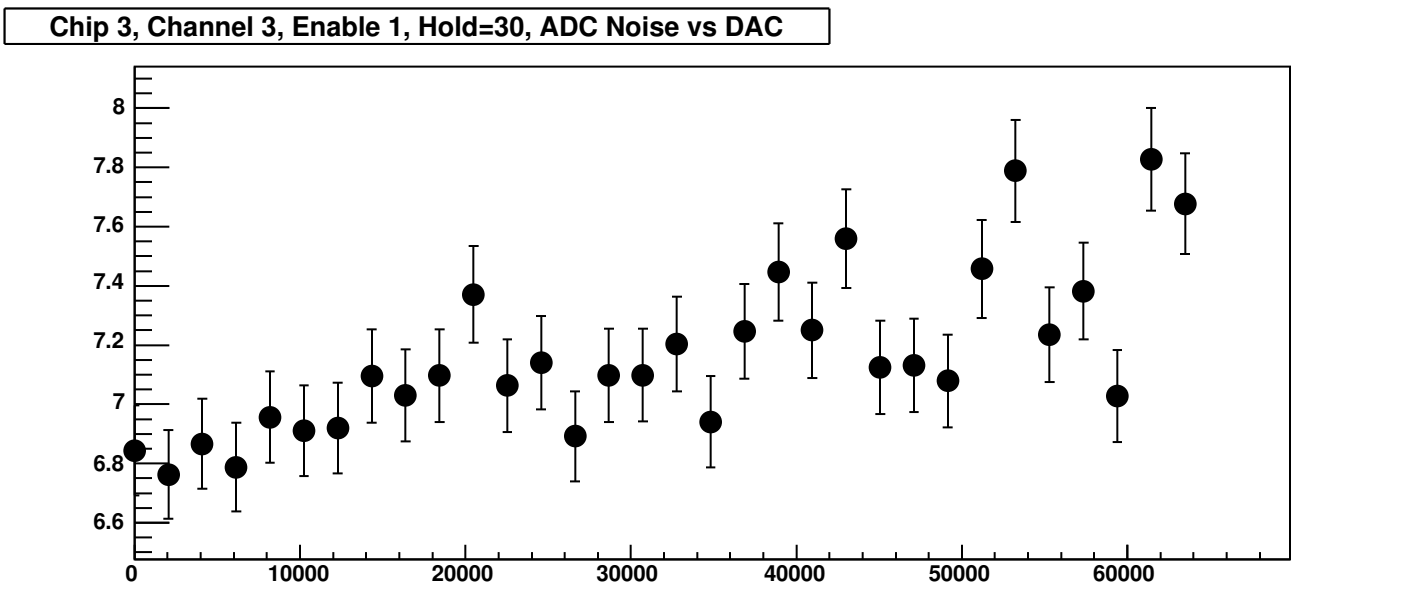

Chip 3, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

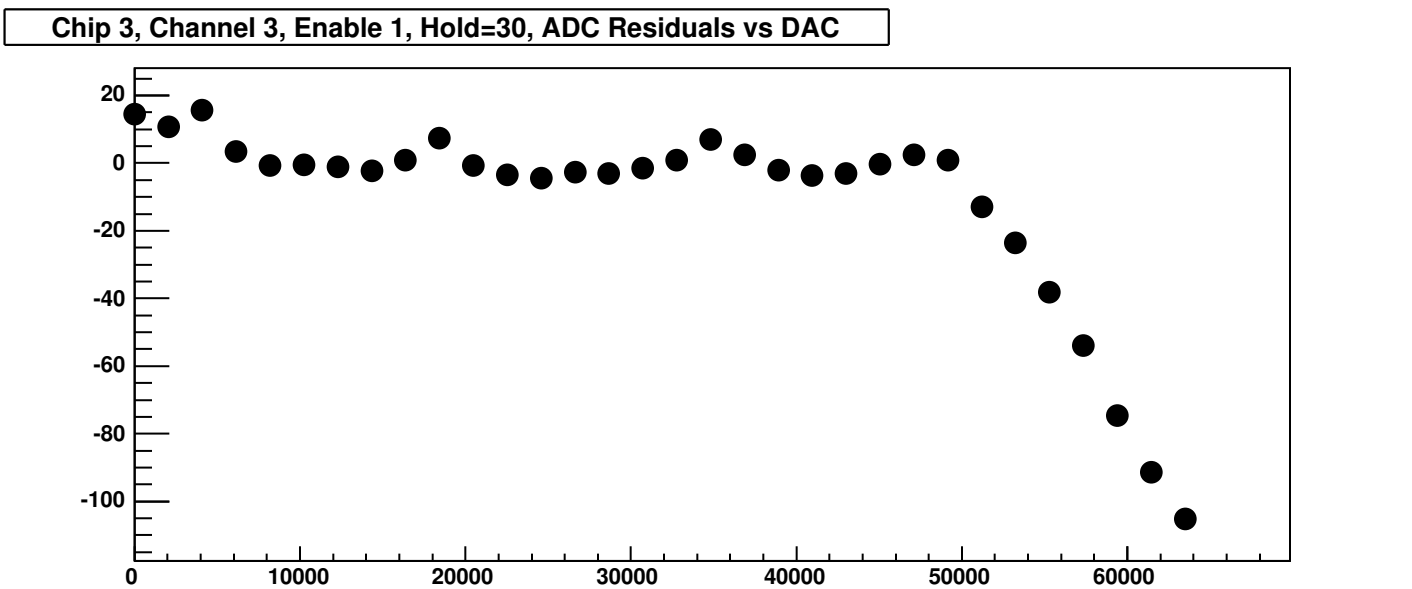

Chip 3, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4422 / 15
p0 1094 ± 0.1265
p1 -0.0001153 ± 5.17e-06
1120
1100
1080
1060
1040
1020
1000
980
0
10000
20000
30000
40000
50000
60000

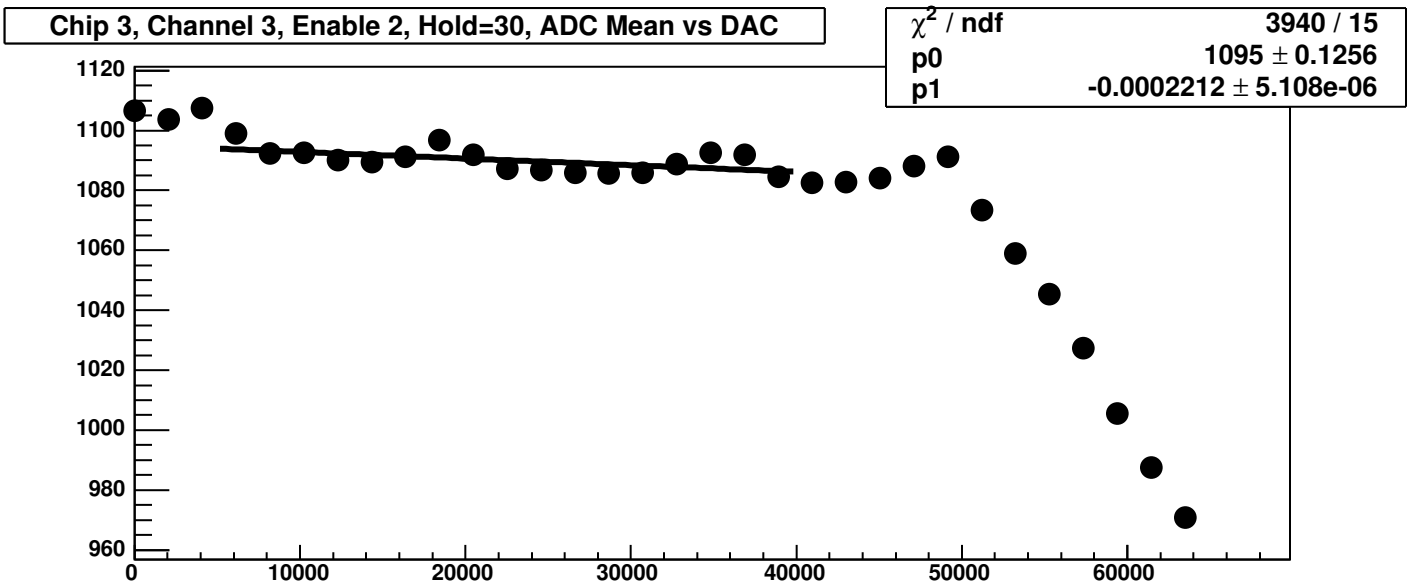

Chip 3, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

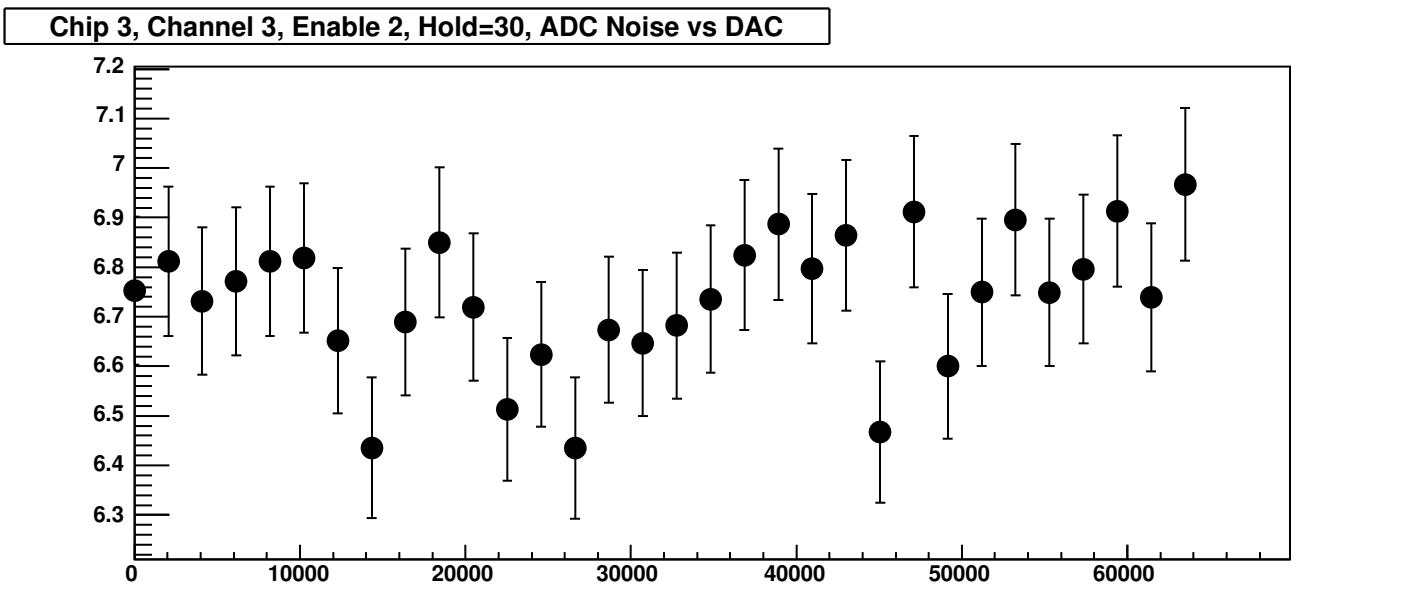

Chip 3, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

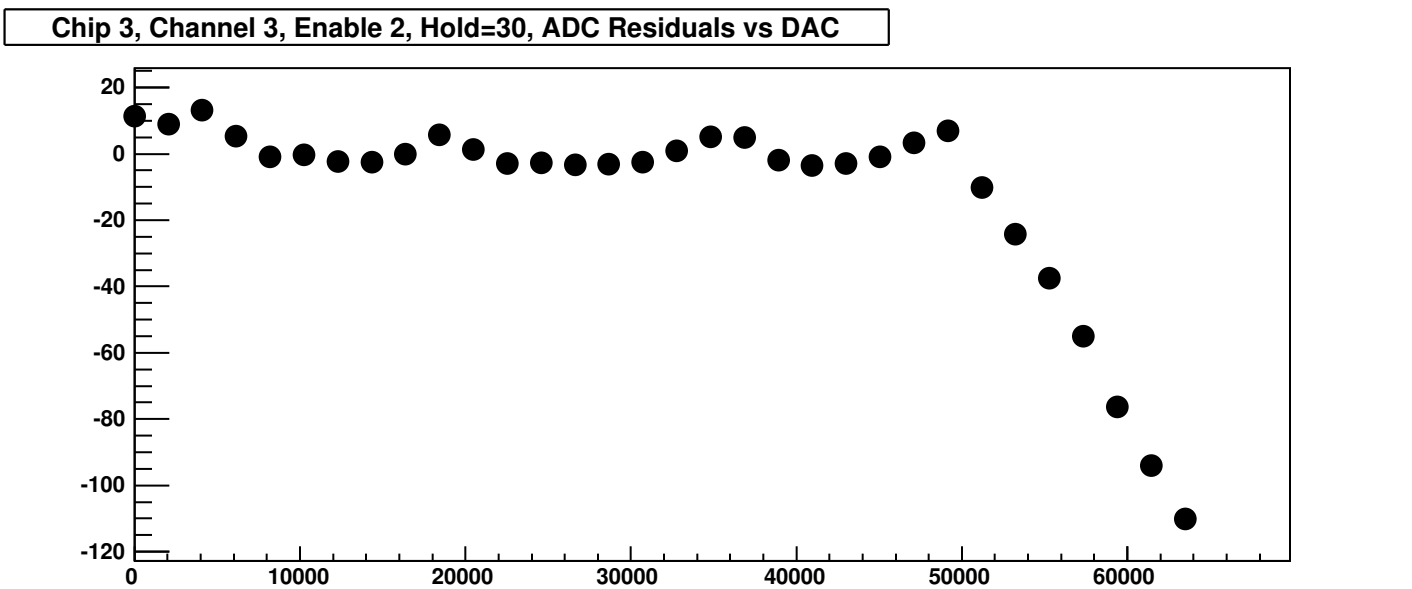

Chip 3, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3769 / 15
p0 1093 ± 0.1302
p1 0.0008867 ± 5.334e-06
1140
1120
1100
1080
1060
1040
0
10000
20000
30000
40000
50000
60000

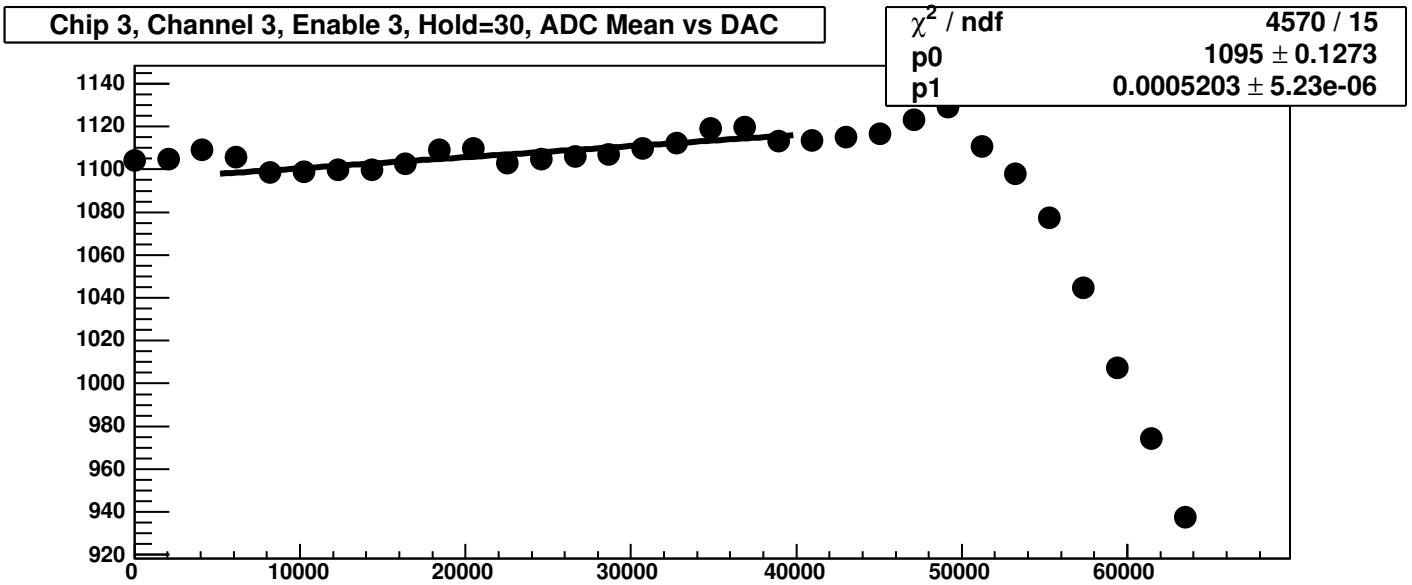

Chip 3, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
8
7.8
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

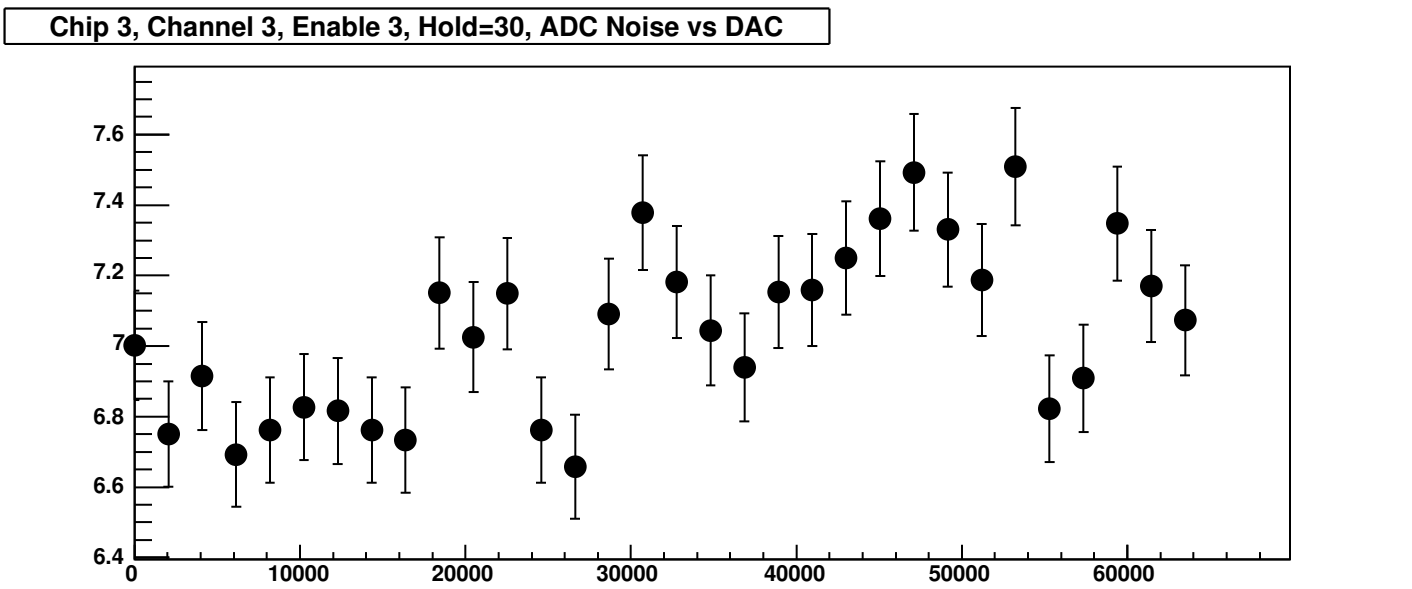

Chip 3, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

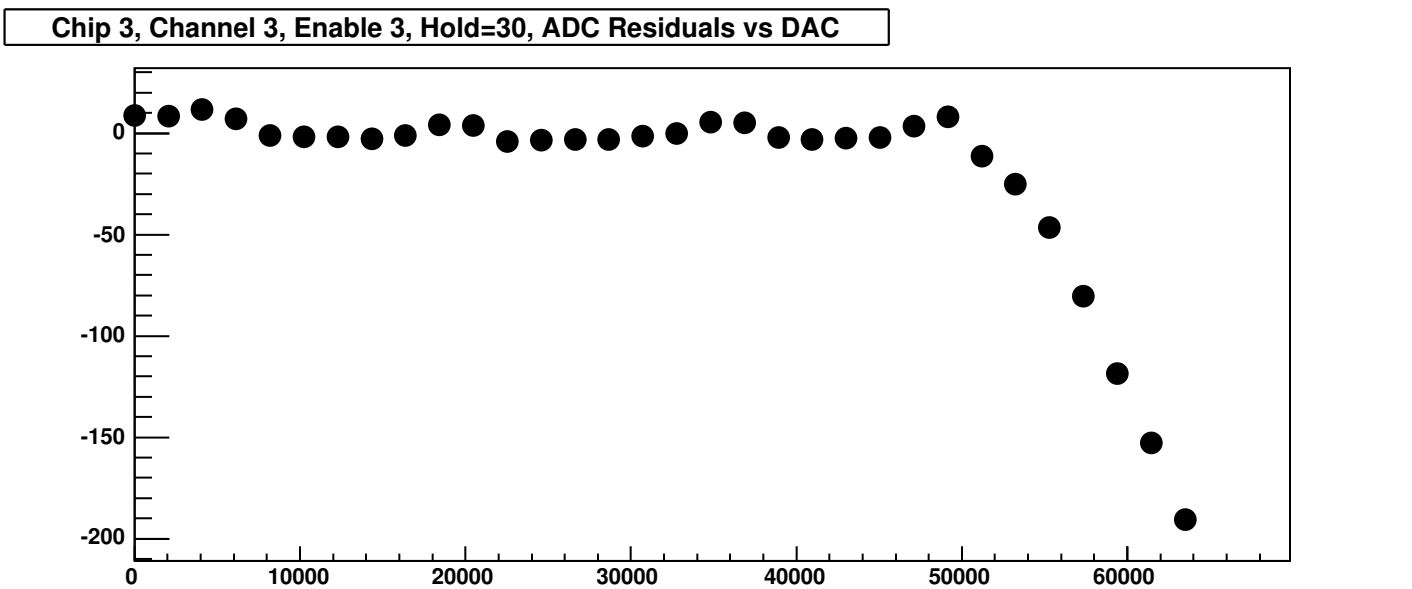

Chip 3, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3940 / 15
p0 1095 ± 0.1256
p1 -0.0002212 ± 5.108e-06
1120
1100
1080
1060
1040
1020
1000
980
960
0
10000
20000
30000
40000
50000
60000

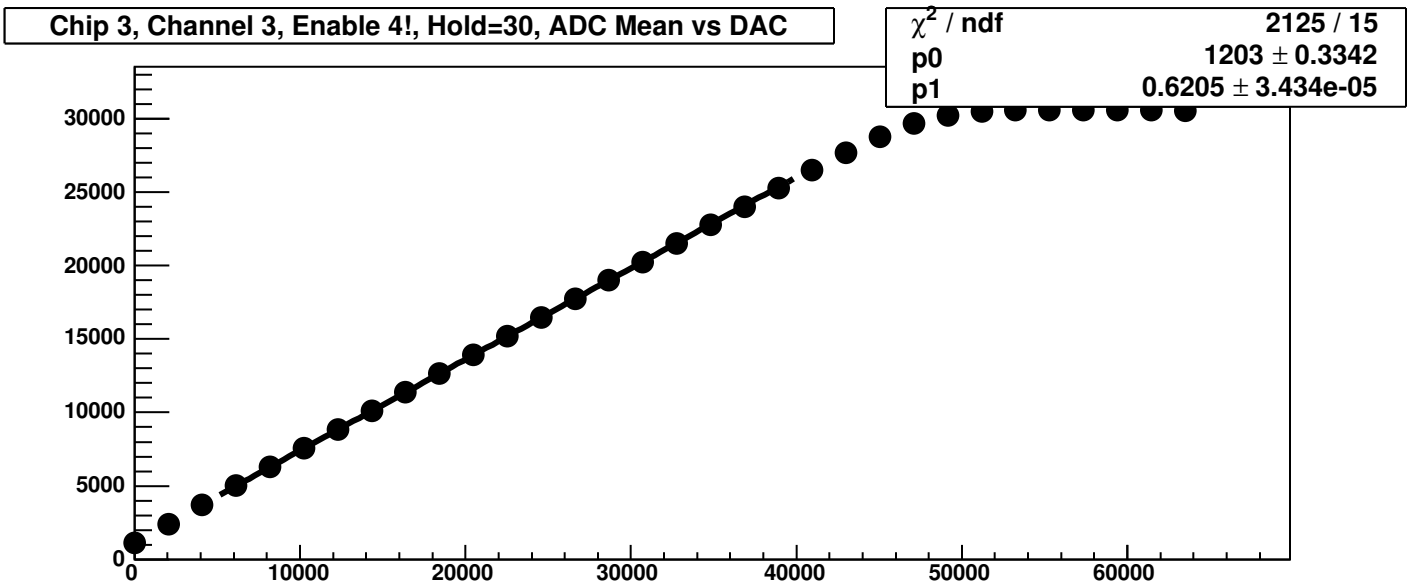

Chip 3, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7.1
7
6.9
6.8
6.7
6.6
6.5
6.4
6.3
0
10000
20000
30000
40000
50000
60000

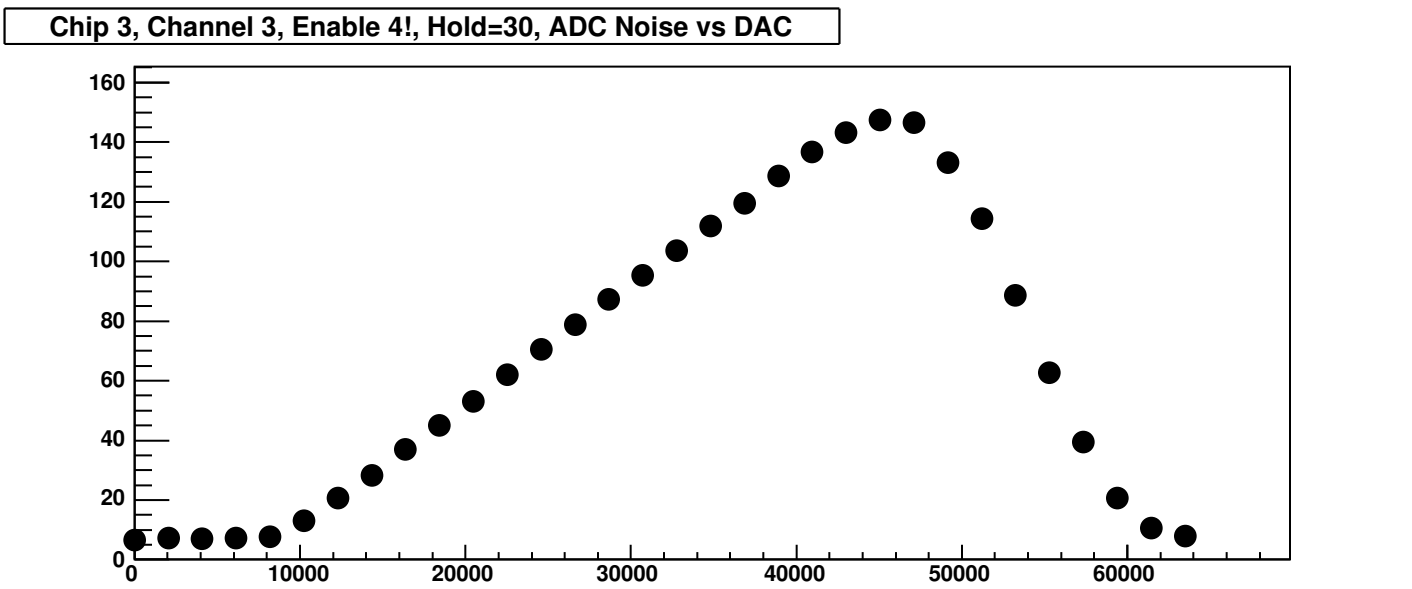

Chip 3, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

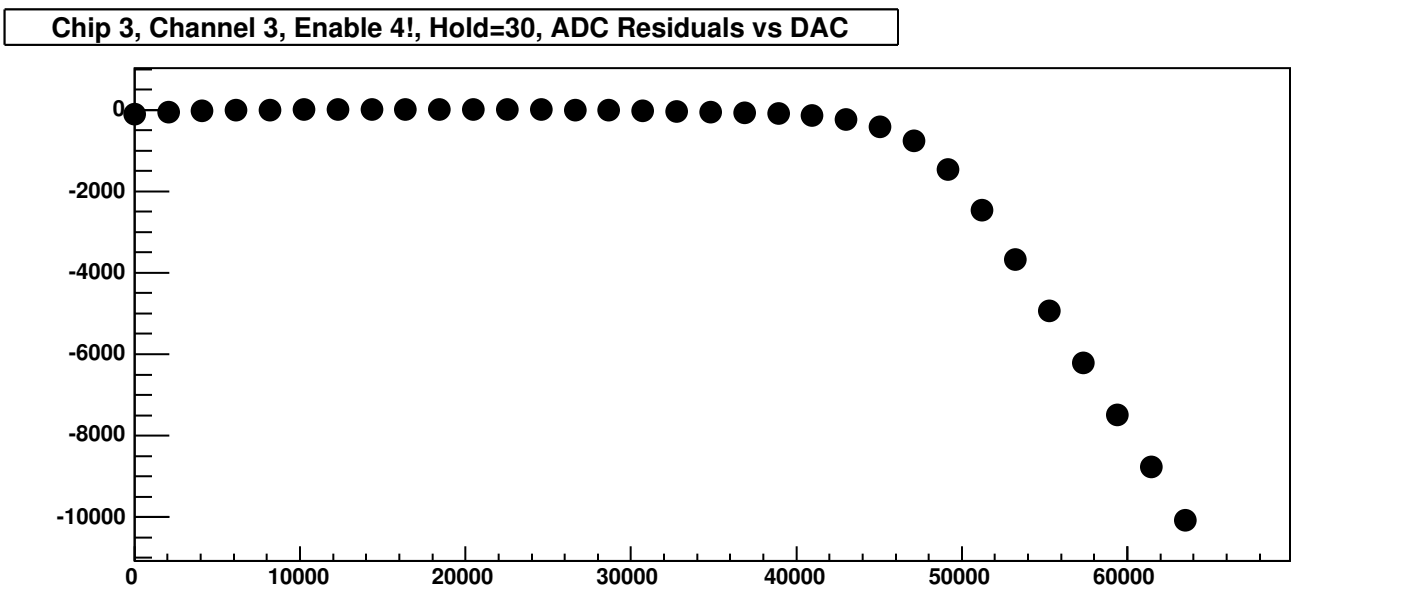

Chip 3, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4570 / 15
p0 1095 ± 0.1273
p1 0.0005203 ± 5.23e-06
1140
1120
1100
1080
1060
1040
1020
1000
980
960
940
920
0
10000
20000
30000
40000
50000
60000

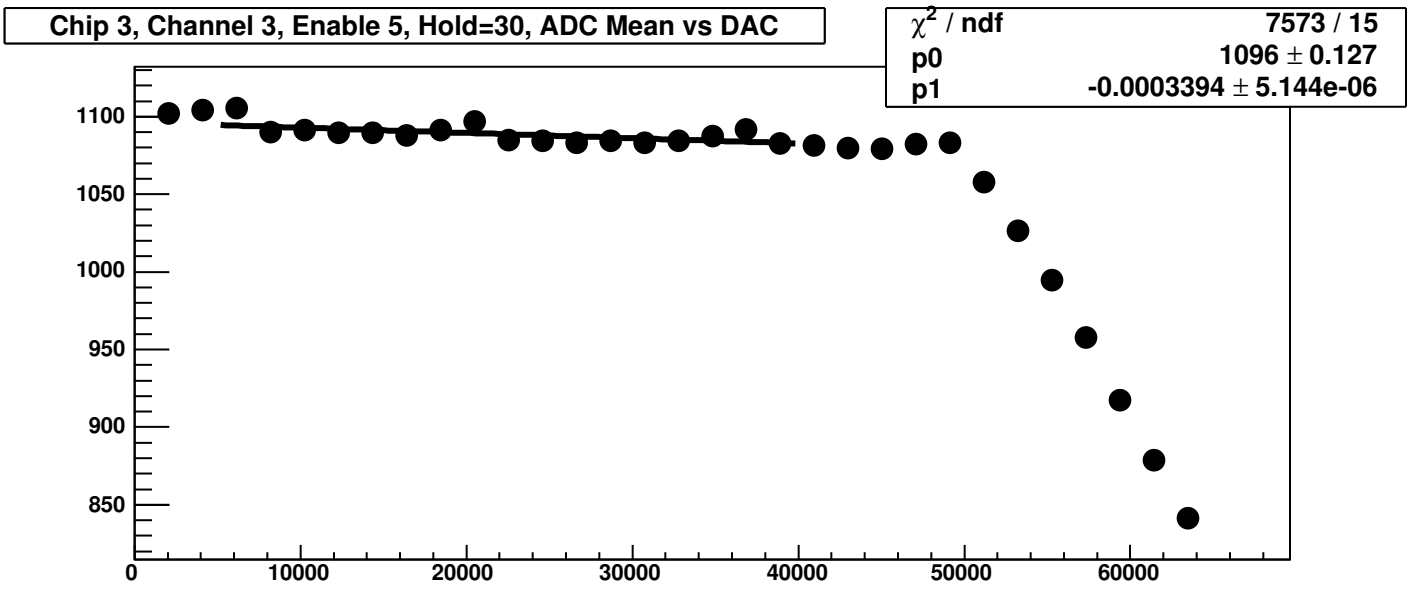

Chip 3, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

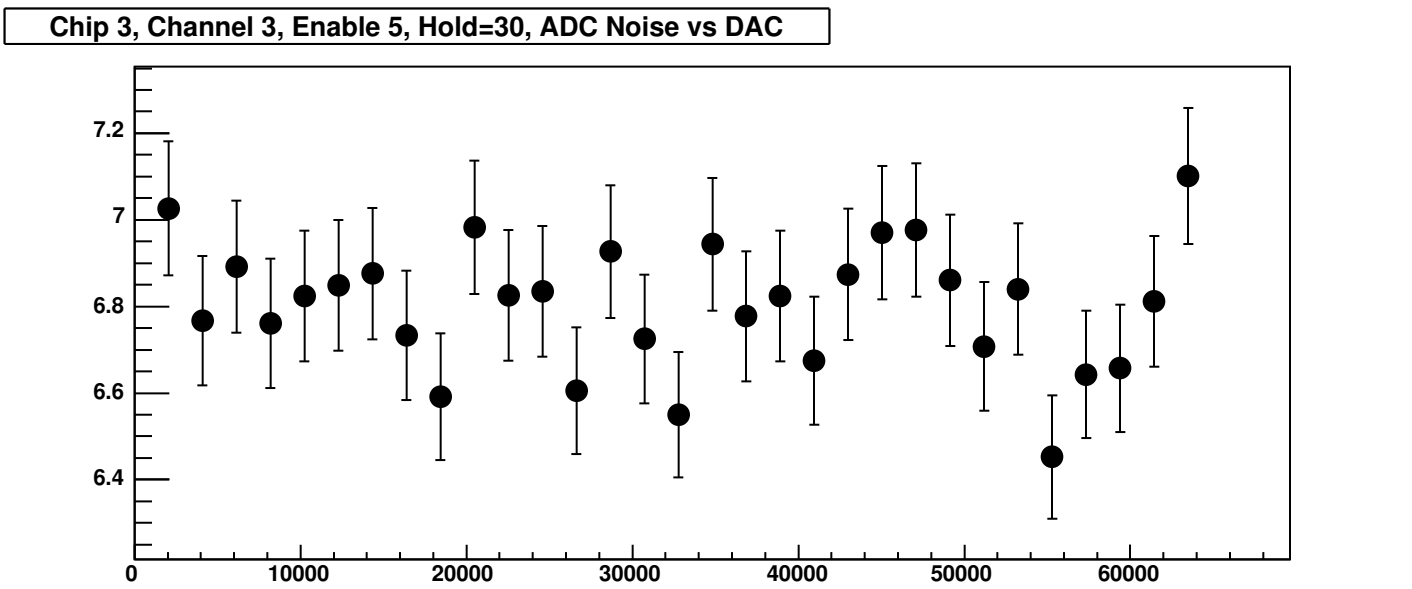

Chip 3, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

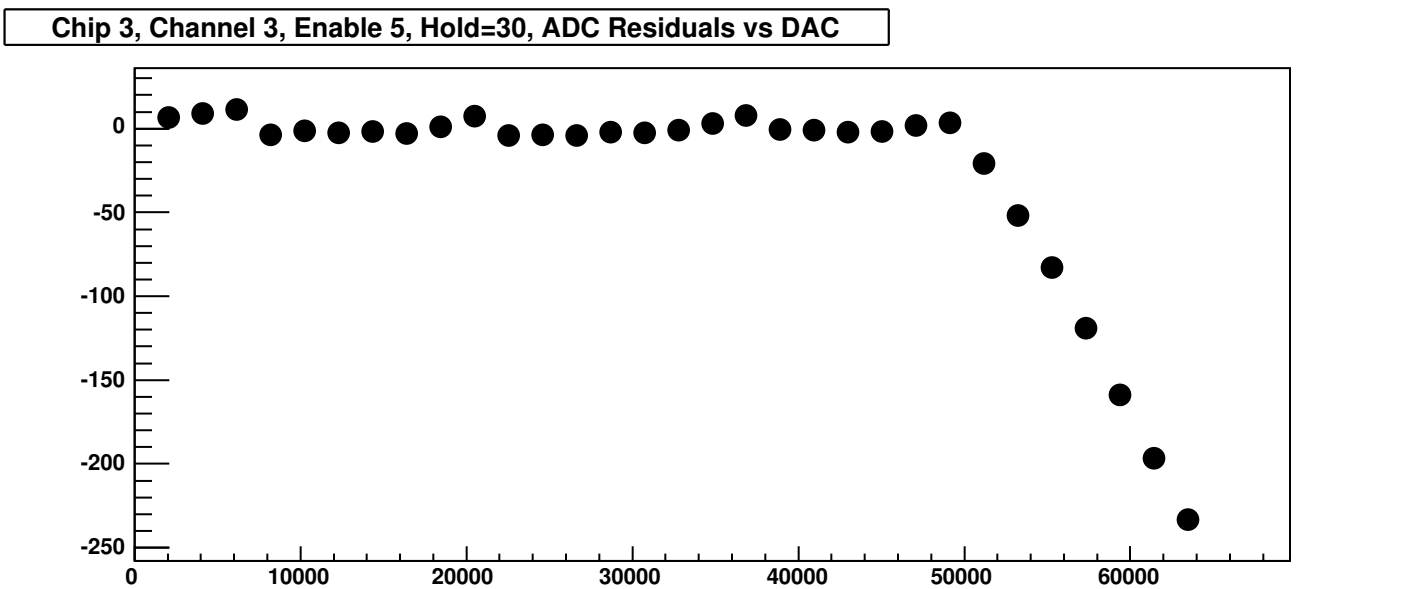

Chip 3, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 2125 / 15
p0 1203 ± 0.3342
p1 0.6205 ± 3.434e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

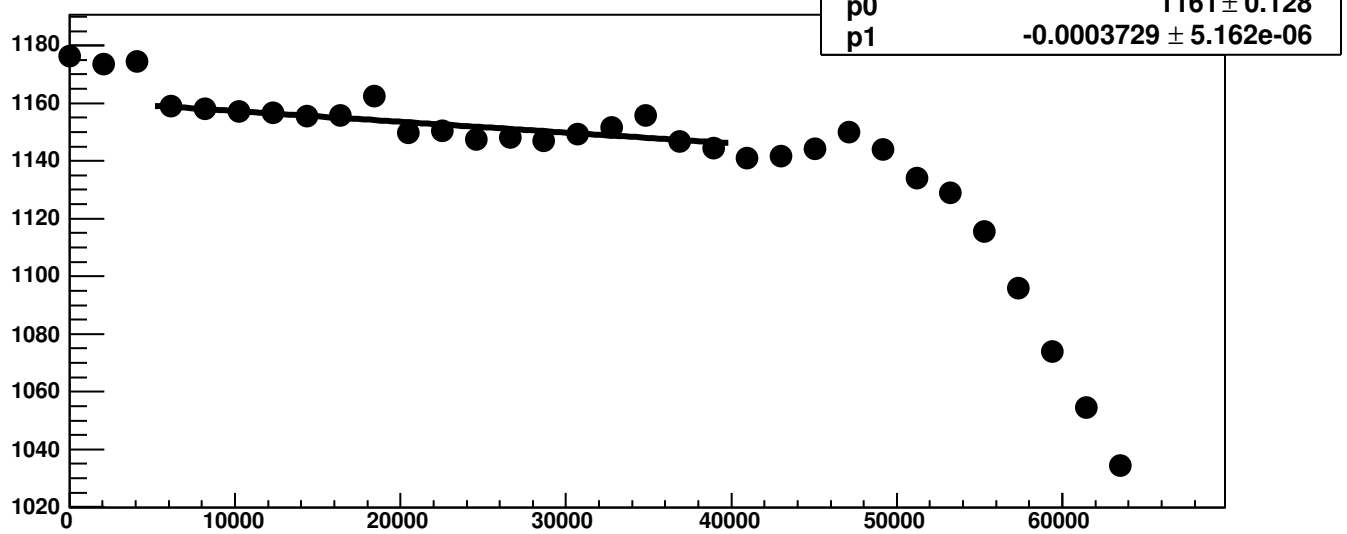

Chip 3, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7573 / 15
p0 1096 ± 0.127
p1 -0.0003394 ± 5.144e-06
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

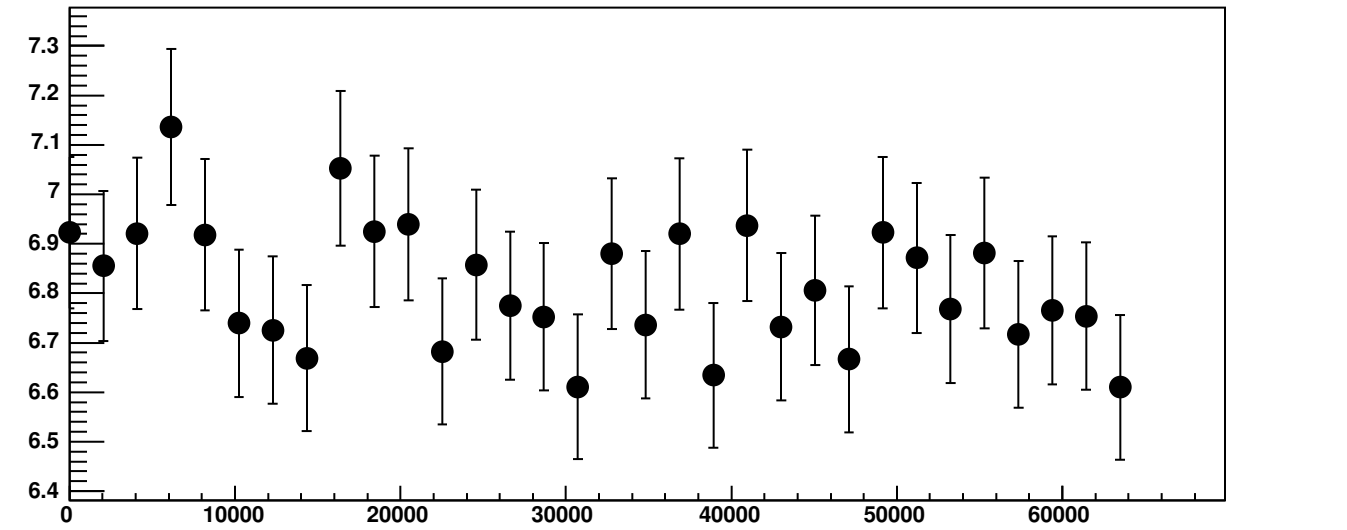

Chip 3, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

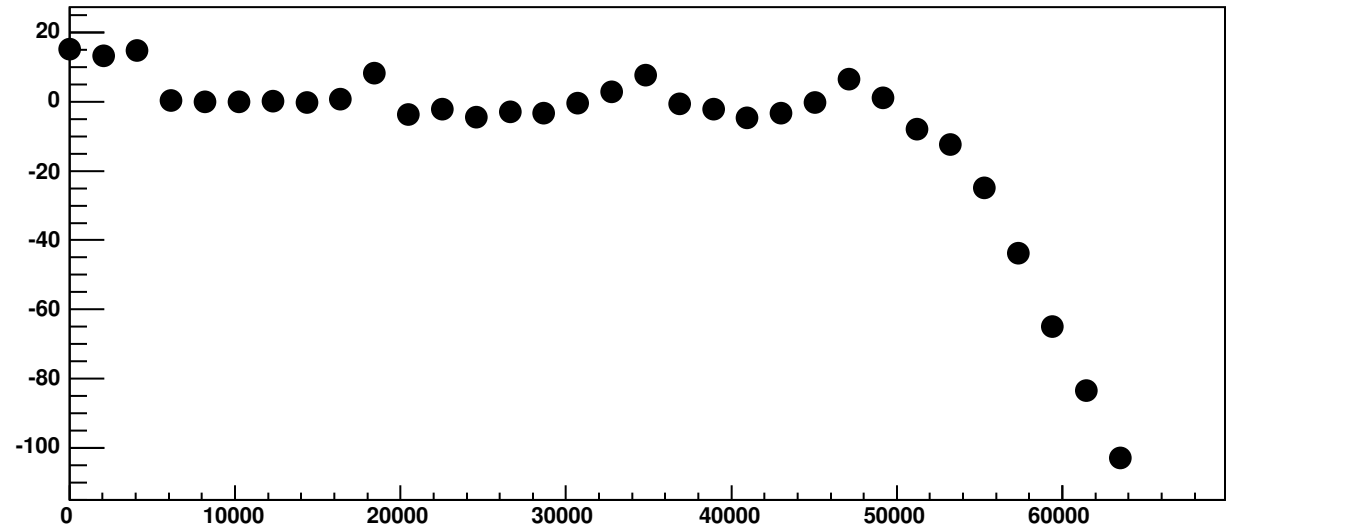
Chip 3, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

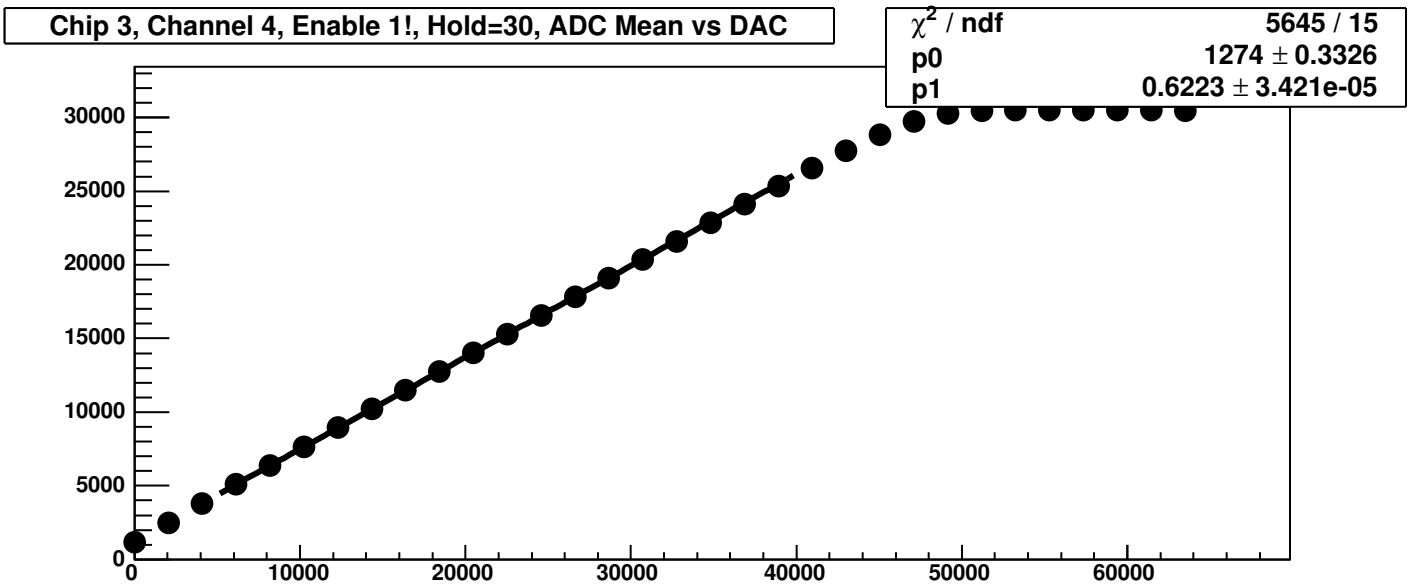


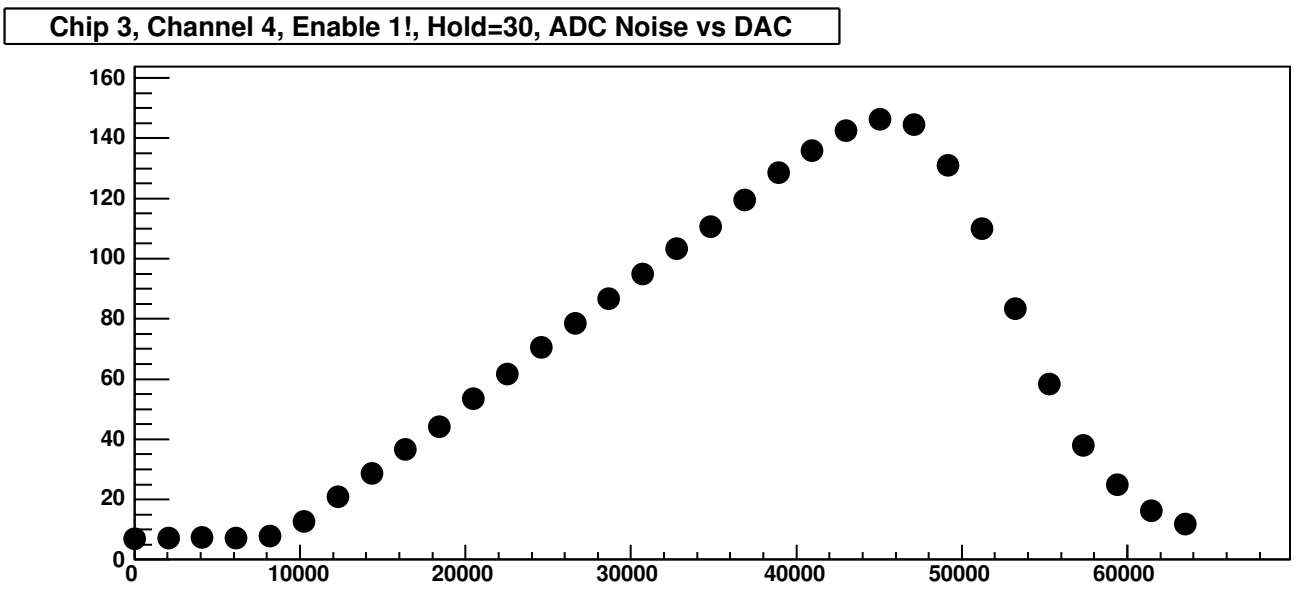
Chip 3, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC

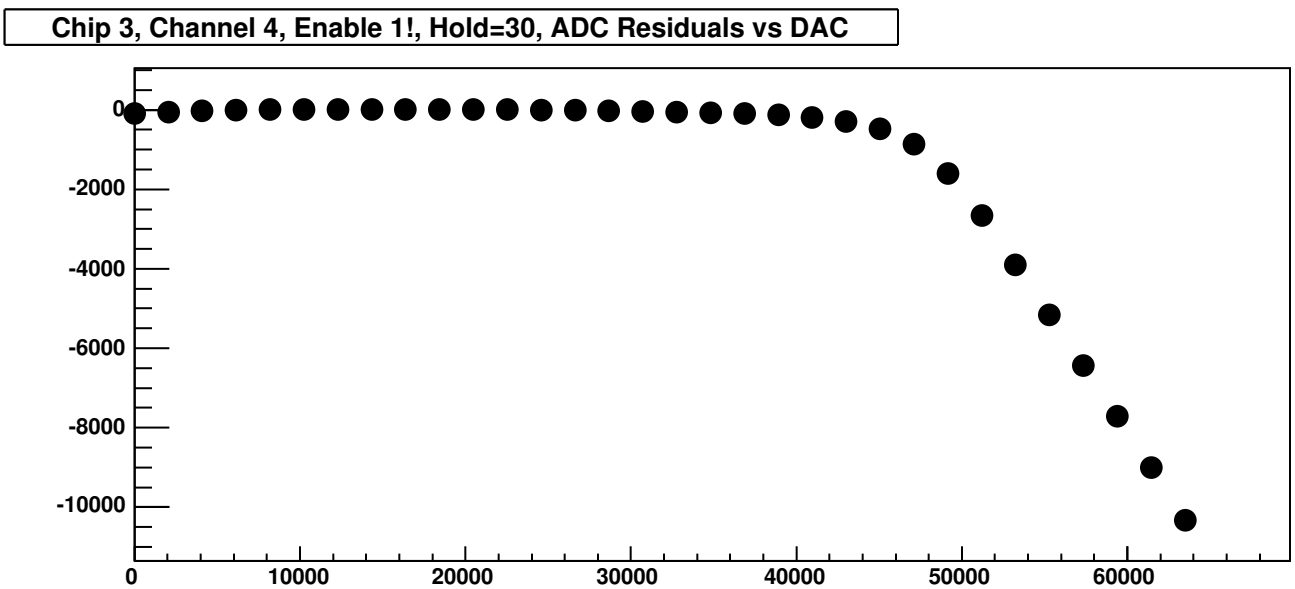


Chip 3, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

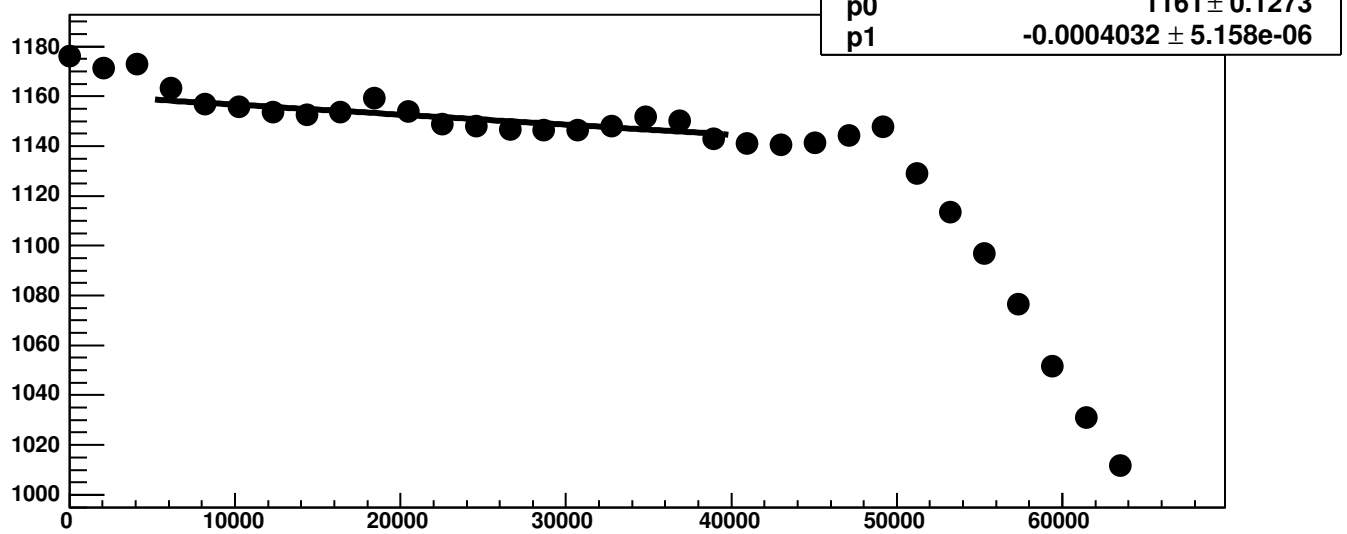



Chip 3, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 5645 / 15
p0 1274 ± 0.3326
p1 0.6223 ± 3.421e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

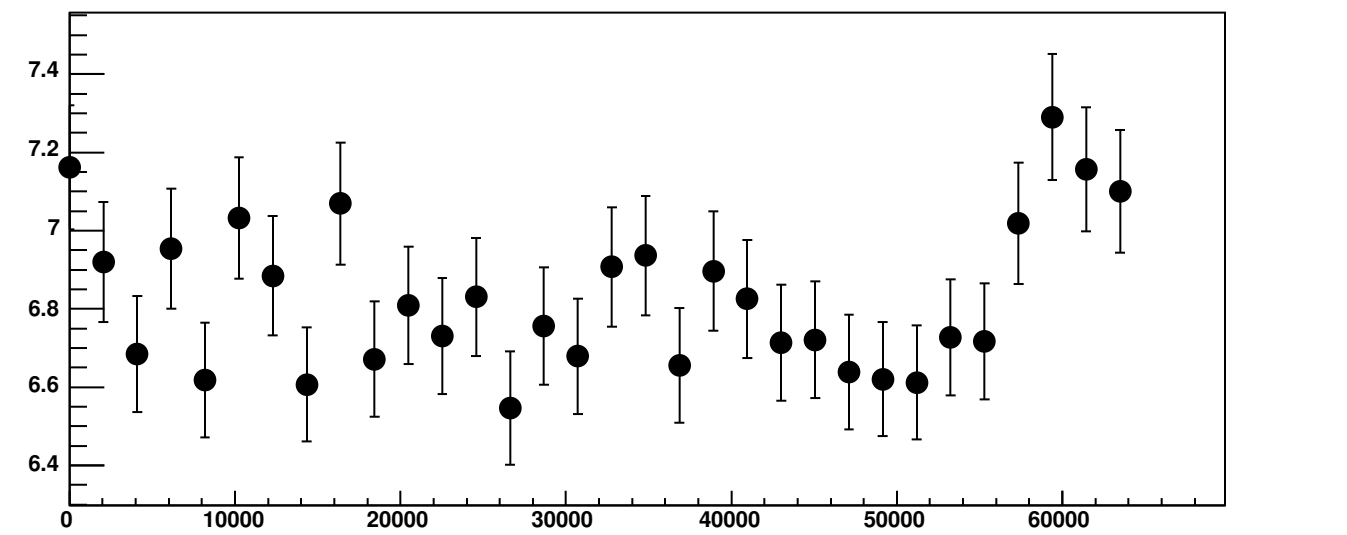

Chip 3, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

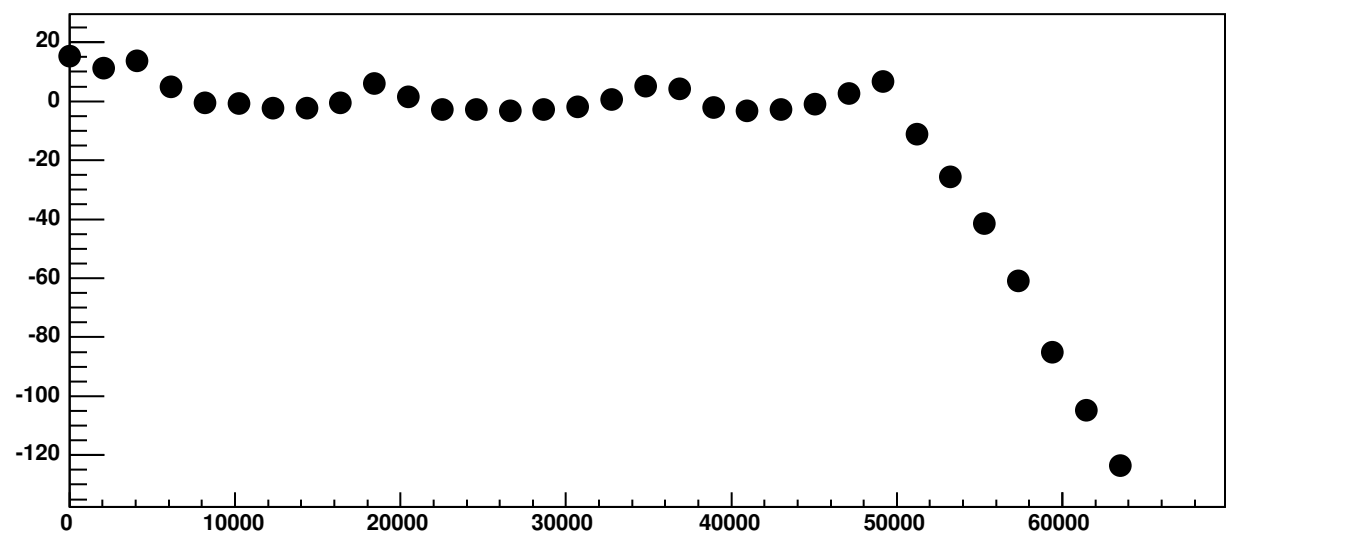
Chip 3, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

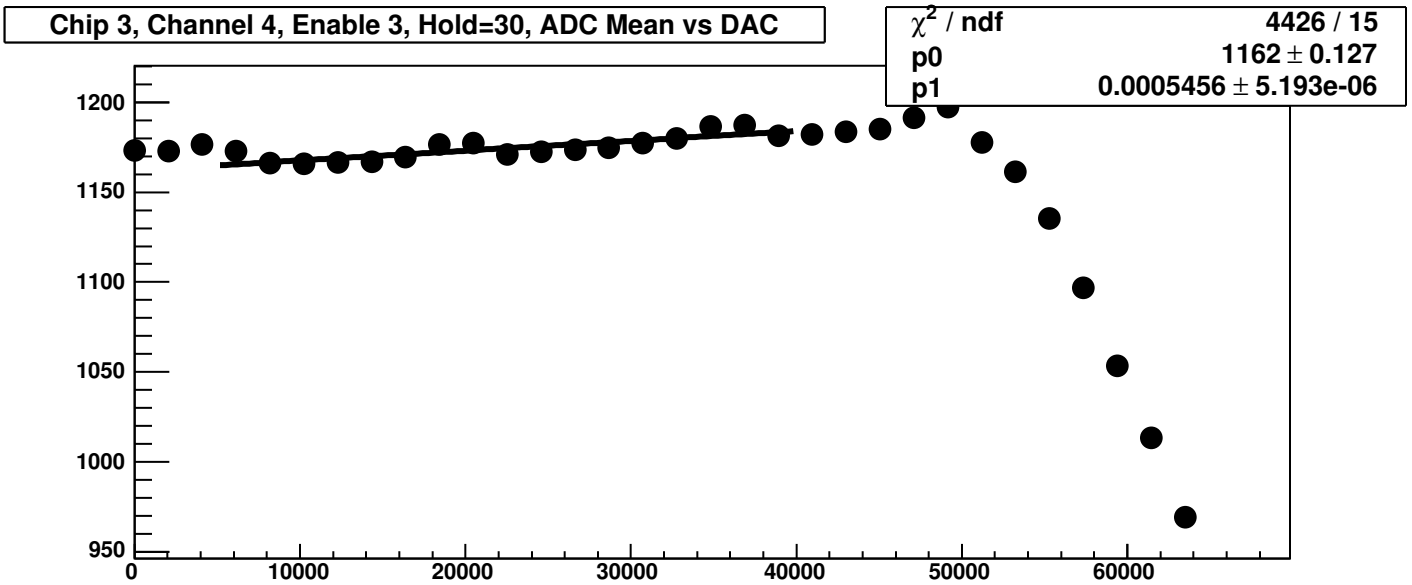


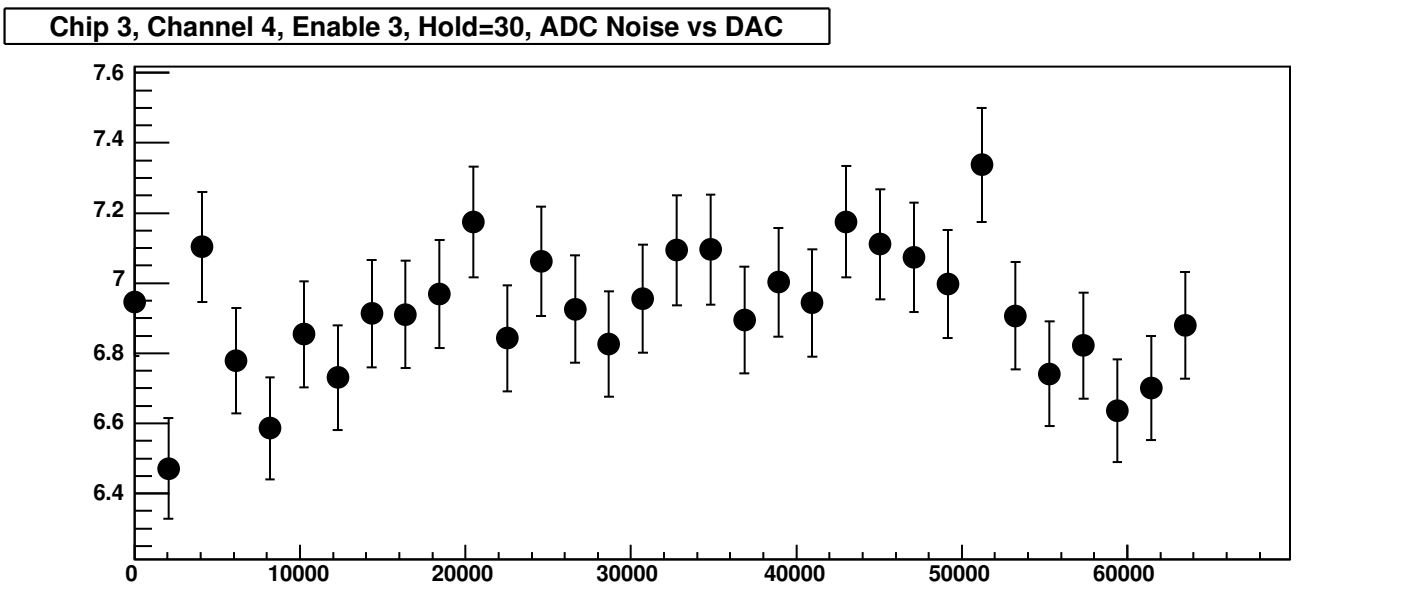
Chip 3, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC

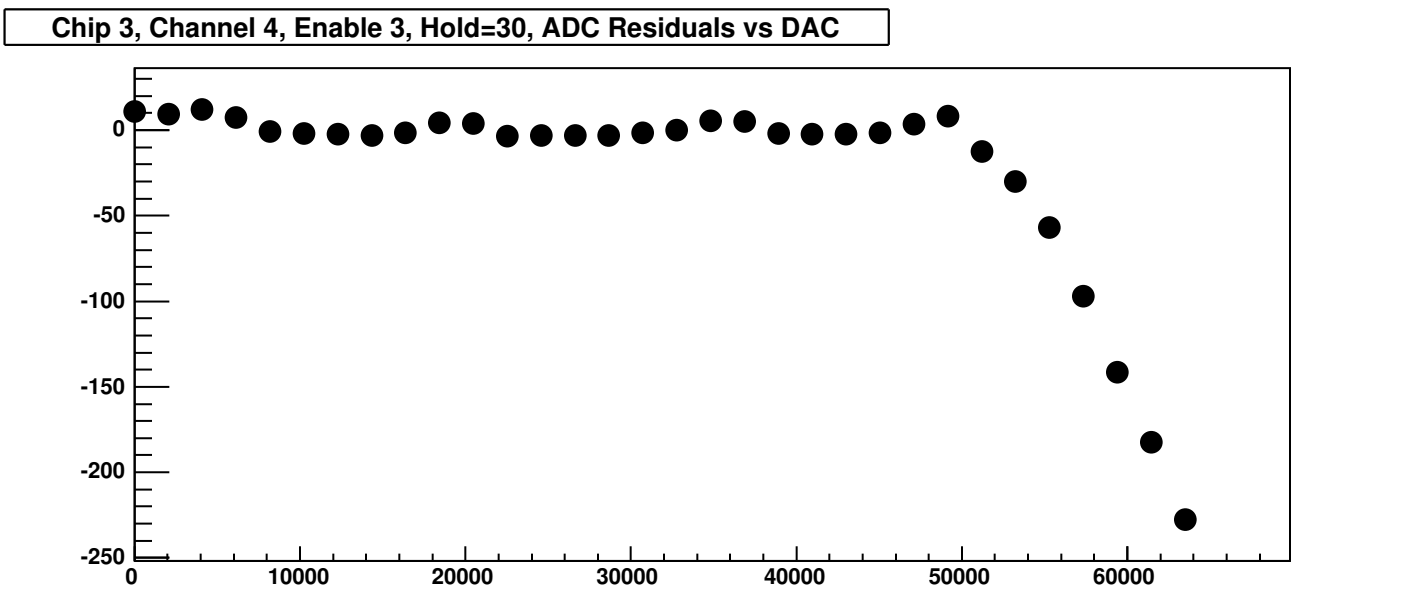


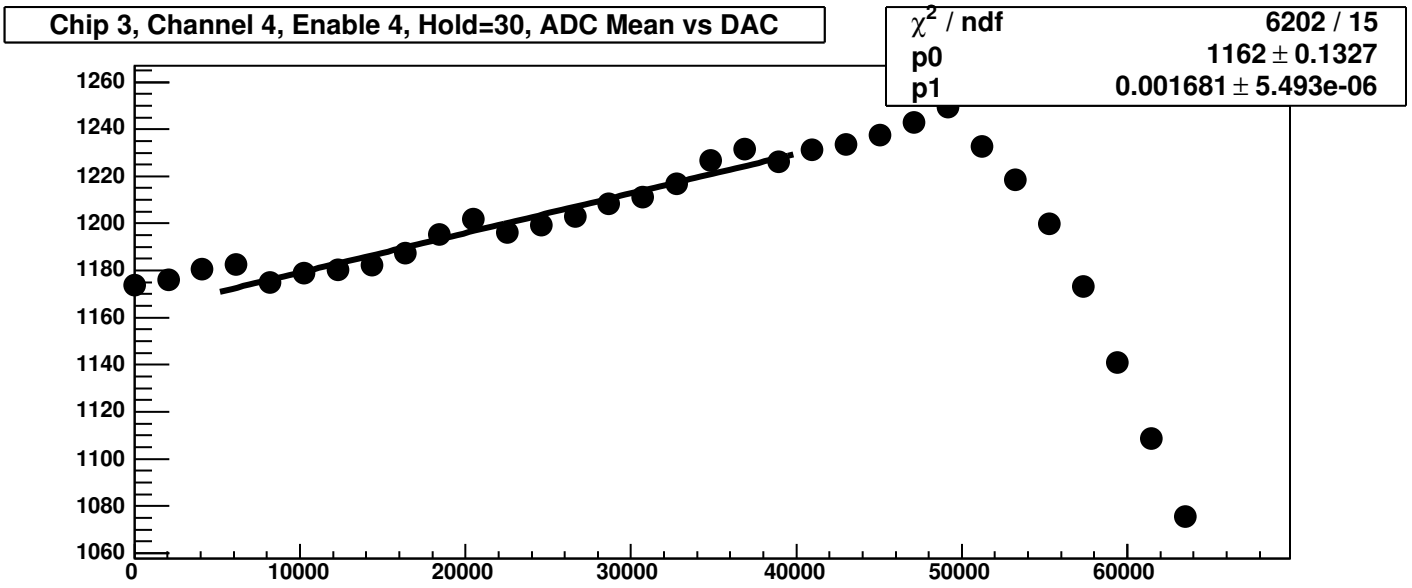
Chip 3, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

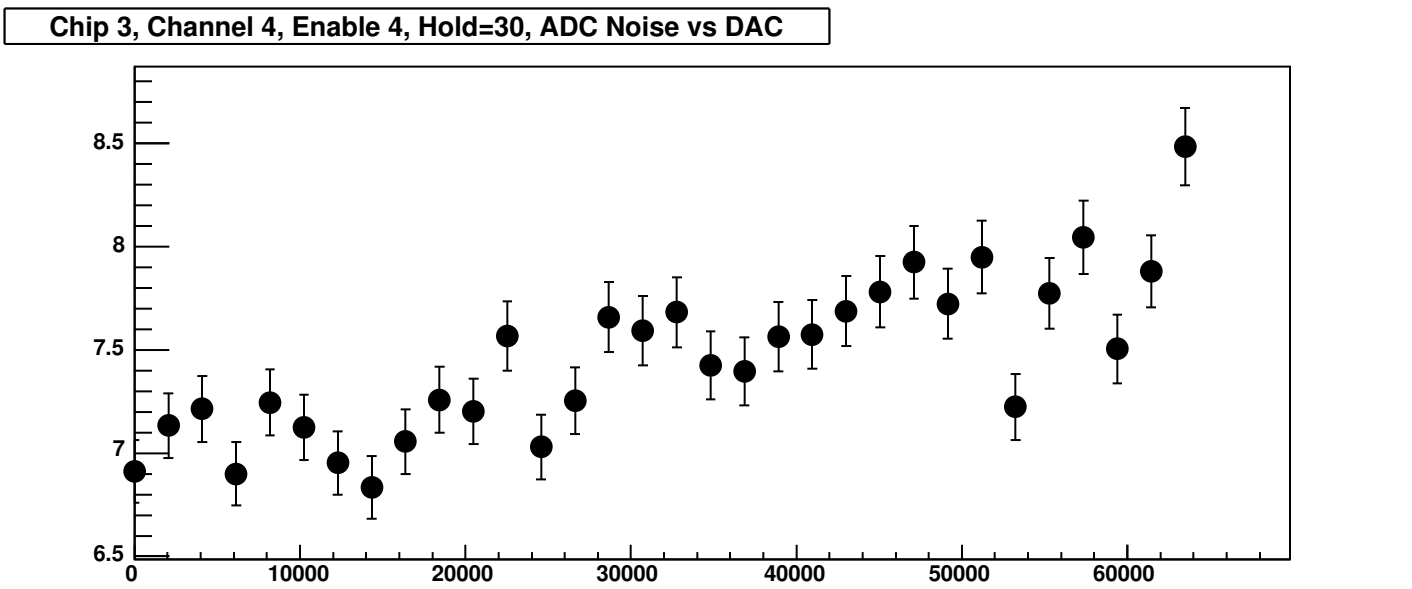


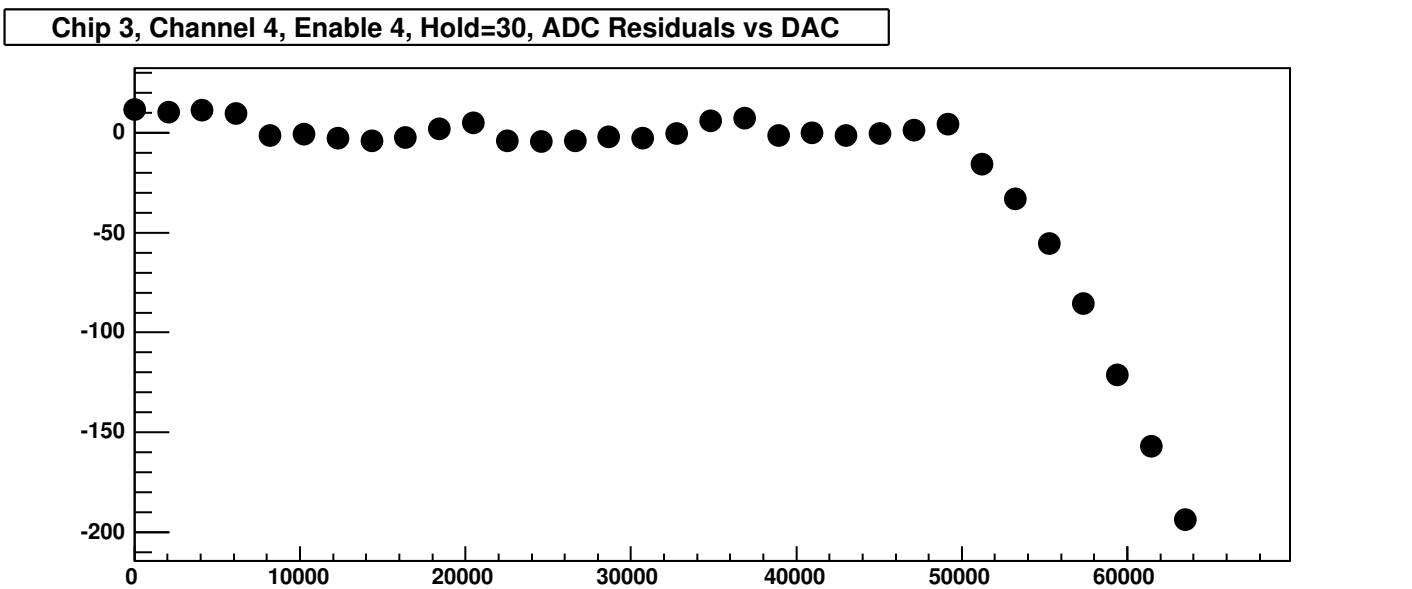

Chip 3, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4426 / 15
p0 1162 ± 0.127
p1 0.0005456 ± 5.193e-06
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

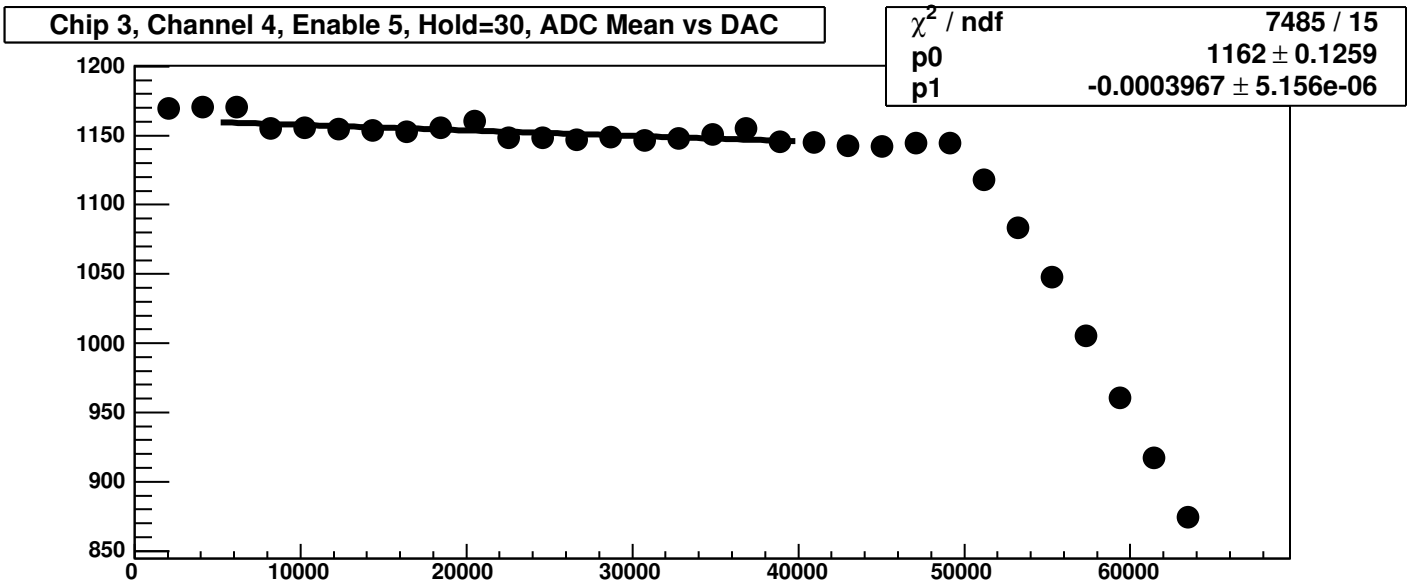

Chip 3, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

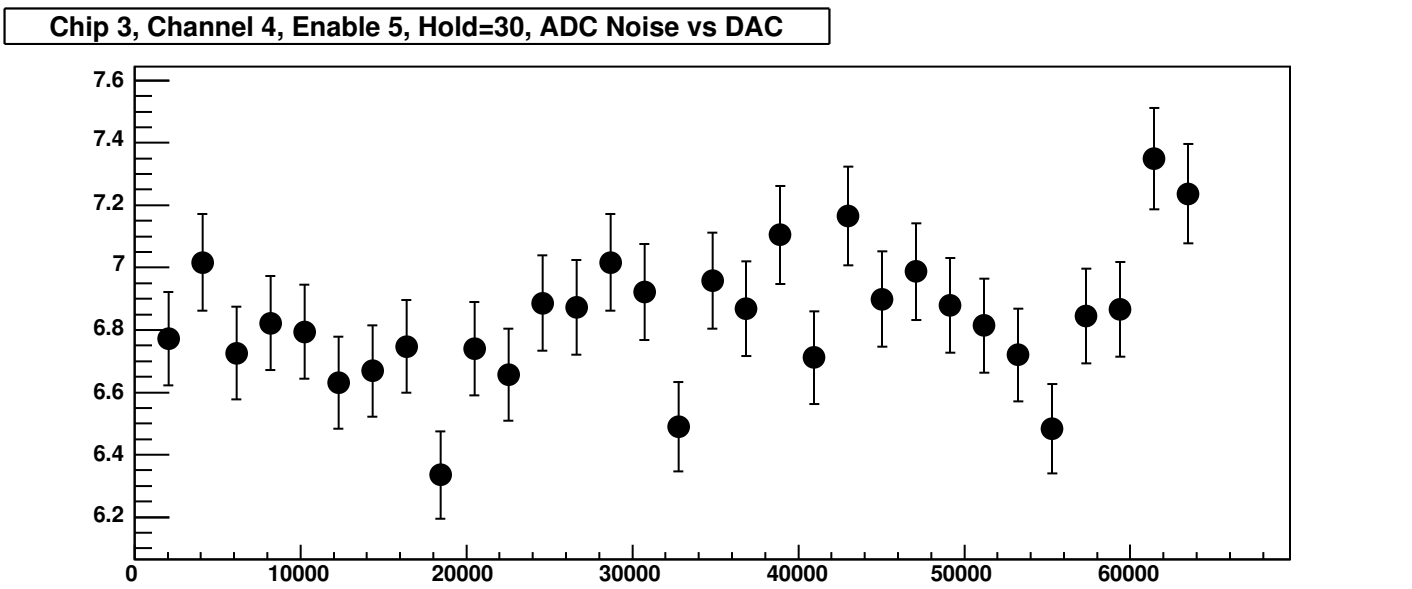

Chip 3, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

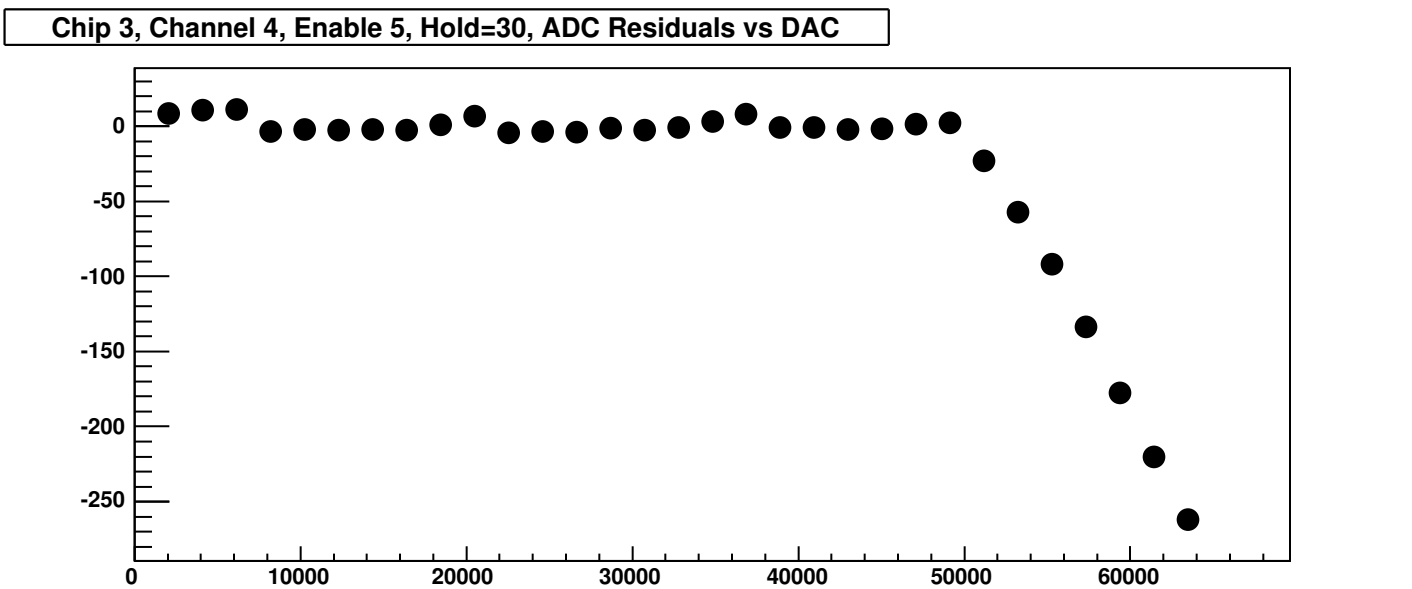

Chip 3, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6202 / 15
p0 1162 ± 0.1327
p1 0.001681 ± 5.493e-06
1260
1240
1220
1200
1180
1160
1140
1120
1100
1080
1060
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000

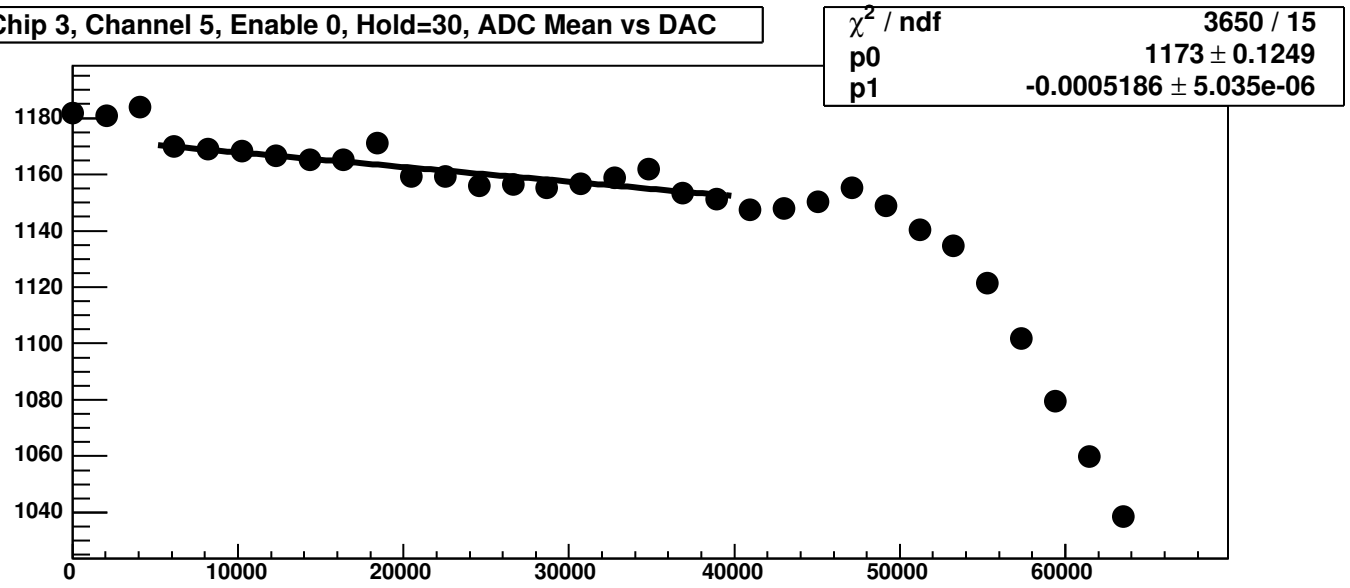

Chip 3, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7485 / 15
p0 1162 ± 0.1259
p1 -0.0003967 ± 5.156e-06
1200
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

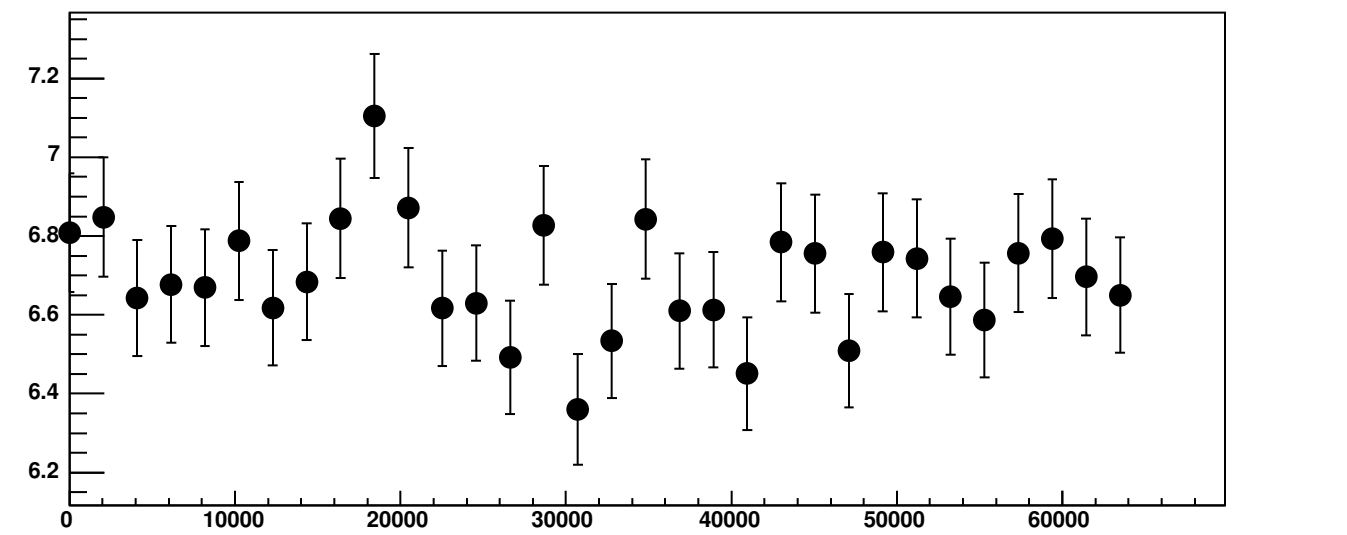

Chip 3, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

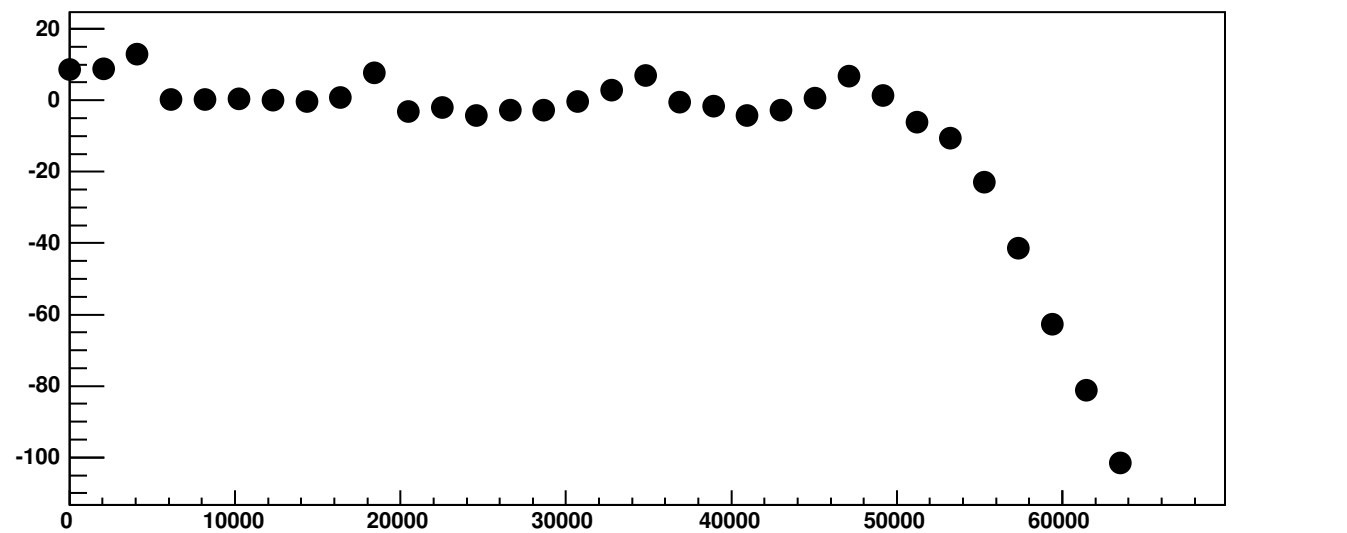
Chip 3, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC

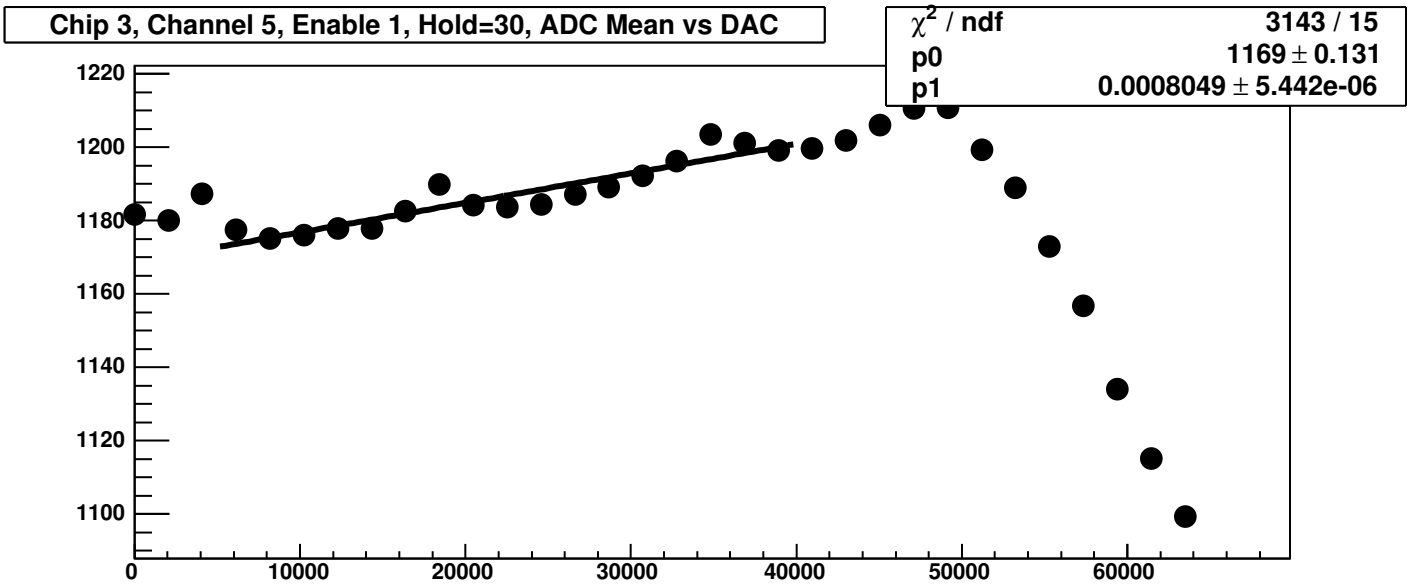


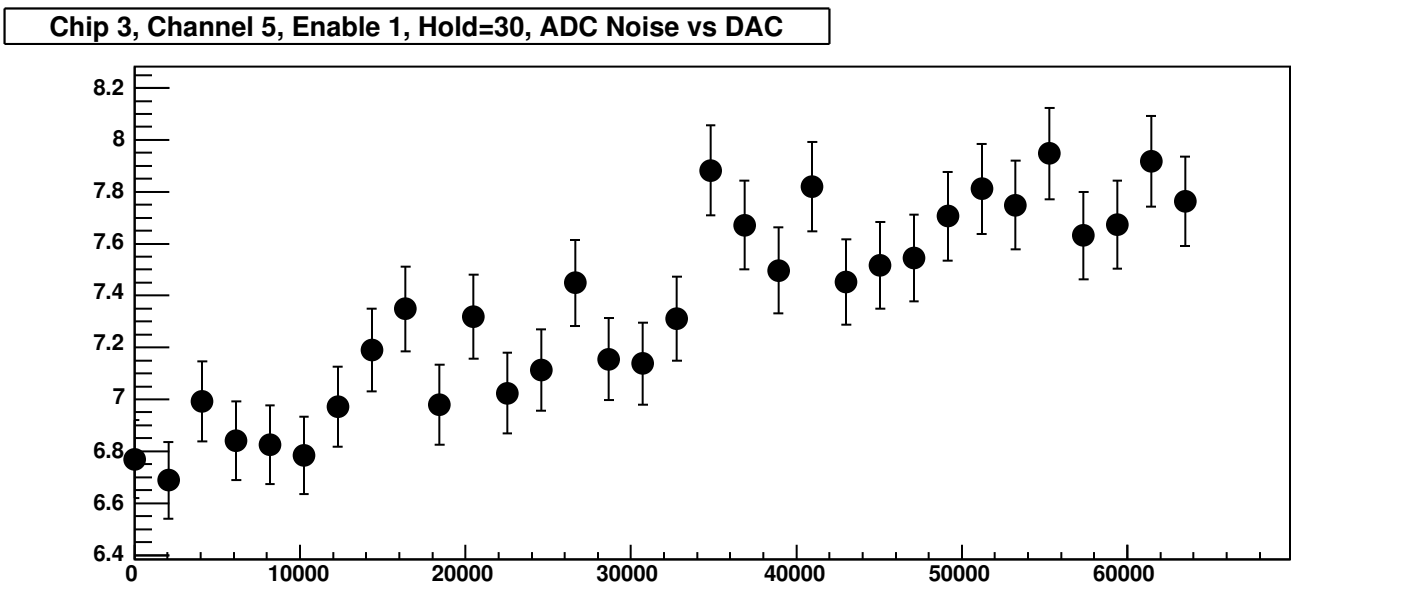
Chip 3, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC

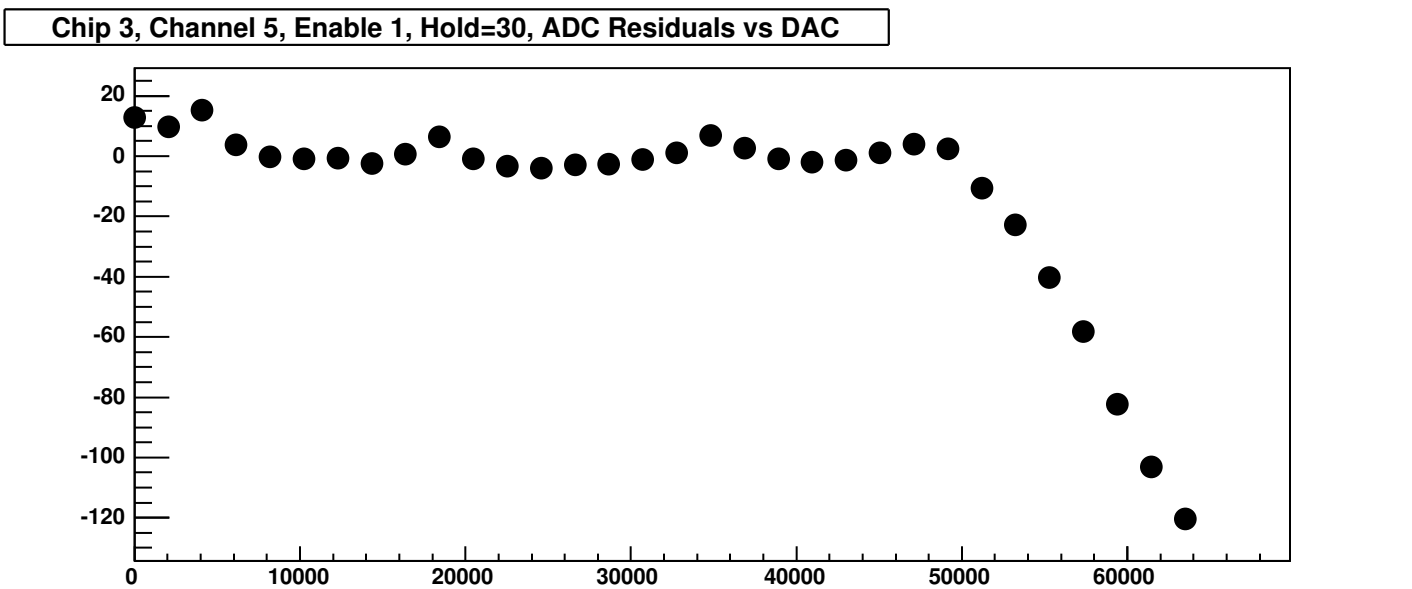


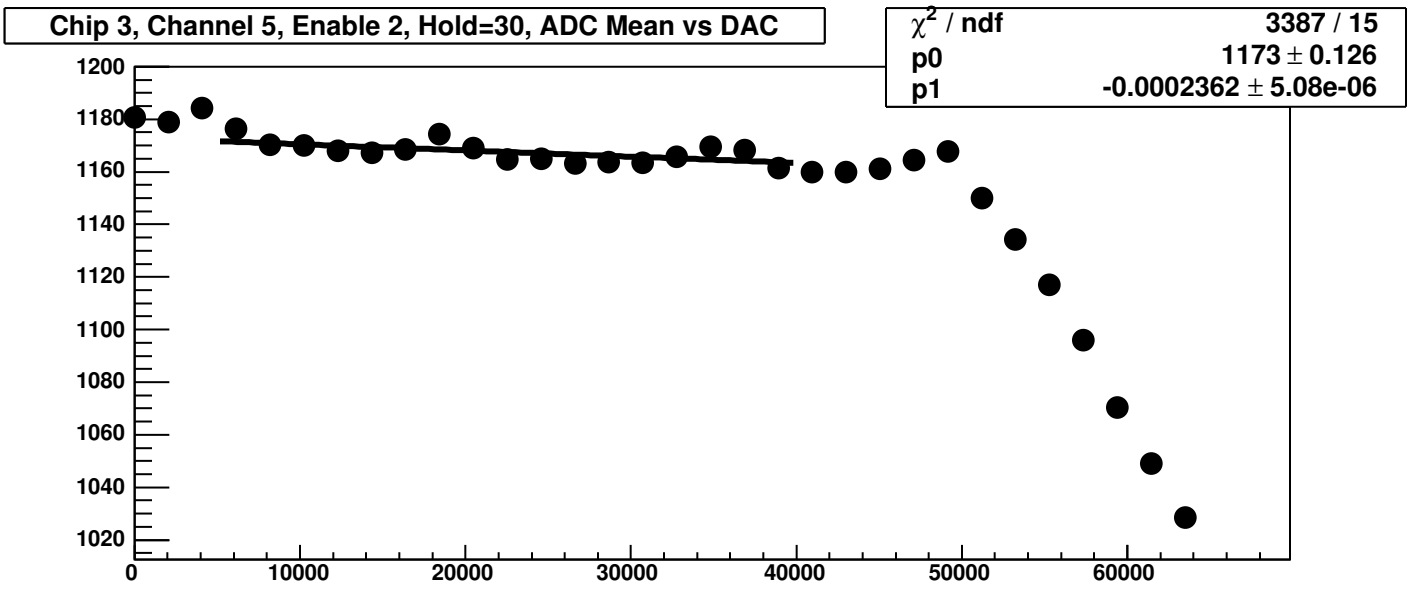
Chip 3, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

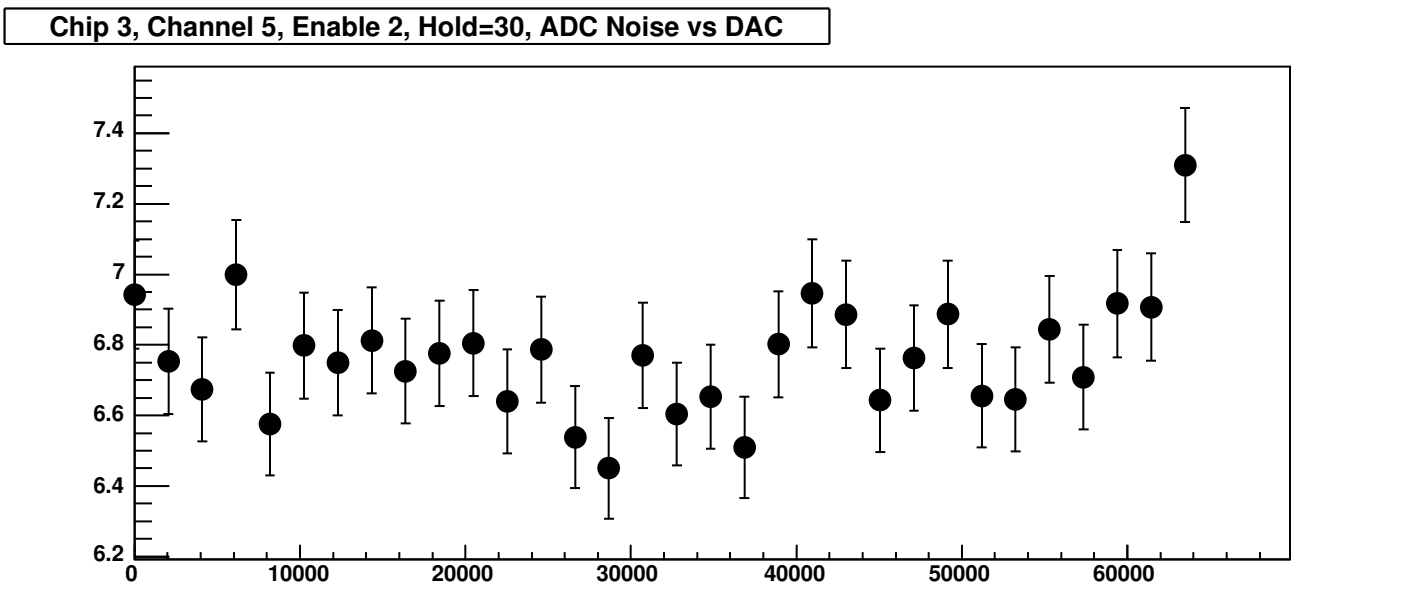


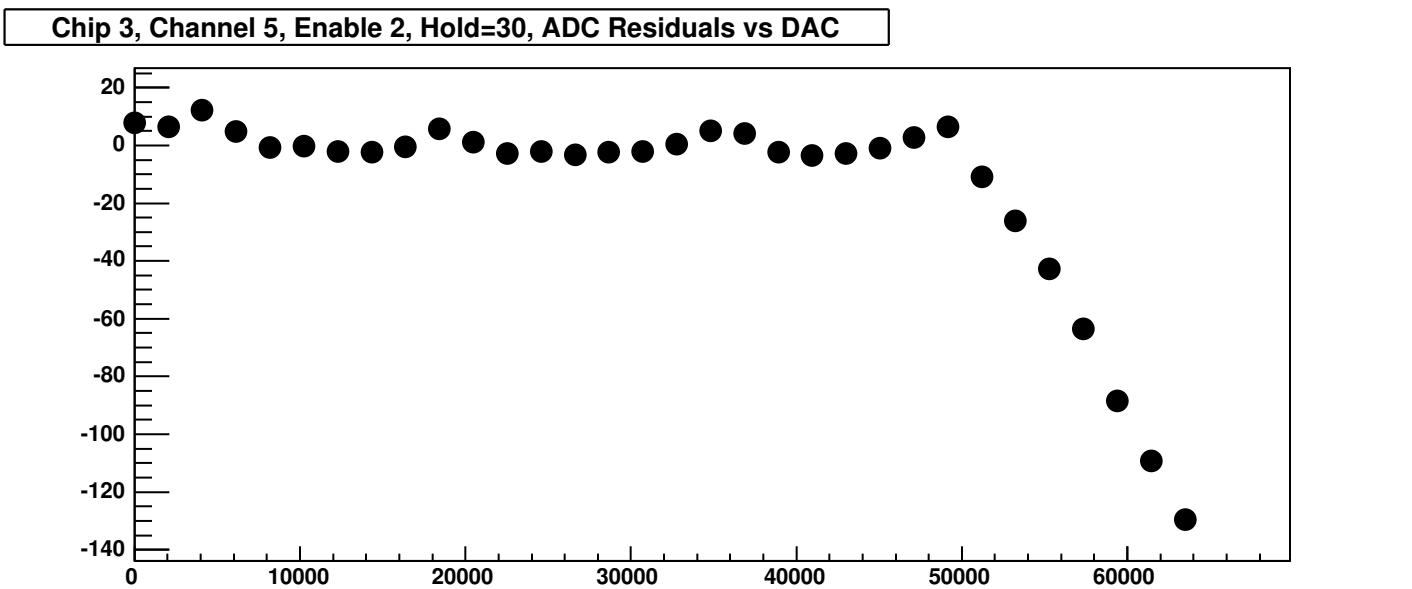

Chip 3, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3143 / 15
p0 1169 ± 0.131
p1 0.0008049 ± 5.442e-06
1220
1200
1180
1160
1140
1120
1100
0
10000
20000
30000
40000
50000
60000

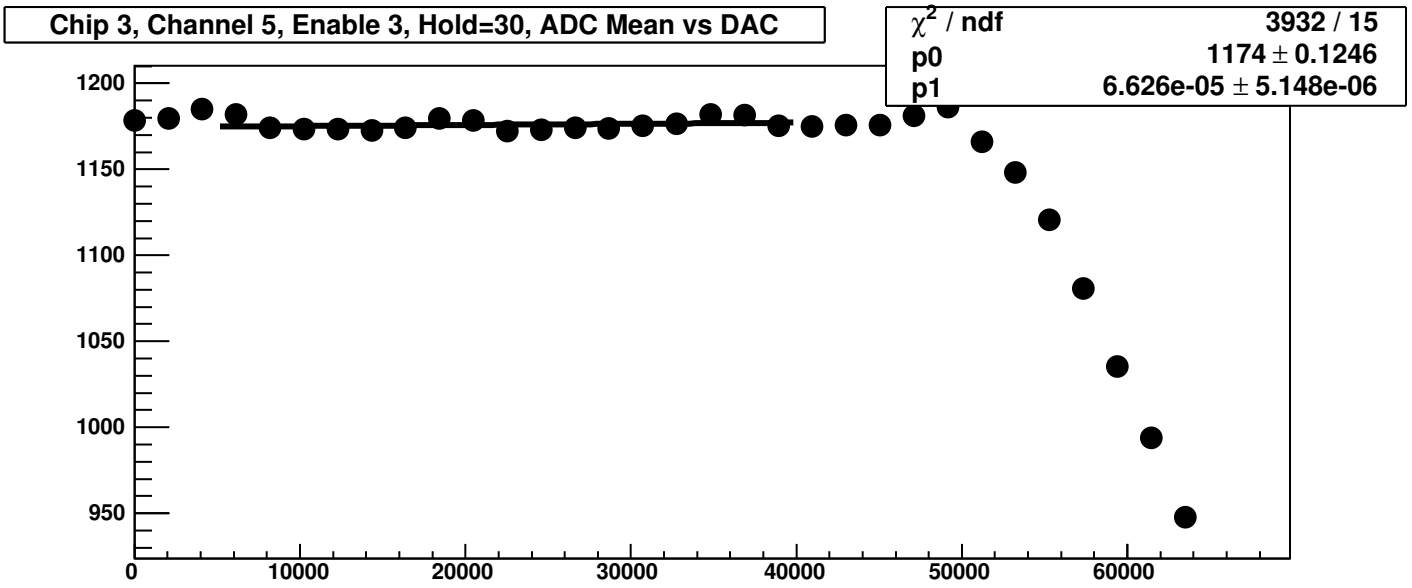

Chip 3, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC
8.2
8
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

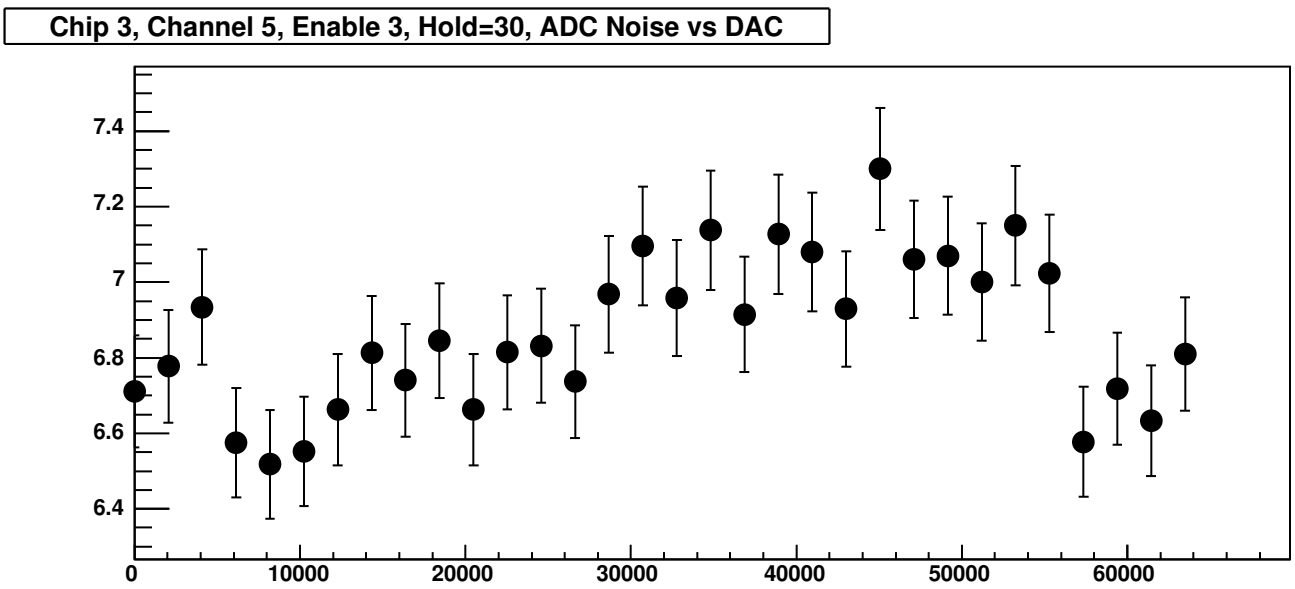

Chip 3, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

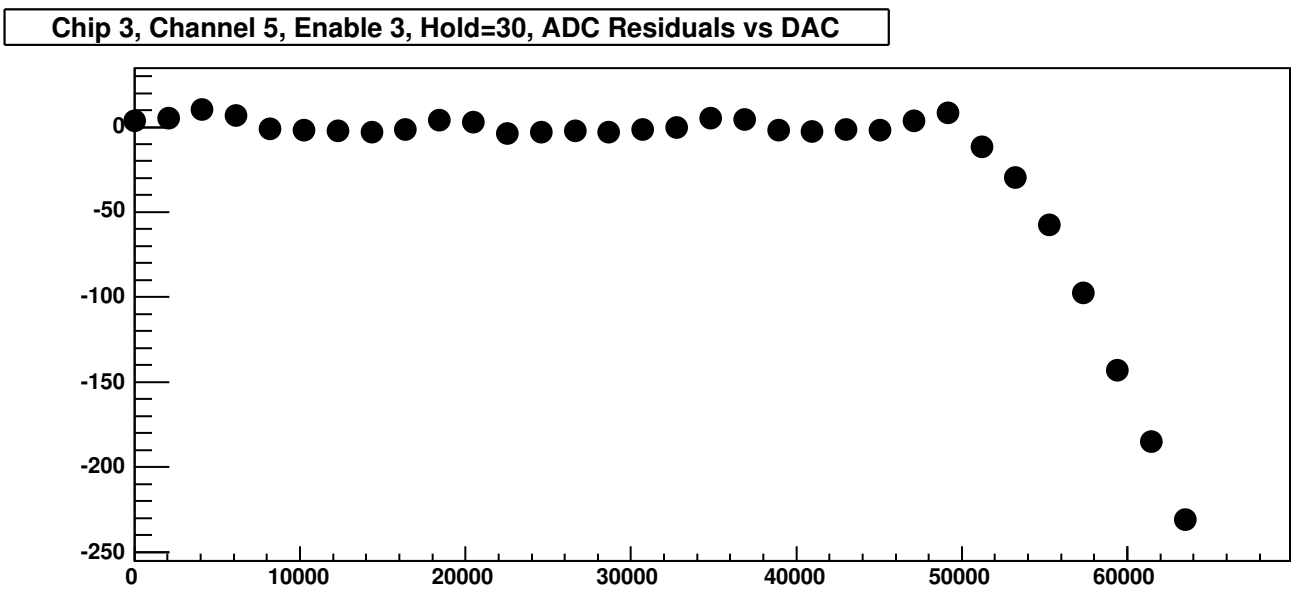

Chip 3, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3387 / 15
p0 1173 ± 0.126
p1 -0.0002362 ± 5.08e-06
1200
1180
1160
1140
1120
1100
1080
1060
1040
1020
0
10000
20000
30000
40000
50000
60000

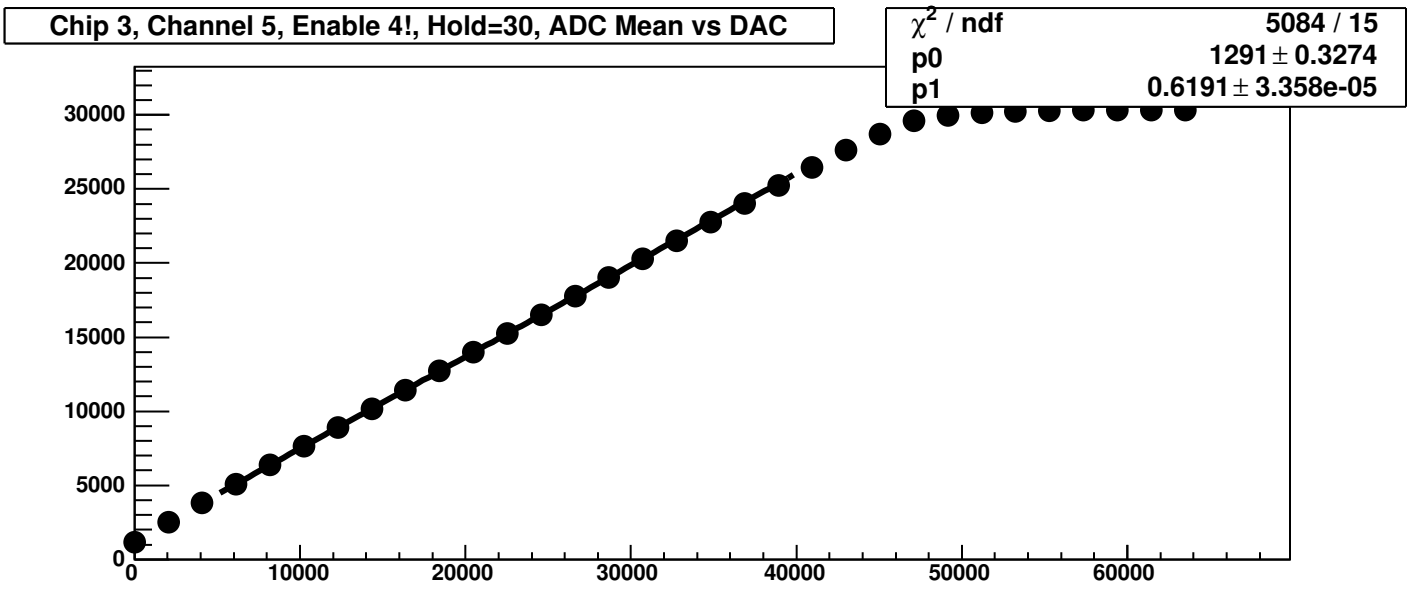

Chip 3, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

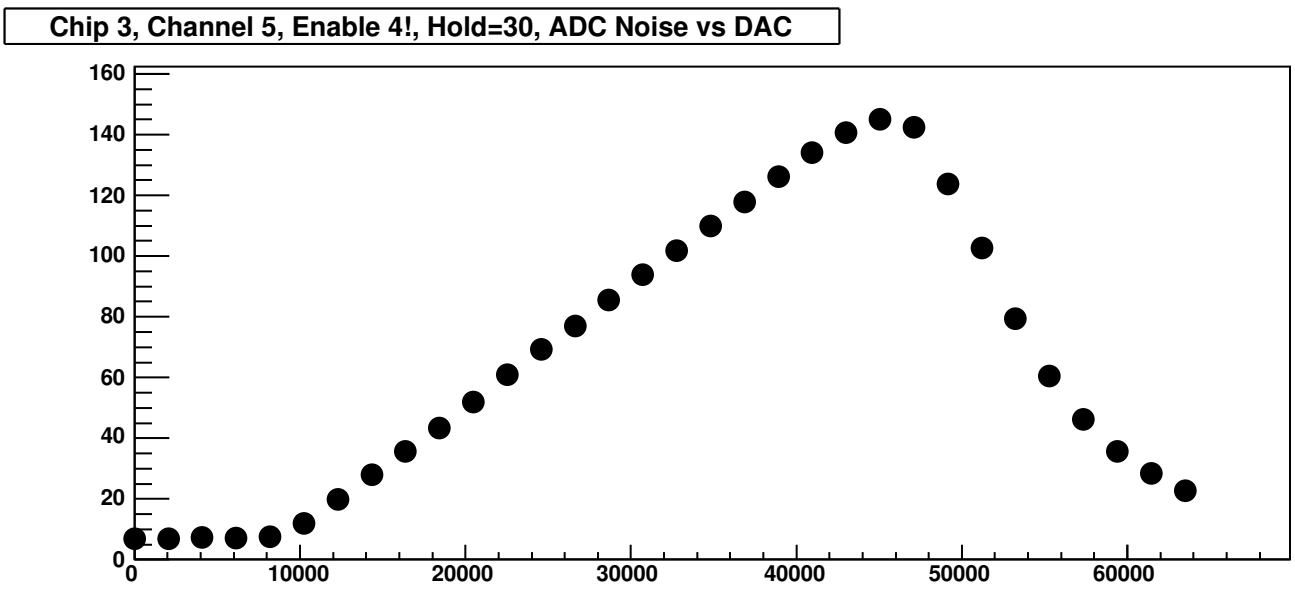

Chip 3, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

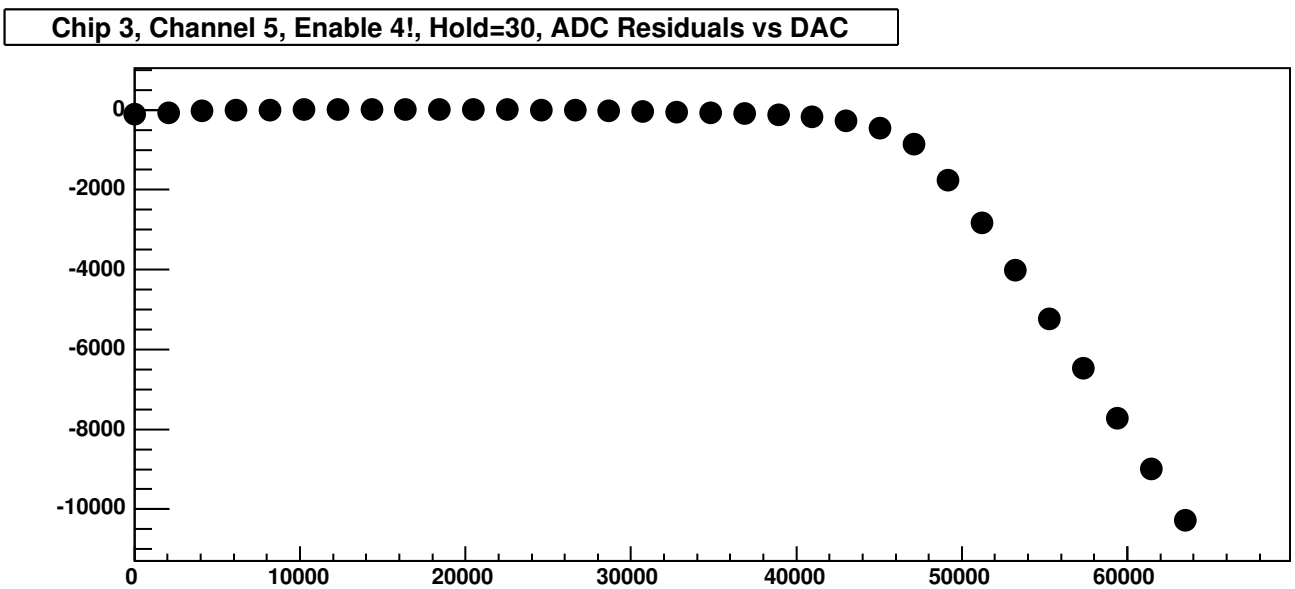

Chip 3, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3932 / 15
p0 1174 ± 0.1246
p1 6.626e-05 ± 5.148e-06
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

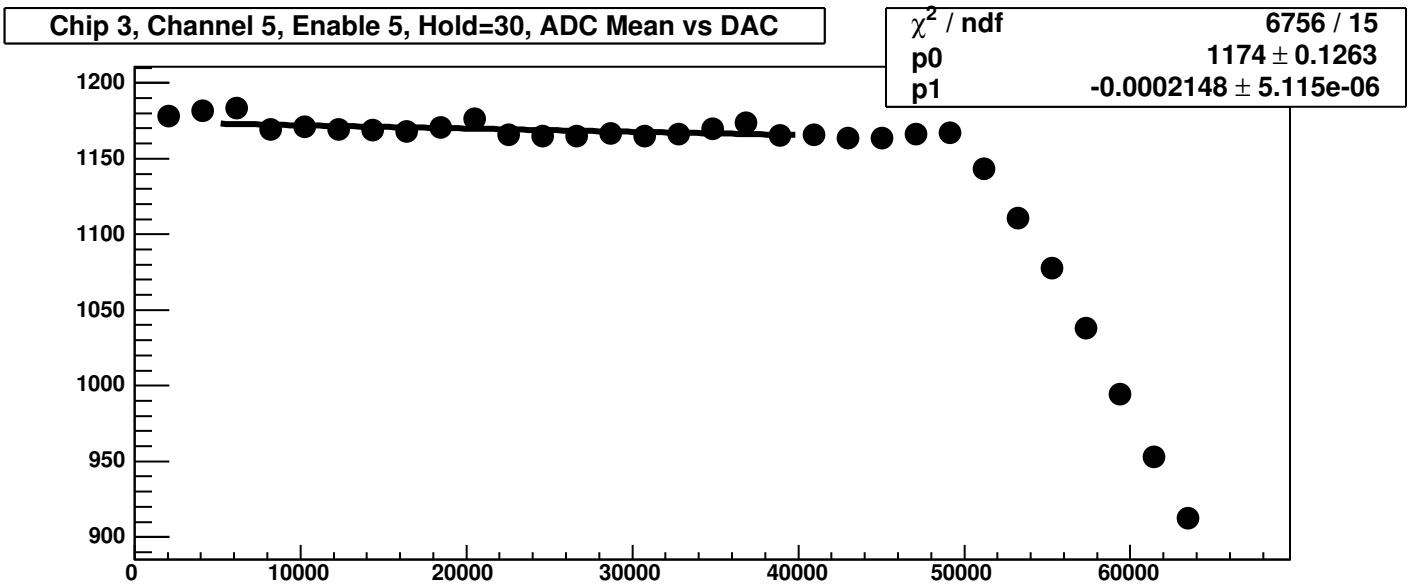

Chip 3, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

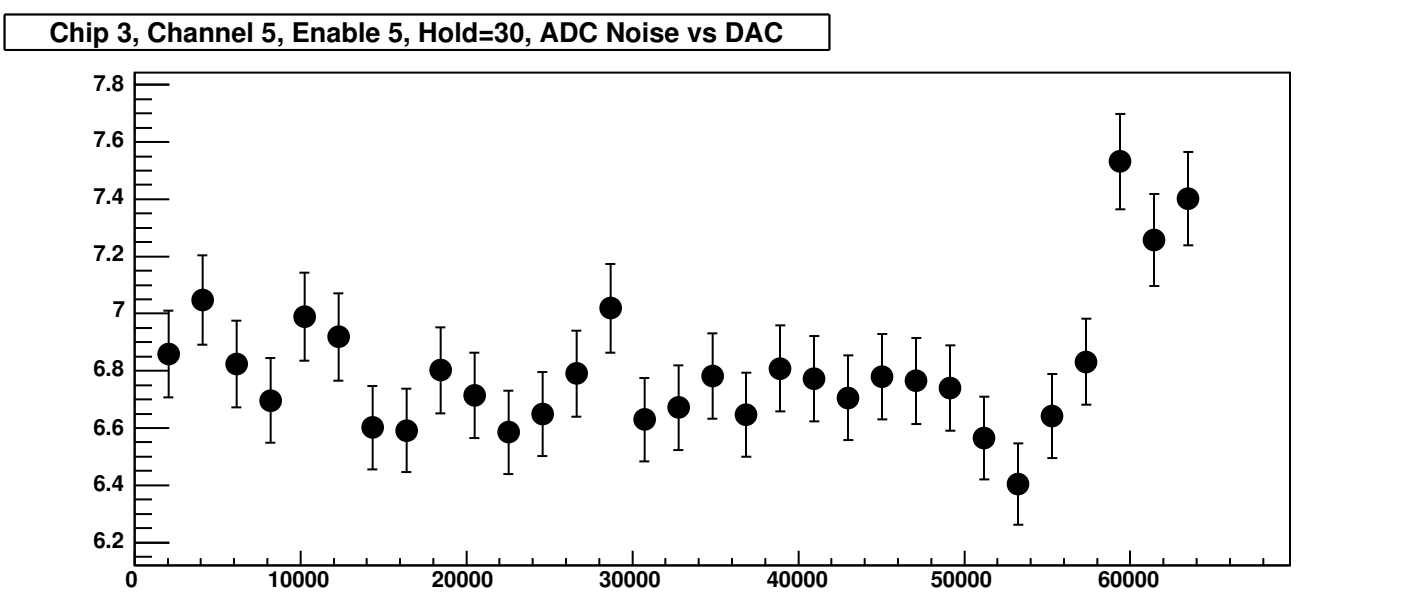

Chip 3, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

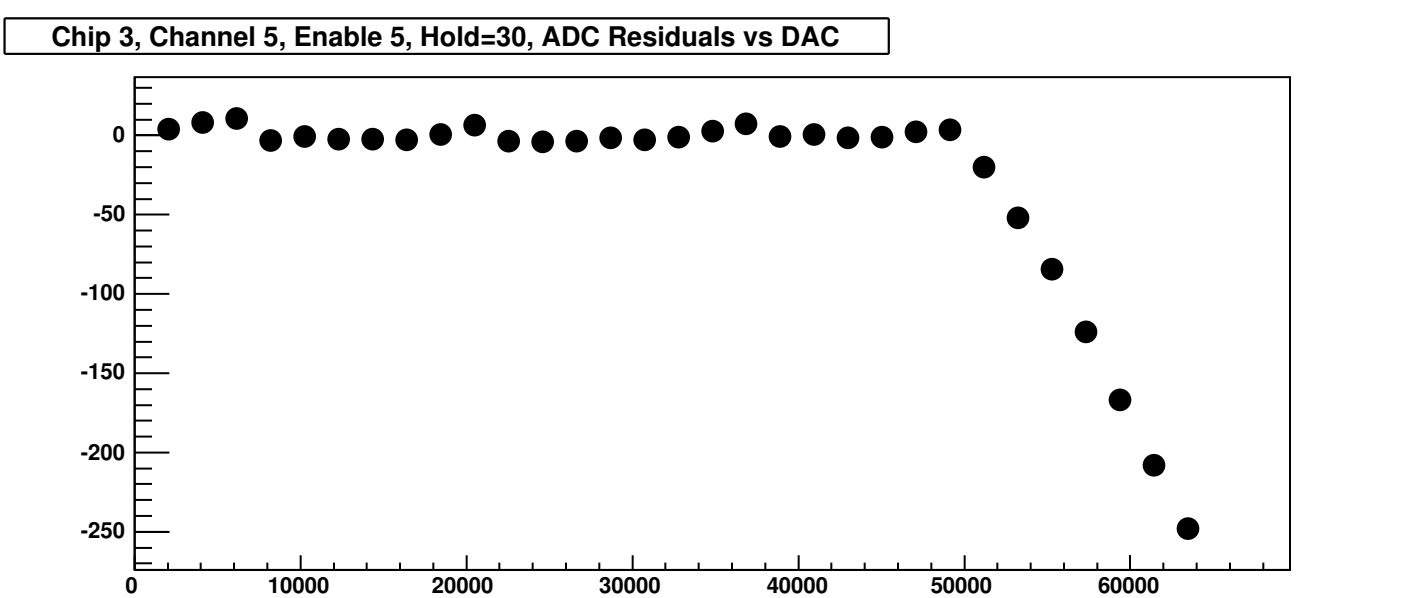

Chip 3, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 5084 / 15
p0 1291 ± 0.3274
p1 0.6191 ± 3.358e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

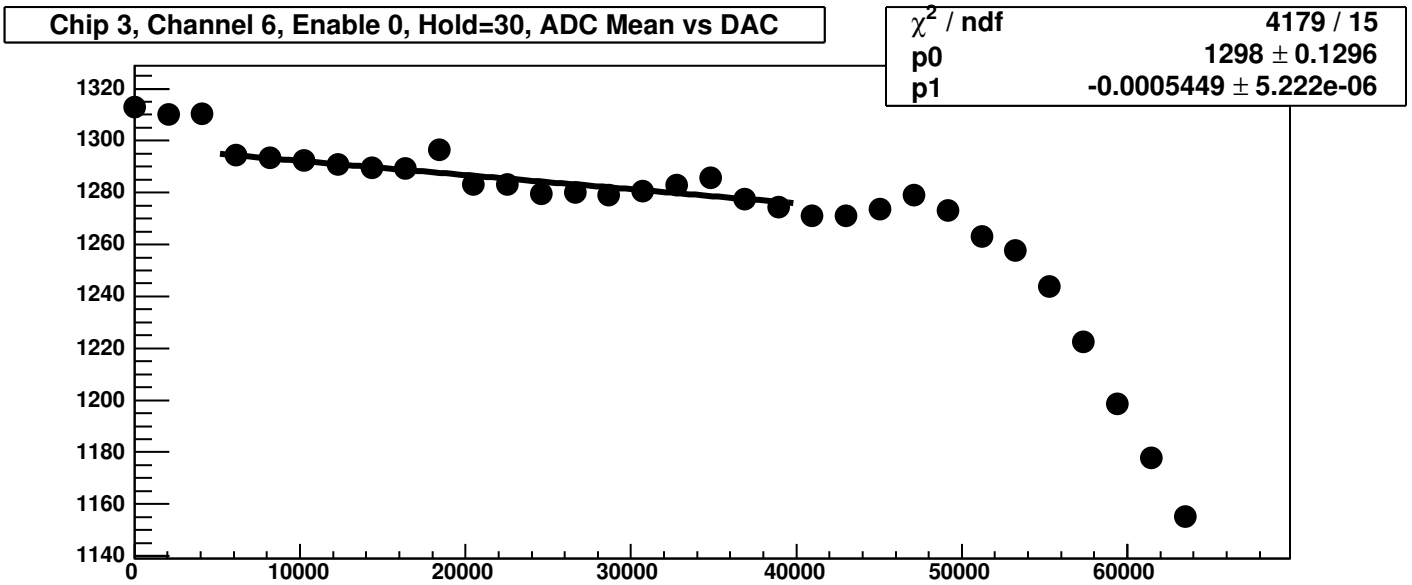

Chip 3, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

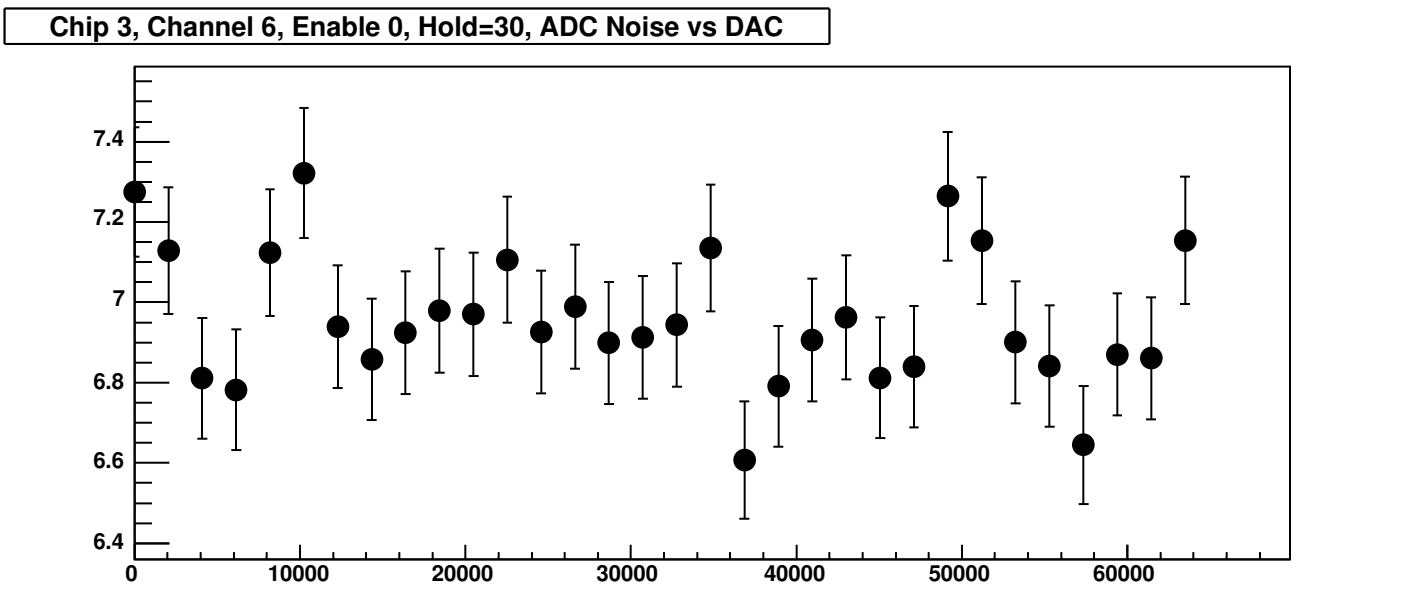

Chip 3, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

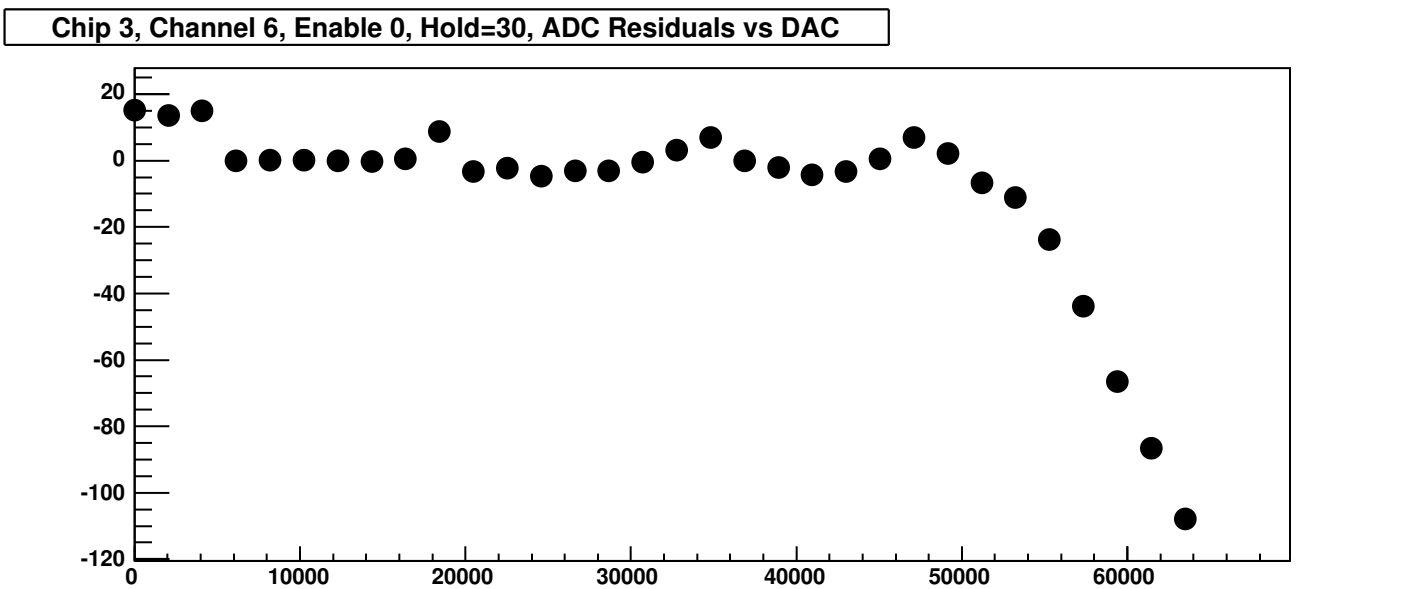

Chip 3, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6756 / 15
p0 1174 ± 0.1263
p1 -0.0002148 ± 5.115e-06
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

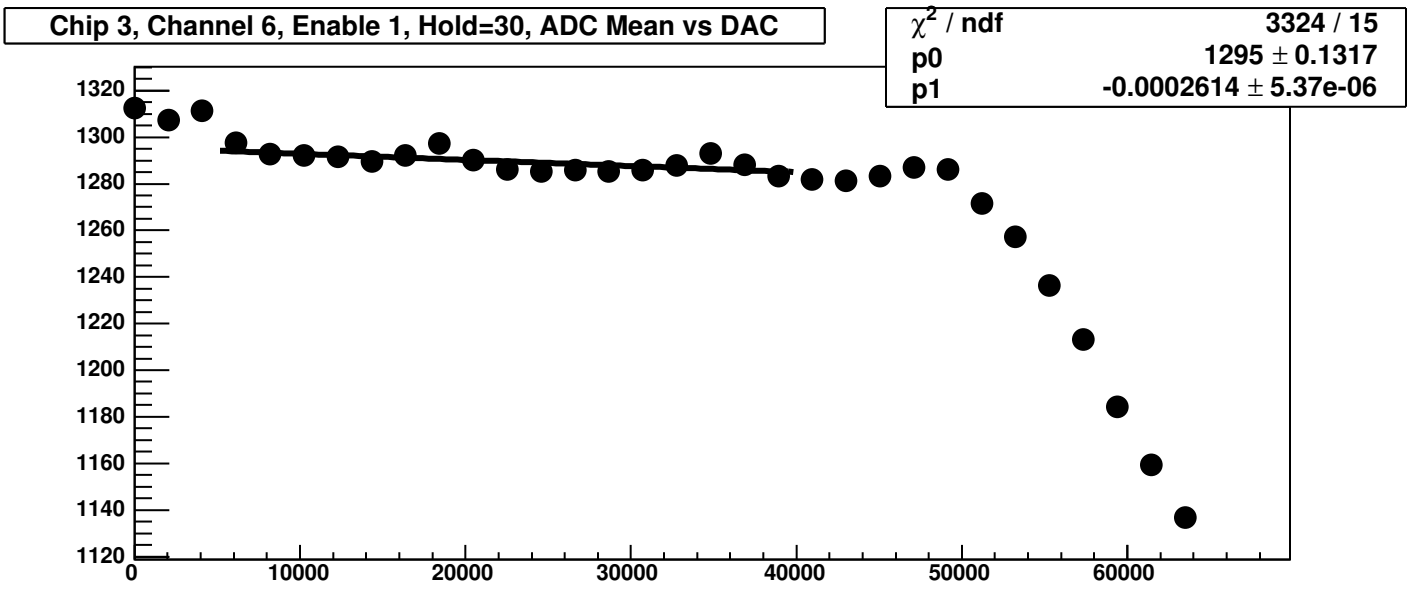

Chip 3, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

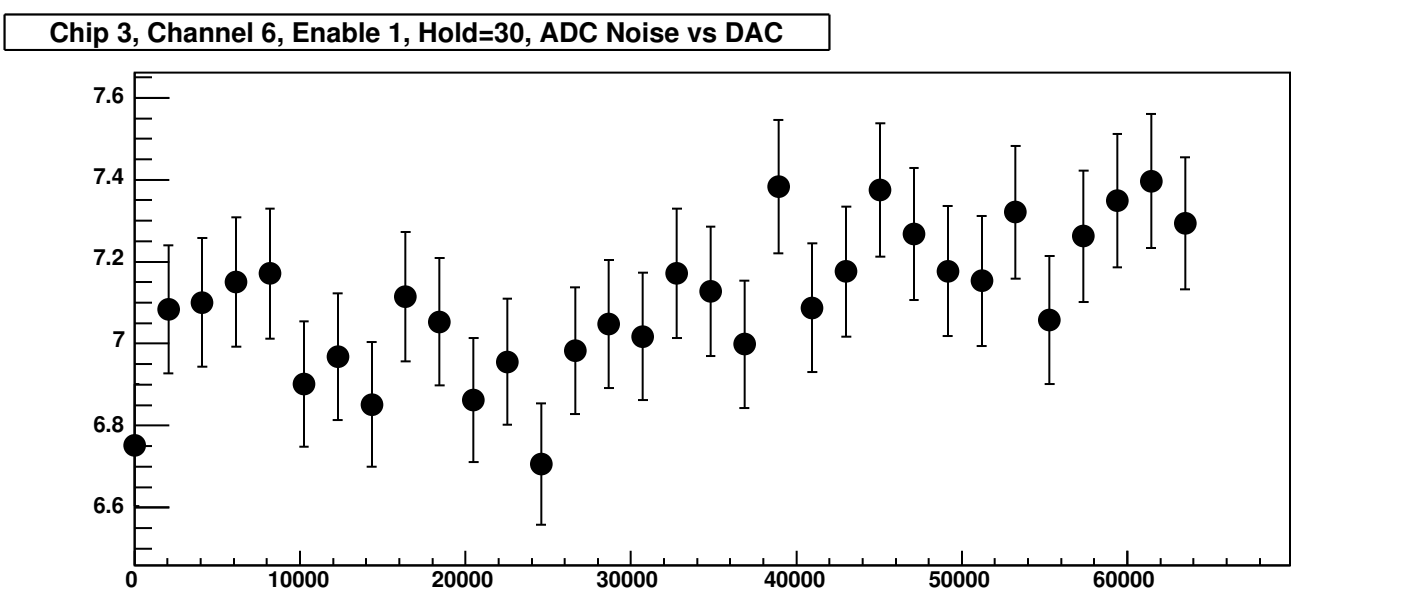

Chip 3, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

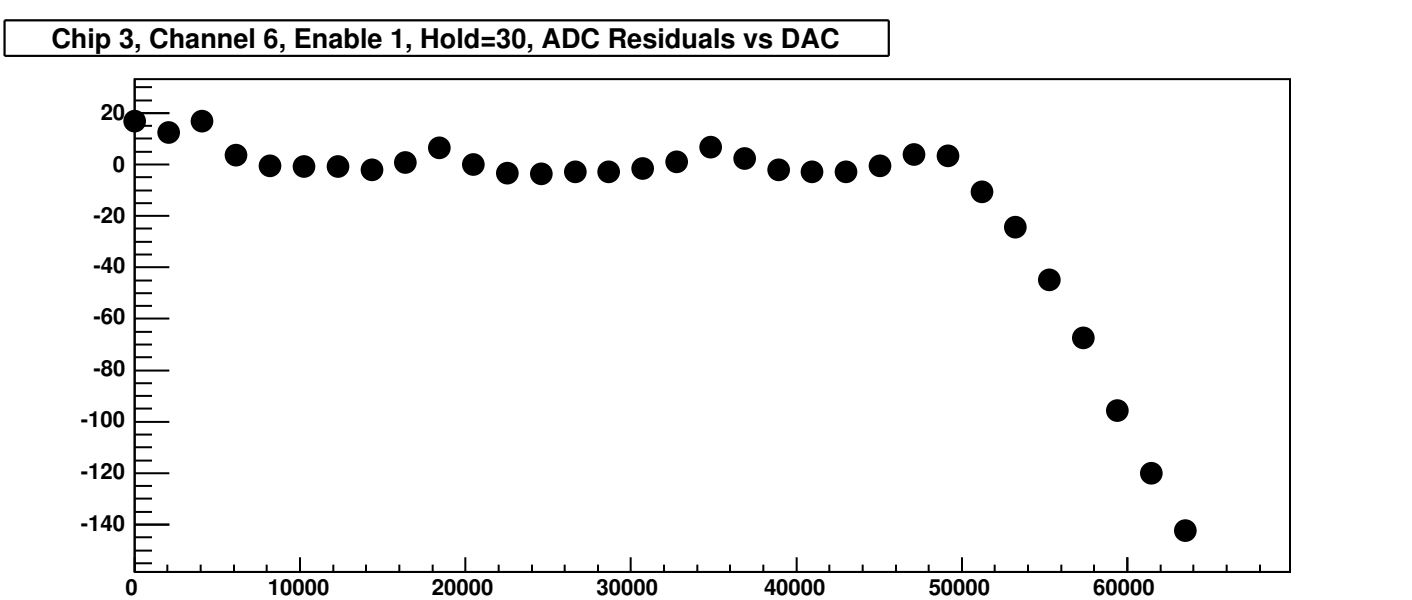

Chip 3, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4179 / 15
p0 1298 ± 0.1296
p1 -0.0005449 ± 5.222e-06
1320
1300
1280
1260
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

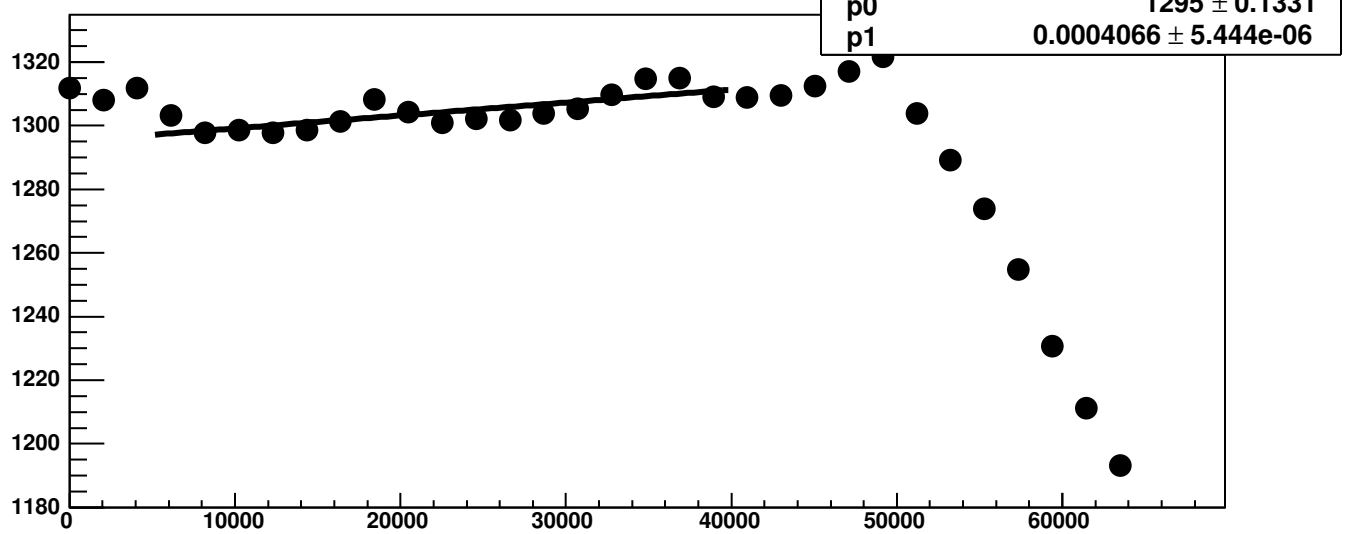

Chip 3, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3324 / 15
p0 1295 ± 0.1317
p1 -0.0002614 ± 5.37e-06
1320
1300
1280
1260
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000

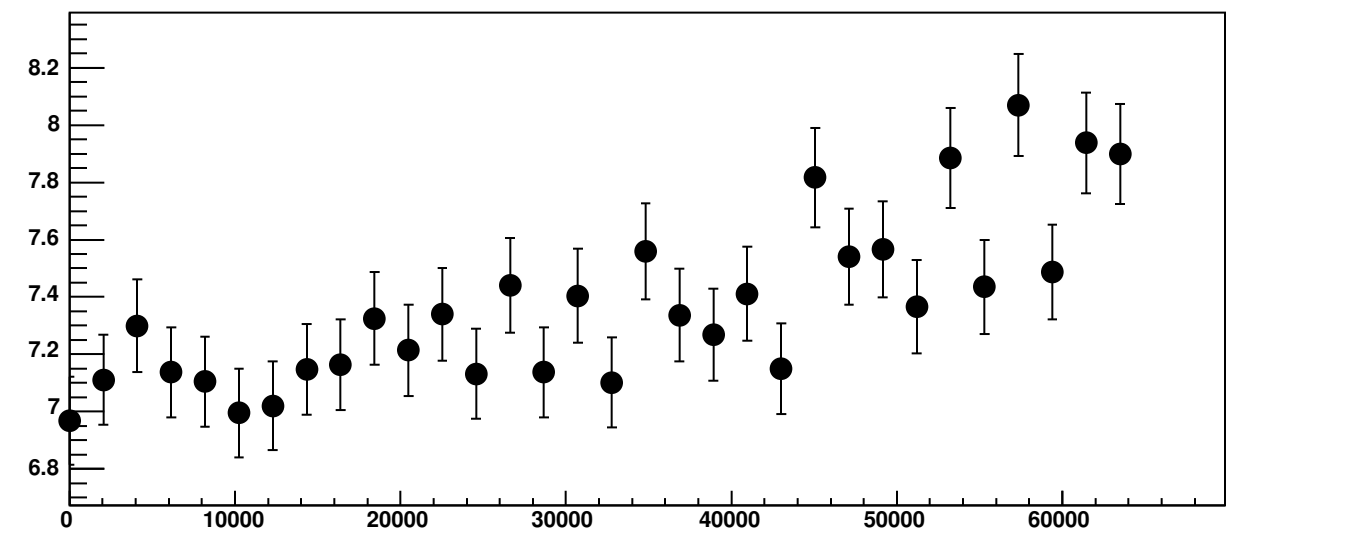

Chip 3, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

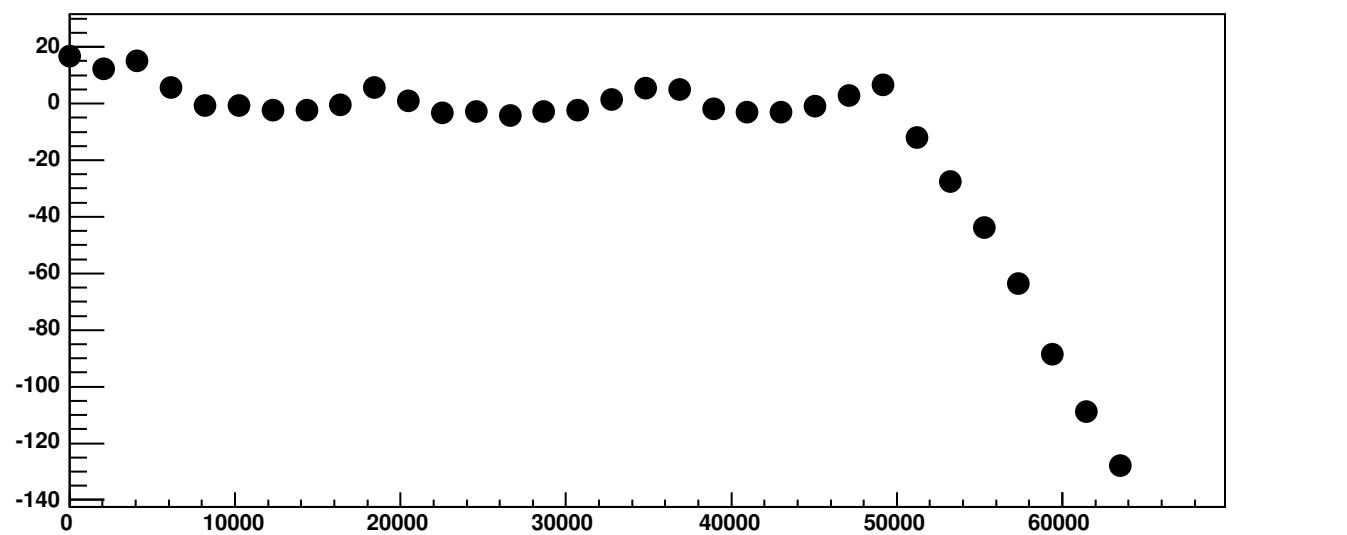
Chip 3, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC

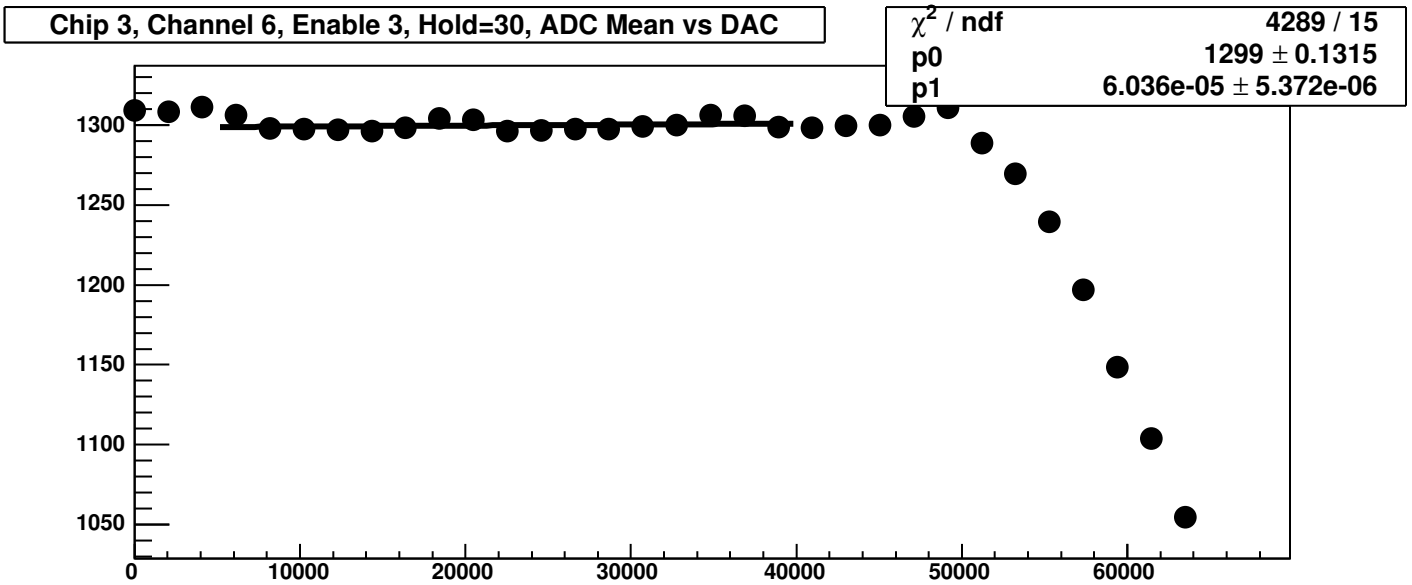


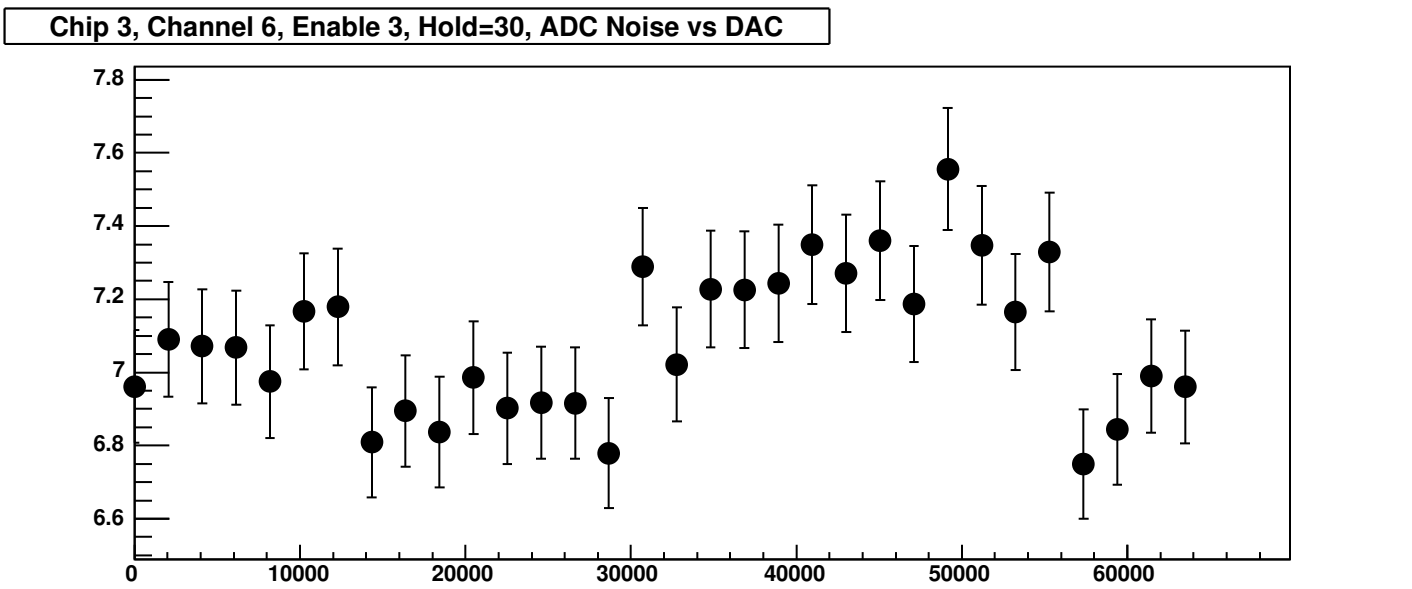
Chip 3, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC

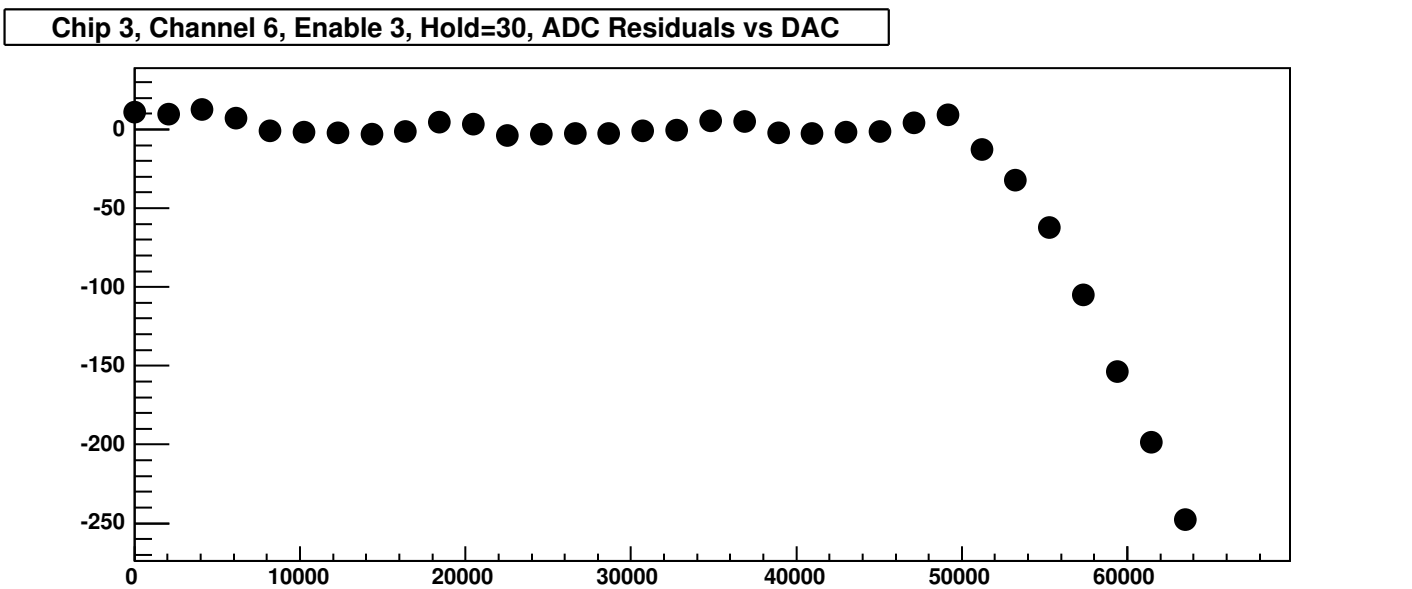


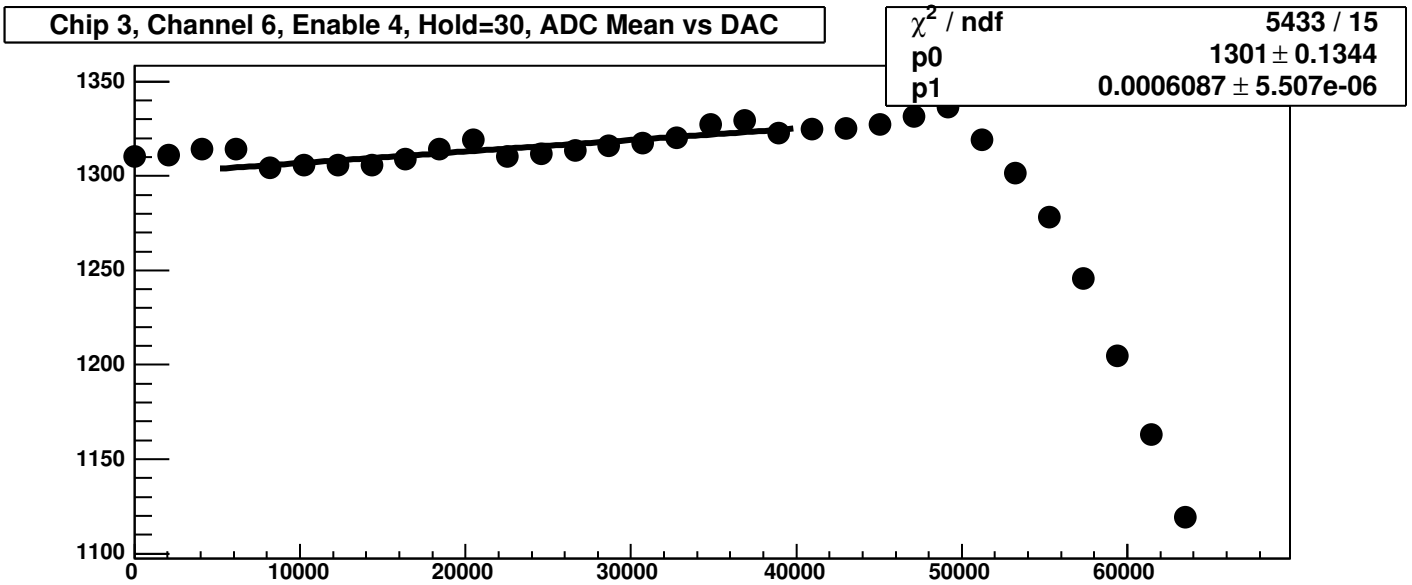
Chip 3, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC

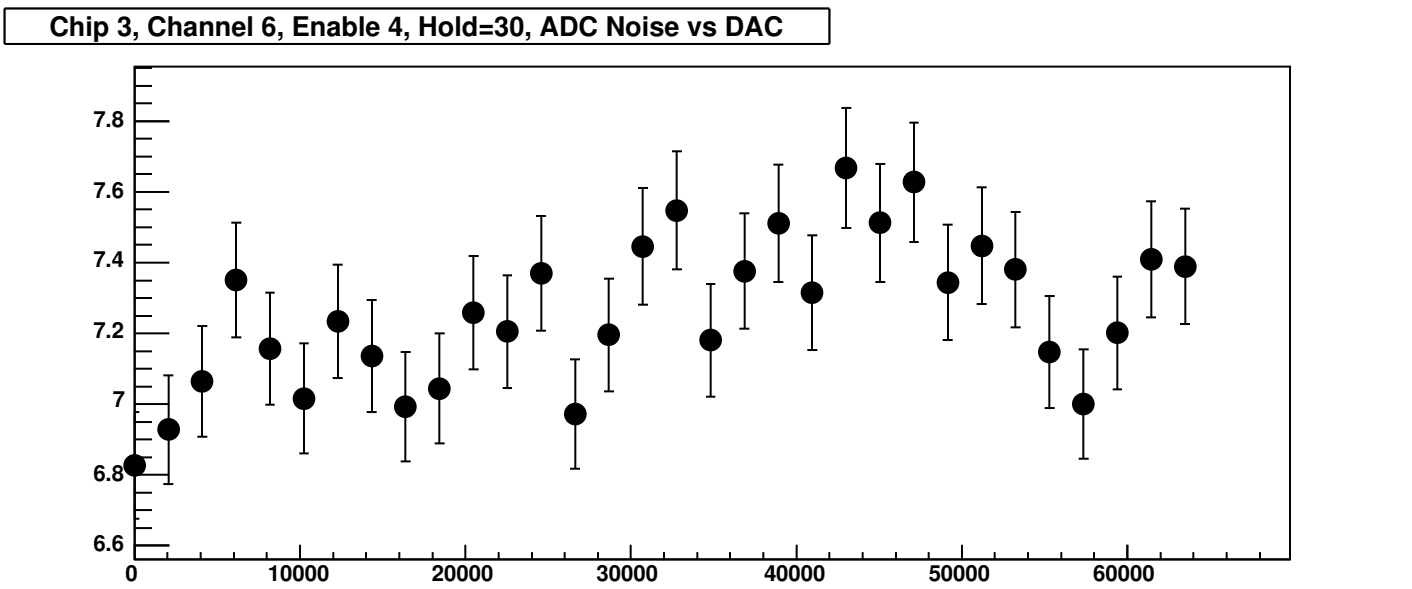


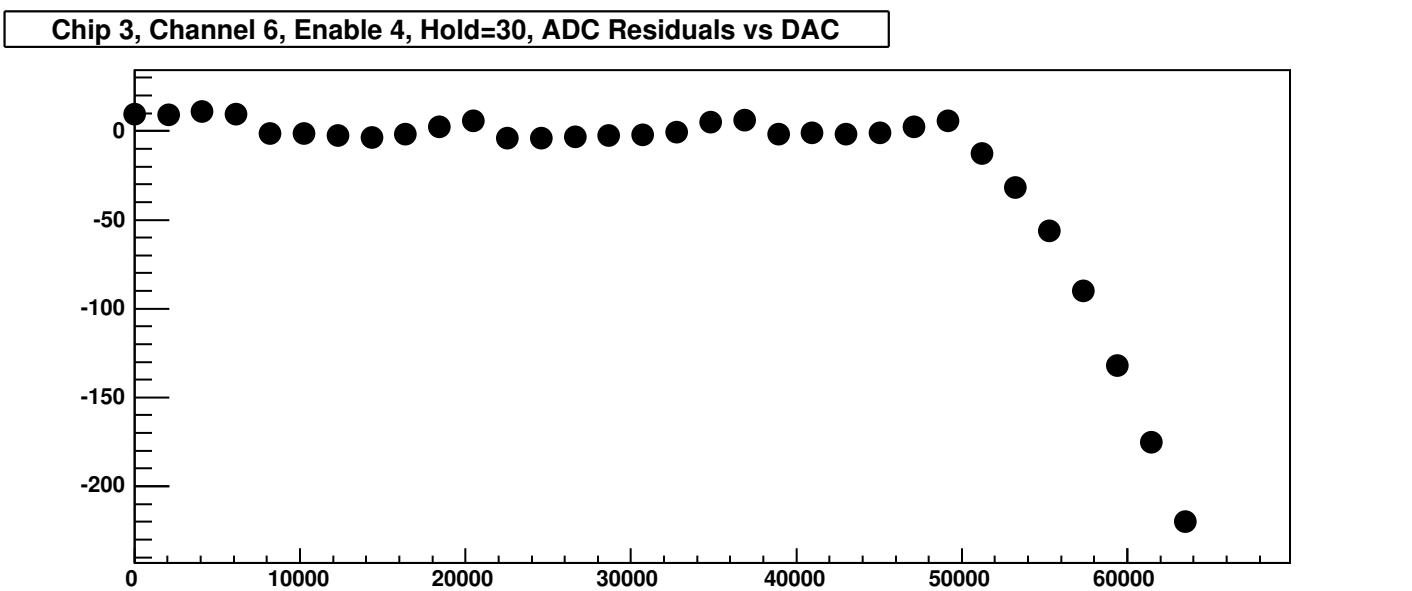

Chip 3, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4289 / 15
p0 1299 ± 0.1315
p1 6.036e-05 ± 5.372e-06
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

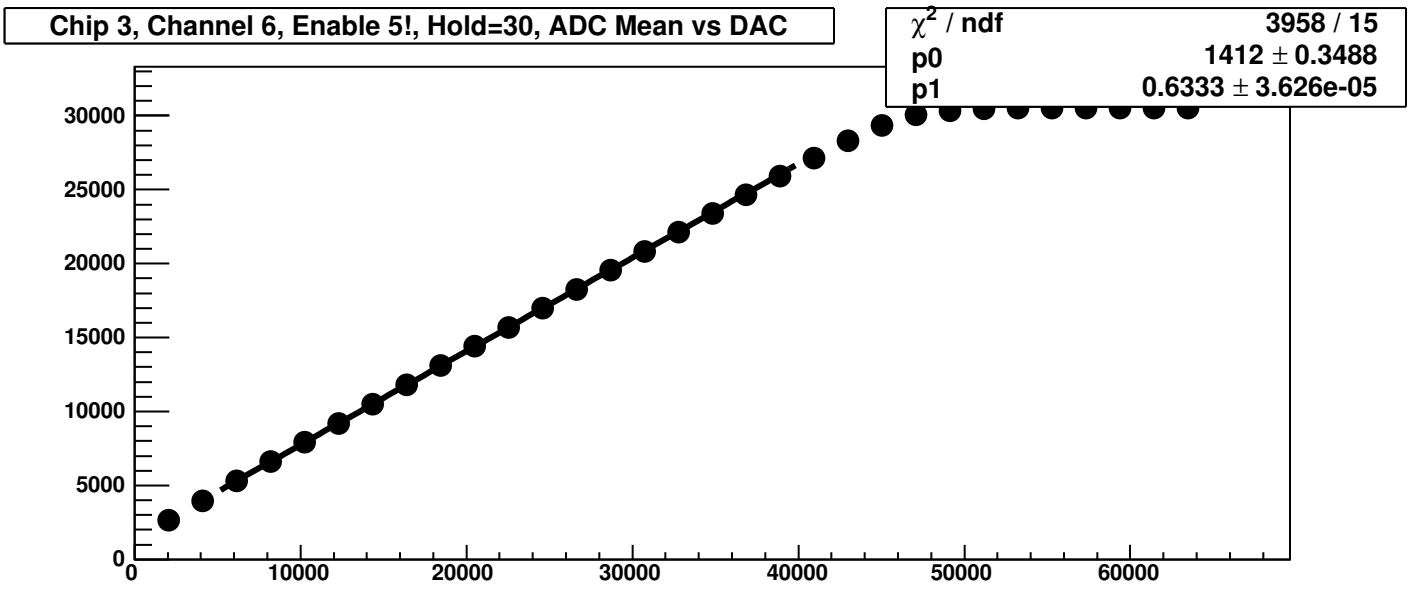

Chip 3, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

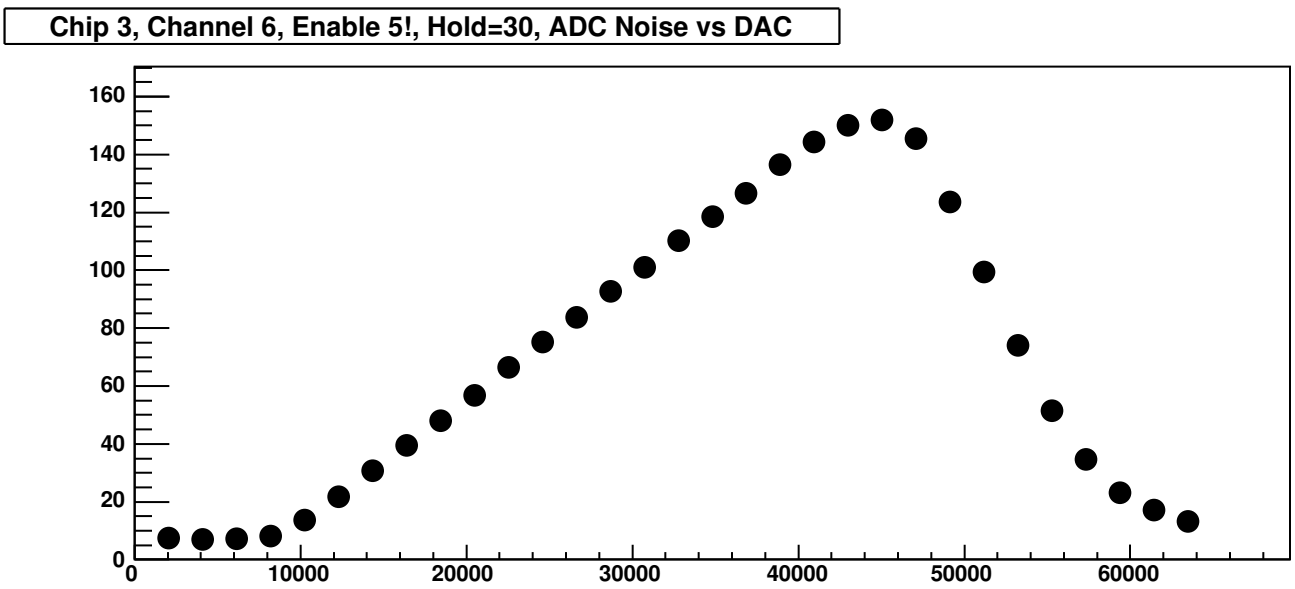

Chip 3, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

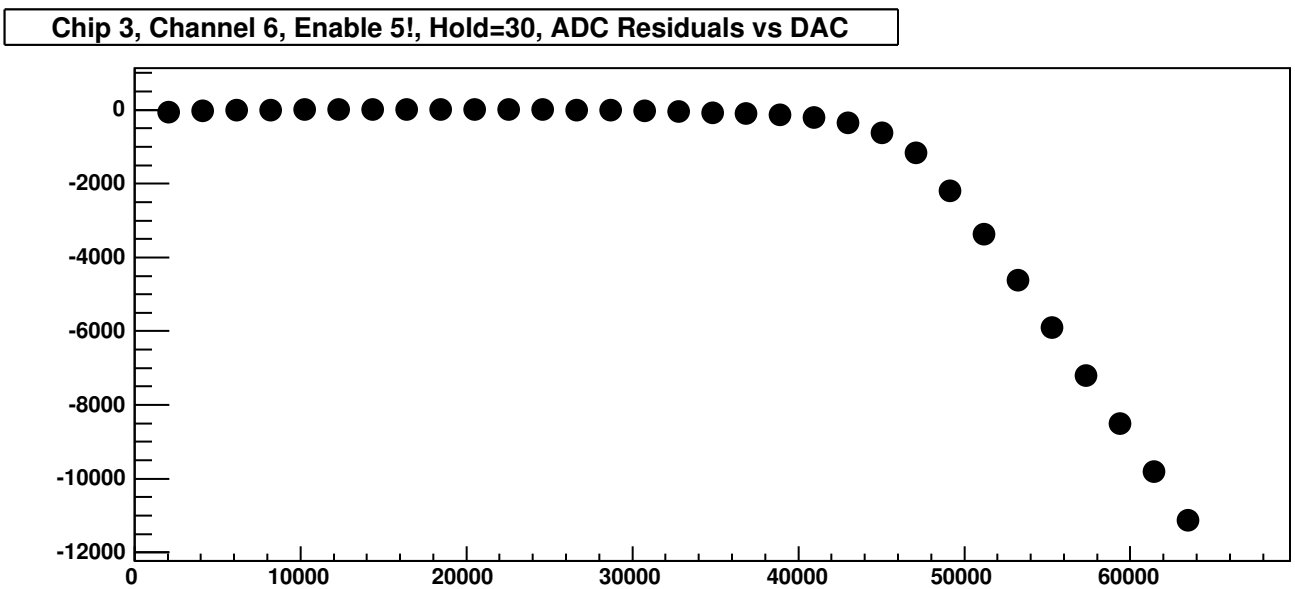

Chip 3, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5433 / 15
p0 1301 ± 0.1344
p1 0.0006087 ± 5.507e-06
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

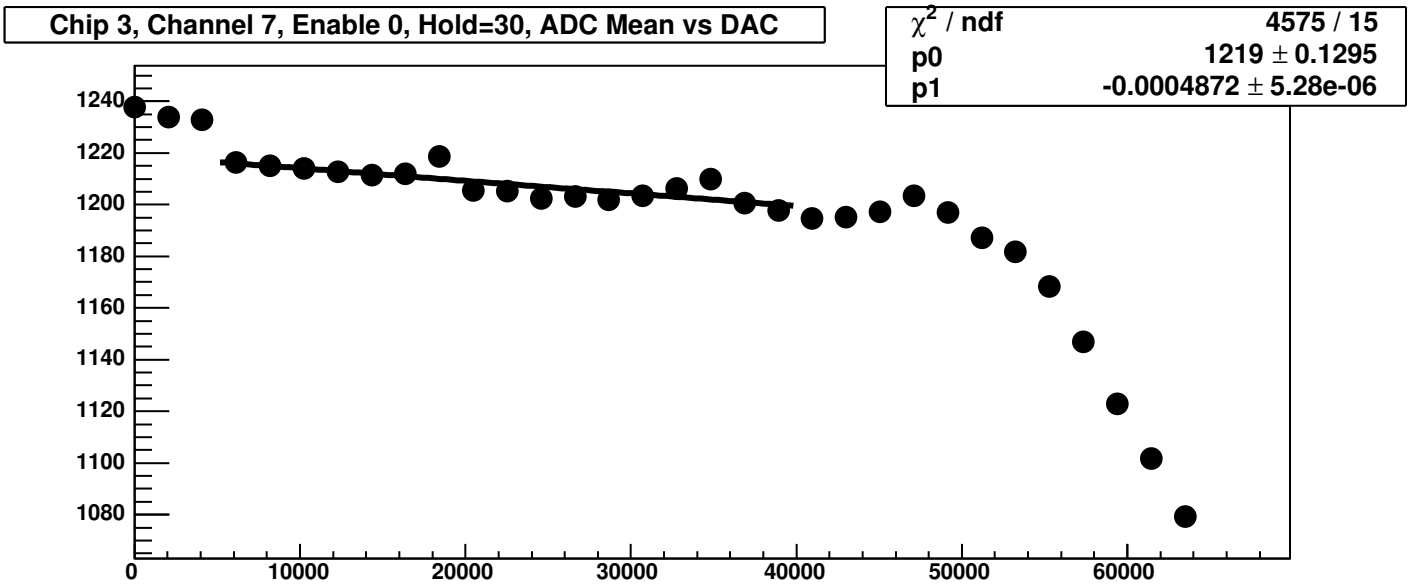

Chip 3, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

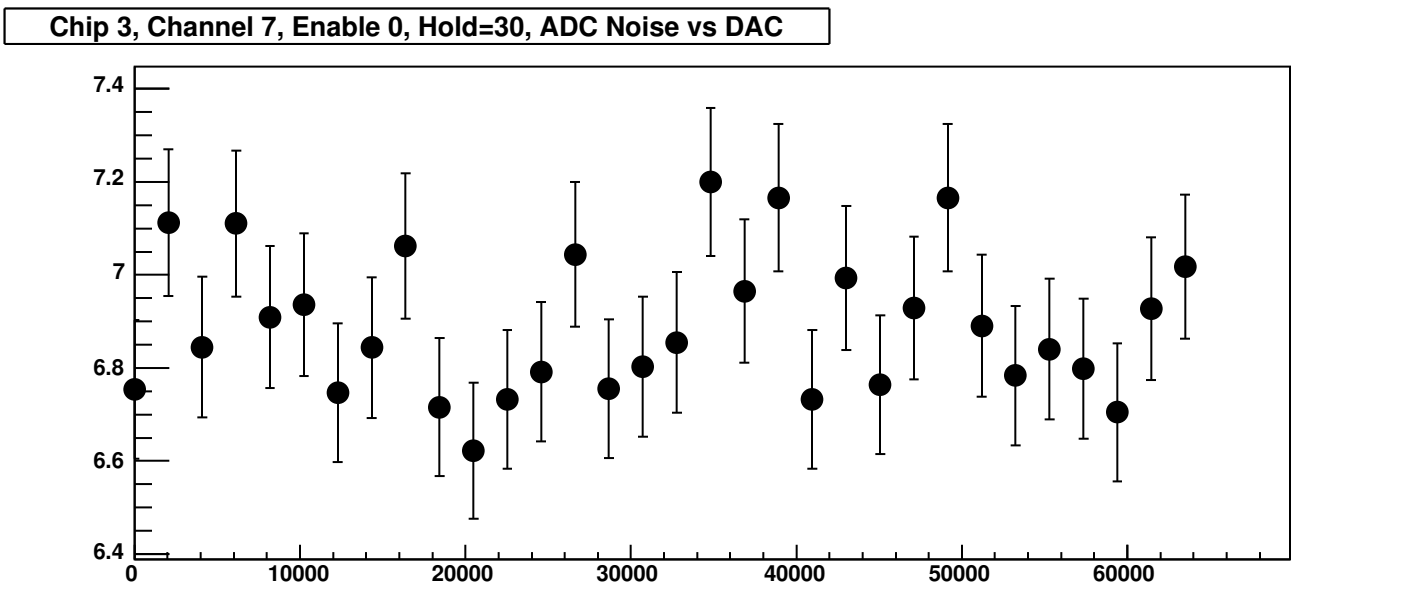

Chip 3, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

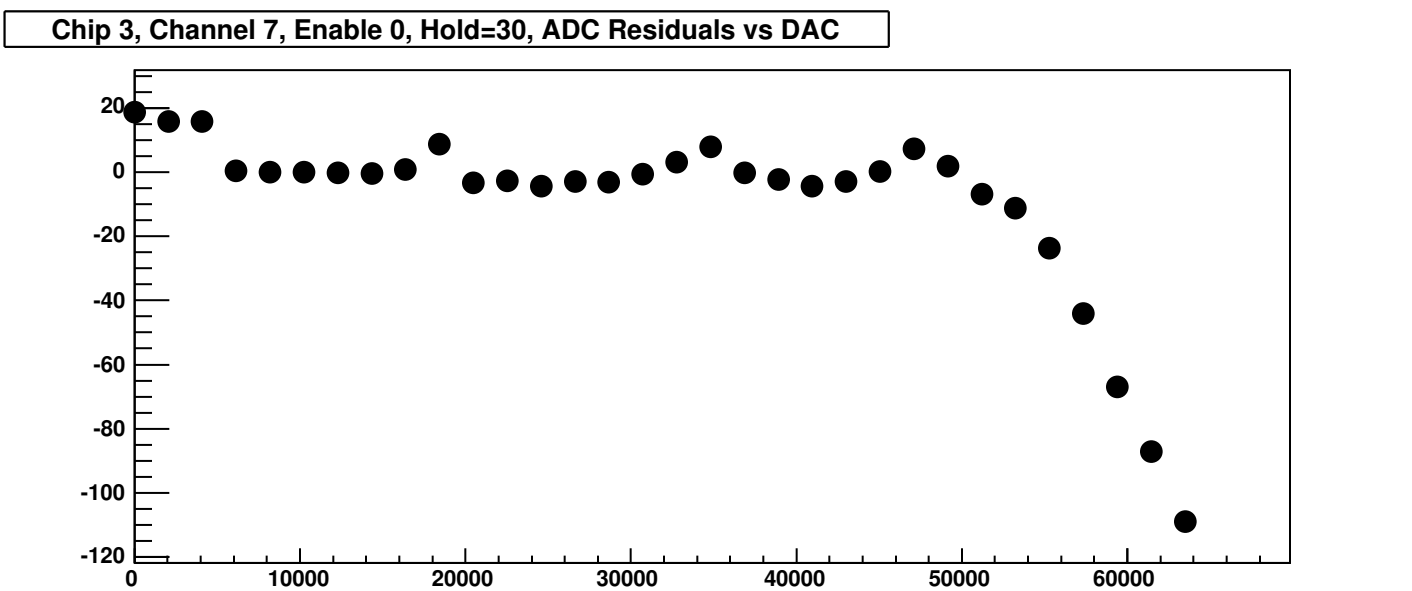

Chip 3, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 3958 / 15
p0 1412 ± 0.3488
p1 0.6333 ± 3.626e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

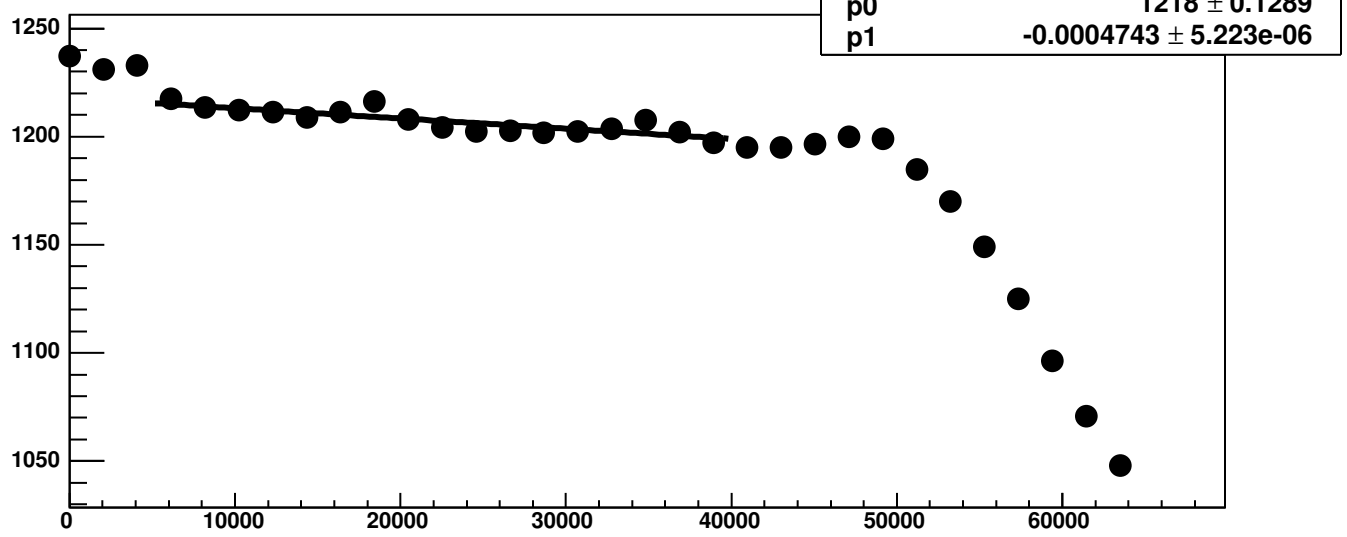

Chip 3, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4575 / 15
p0 1219 ± 0.1295
p1 -0.0004872 ± 5.28e-06
1240
1220
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

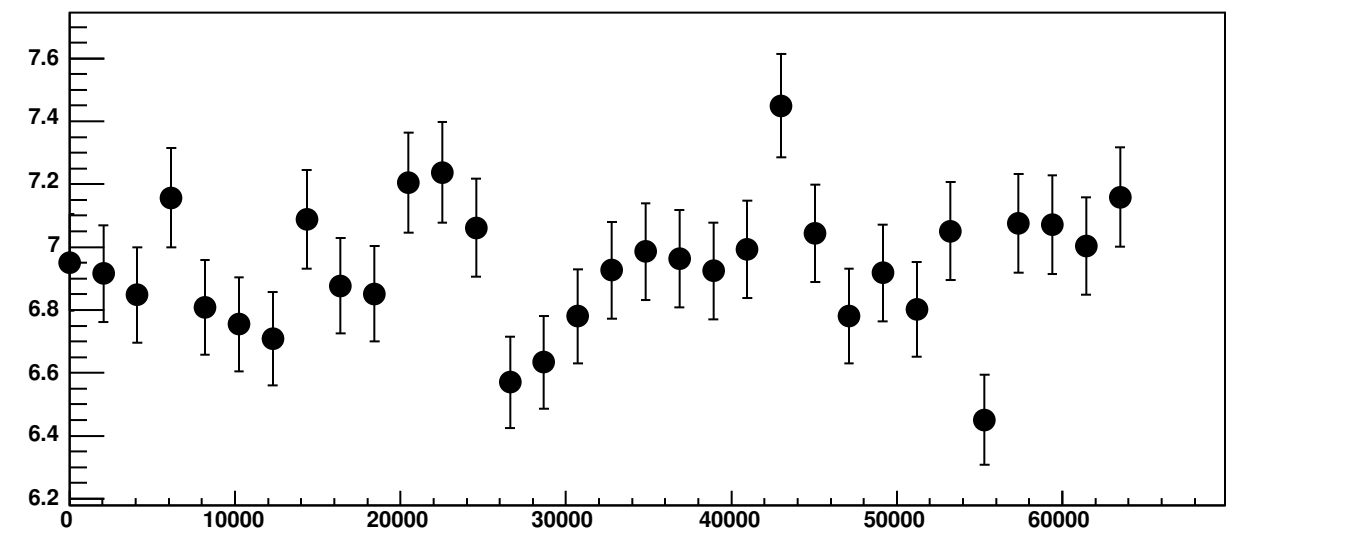

Chip 3, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

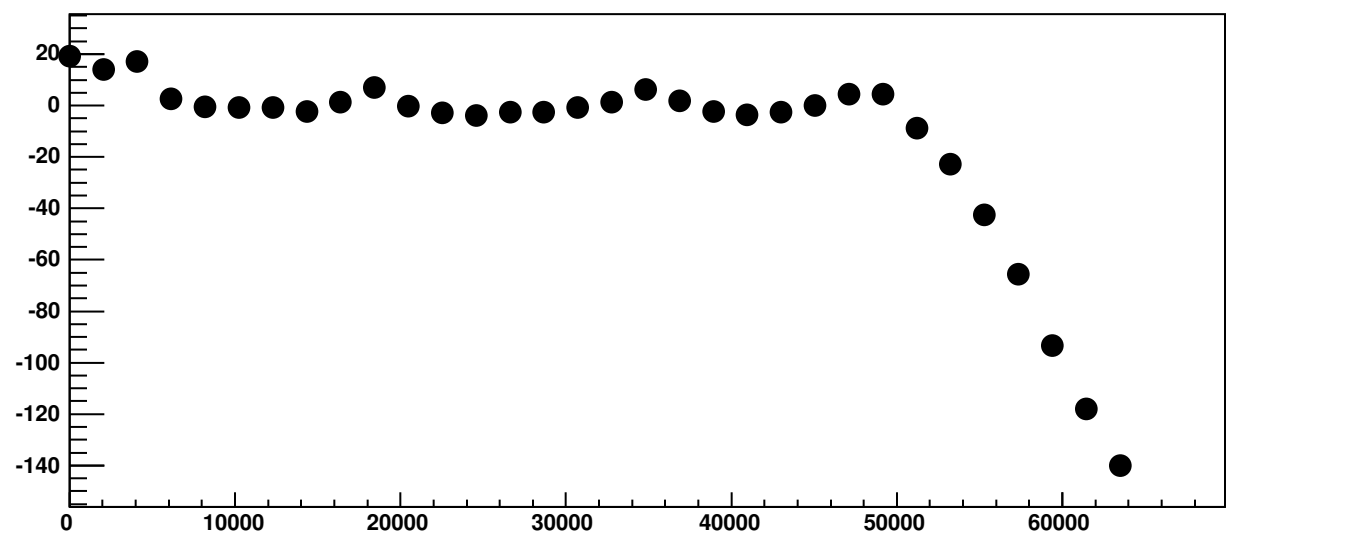
Chip 3, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

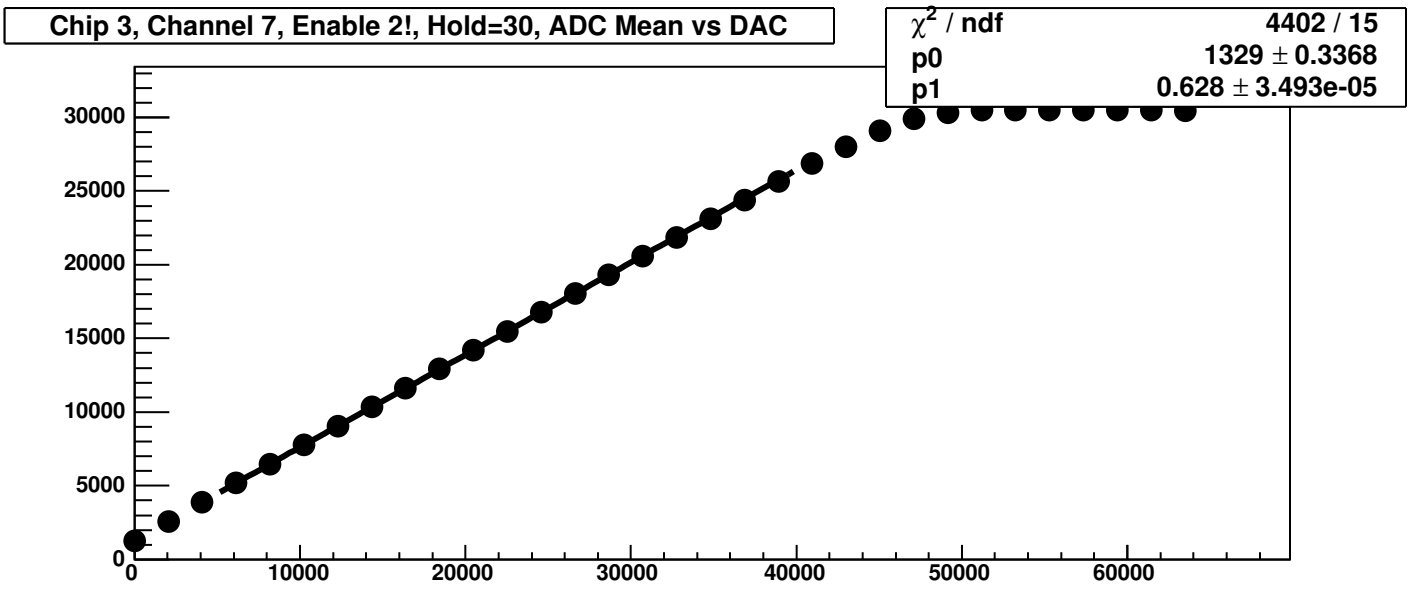


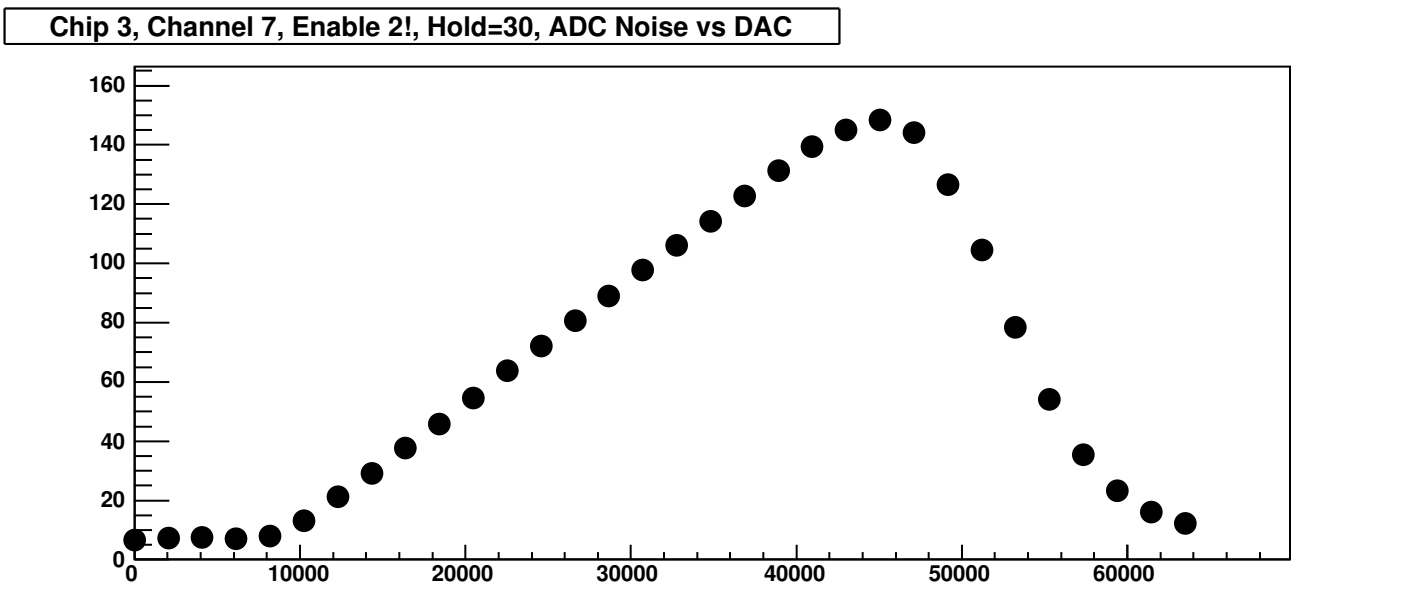
Chip 3, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC

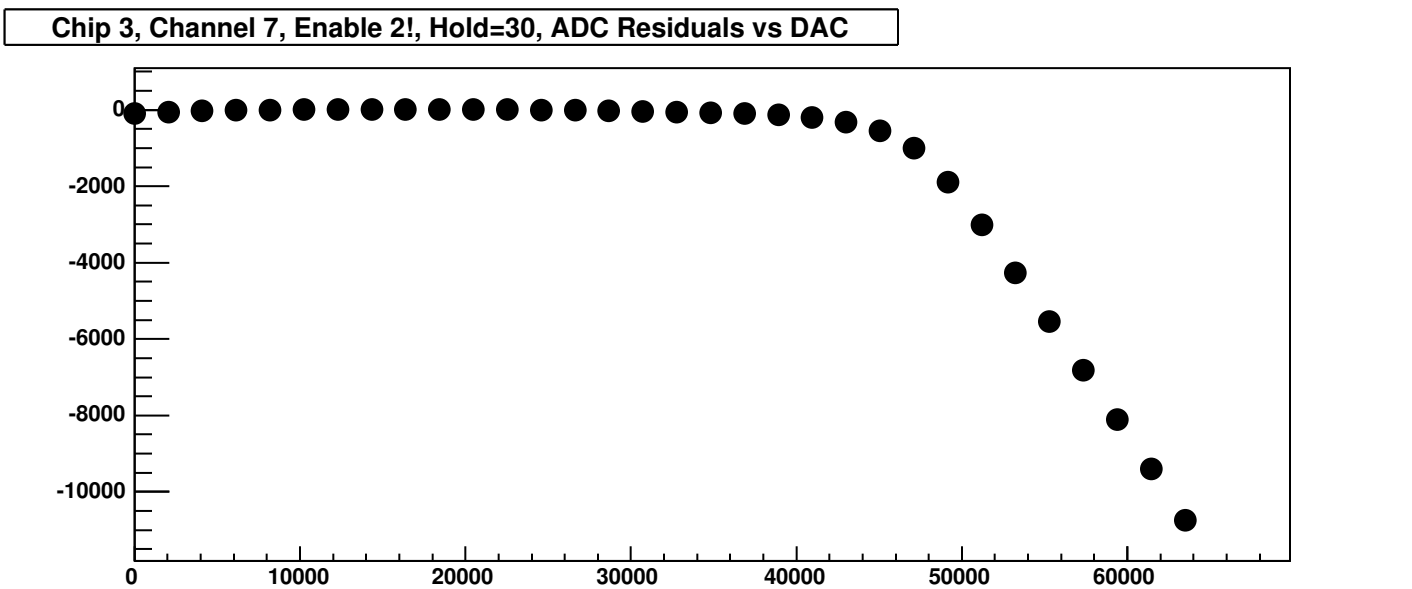


Chip 3, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

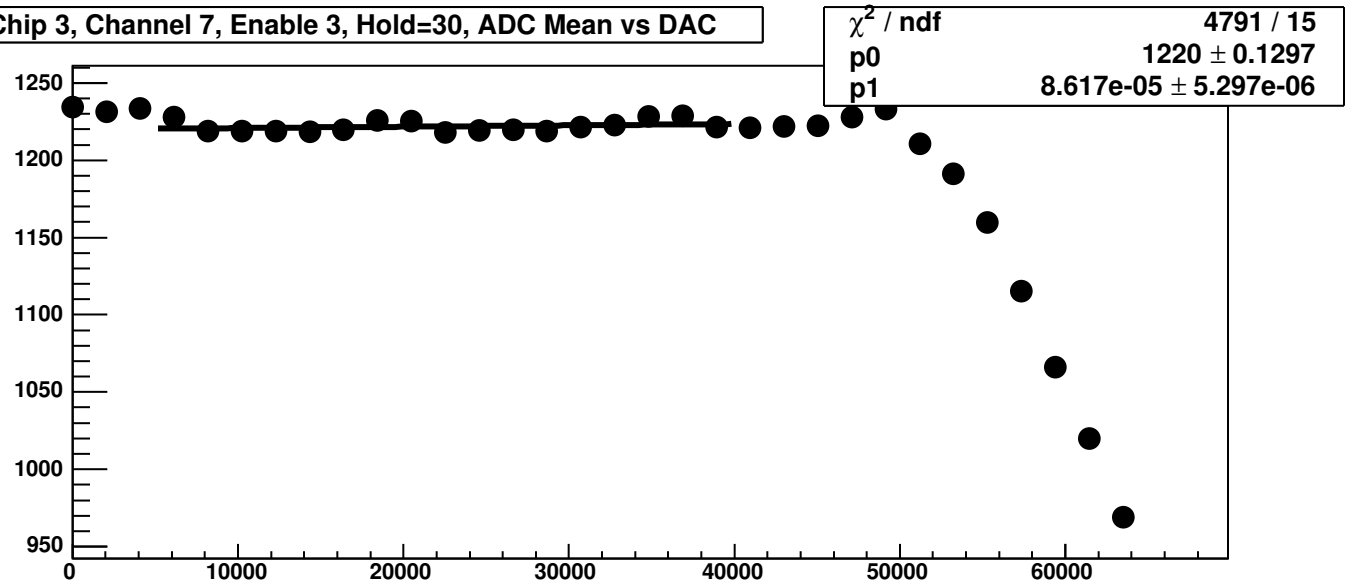



Chip 3, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 4402 / 15
p0 1329 ± 0.3368
p1 0.628 ± 3.493e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

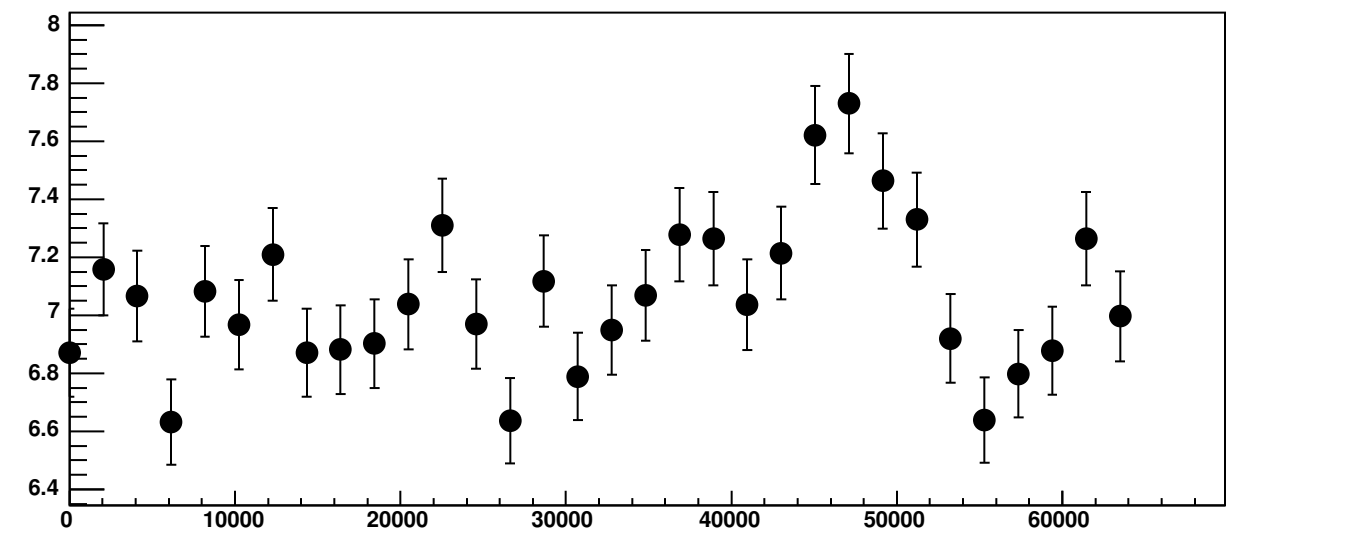

Chip 3, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

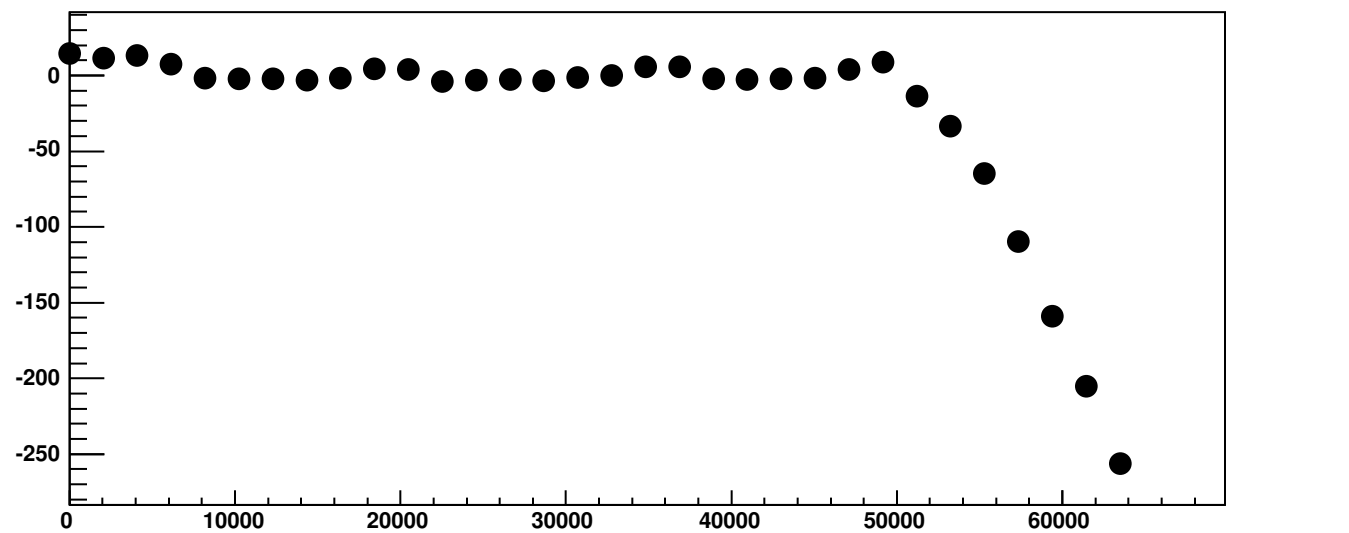
Chip 3, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

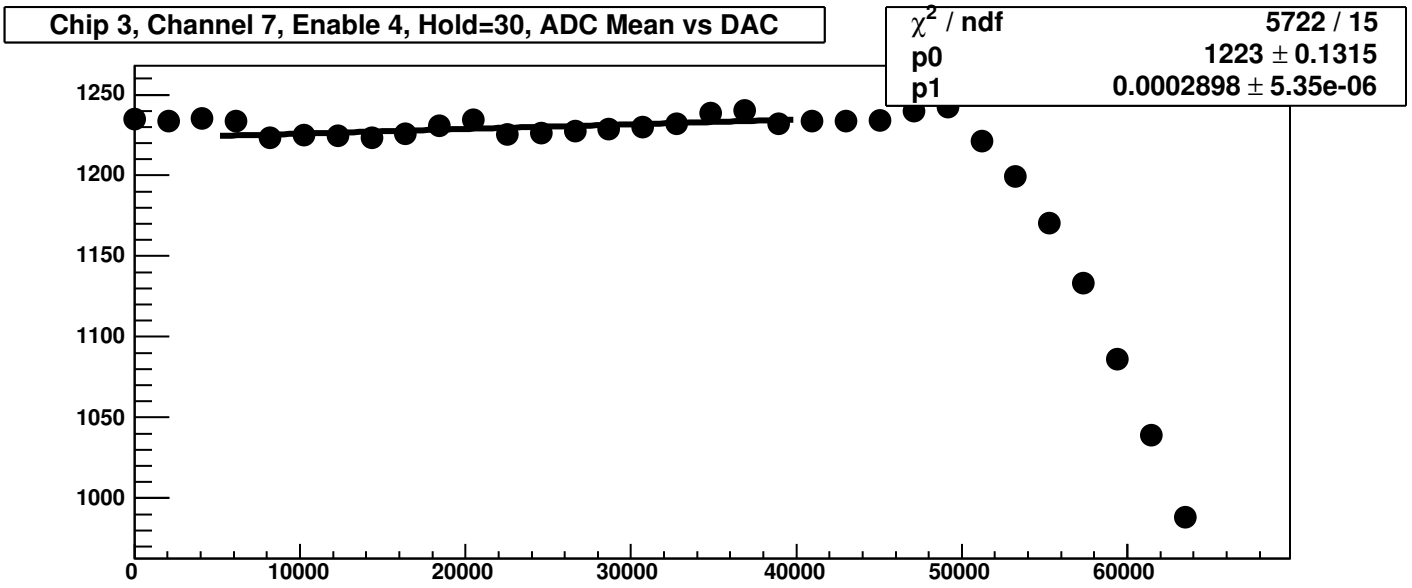


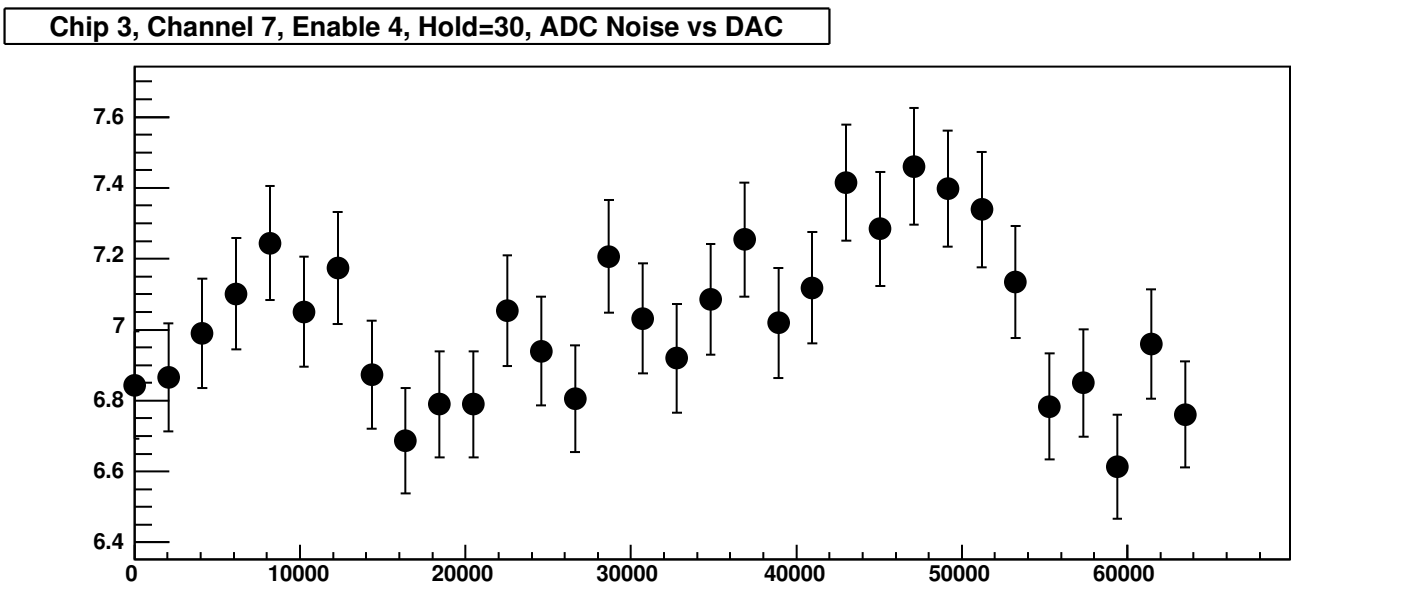
Chip 3, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

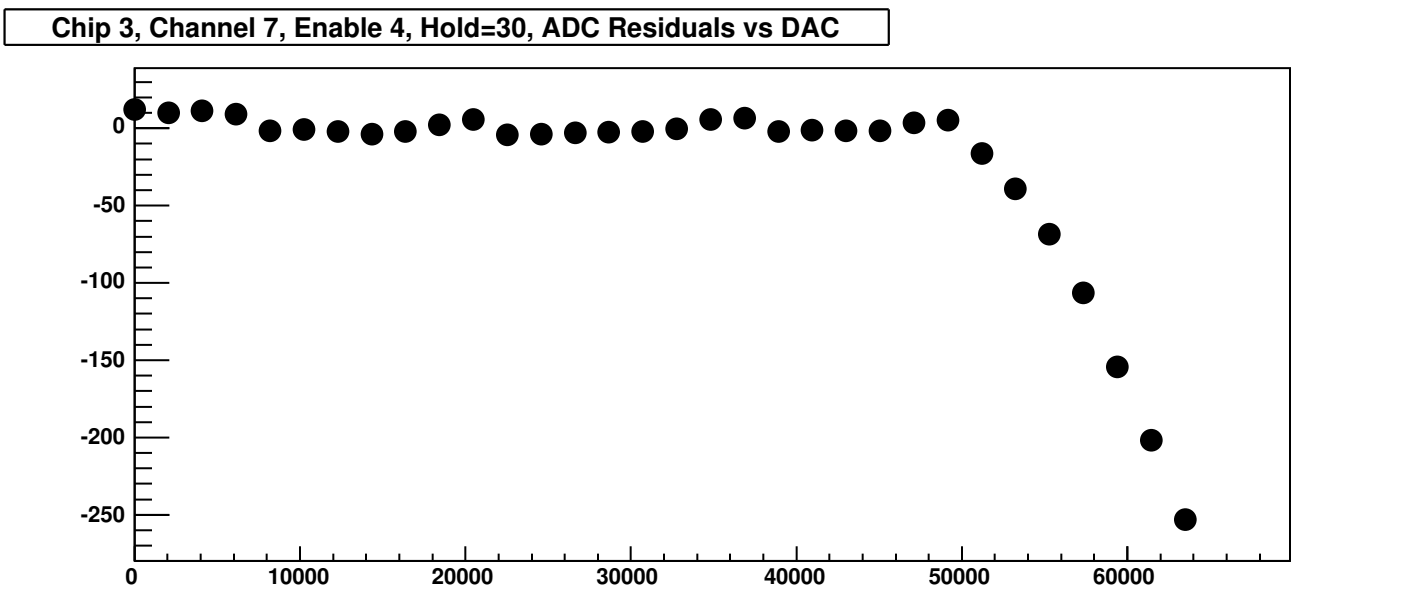


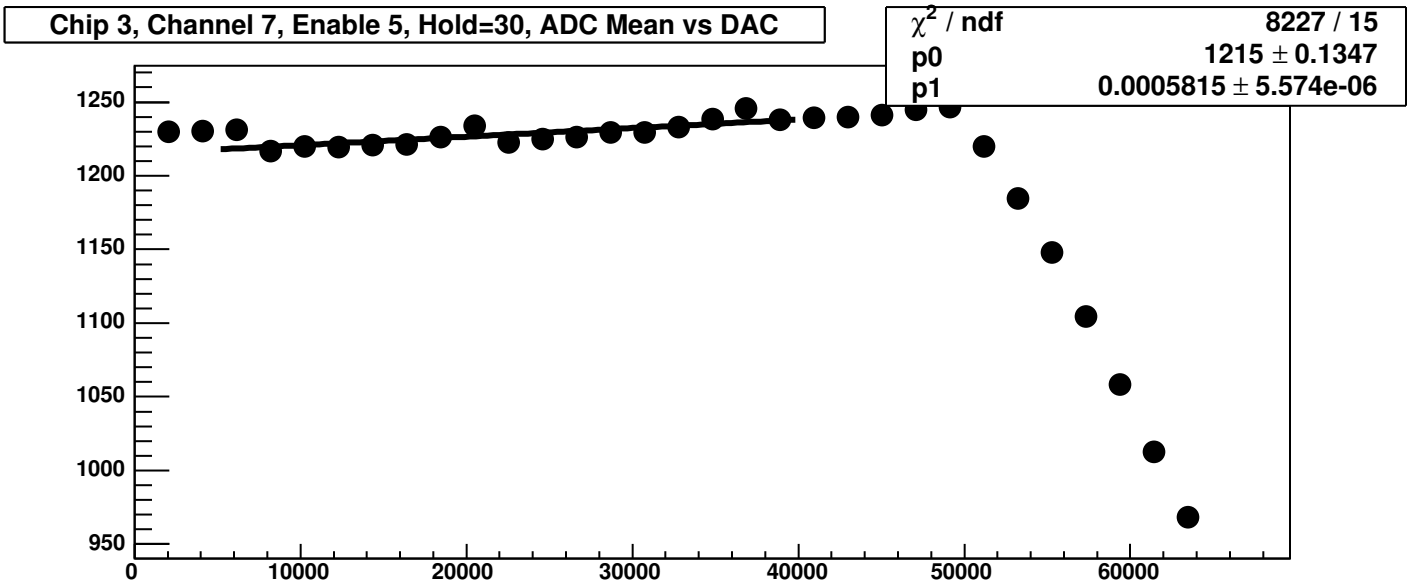
Chip 3, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

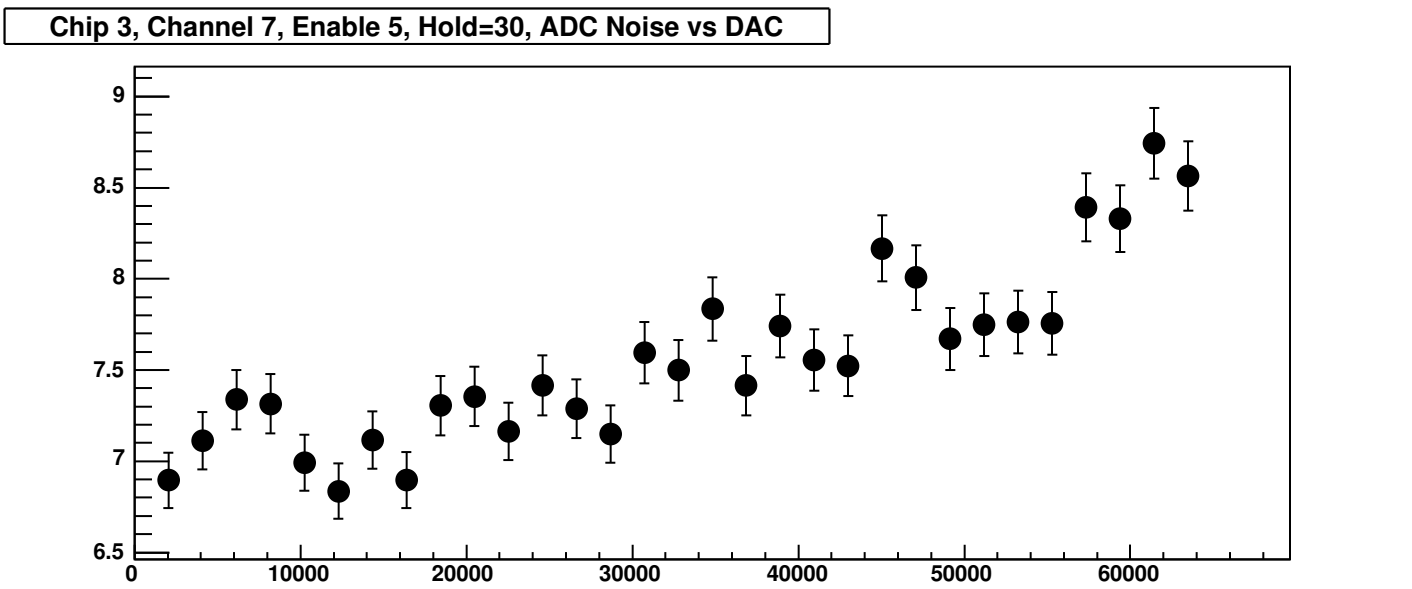


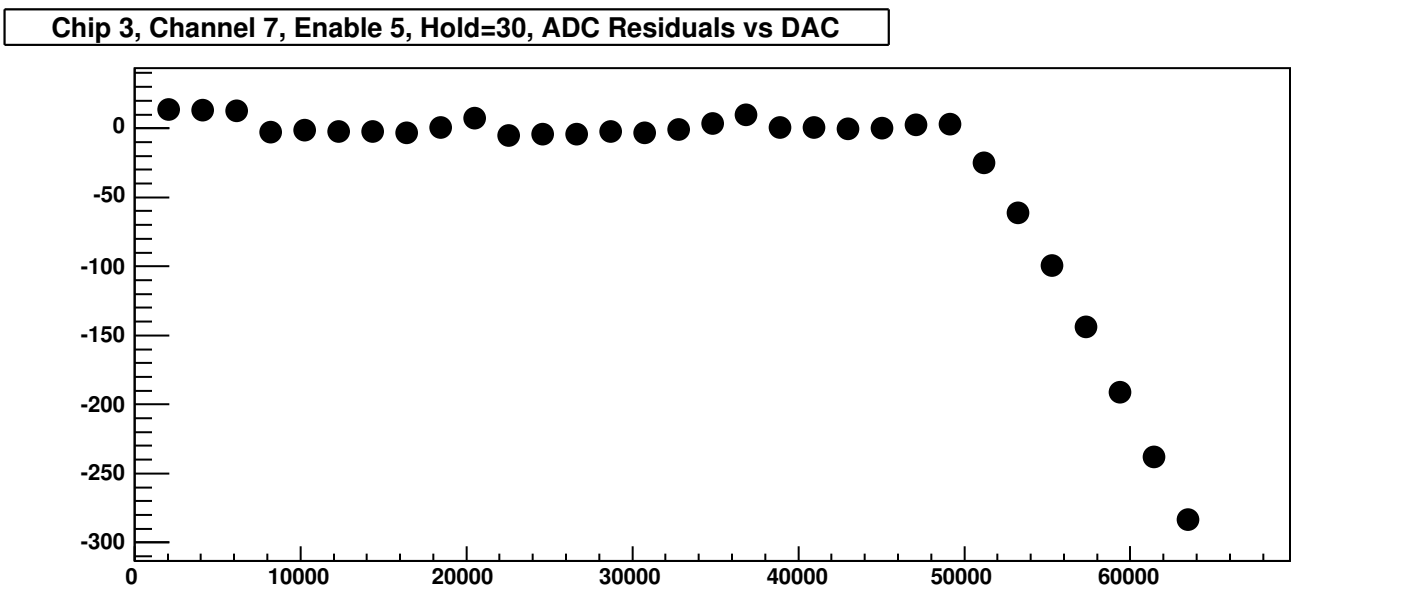

Chip 3, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5722 / 15
p0 1223 ± 0.1315
p1 0.0002898 ± 5.35e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

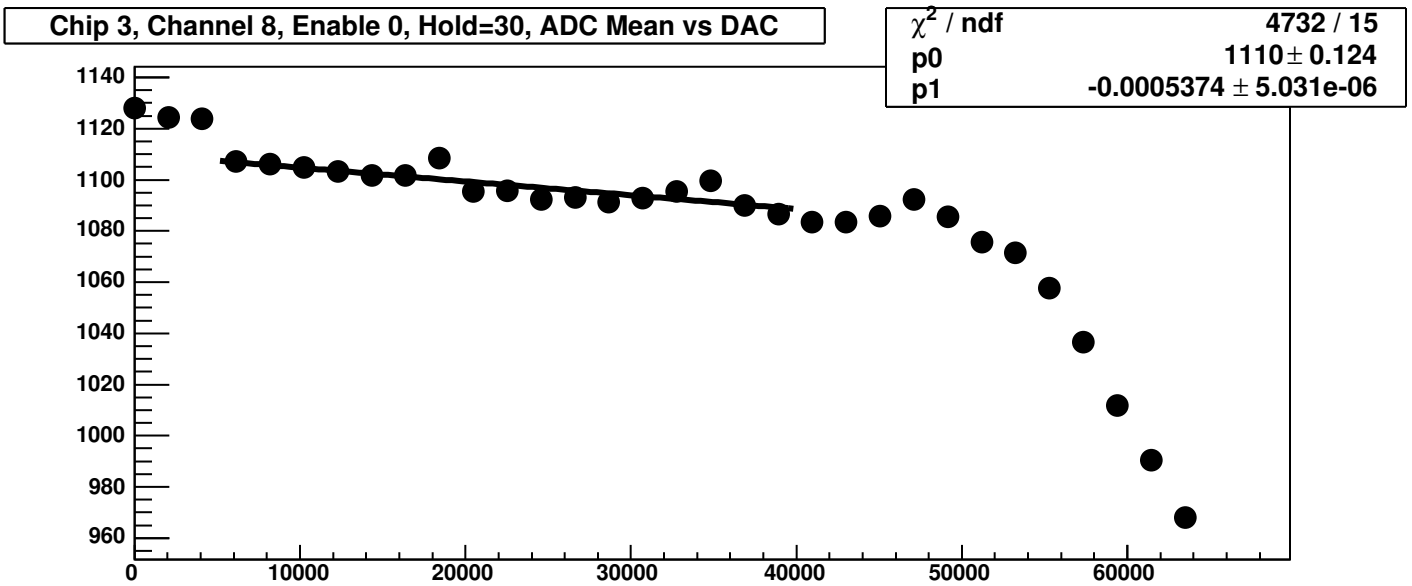

Chip 3, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

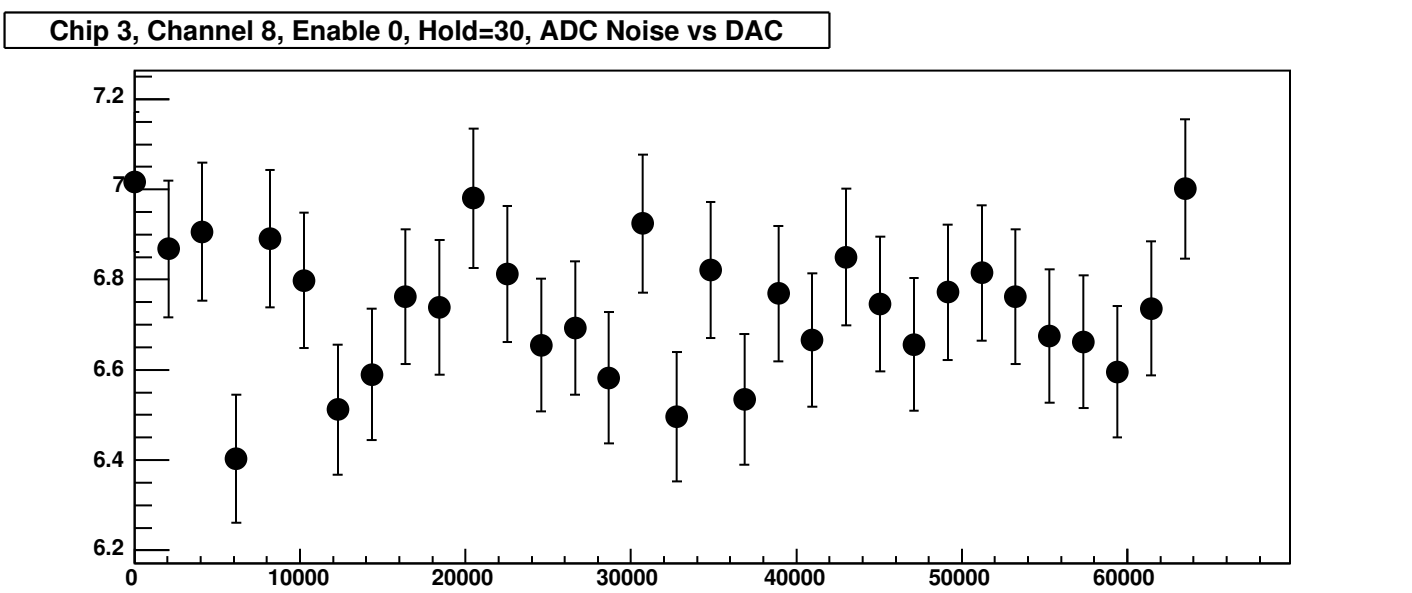

Chip 3, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

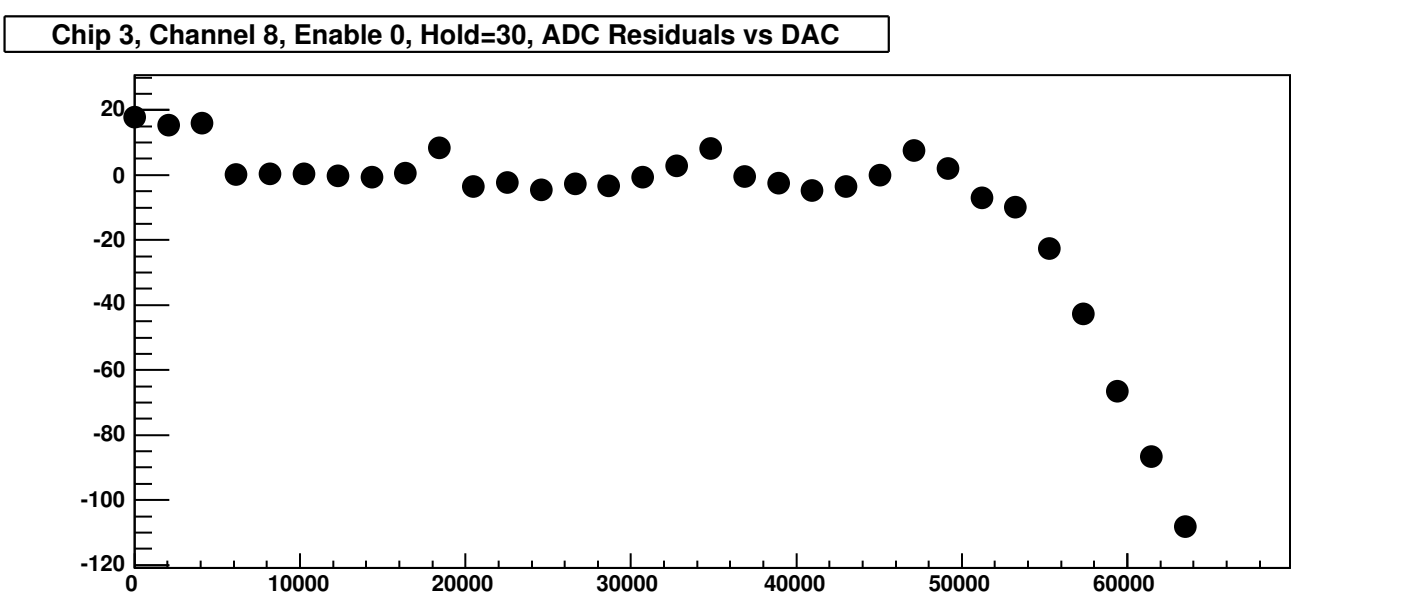

Chip 3, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8227 / 15
p0 1215 ± 0.1347
p1 0.0005815 ± 5.574e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

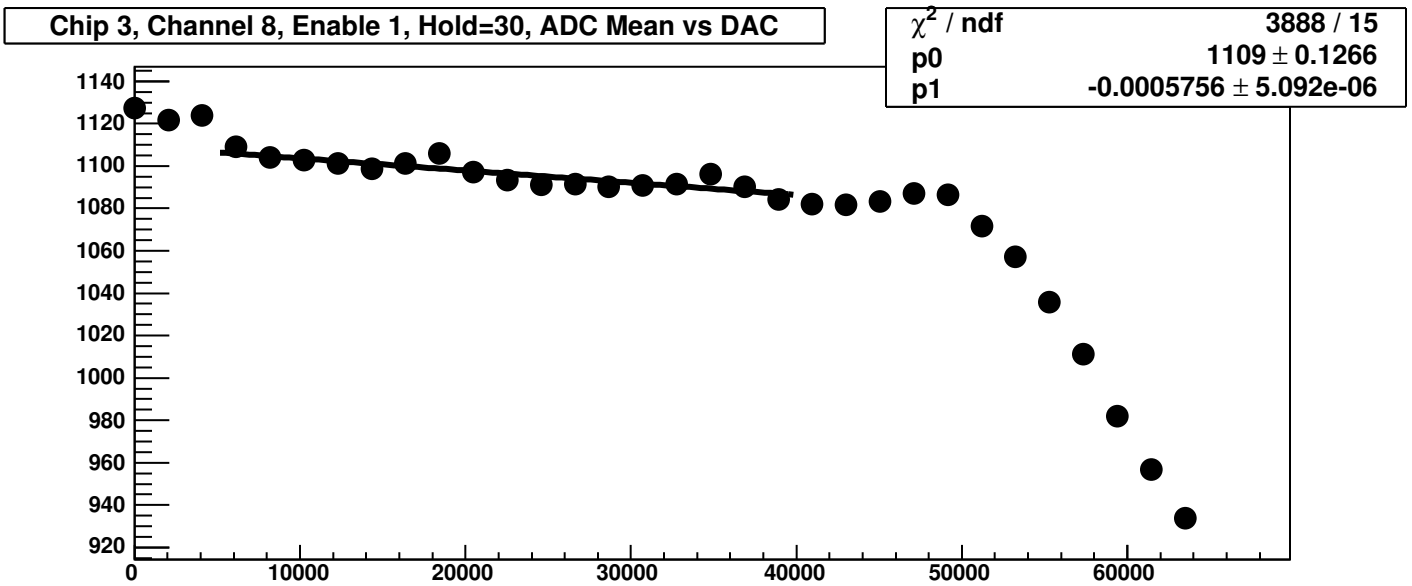

Chip 3, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
9
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000

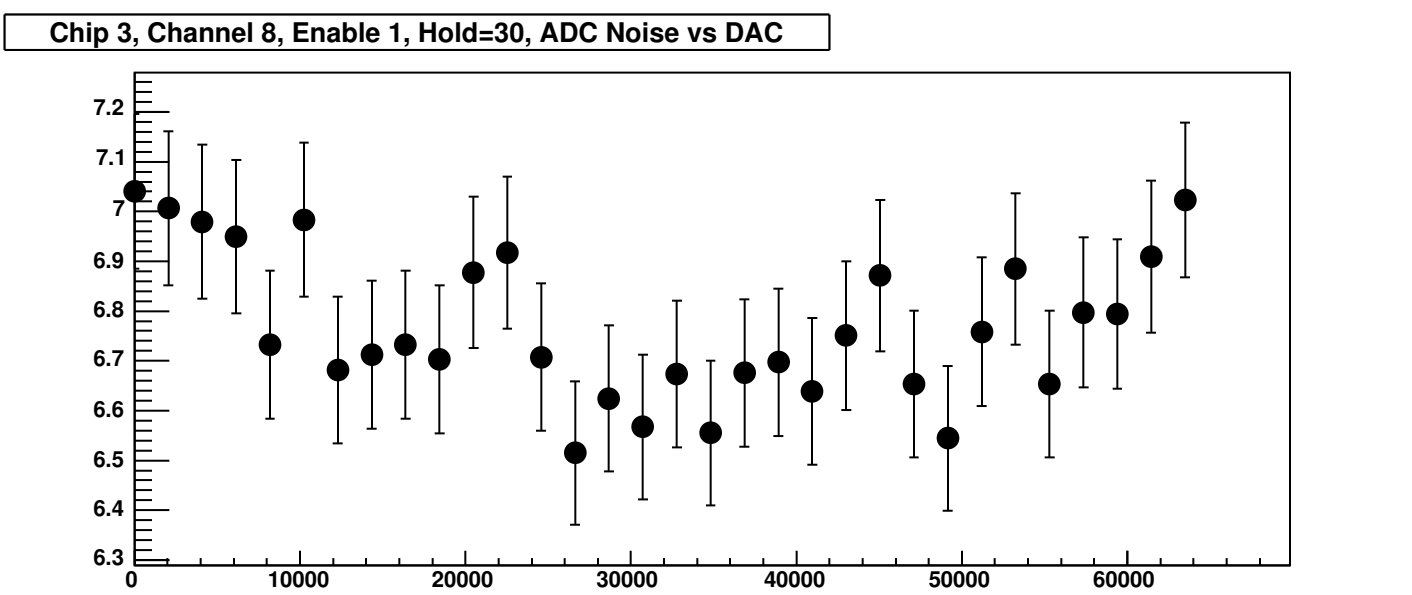

Chip 3, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

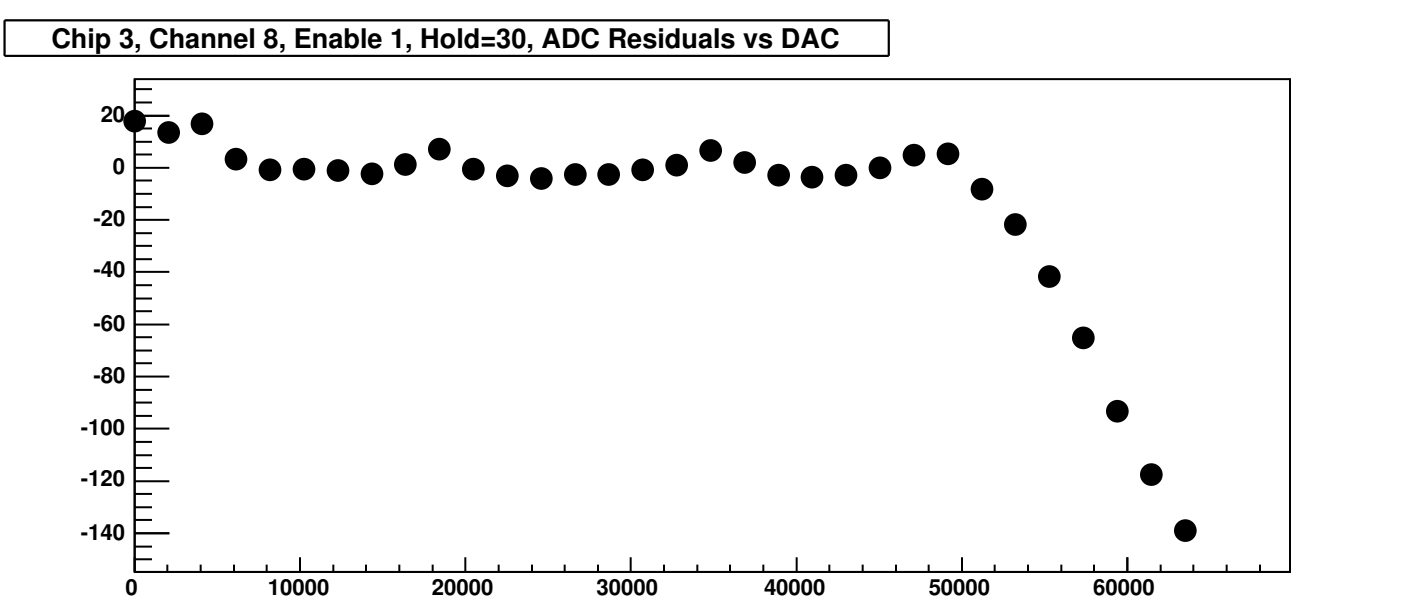

Chip 3, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4732 / 15
p0 1110 ± 0.124
p1 -0.0005374 ± 5.031e-06
1140
1120
1100
1080
1060
1040
1020
1000
980
960
0
10000
20000
30000
40000
50000
60000

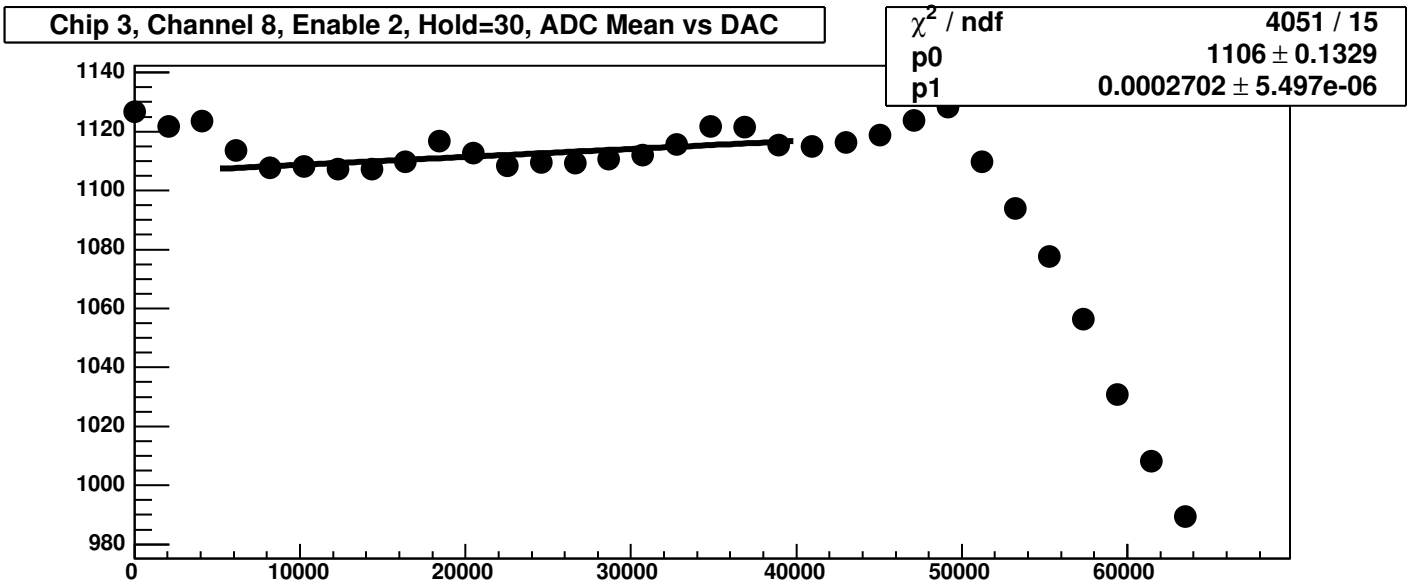

Chip 3, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

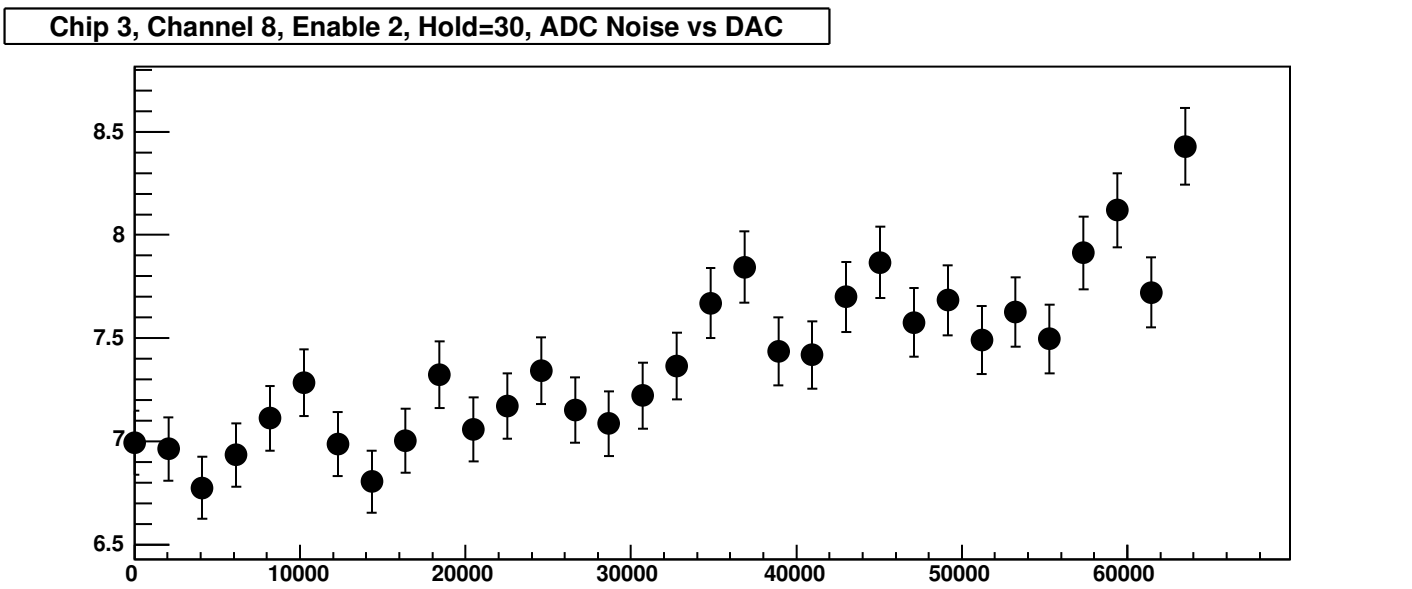

Chip 3, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

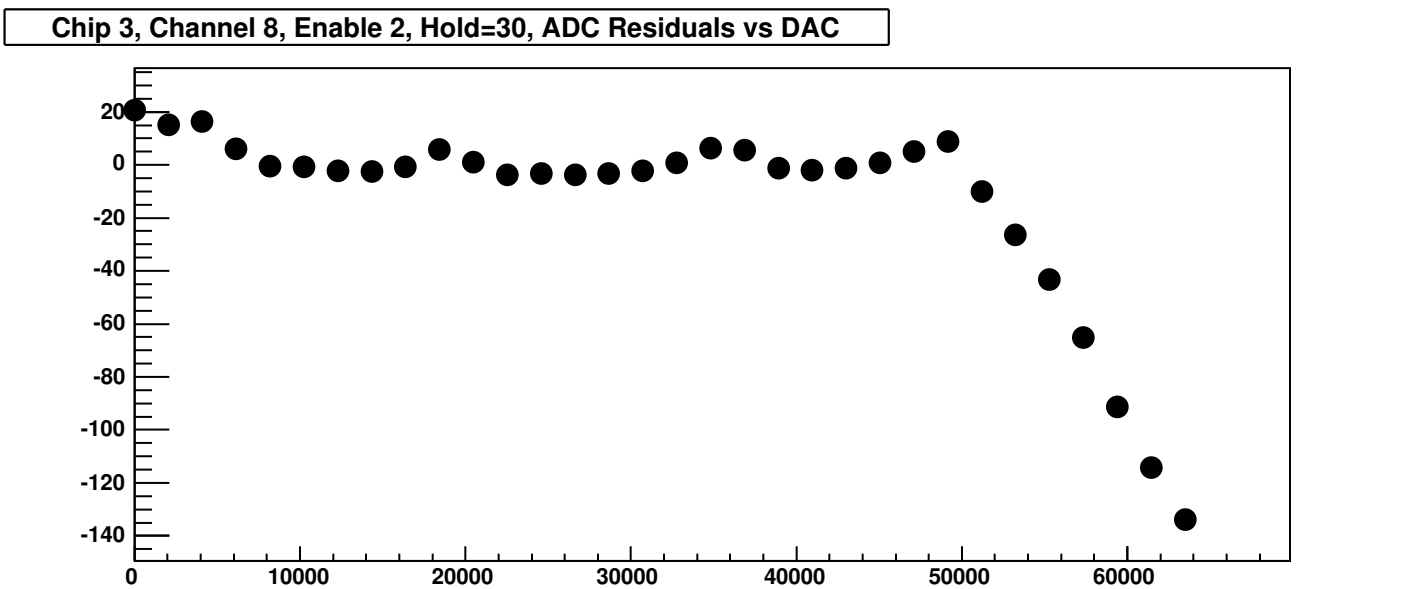

Chip 3, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3888 / 15
p0 1109 ± 0.1266
p1 -0.0005756 ± 5.092e-06
1140
1120
1100
1080
1060
1040
1020
1000
980
960
940
920
0
10000
20000
30000
40000
50000
60000

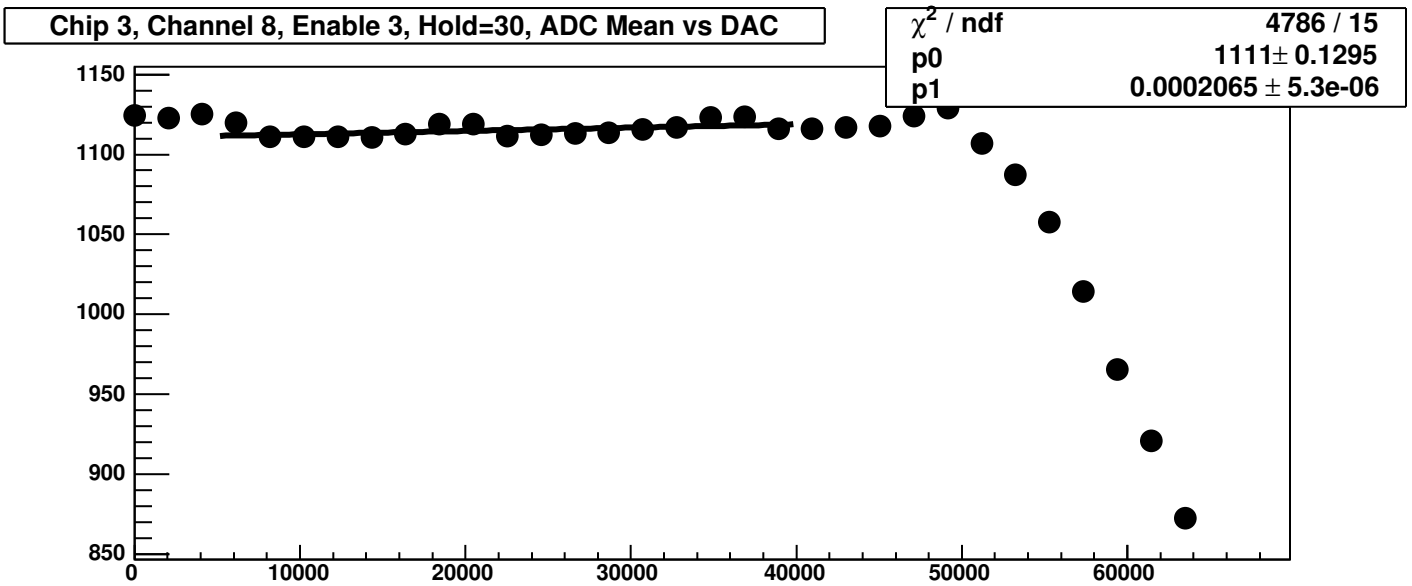

Chip 3, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7.1
7
6.9
6.8
6.7
6.6
6.5
6.4
6.3
0
10000
20000
30000
40000
50000
60000

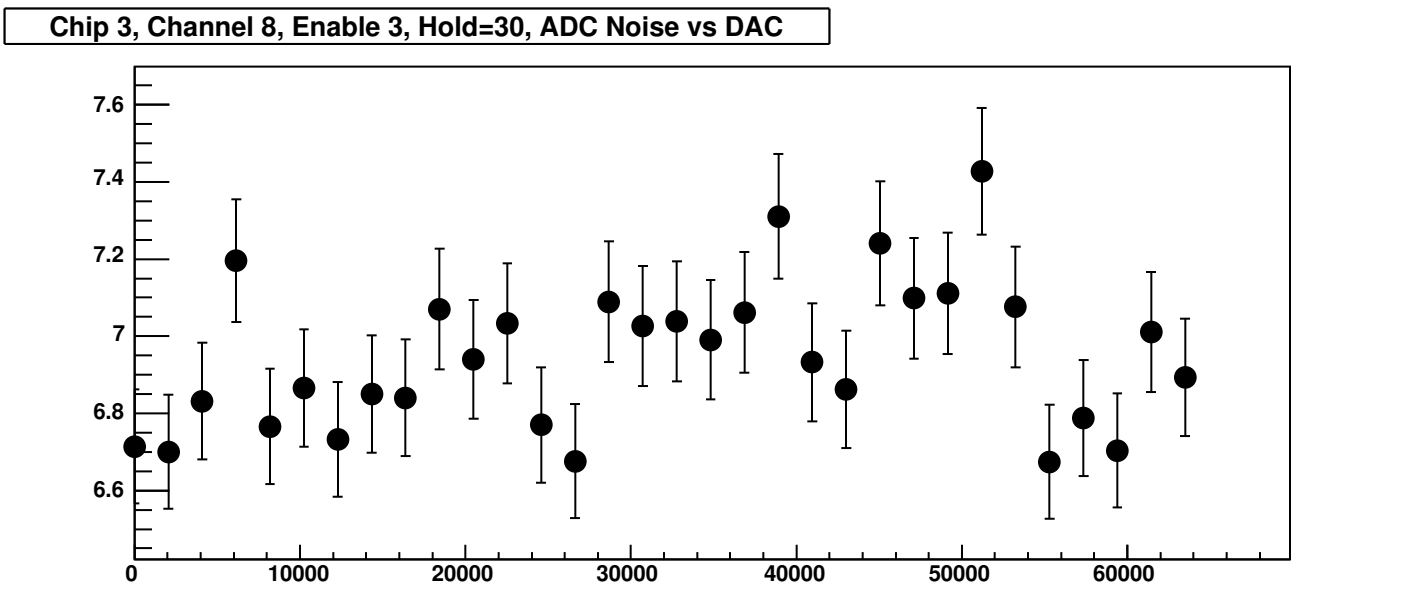

Chip 3, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

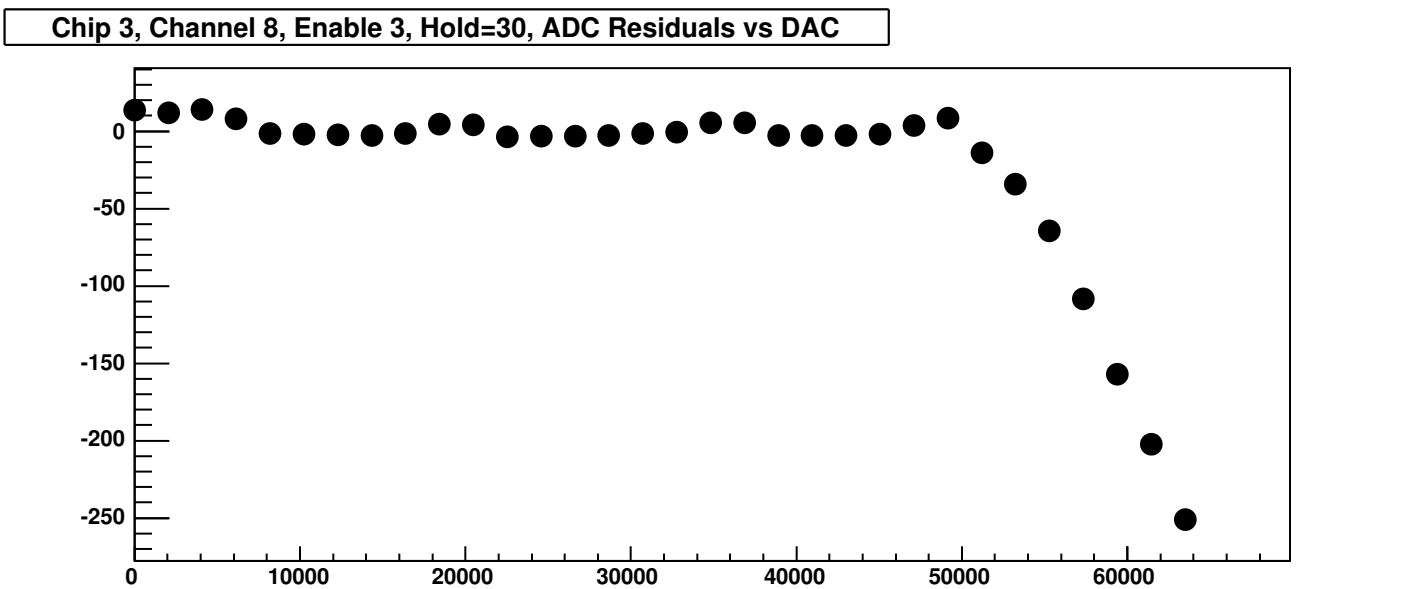

Chip 3, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4051 / 15
p0 1106 ± 0.1329
p1 0.0002702 ± 5.497e-06
1140
1120
1100
1080
1060
1040
1020
1000
980
0
10000
20000
30000
40000
50000
60000

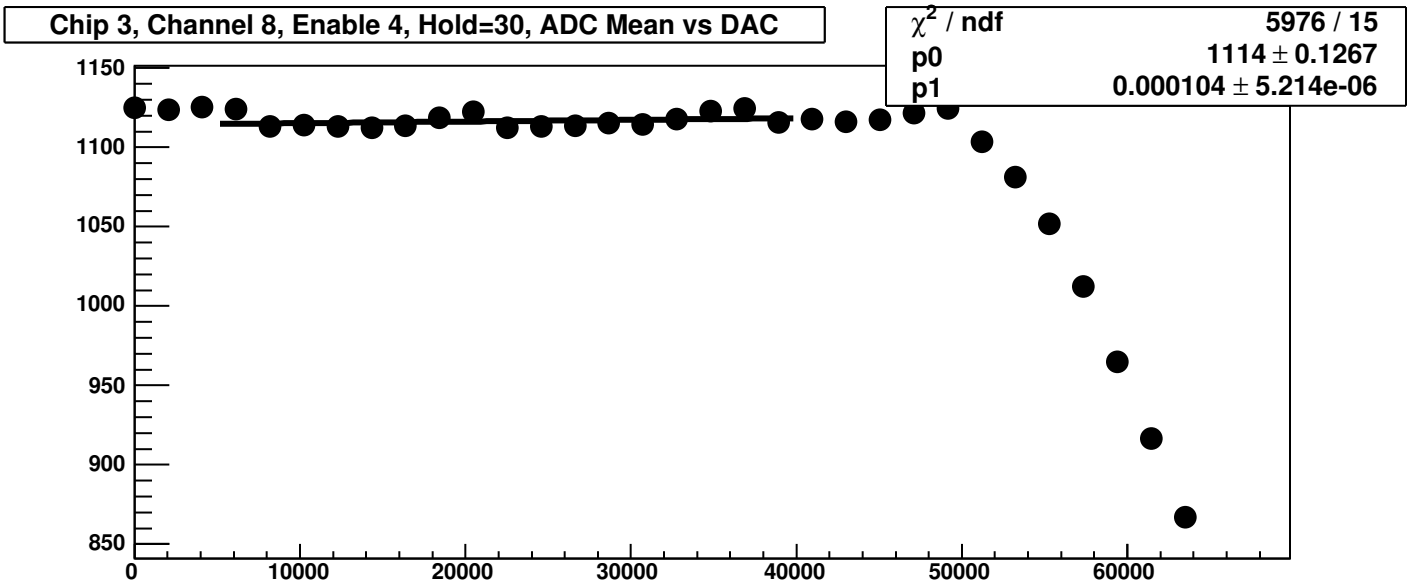

Chip 3, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000

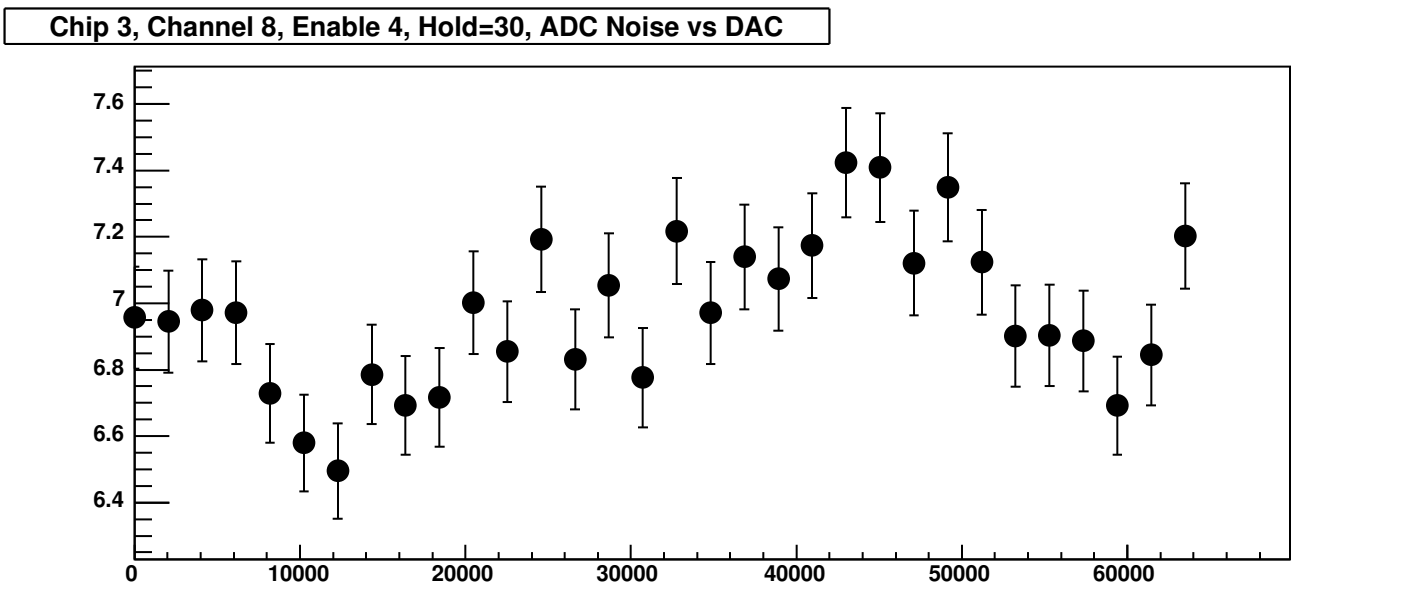

Chip 3, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

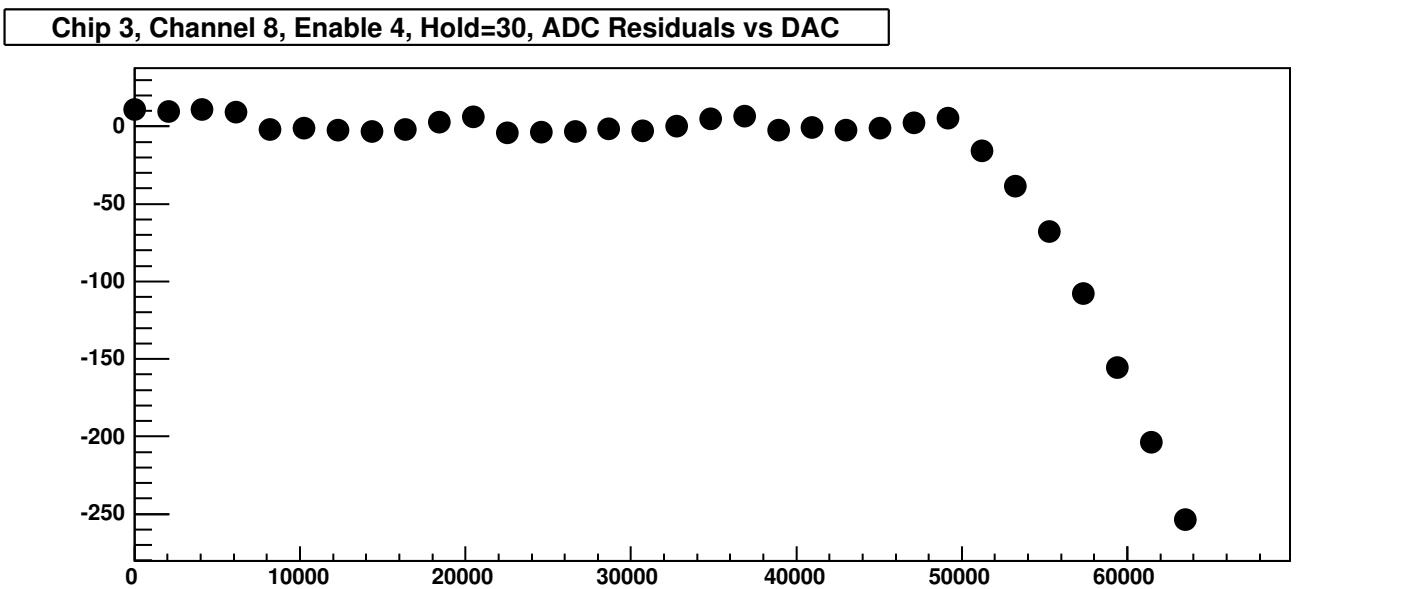

Chip 3, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4786 / 15
p0 1111 ± 0.1295
p1 0.0002065 ± 5.3e-06
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

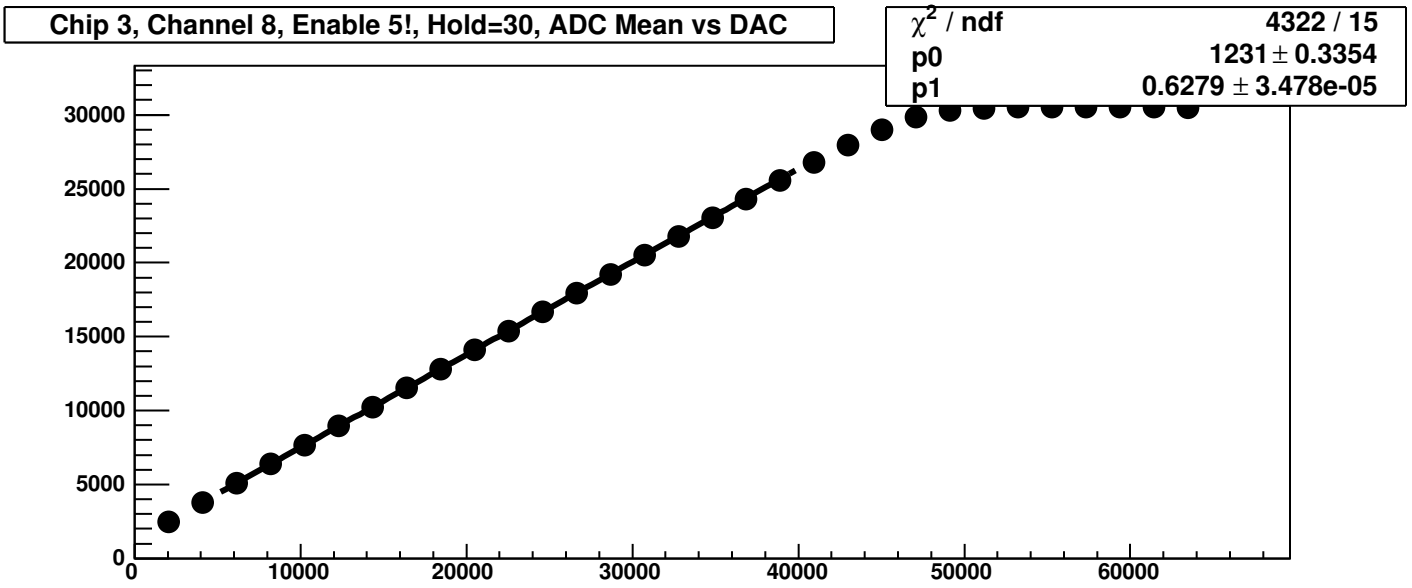

Chip 3, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

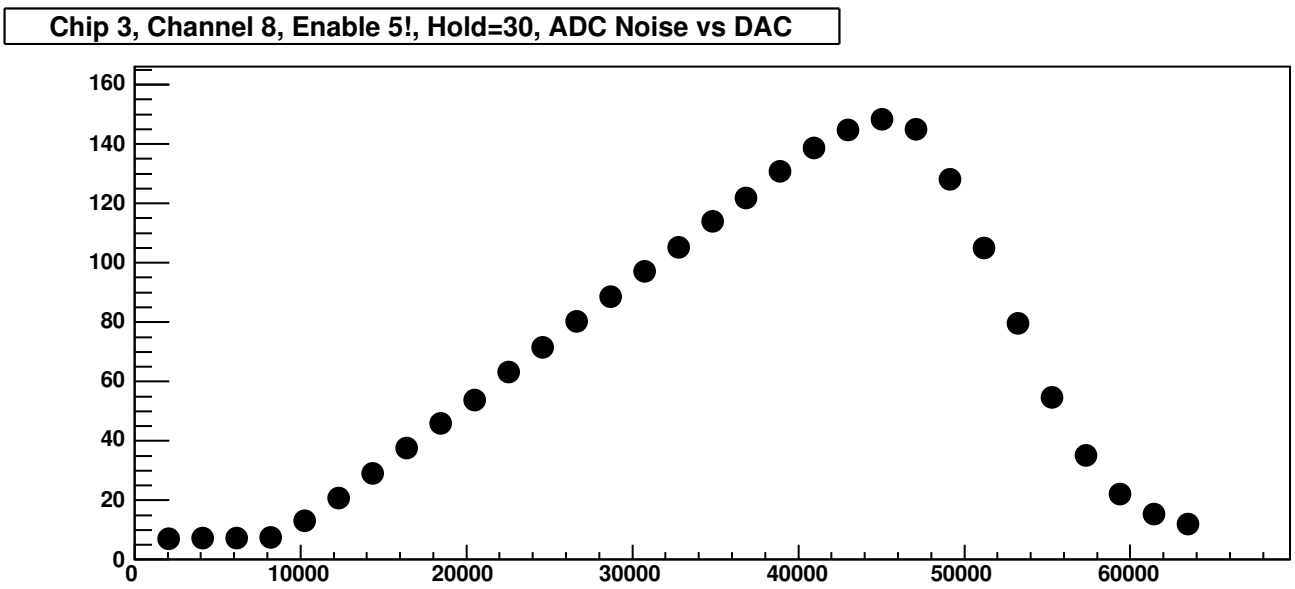

Chip 3, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

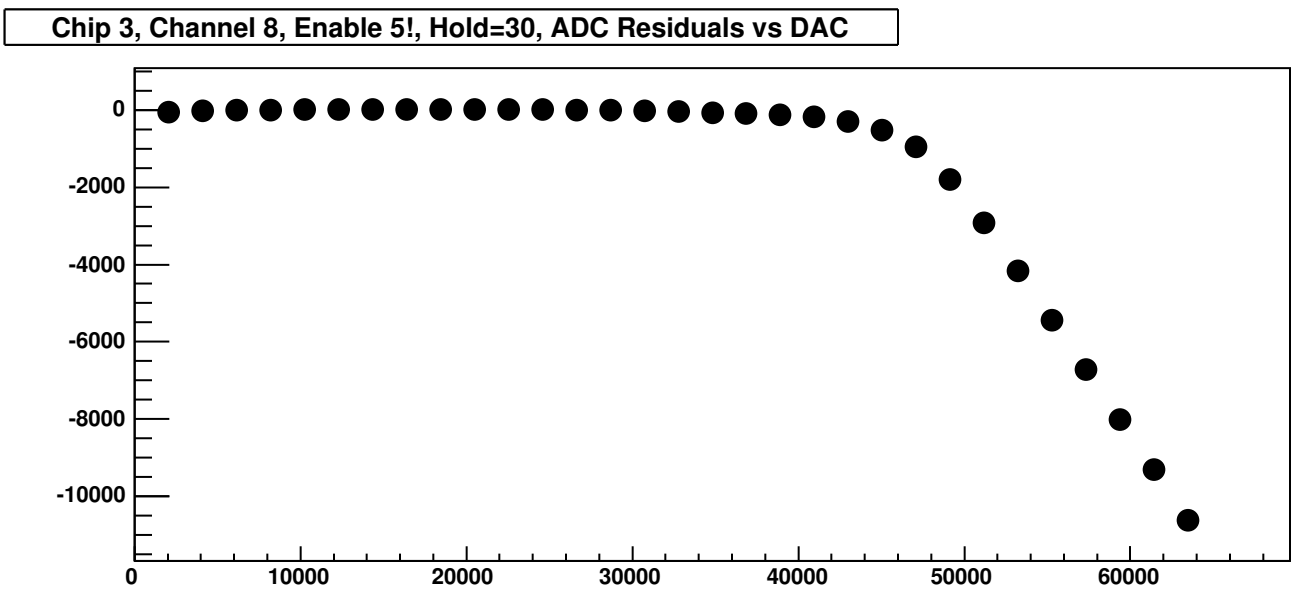

Chip 3, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5976 / 15
p0 1114 ± 0.1267
p1 0.000104 ± 5.214e-06
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

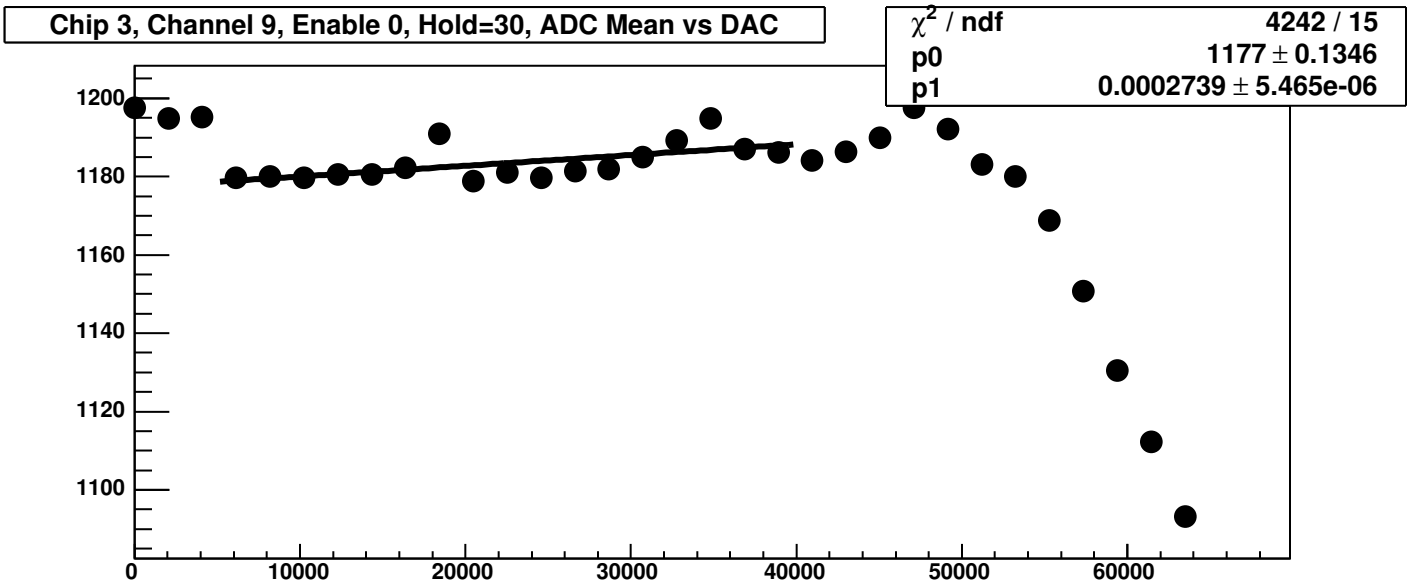

Chip 3, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

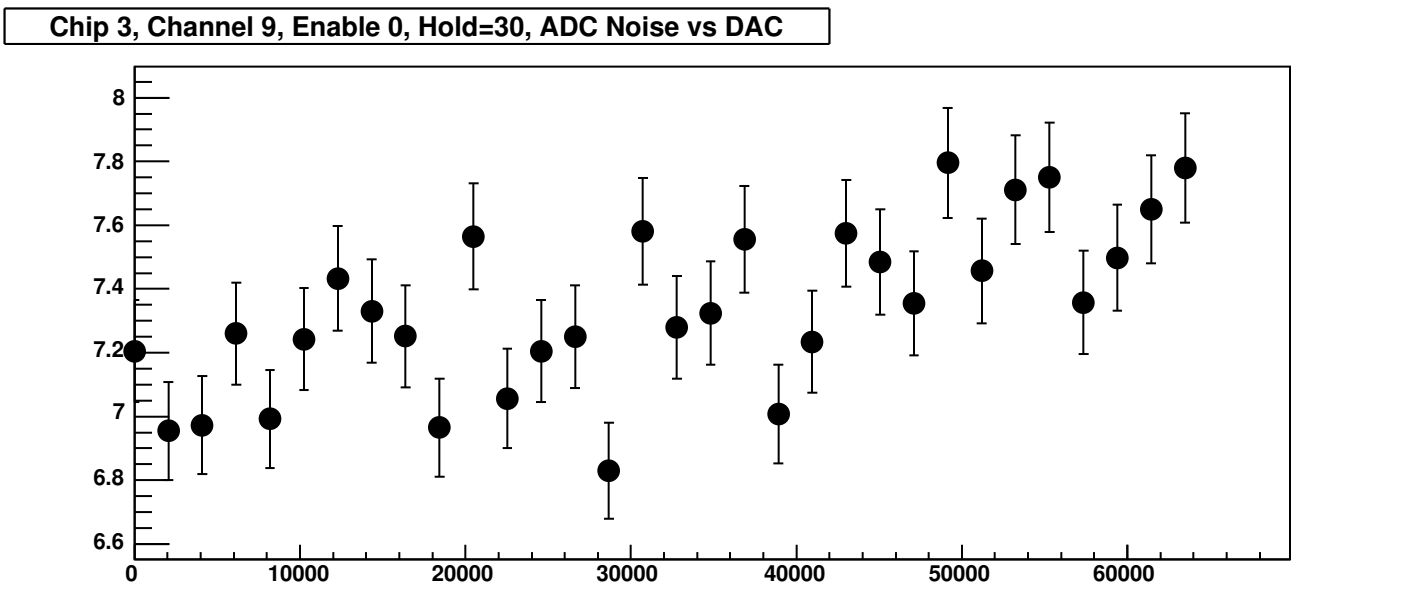

Chip 3, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

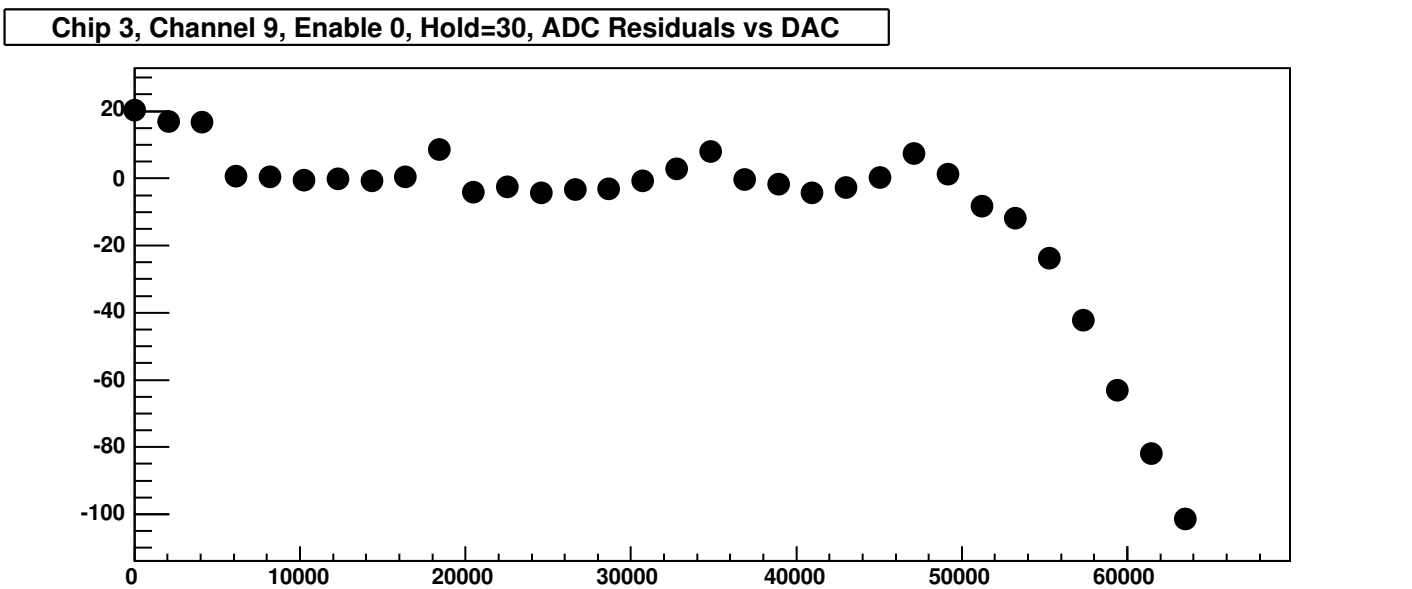

Chip 3, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 4322 / 15
p0 1231 ± 0.3354
p1 0.6279 ± 3.478e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

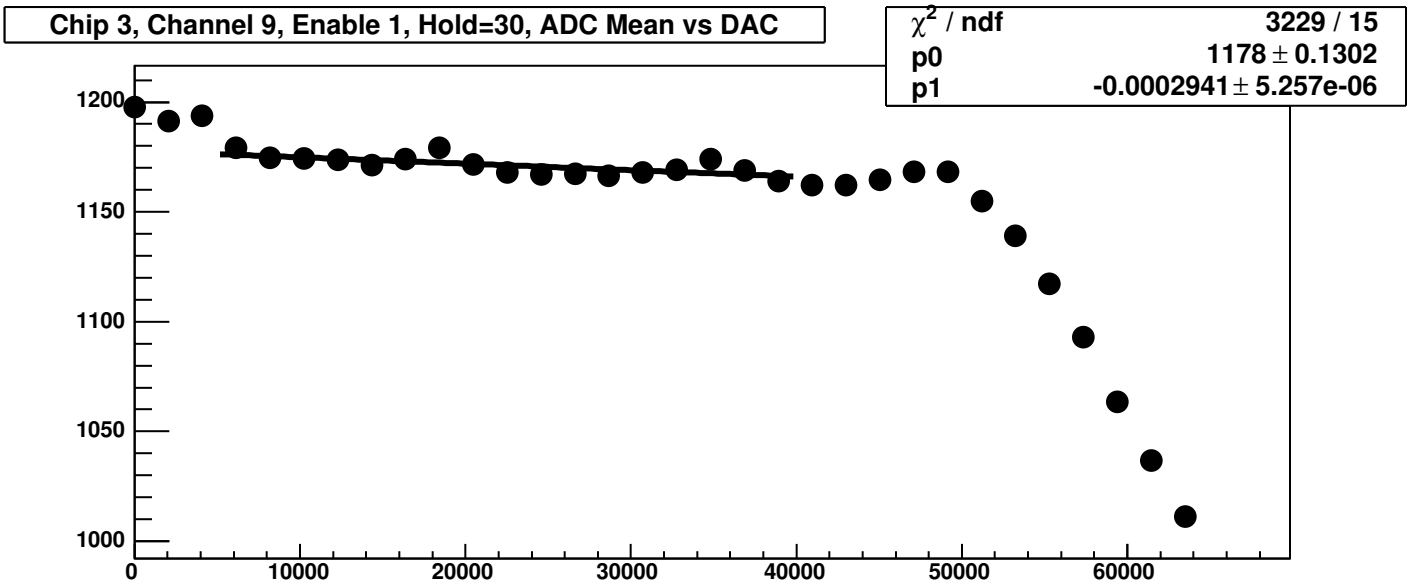

Chip 3, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

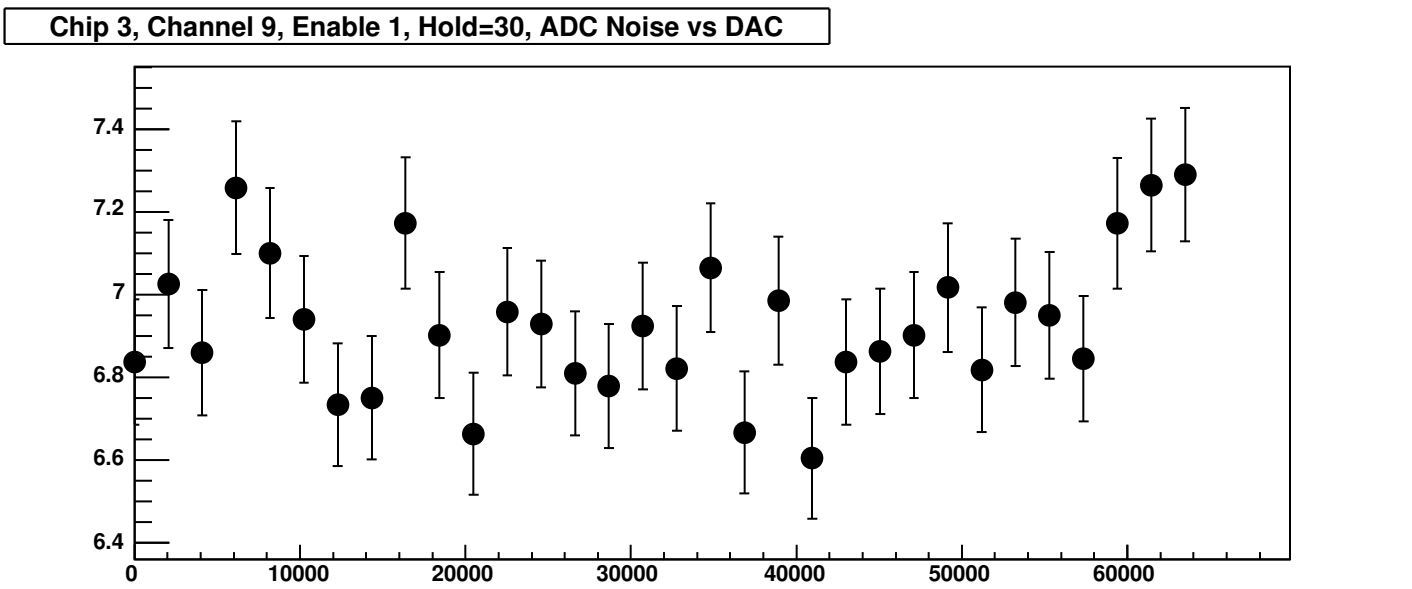

Chip 3, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

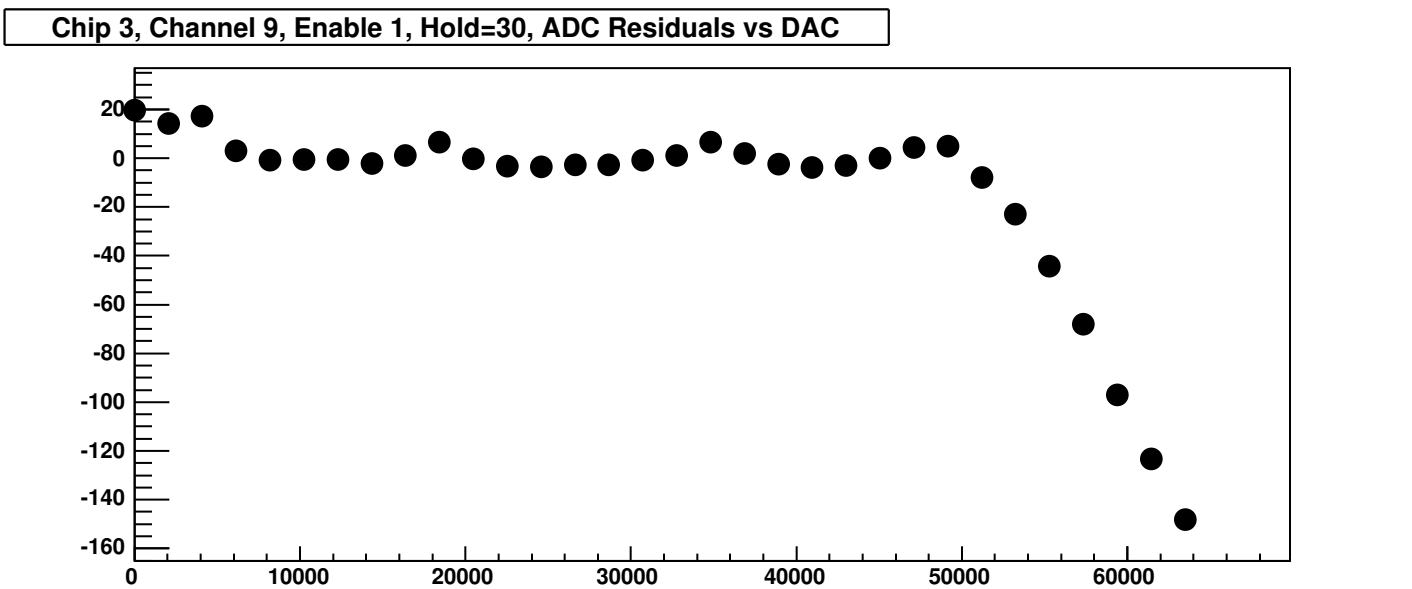

Chip 3, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4242 / 15
p0 1177 ± 0.1346
p1 0.0002739 ± 5.465e-06
1200
1180
1160
1140
1120
1100
0
10000
20000
30000
40000
50000
60000

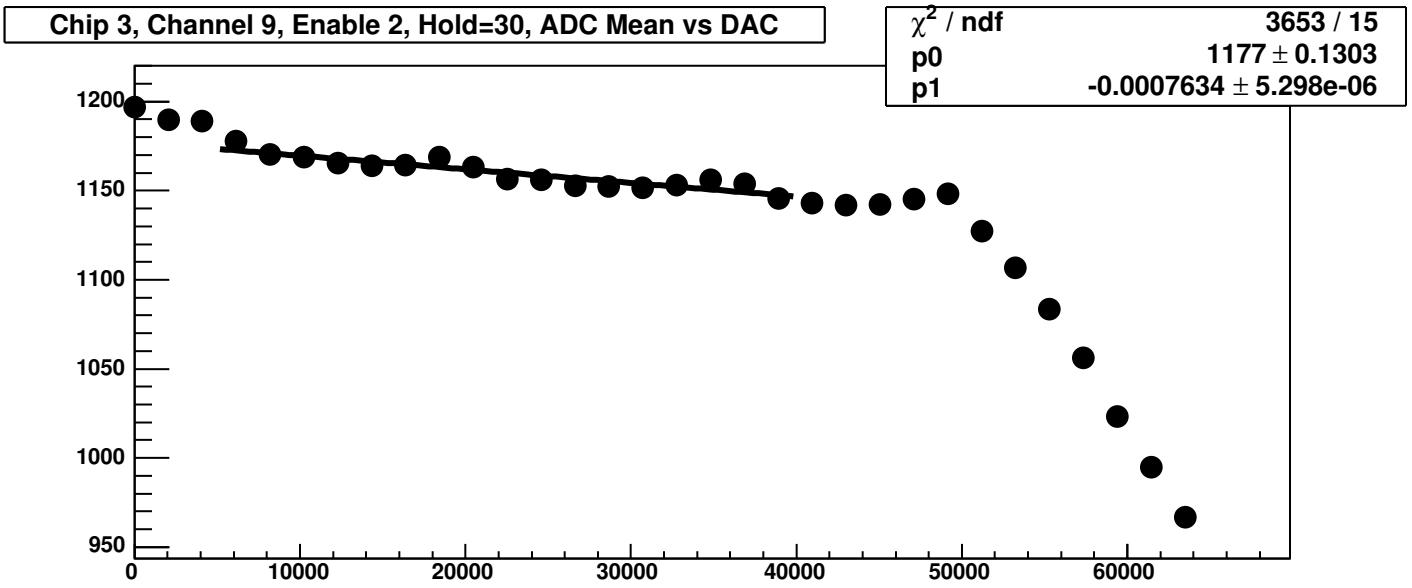

Chip 3, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
8
7.8
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

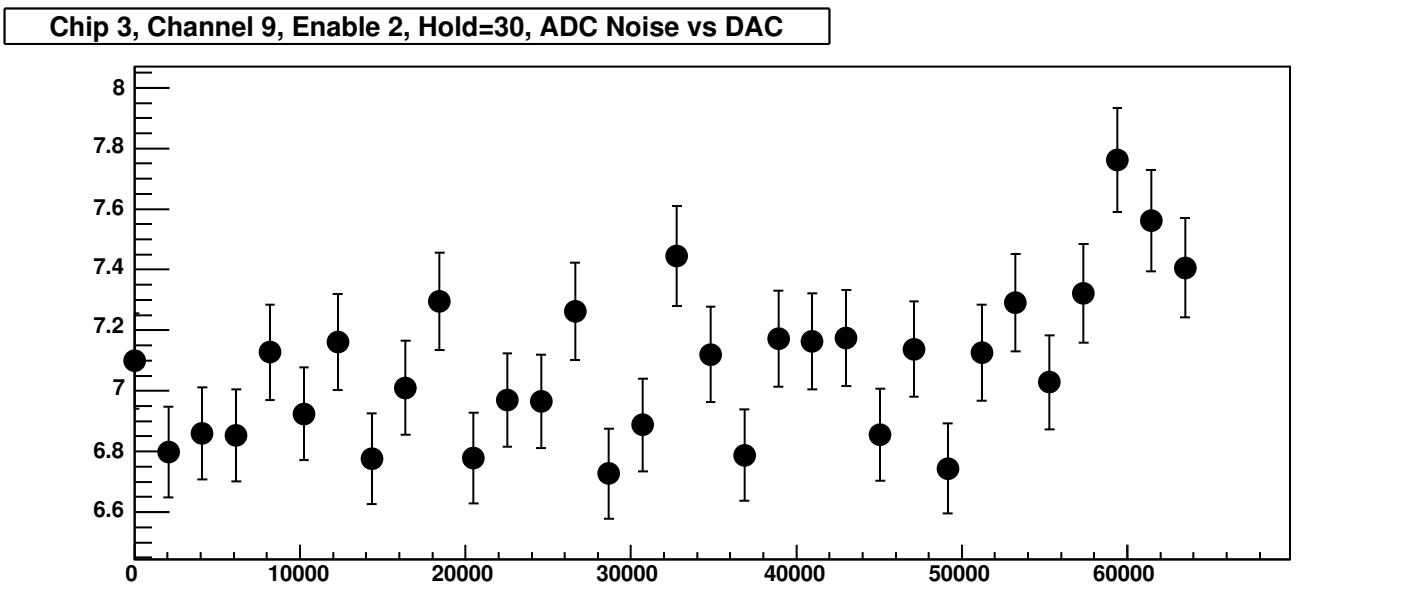

Chip 3, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

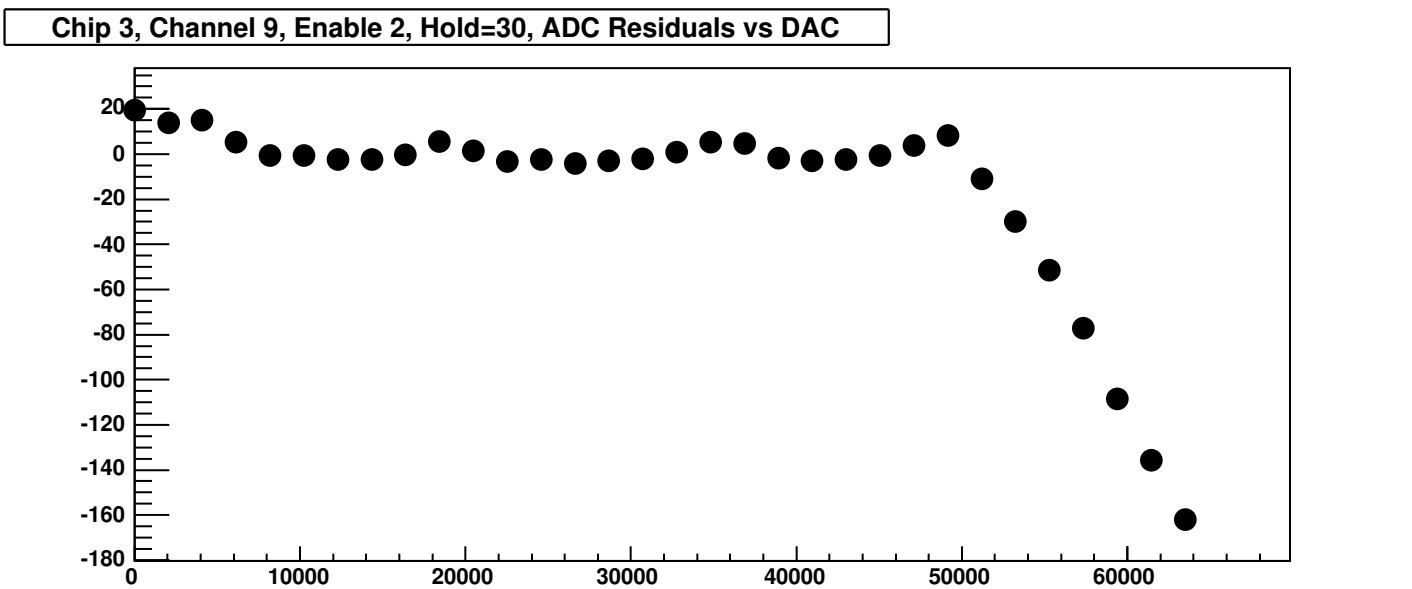

Chip 3, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3229 / 15
p0 1178 ± 0.1302
p1 -0.0002941 ± 5.257e-06
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

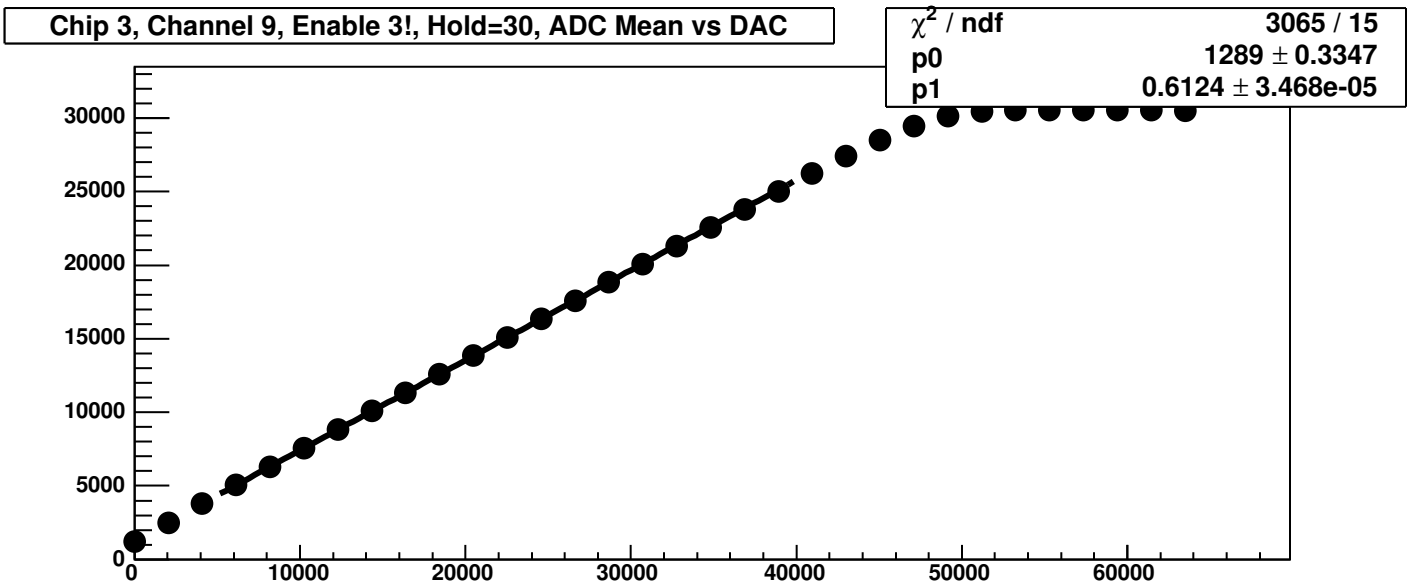

Chip 3, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

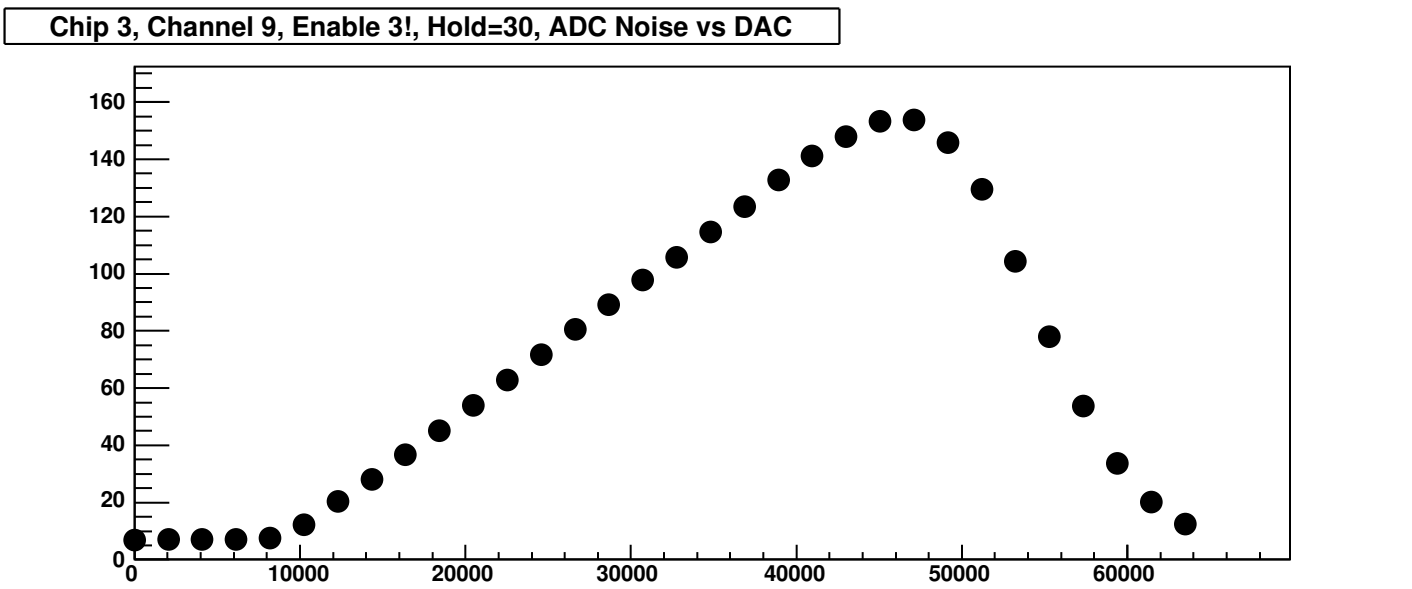

Chip 3, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

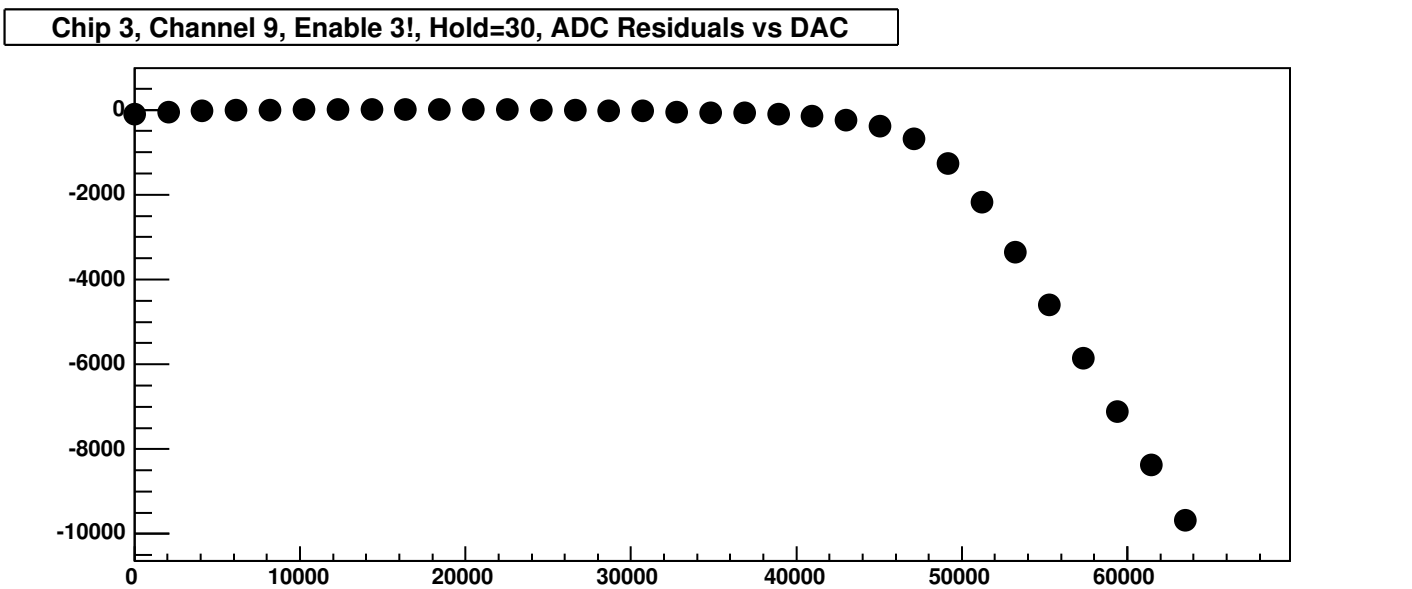

Chip 3, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3653 / 15
p0 1177 ± 0.1303
p1 -0.0007634 ± 5.298e-06
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

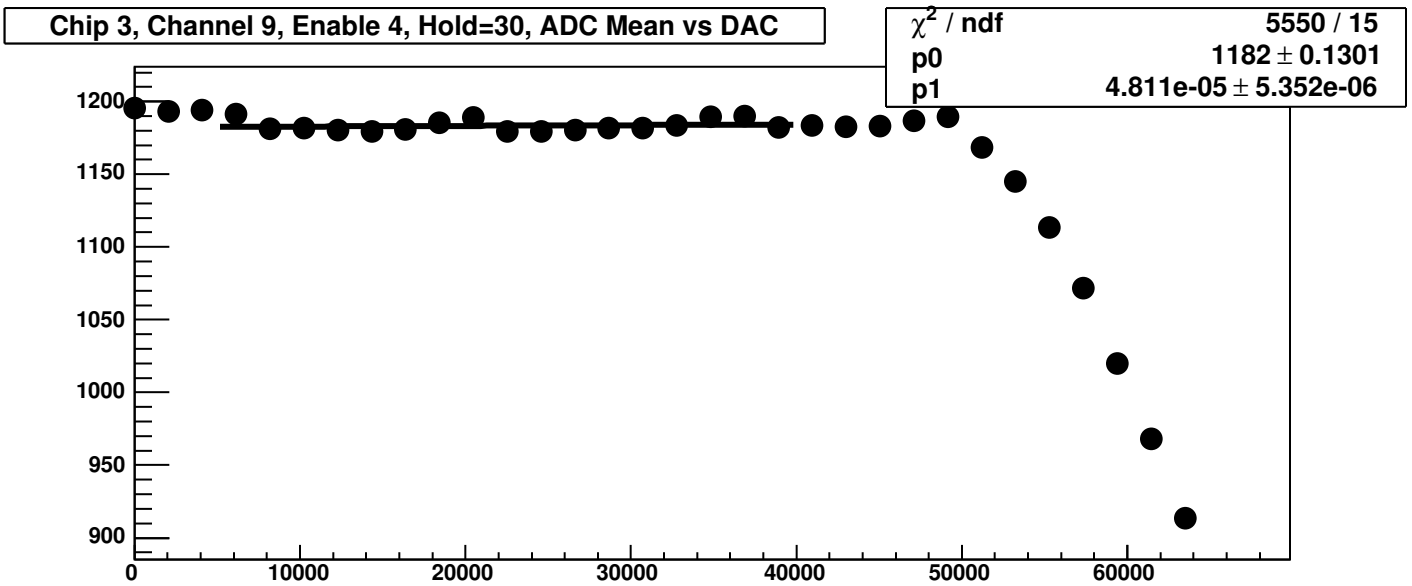

Chip 3, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
8
7.8
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

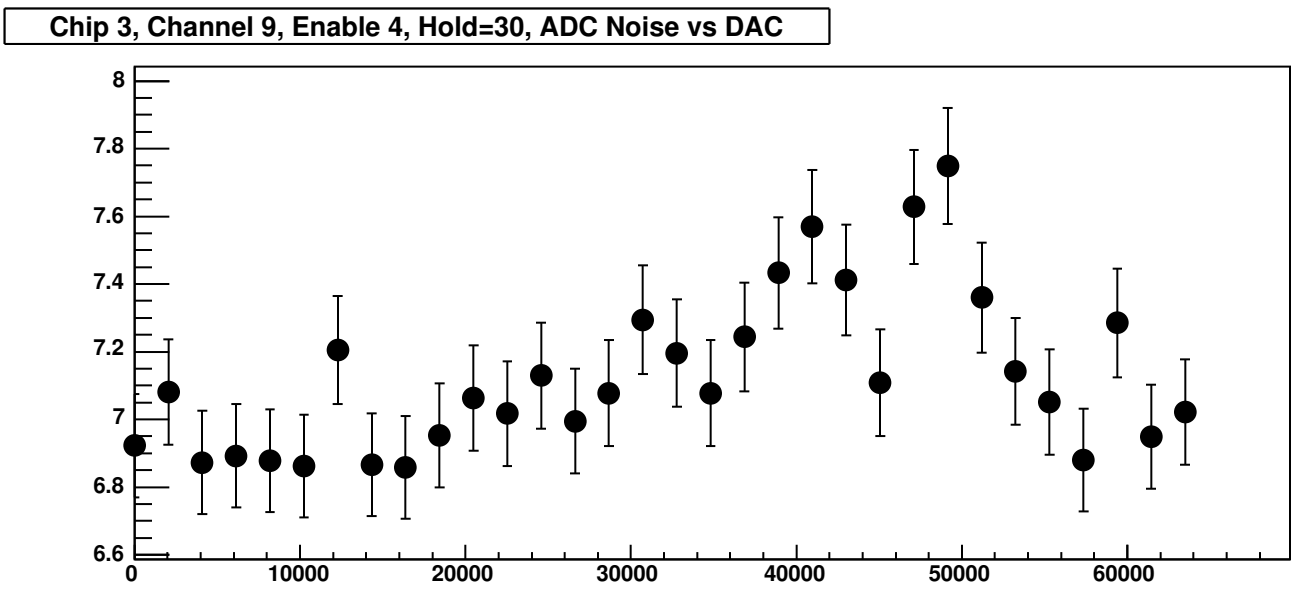

Chip 3, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

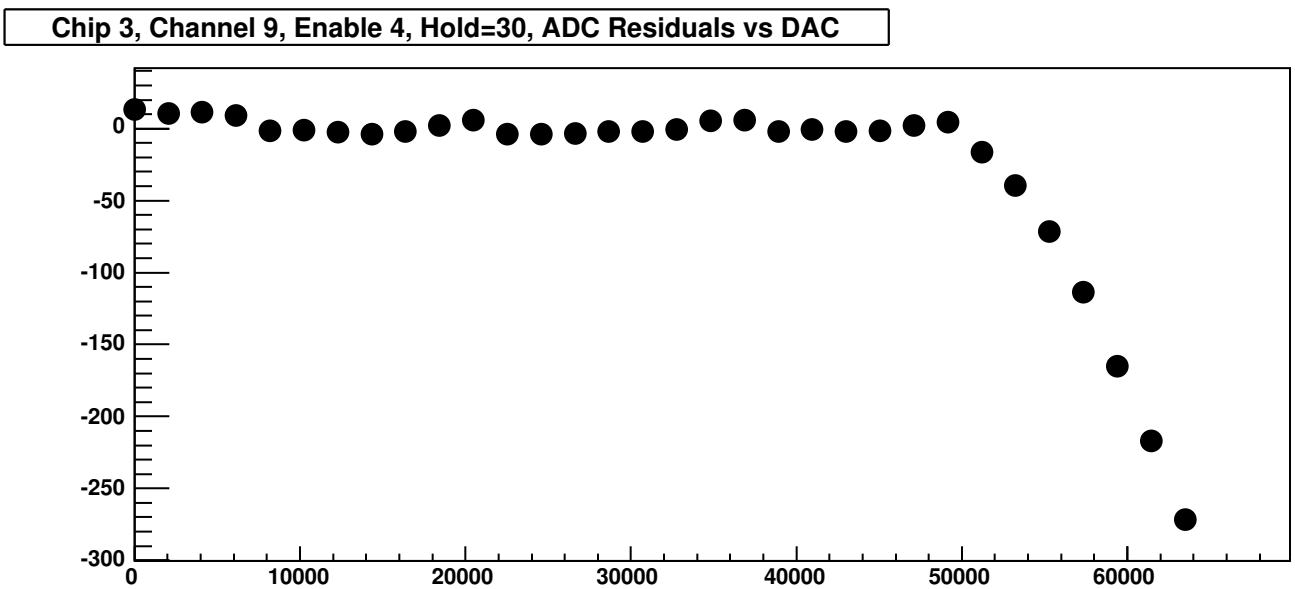

Chip 3, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 3065 / 15
p0 1289 ± 0.3347
p1 0.6124 ± 3.468e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

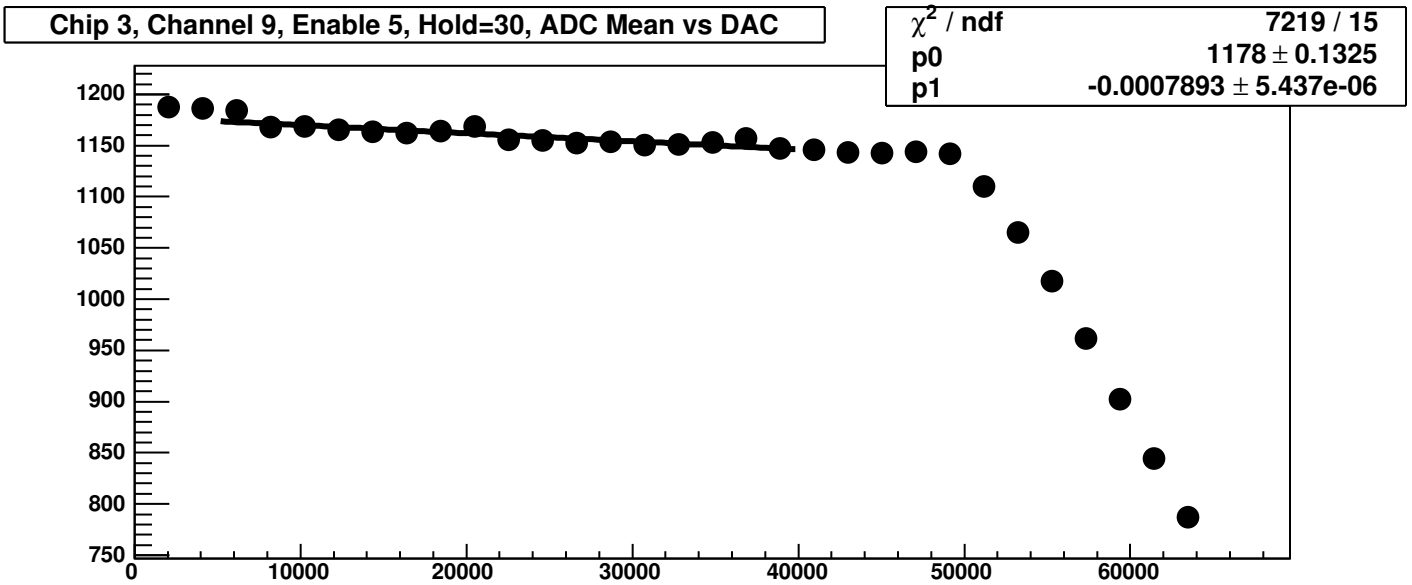

Chip 3, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

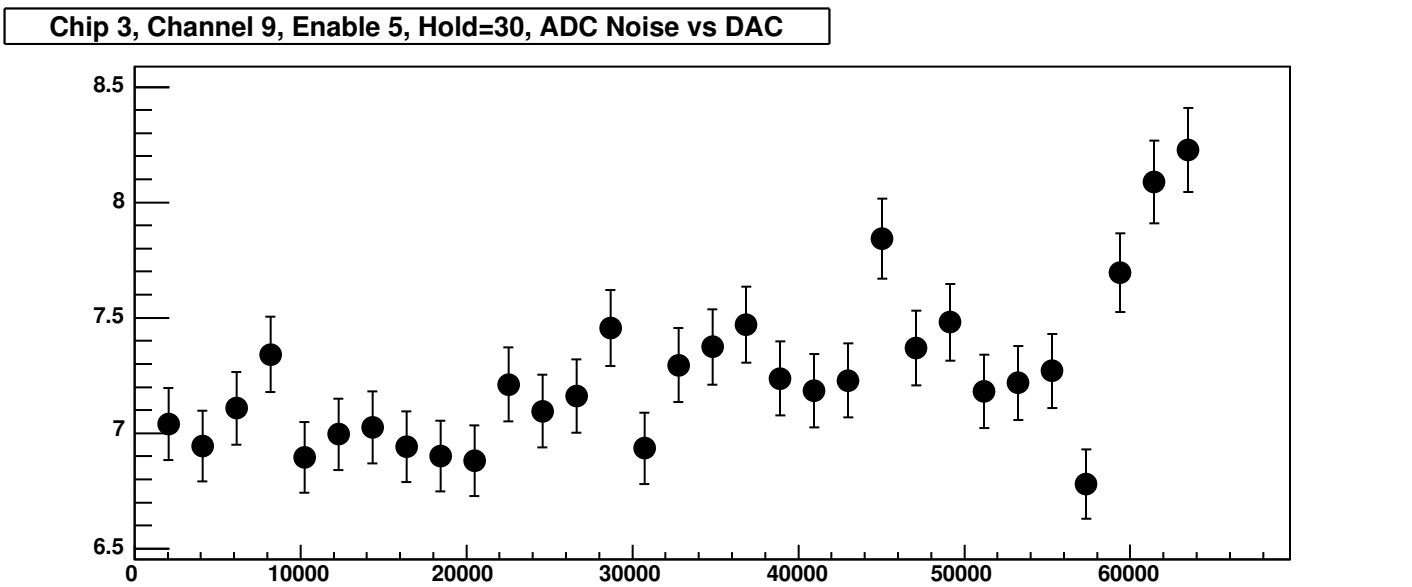

Chip 3, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

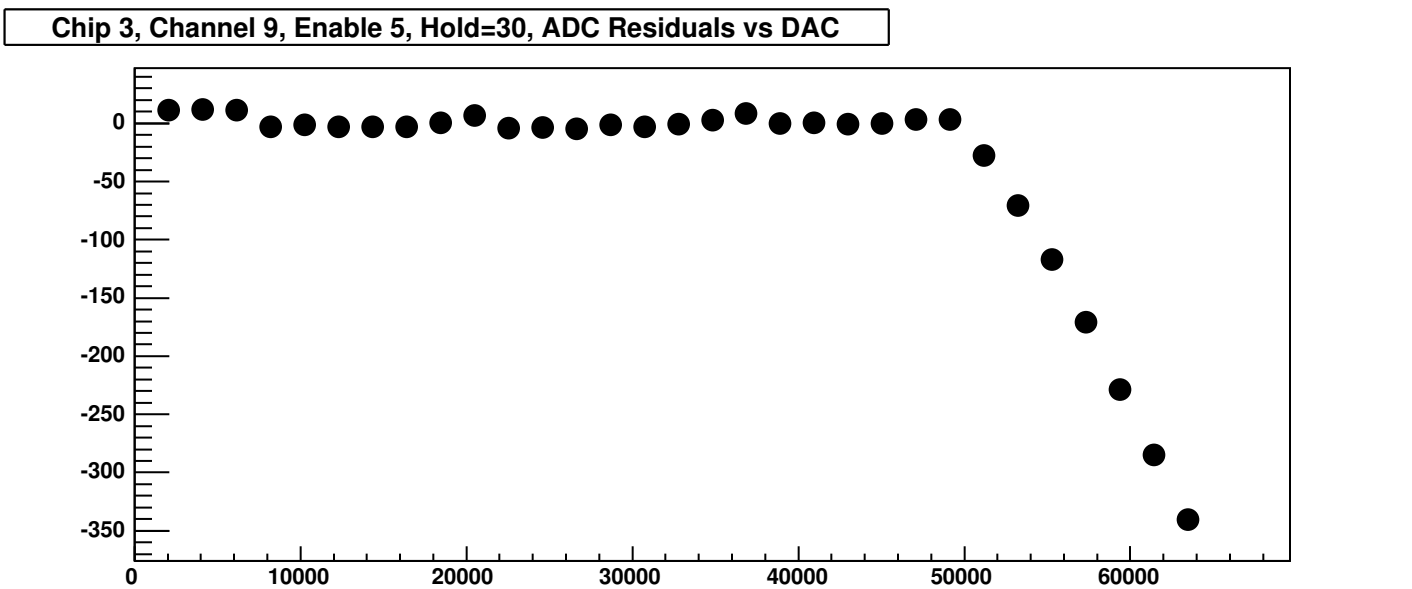

Chip 3, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5550 / 15
p0 1182 ± 0.1301
p1 4.811e-05 ± 5.352e-06
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

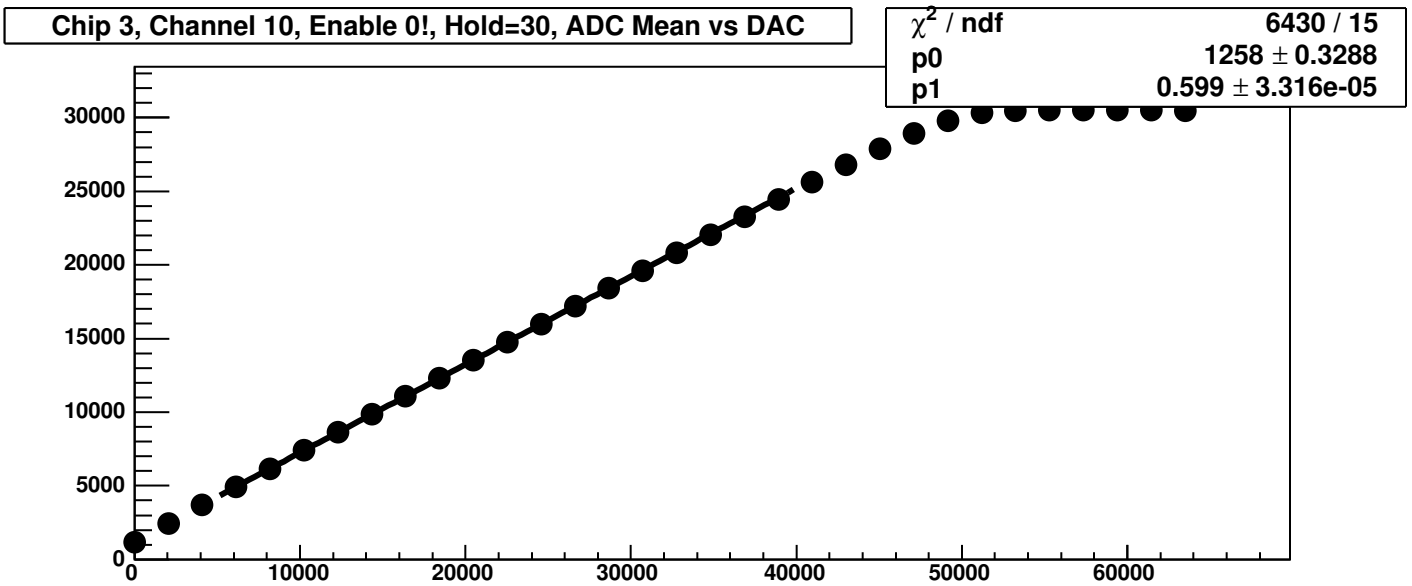

Chip 3, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
8
7.8
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

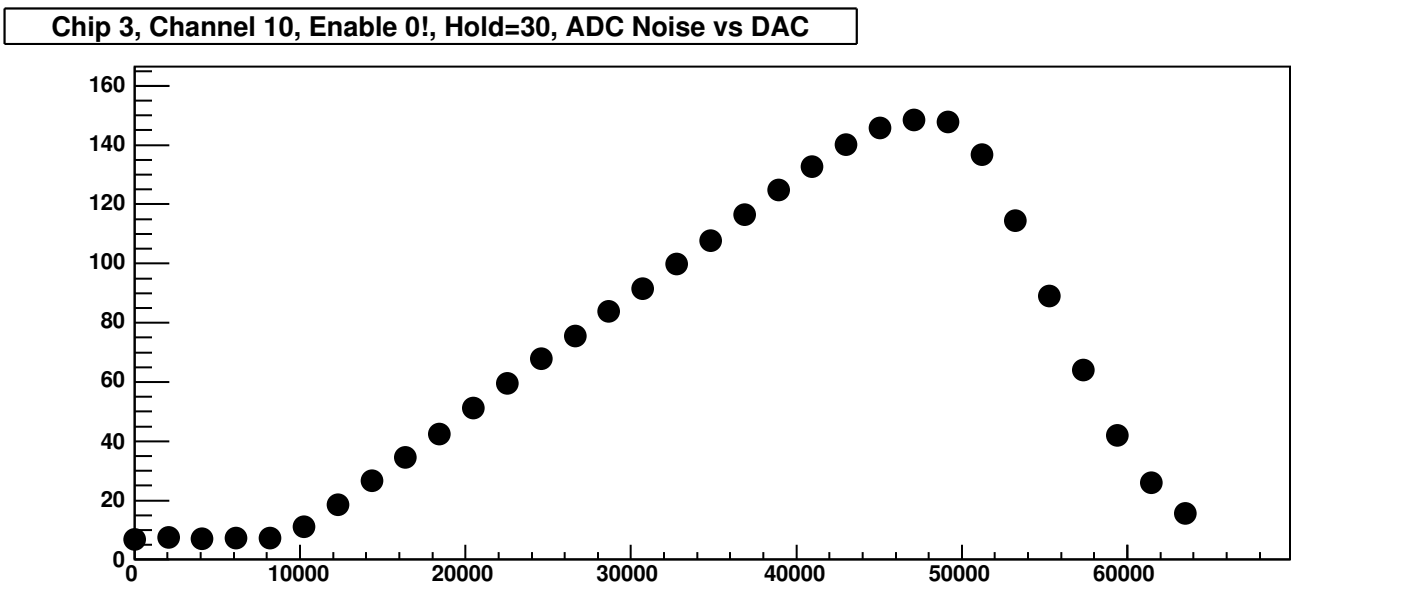

Chip 3, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

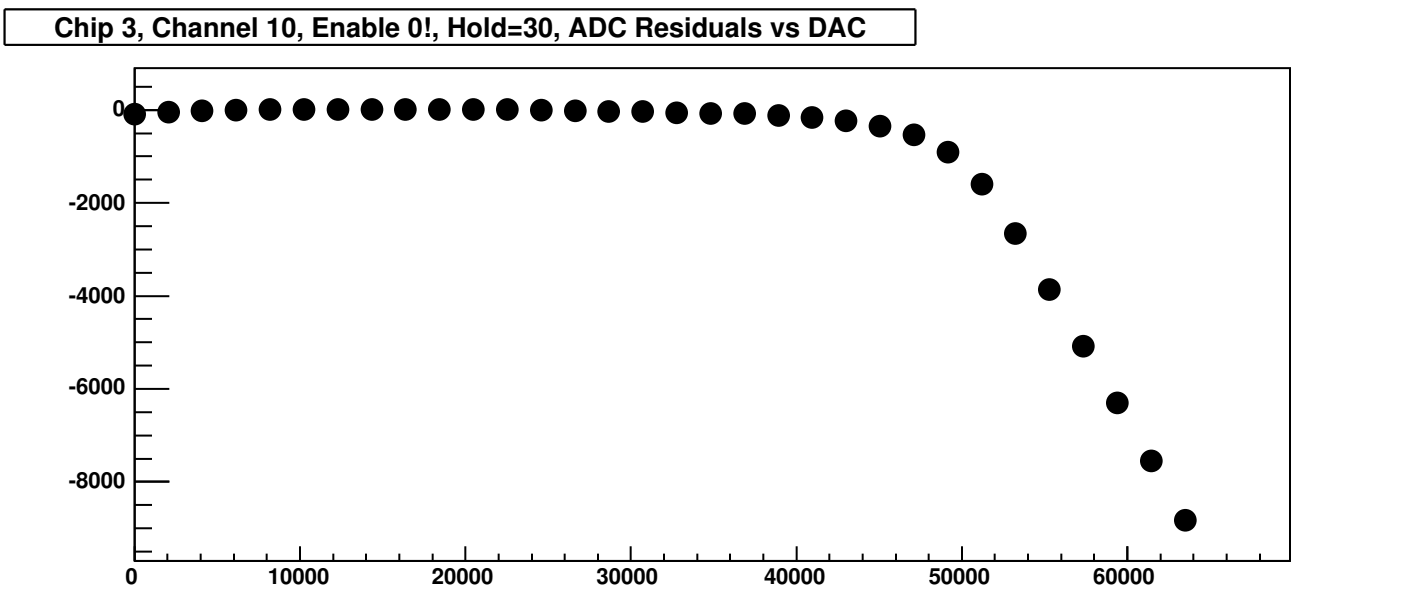

Chip 3, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7219 / 15
p0 1178 ± 0.1325
p1 -0.0007893 ± 5.437e-06
1200
1150
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

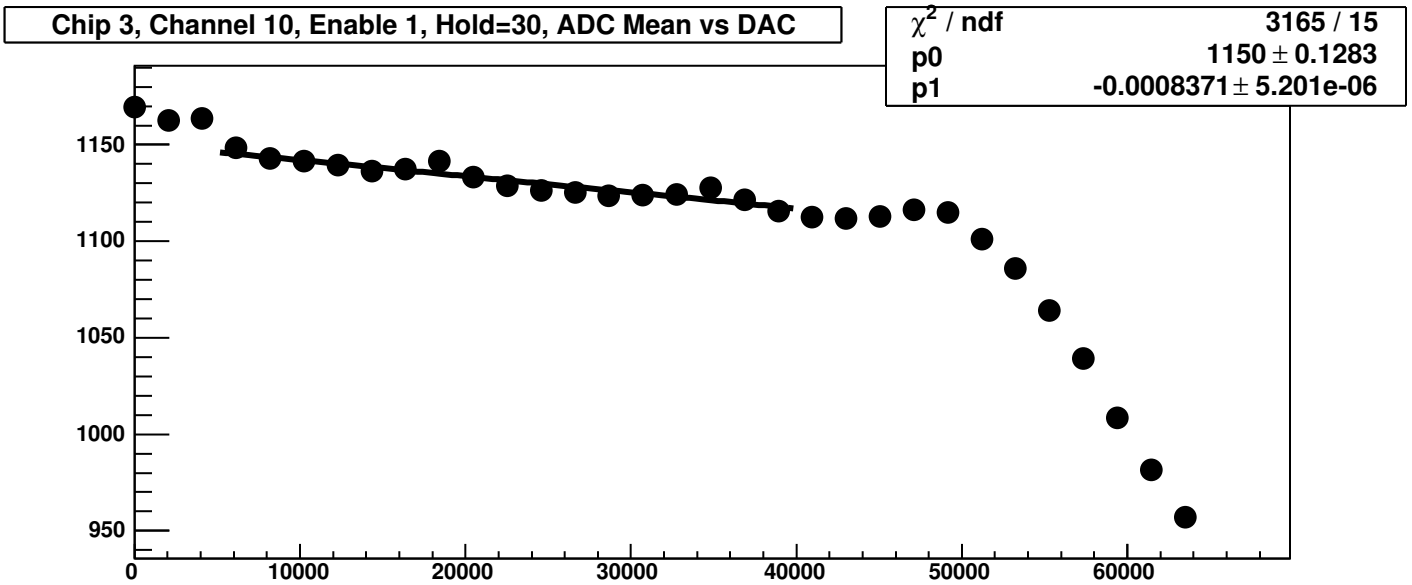

Chip 3, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000

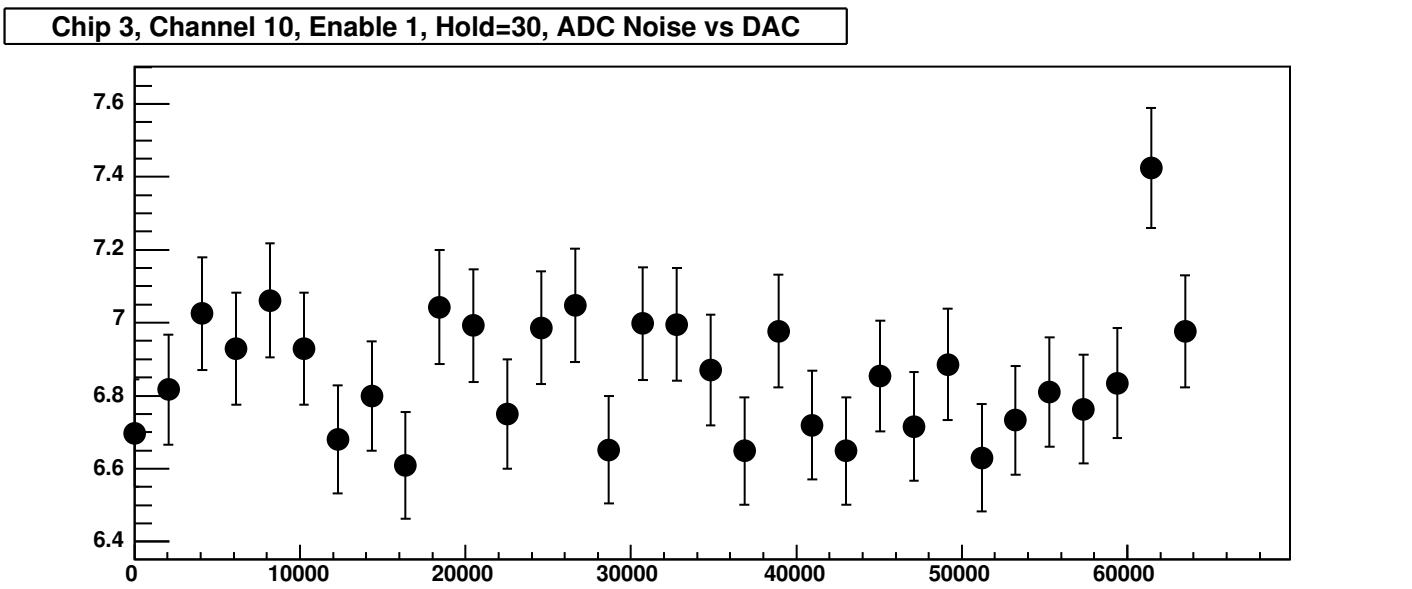

Chip 3, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

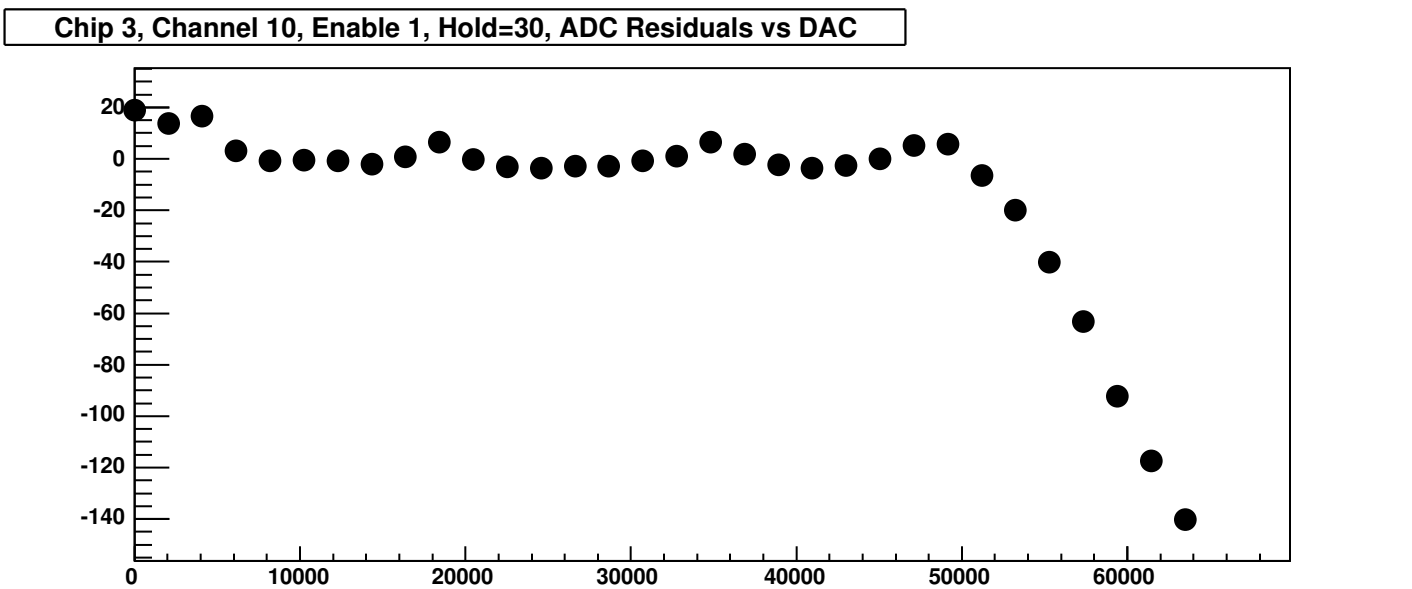

Chip 3, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 6430 / 15
p0 1258 ± 0.3288
p1 0.599 ± 3.316e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

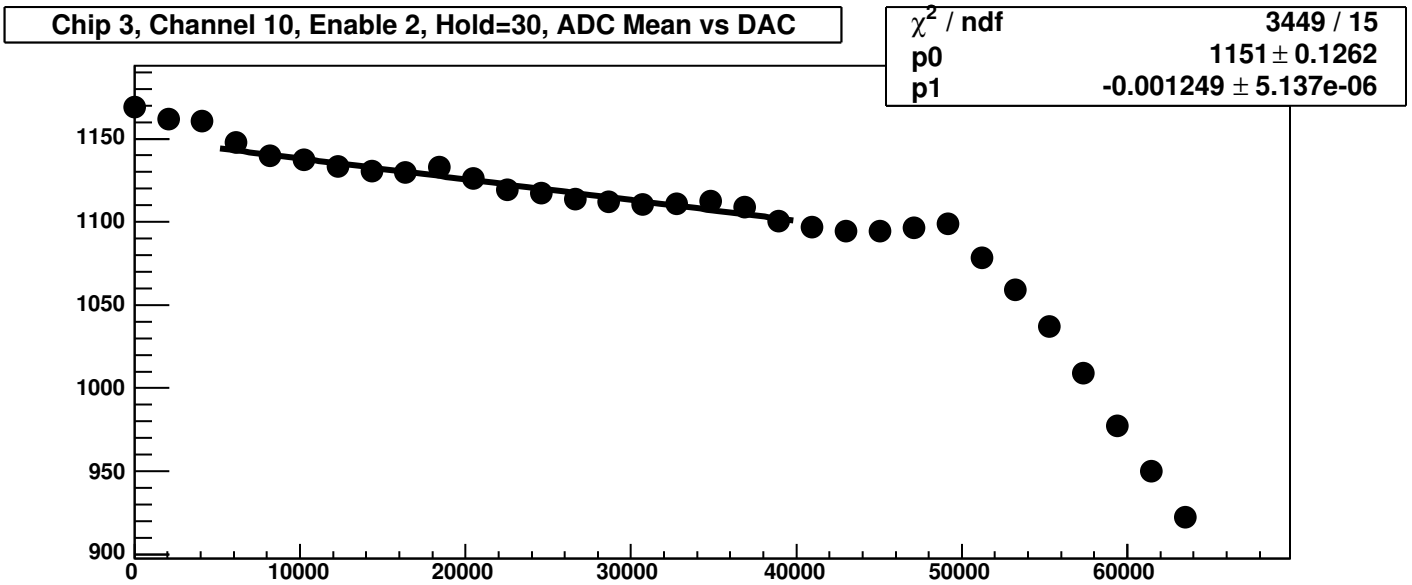

Chip 3, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

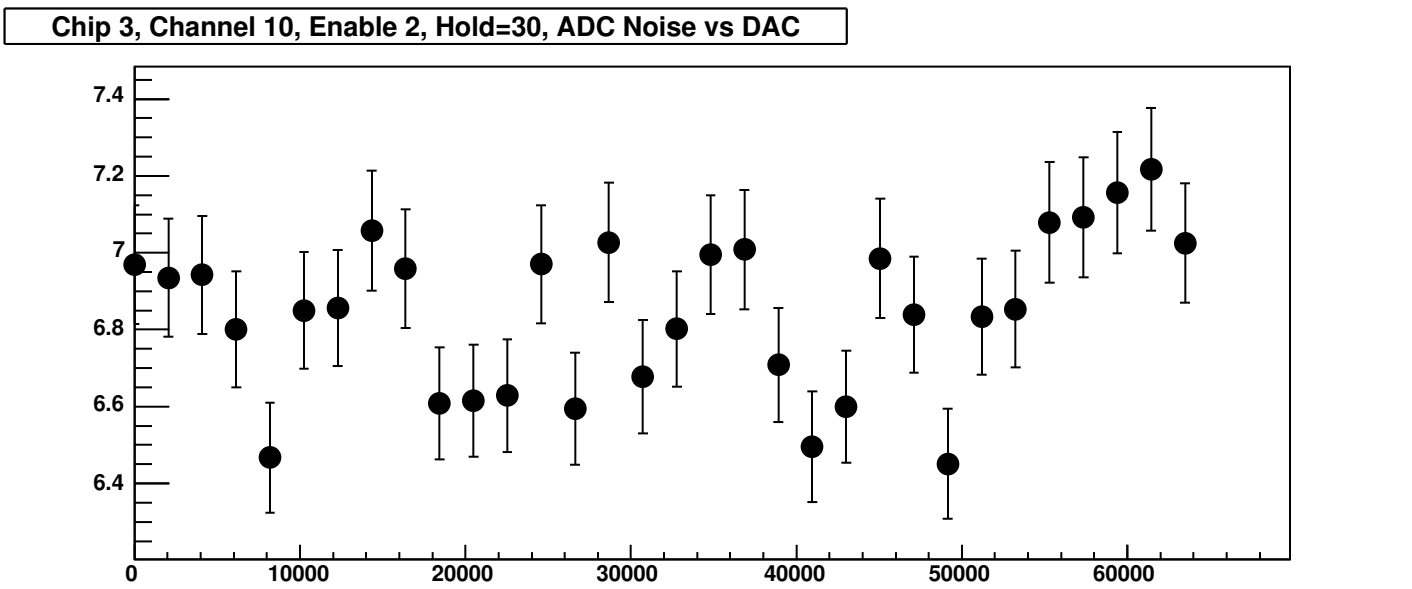

Chip 3, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
0
10000
20000
30000
40000
50000
60000

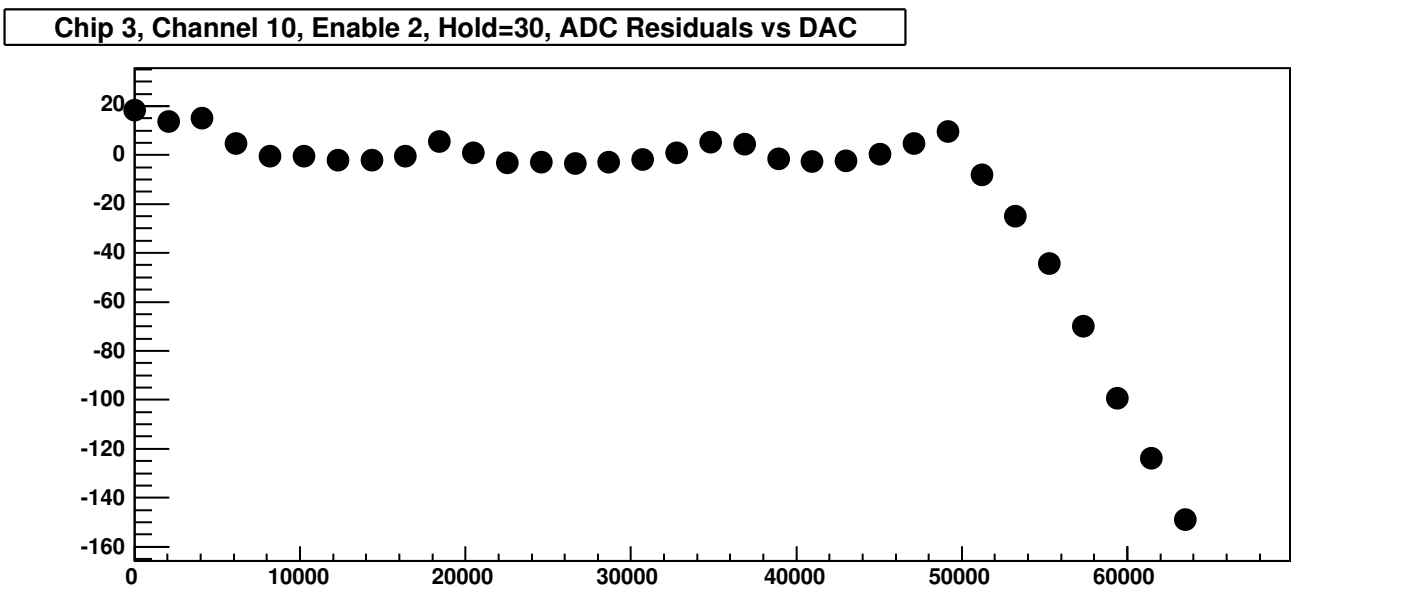

Chip 3, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3165 / 15
p0 1150 ± 0.1283
p1 -0.0008371 ± 5.201e-06
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

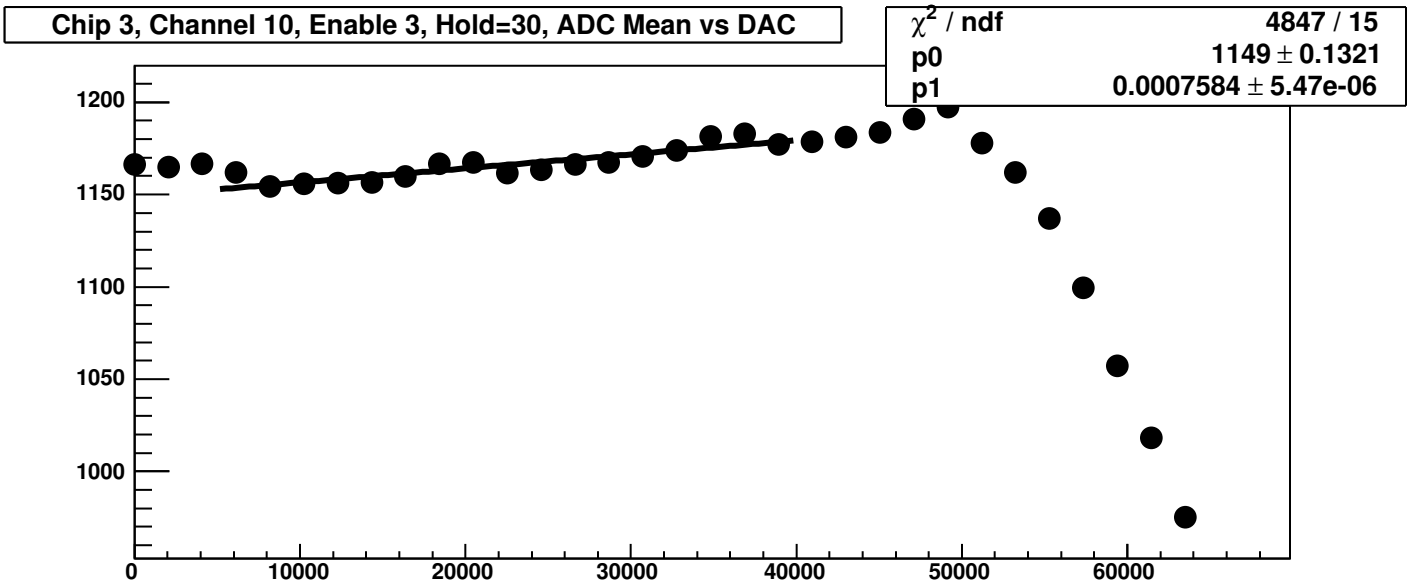

Chip 3, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

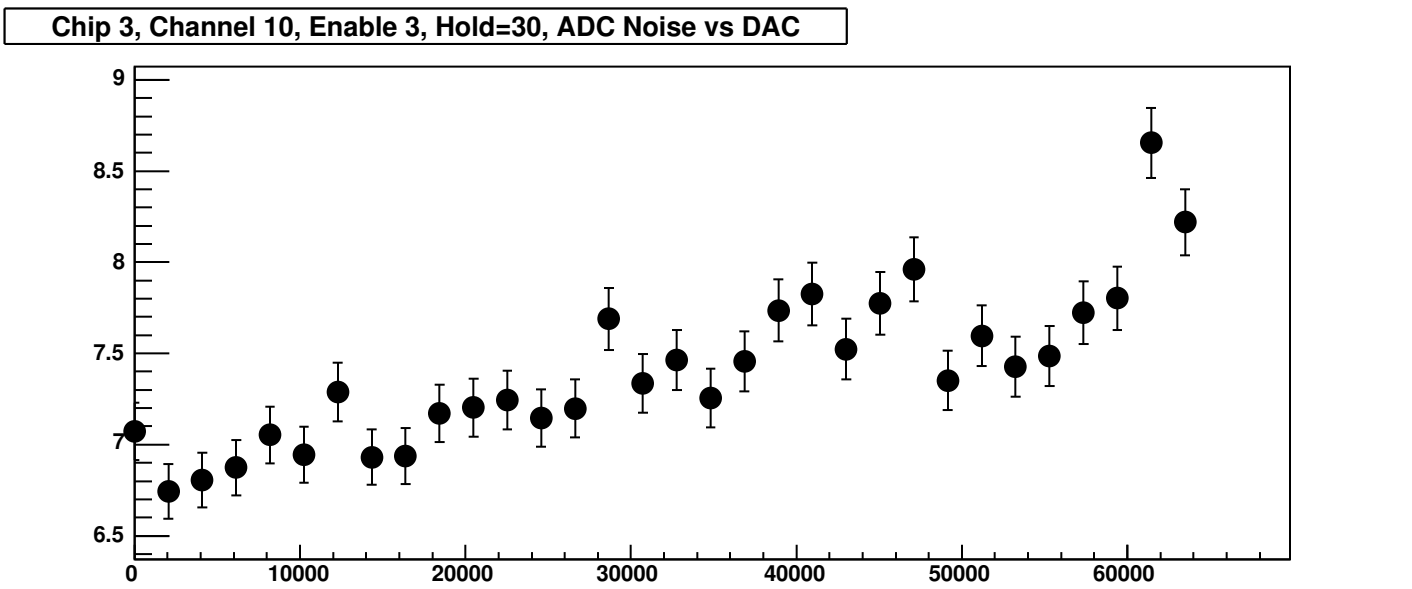

Chip 3, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

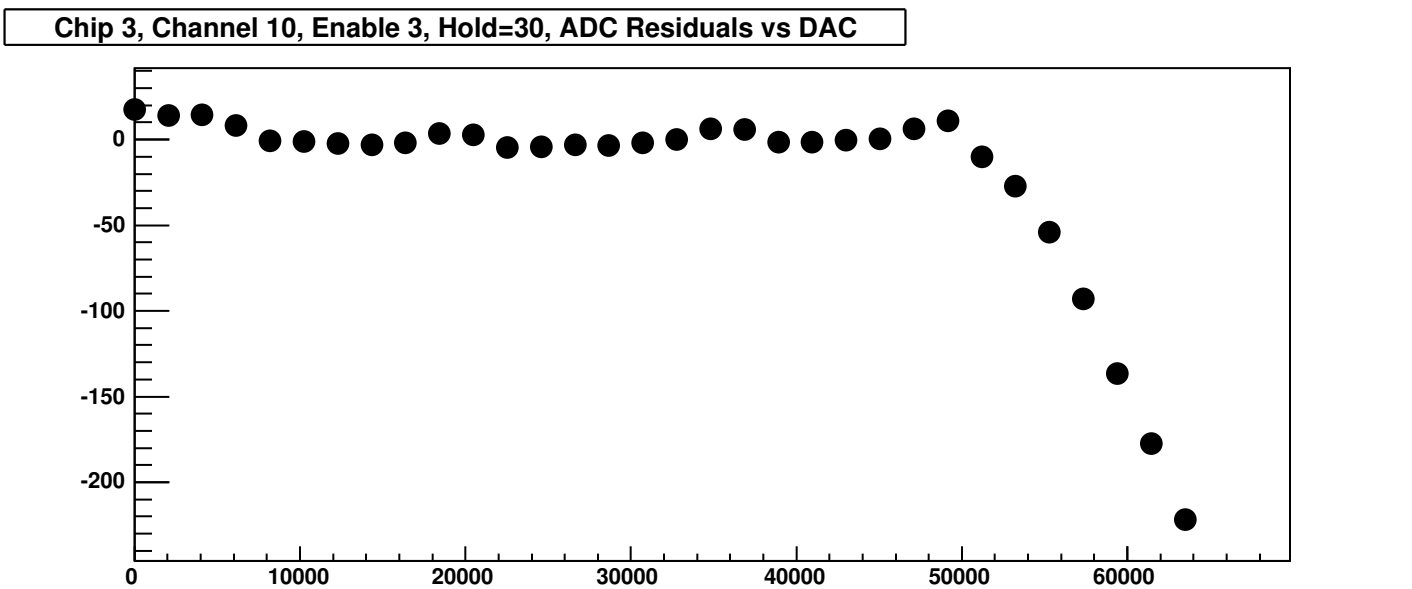

Chip 3, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3449 / 15
p0 1151 ± 0.1262
p1 -0.001249 ± 5.137e-06
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

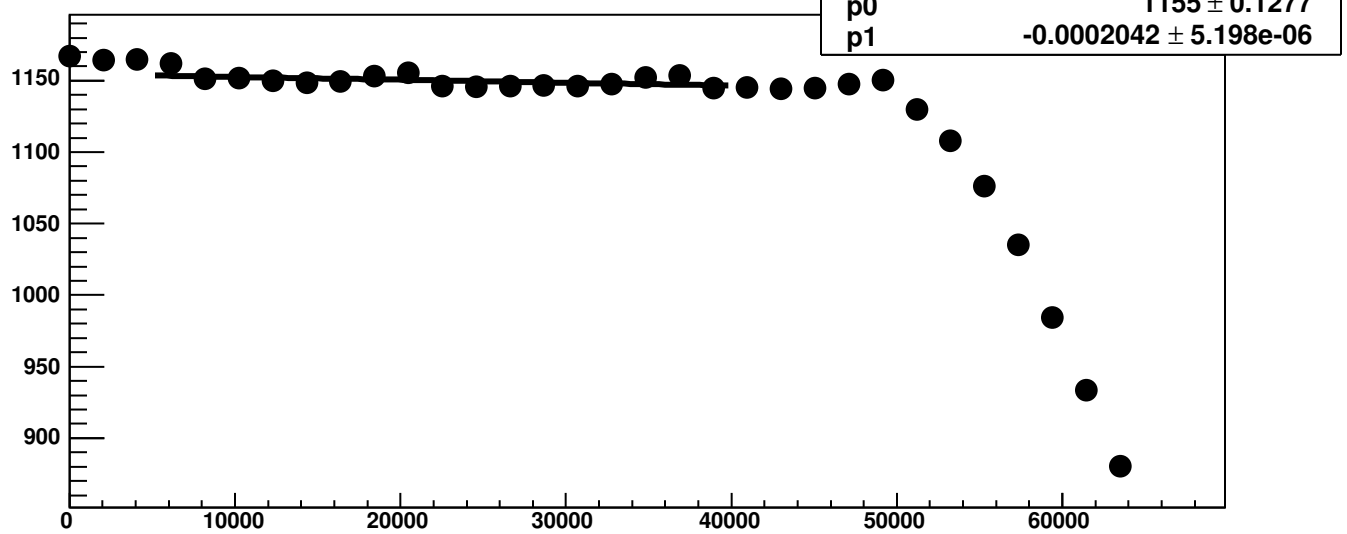

Chip 3, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4847 / 15
p0 1149 ± 0.1321
p1 0.0007584 ± 5.47e-06
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

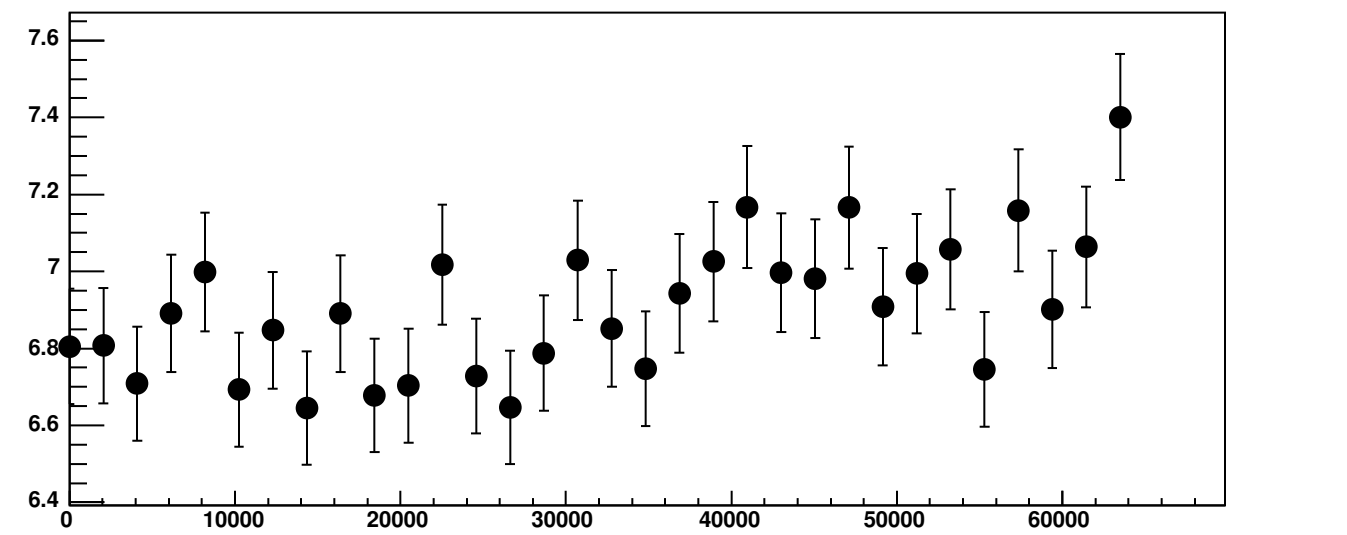

Chip 3, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC
9
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

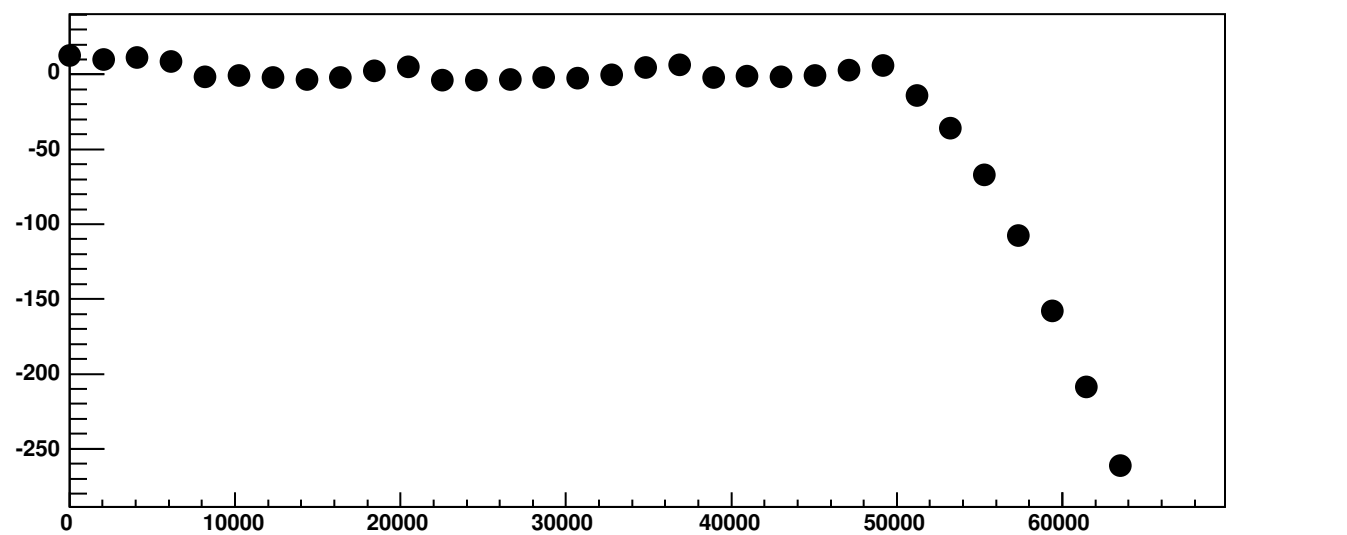
Chip 3, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

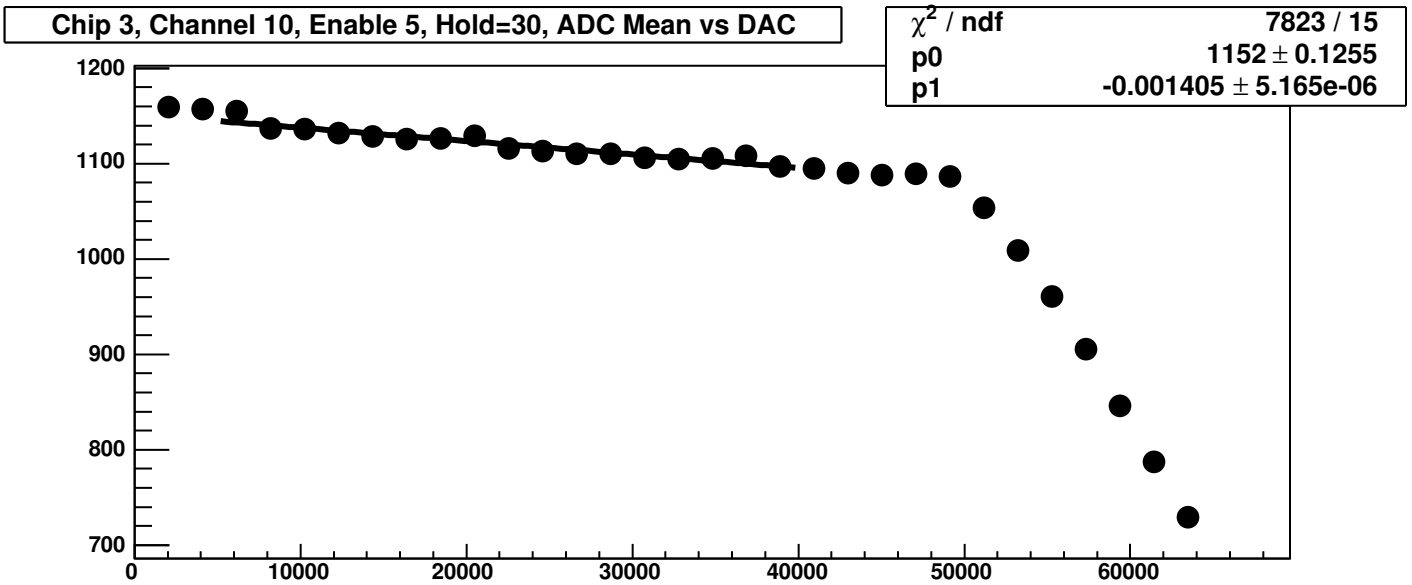


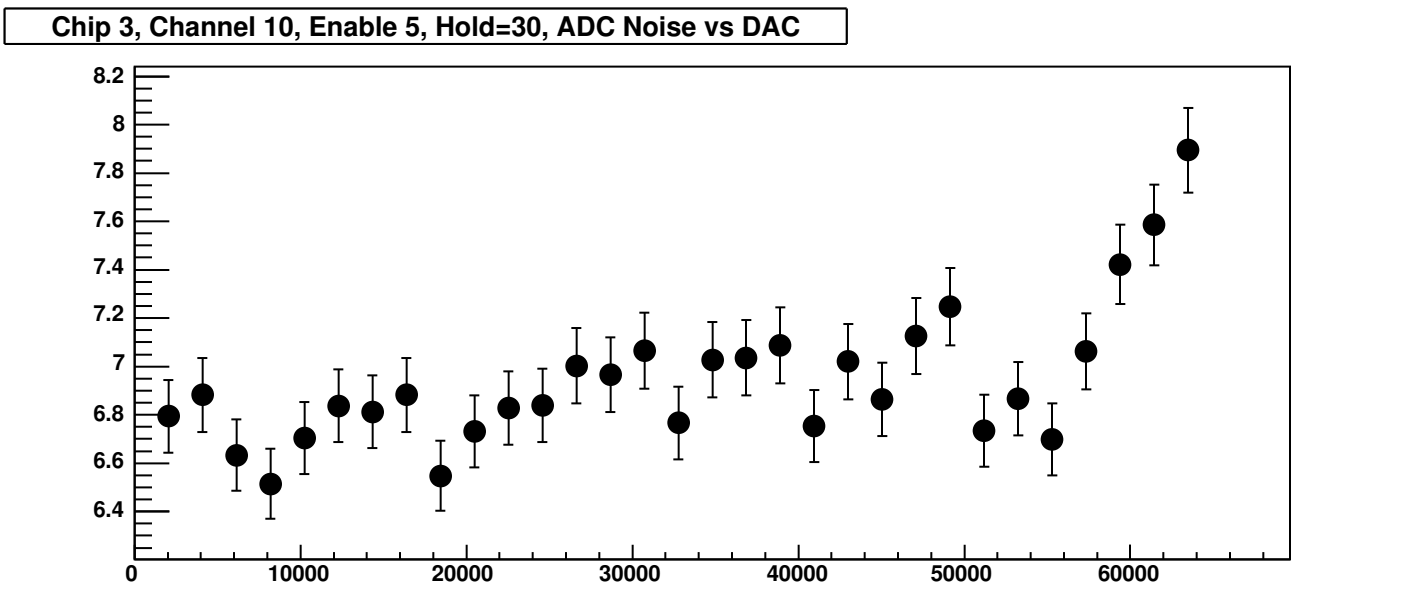
Chip 3, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC

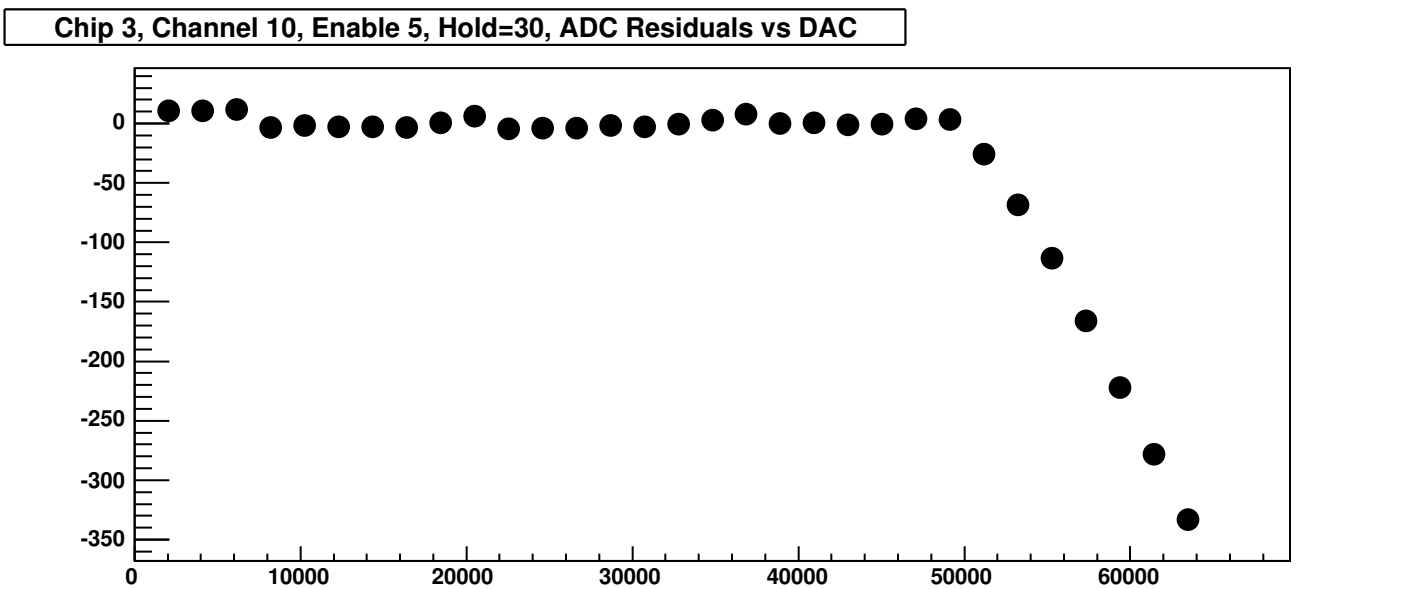


Chip 3, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC

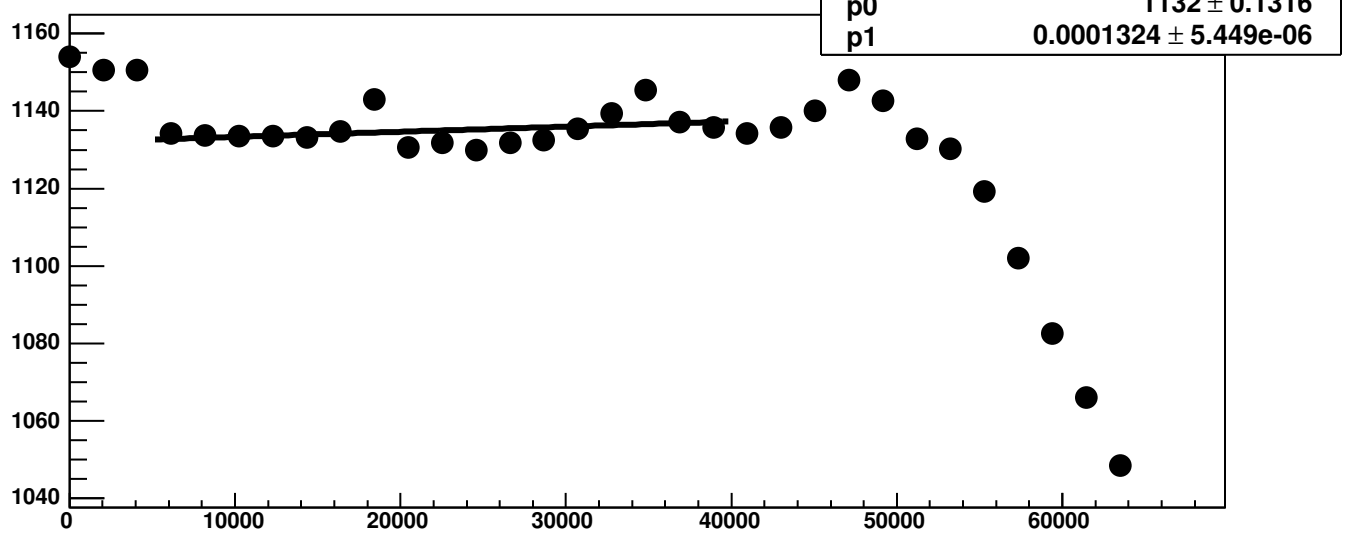



Chip 3, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7823 / 15
p0 1152 ± 0.1255
p1 -0.001405 ± 5.165e-06
1200
1100
1000
900
800
700
0
10000
20000
30000
40000
50000
60000

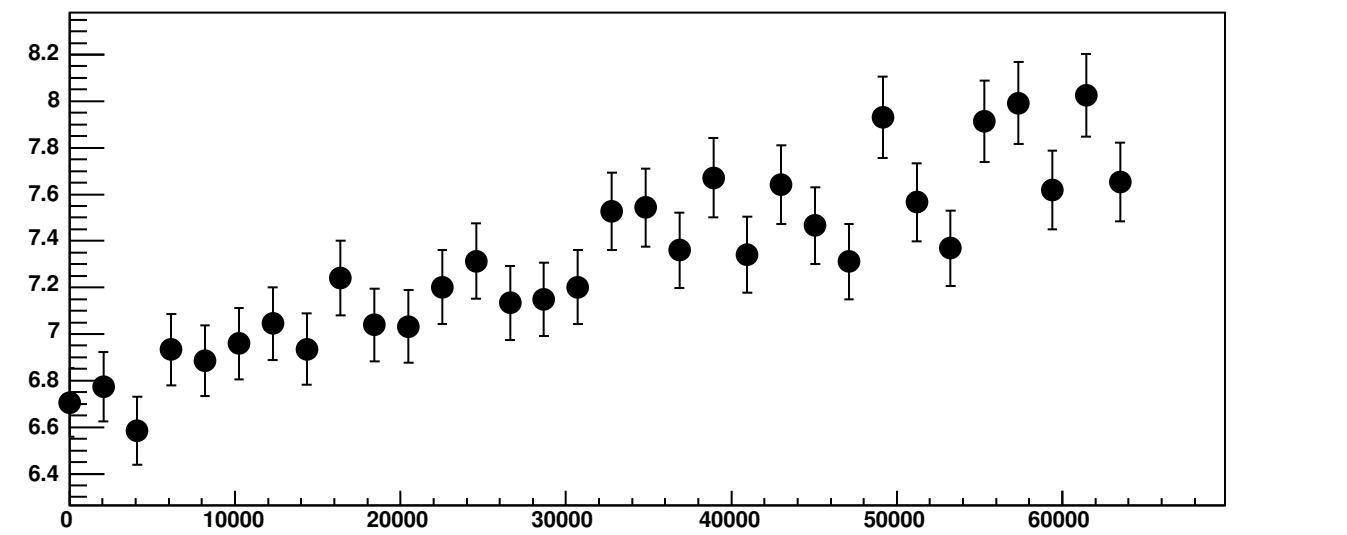

Chip 3, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
8.2
8
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

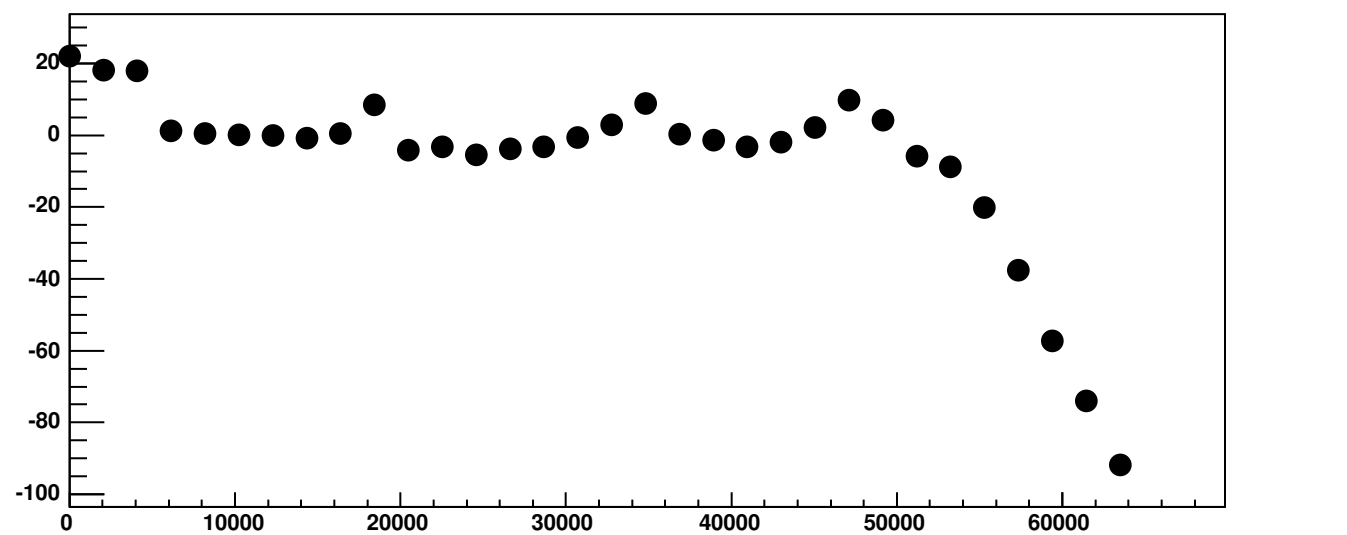
Chip 3, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC



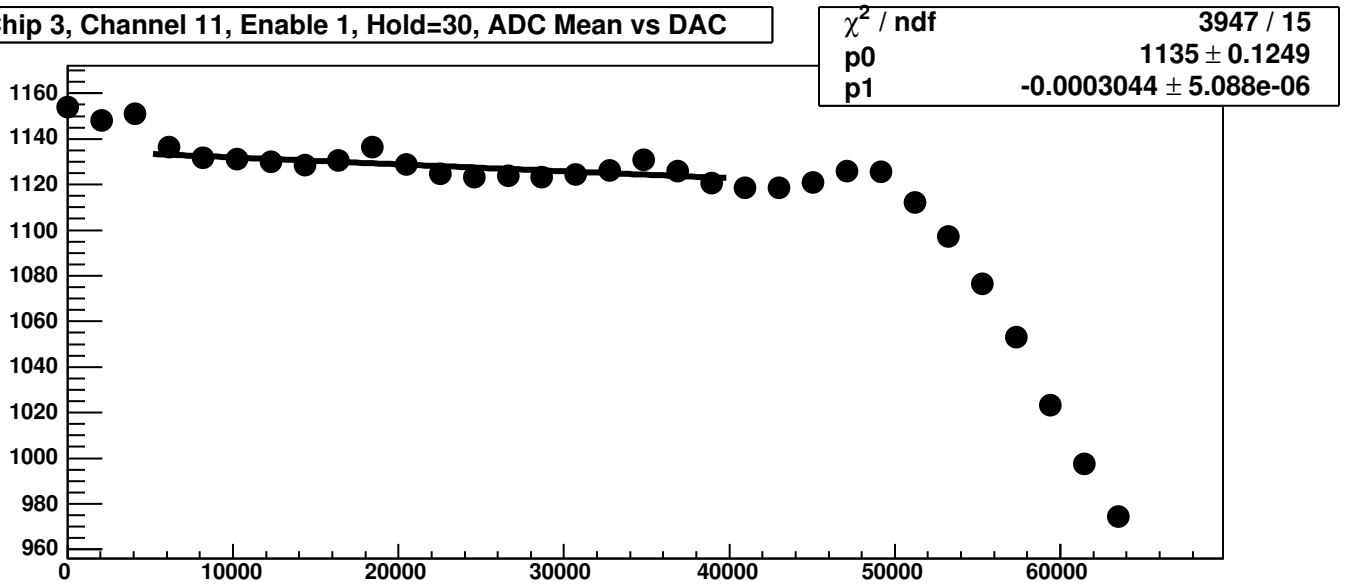
Chip 3, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC



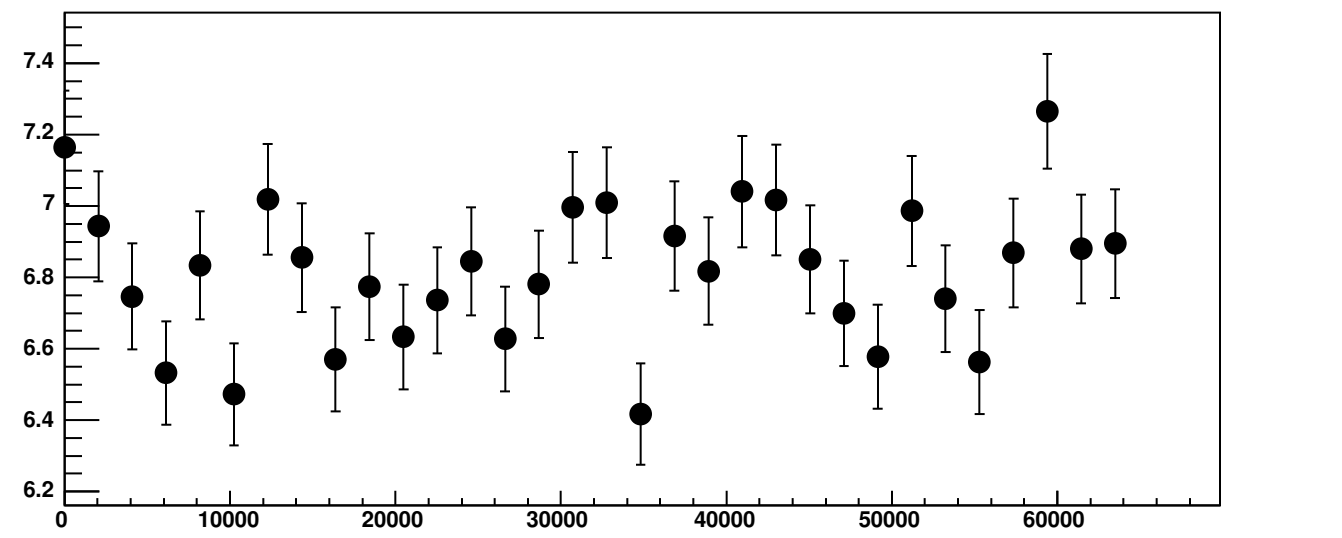
Chip 3, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC



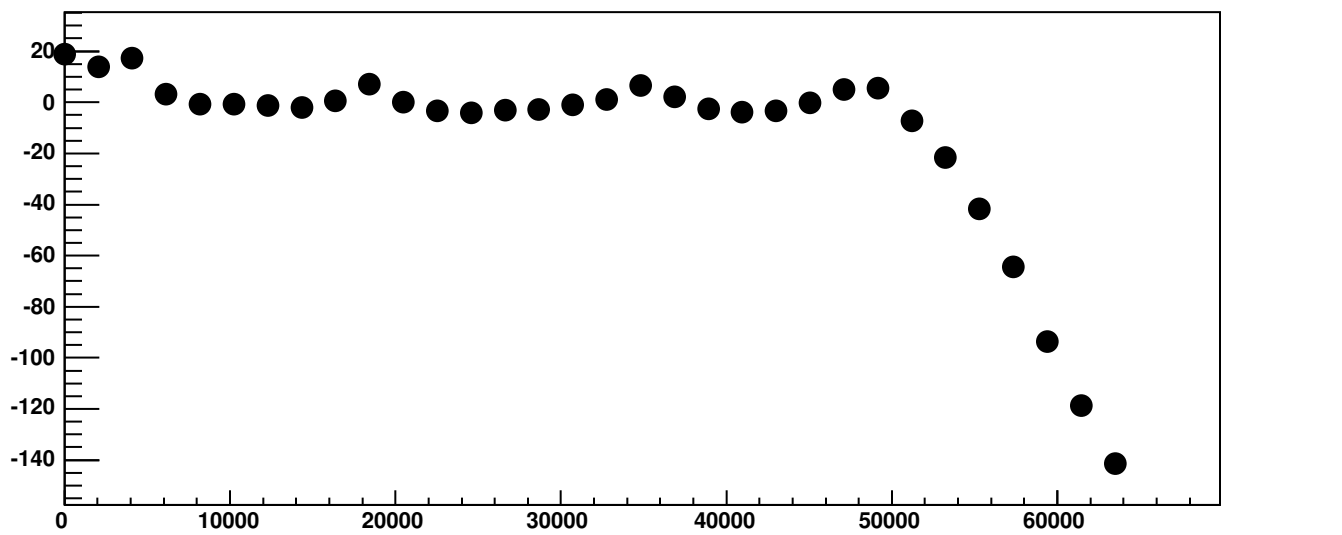
Chip 3, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

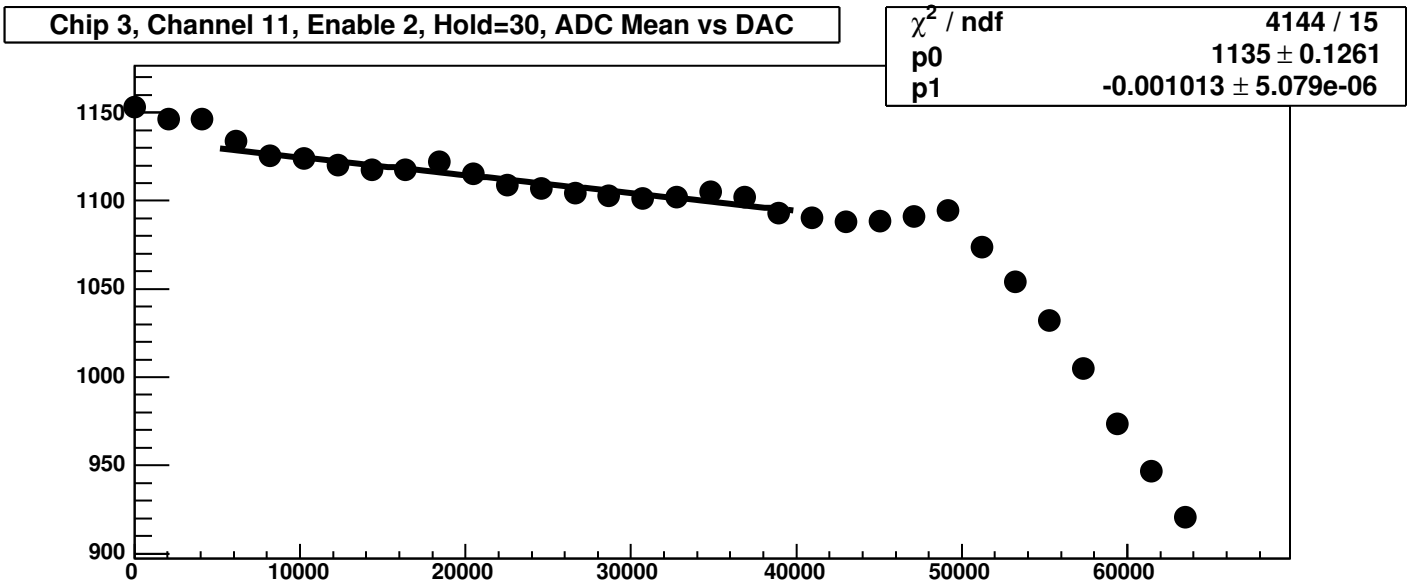


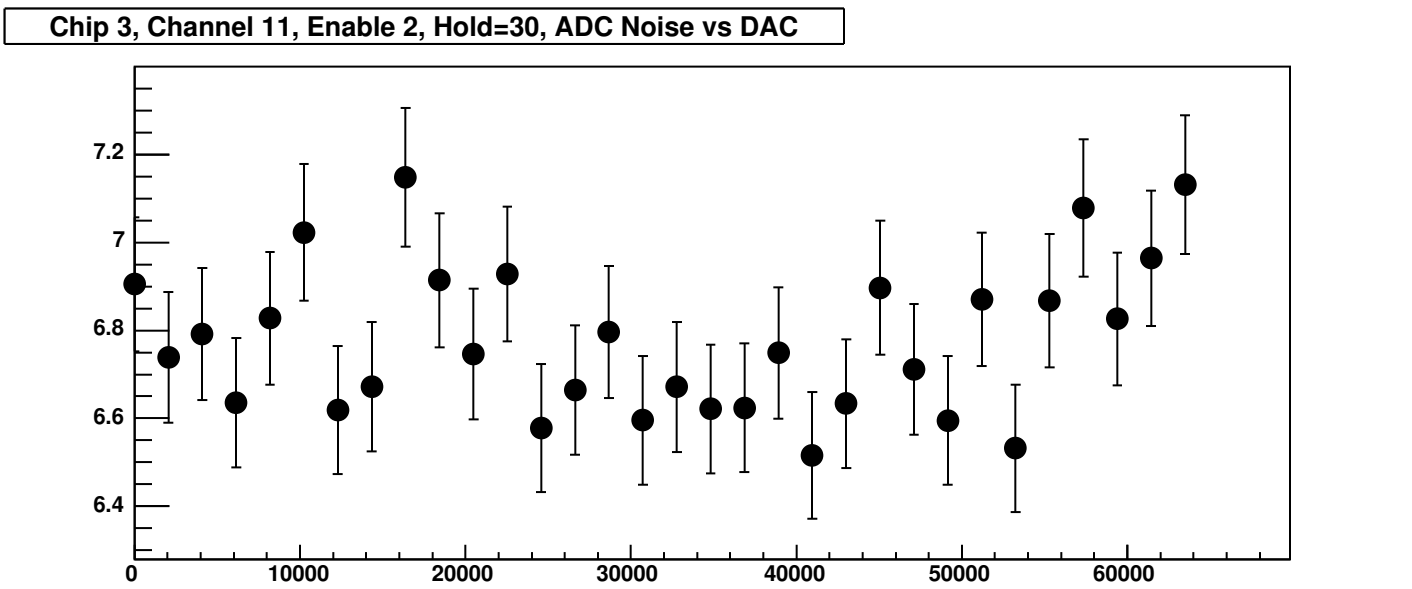
Chip 3, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC

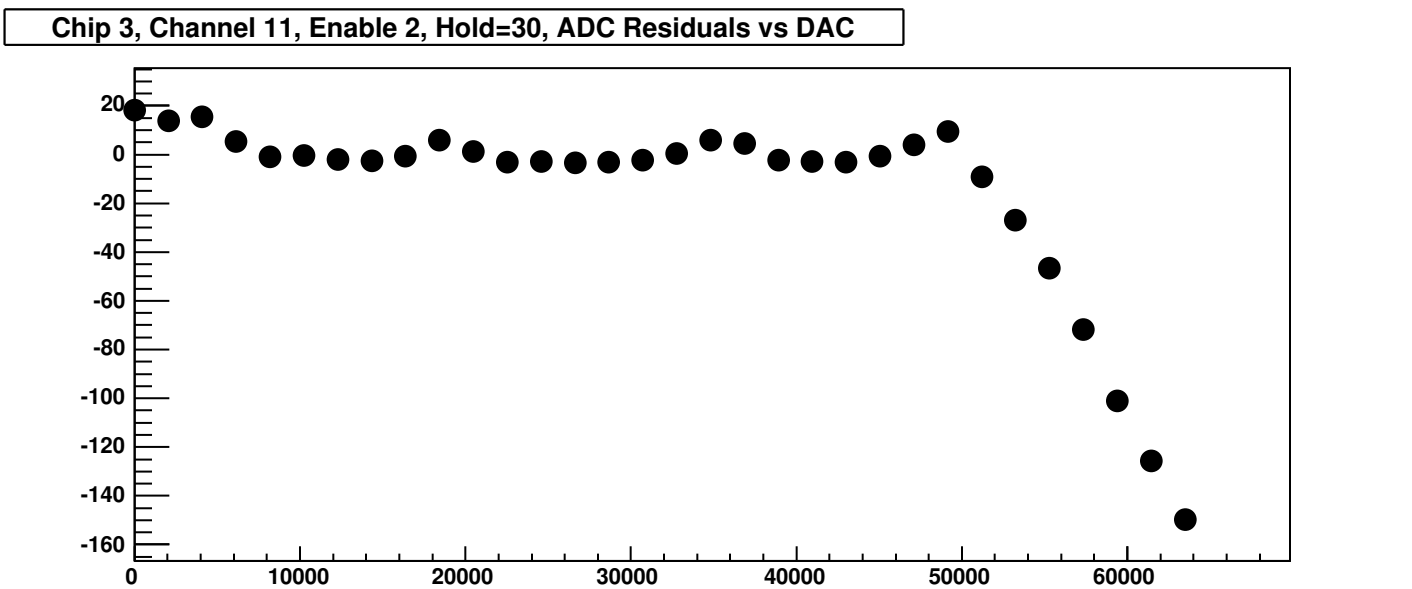


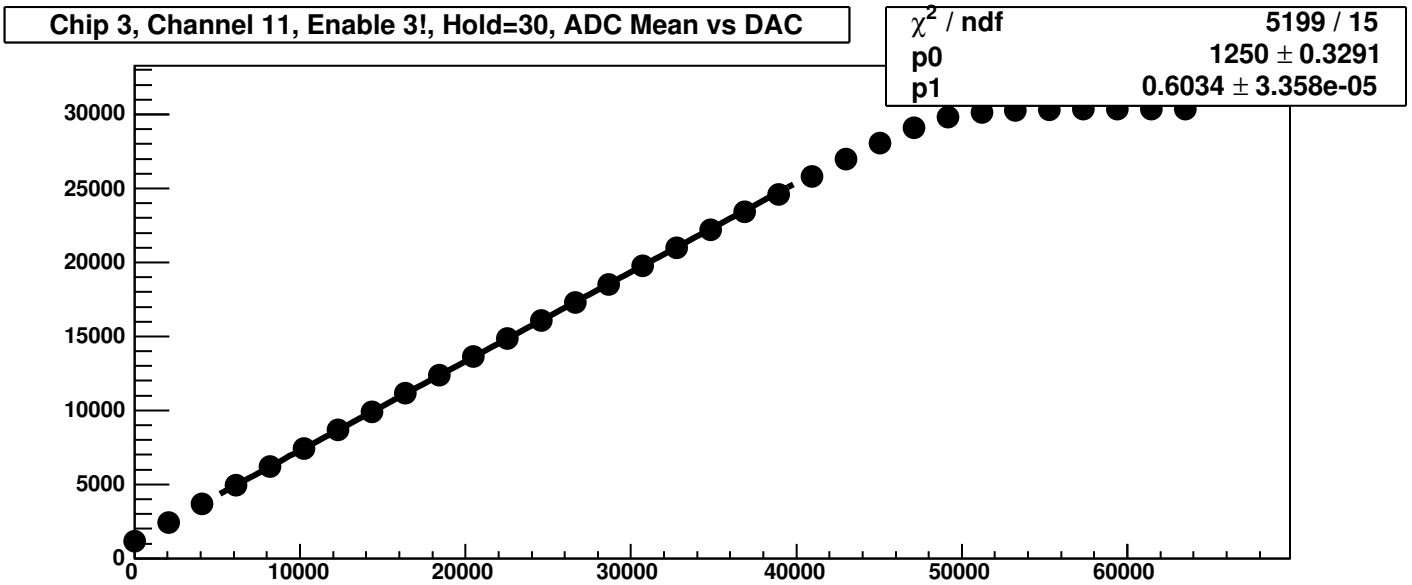
Chip 3, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

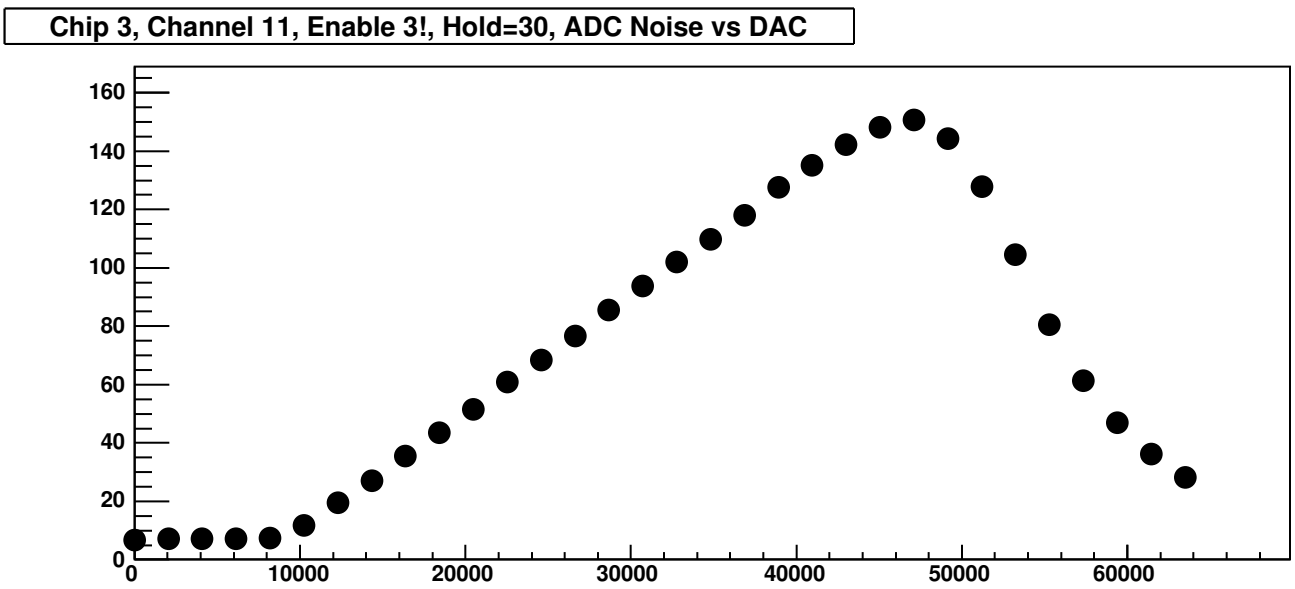


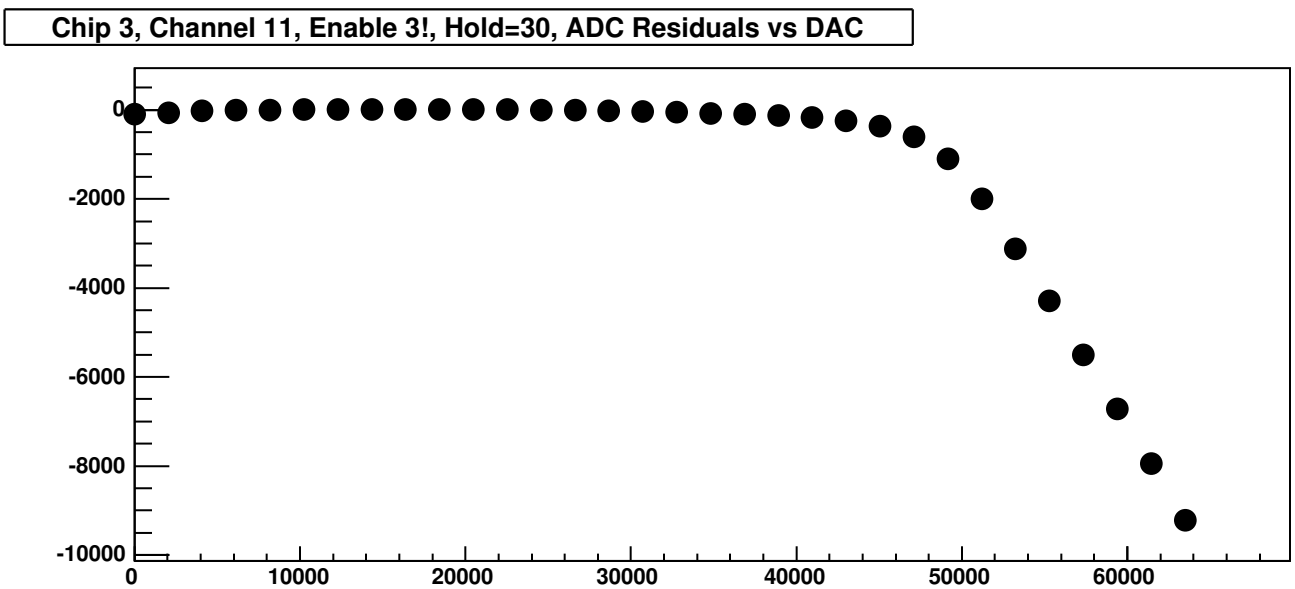

Chip 3, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4144 / 15
p0 1135 ± 0.1261
p1 -0.001013 ± 5.079e-06
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

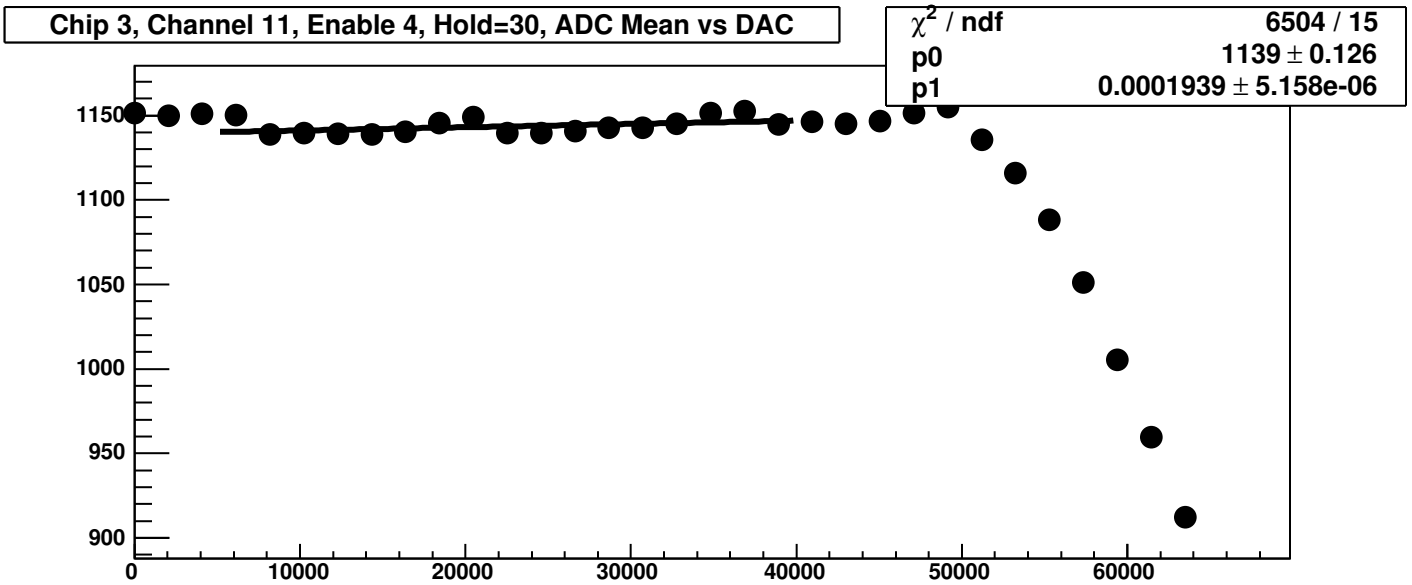

Chip 3, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

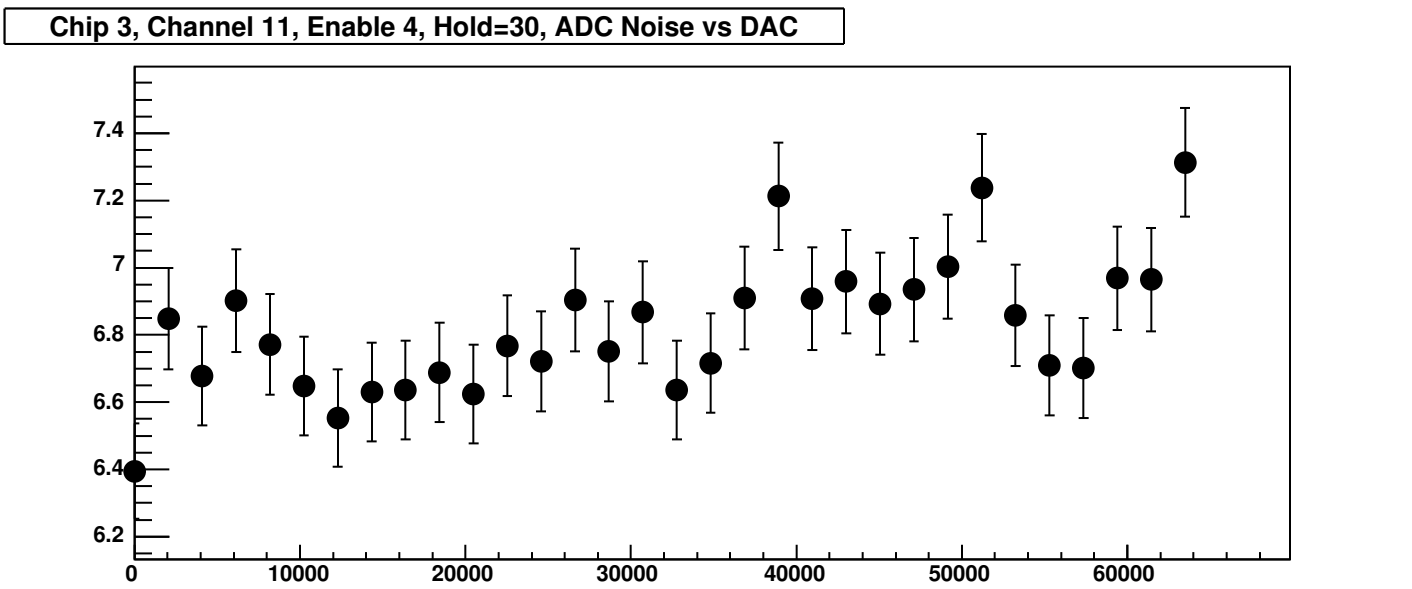

Chip 3, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

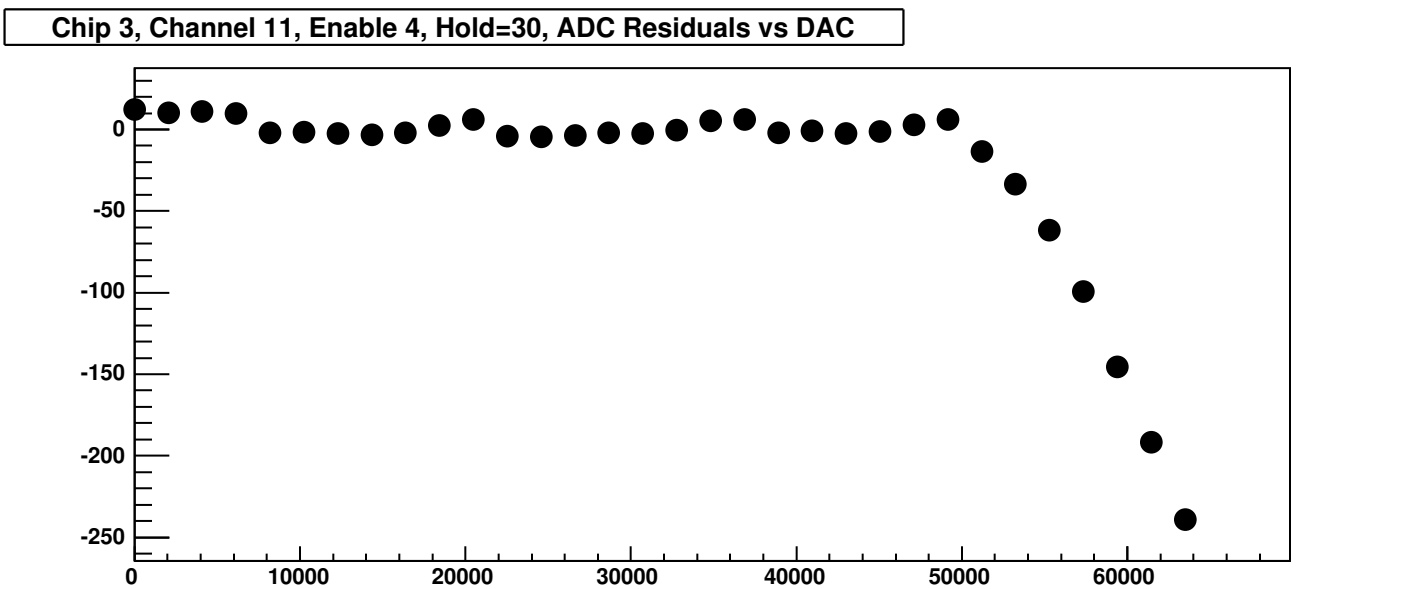

Chip 3, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 5199 / 15
p0 1250 ± 0.3291
p1 0.6034 ± 3.358e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

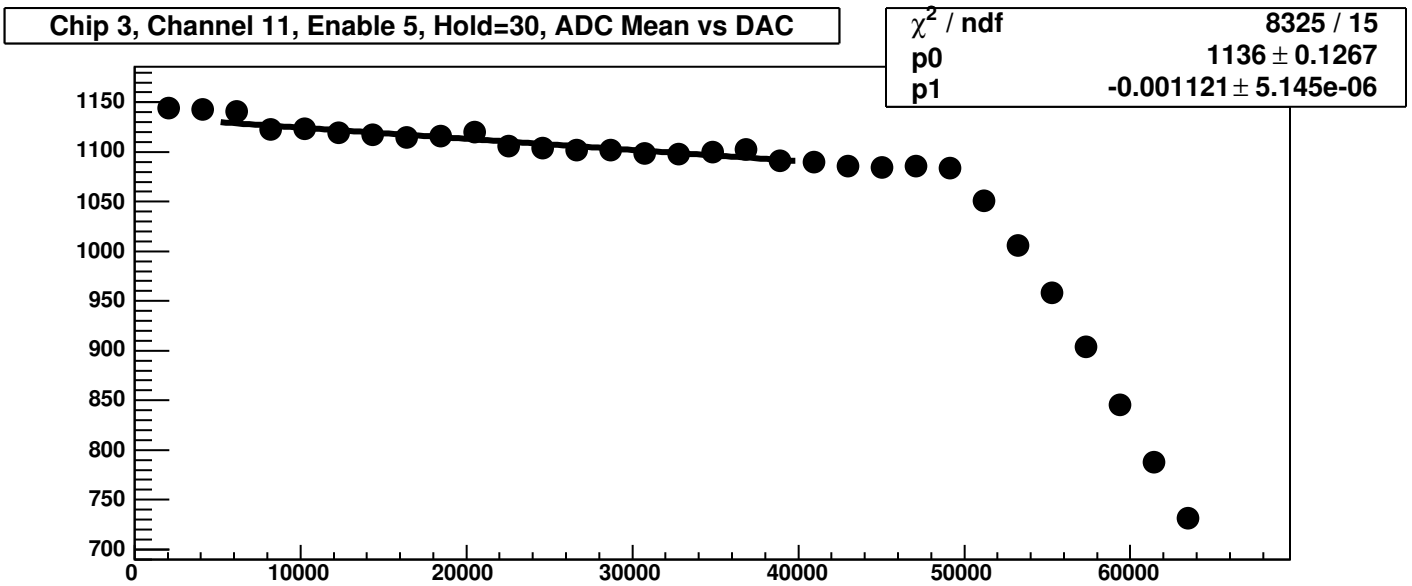

Chip 3, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

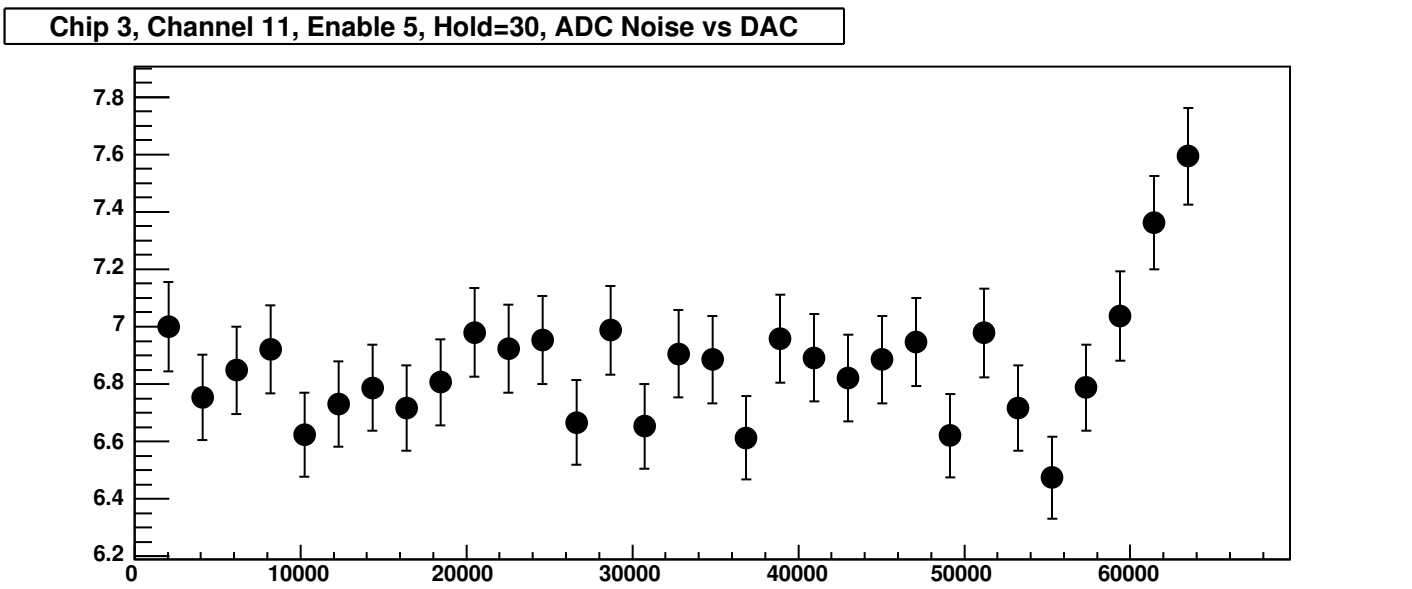

Chip 3, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

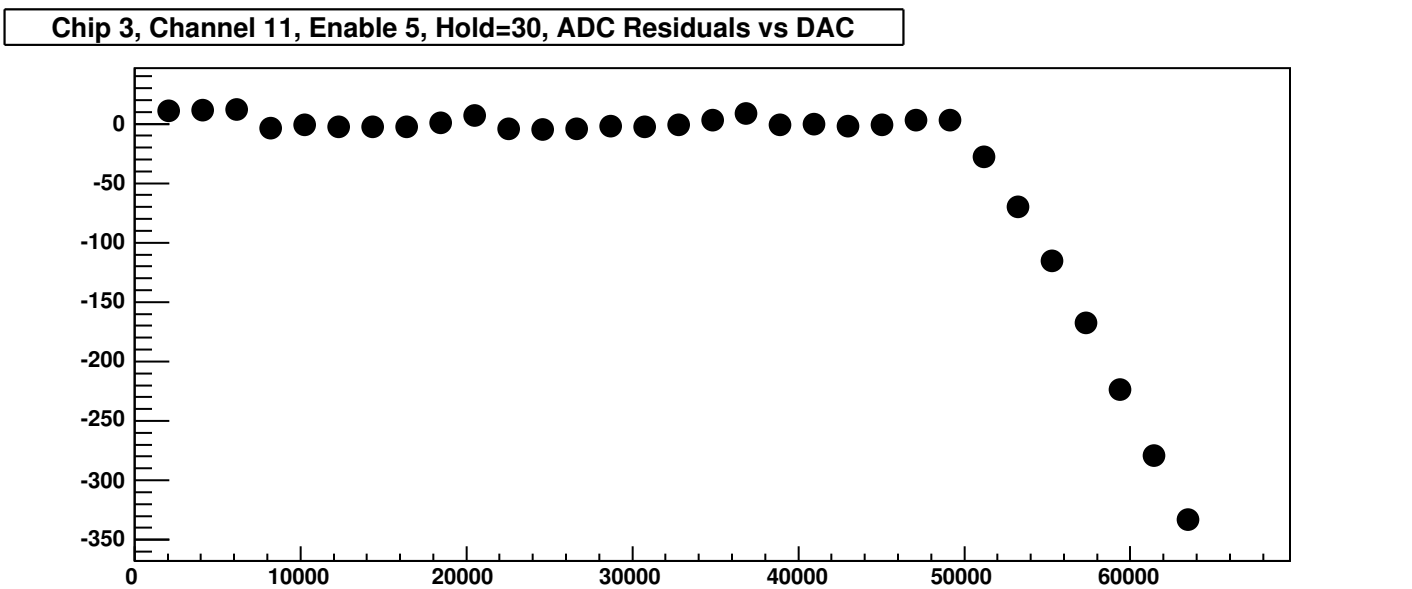

Chip 3, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6504 / 15
p0 1139 ± 0.126
p1 0.0001939 ± 5.158e-06
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

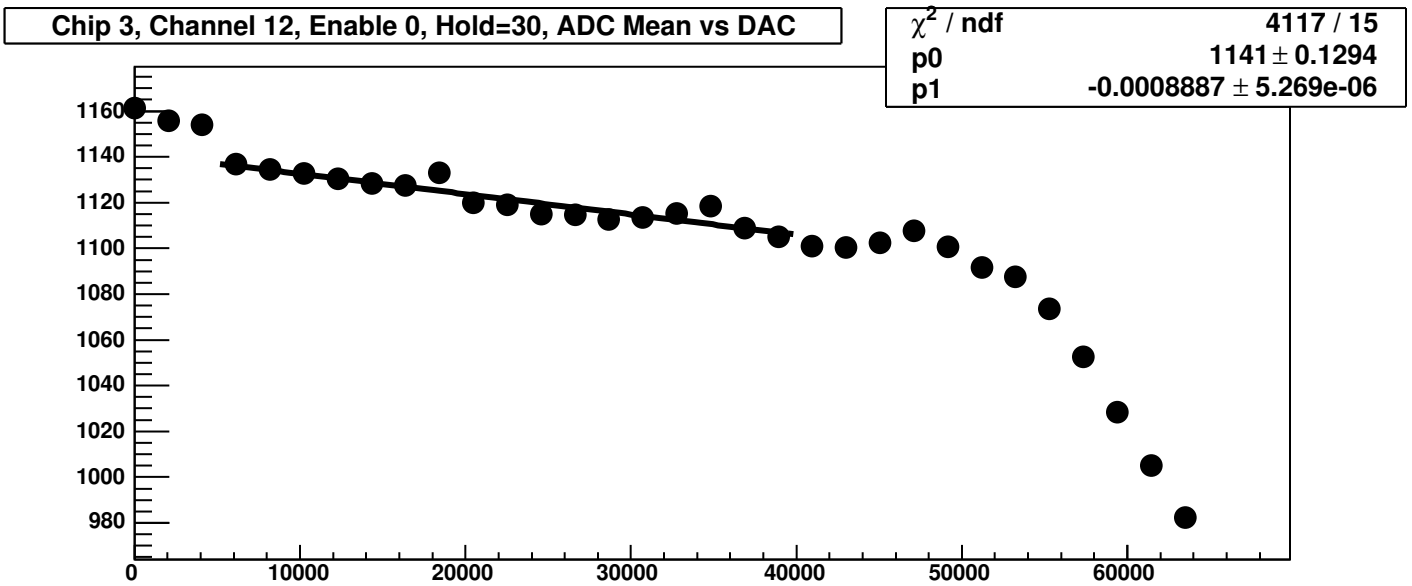

Chip 3, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

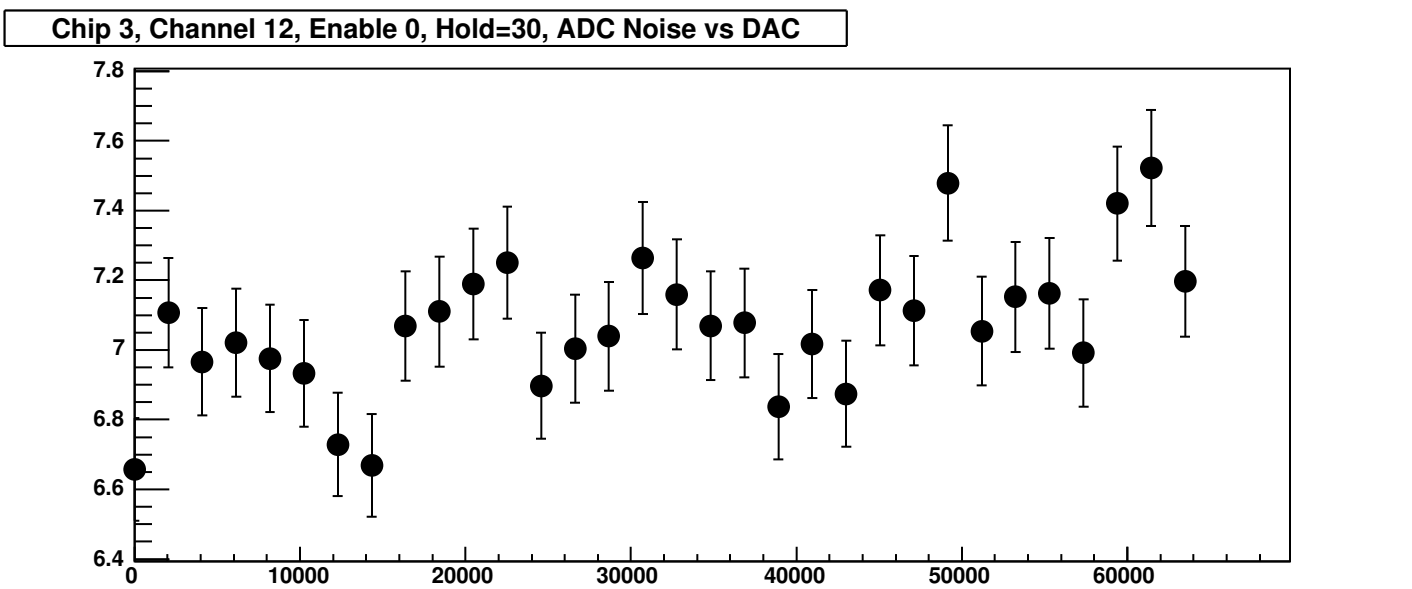

Chip 3, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

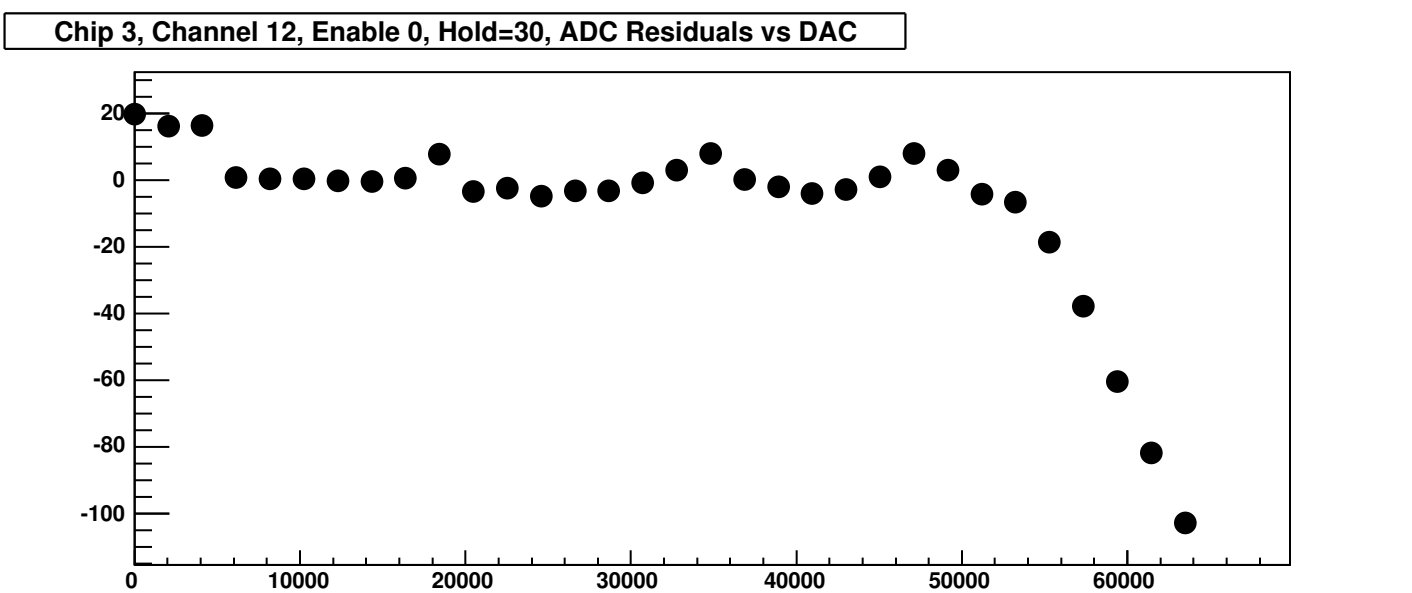

Chip 3, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8325 / 15
p0 1136 ± 0.1267
p1 -0.001121 ± 5.145e-06
1150
1100
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

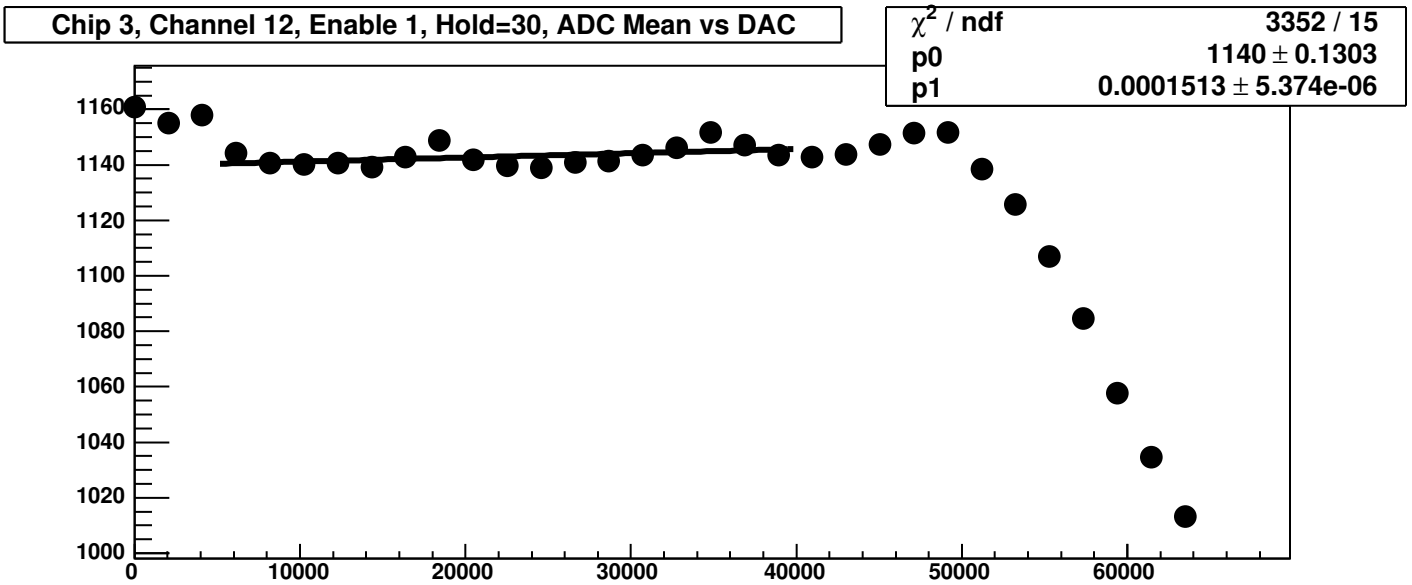

Chip 3, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

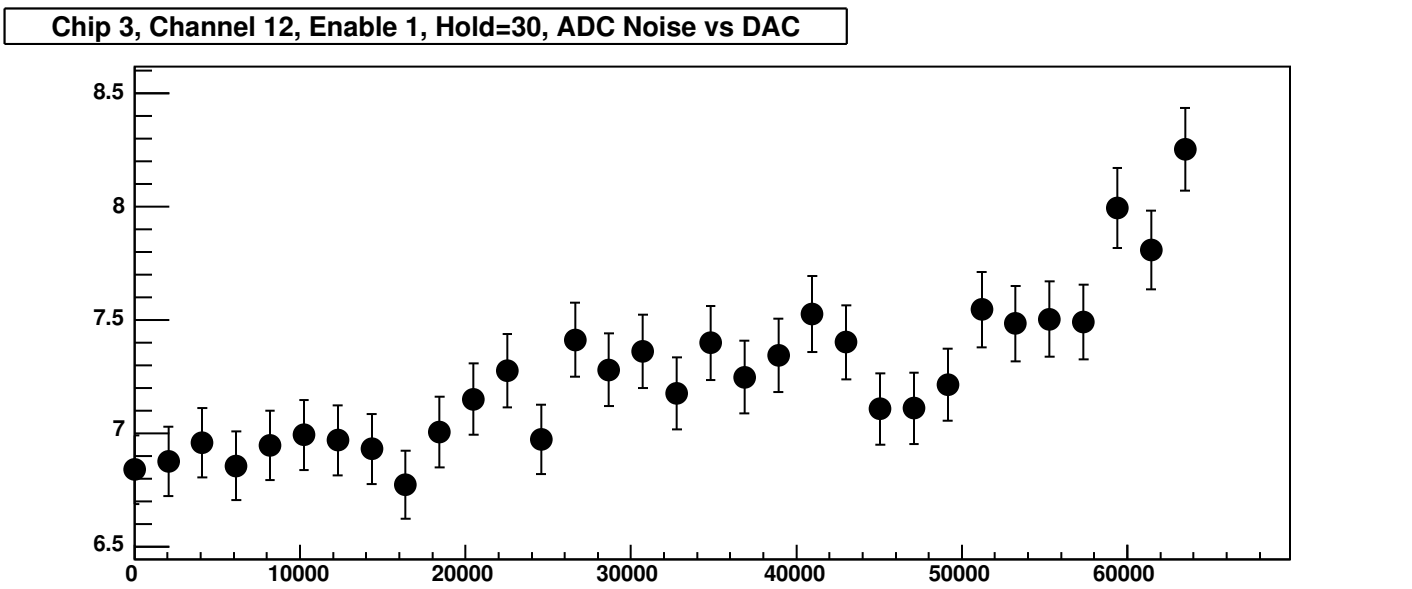

Chip 3, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

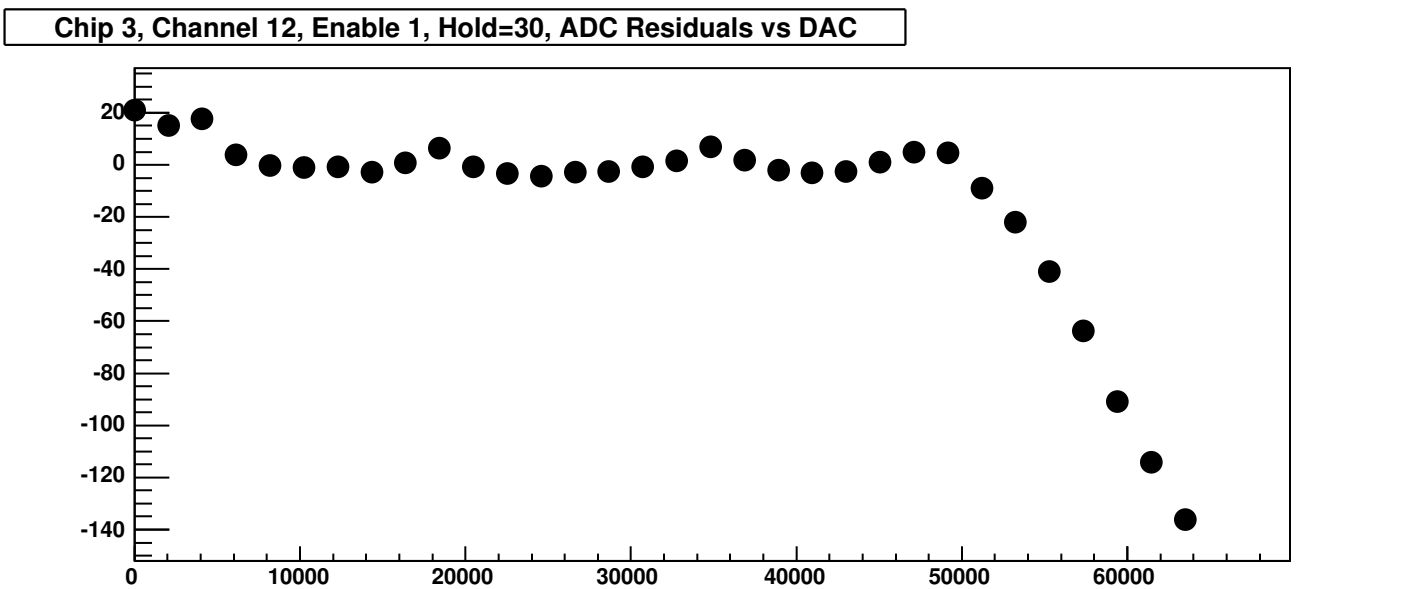

Chip 3, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4117 / 15
p0 1141 ± 0.1294
p1 -0.0008887 ± 5.269e-06
1160
1140
1120
1100
1080
1060
1040
1020
1000
980
0
10000
20000
30000
40000
50000
60000

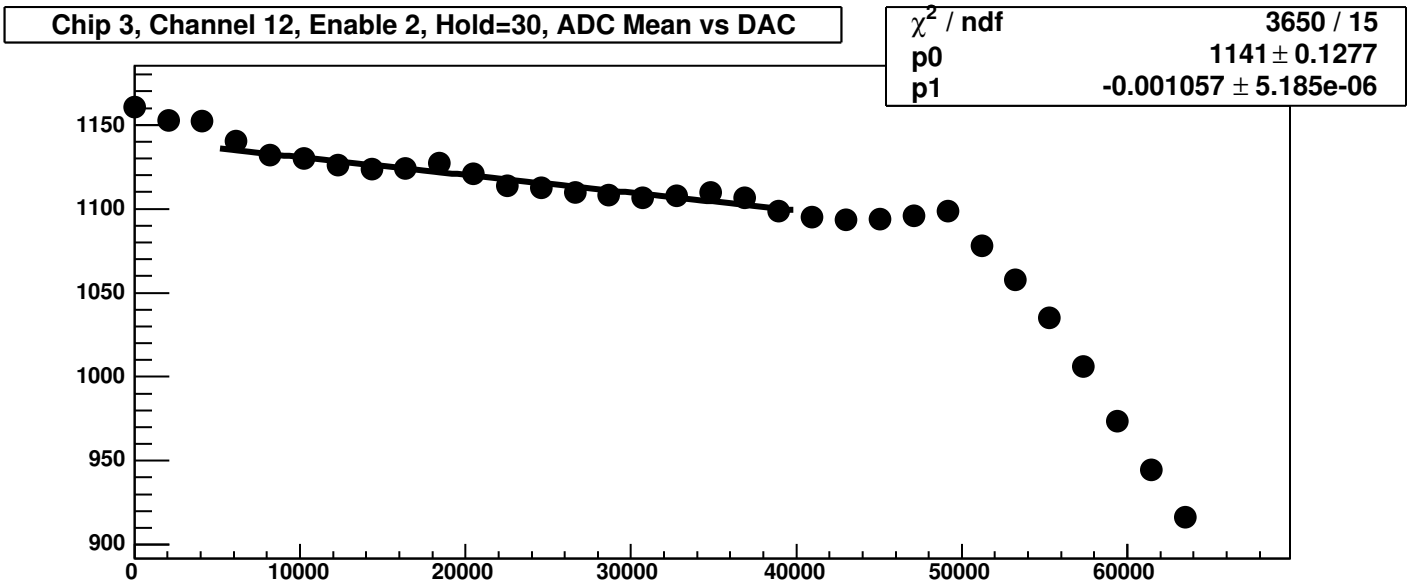

Chip 3, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

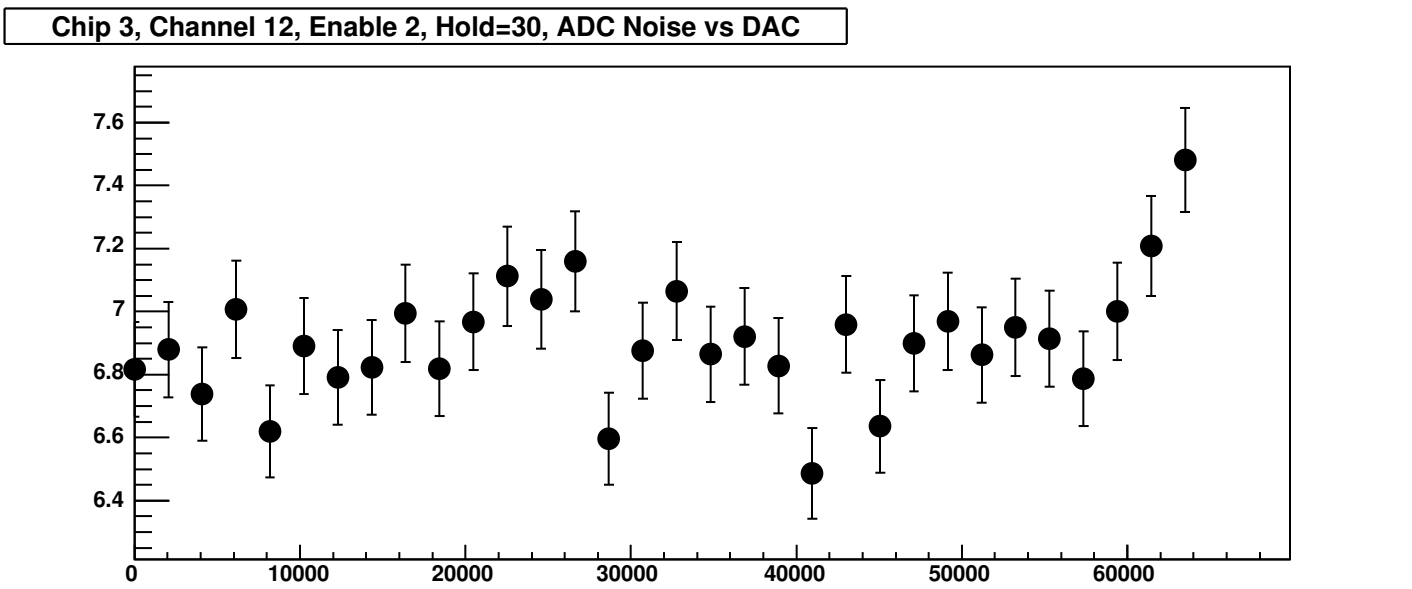

Chip 3, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

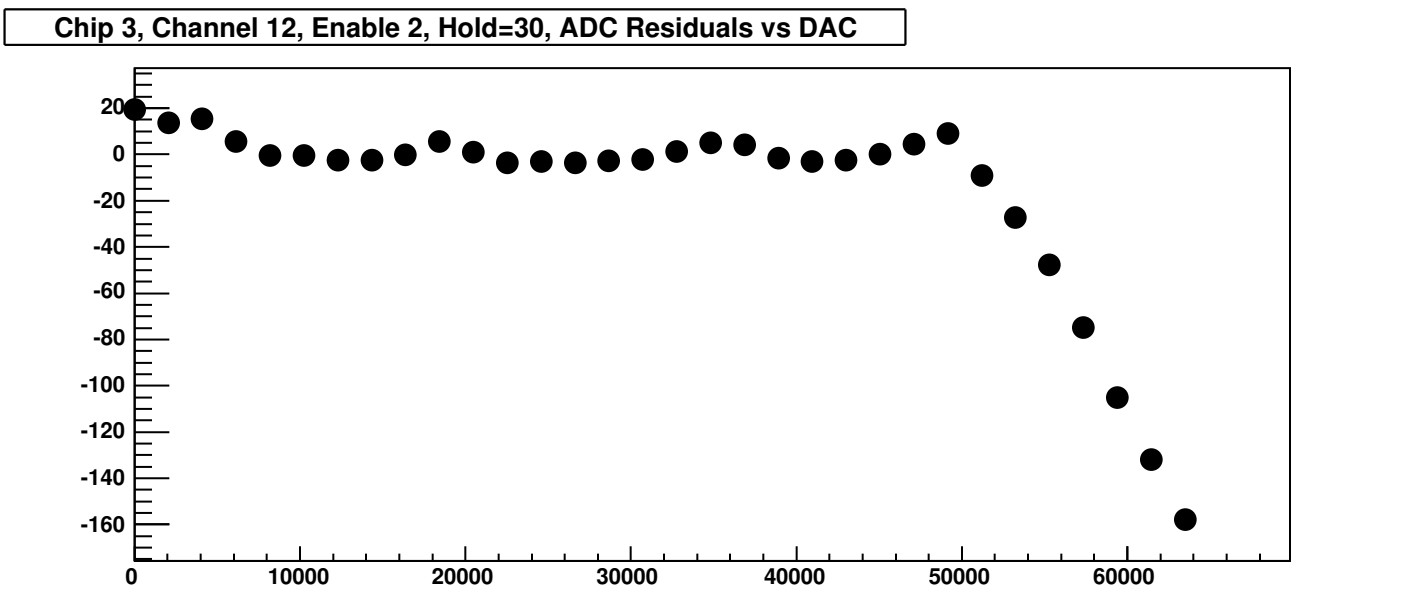

Chip 3, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3352 / 15
p0 1140 ± 0.1303
p1 0.0001513 ± 5.374e-06
1160
1140
1120
1100
1080
1060
1040
1020
1000
0
10000
20000
30000
40000
50000
60000

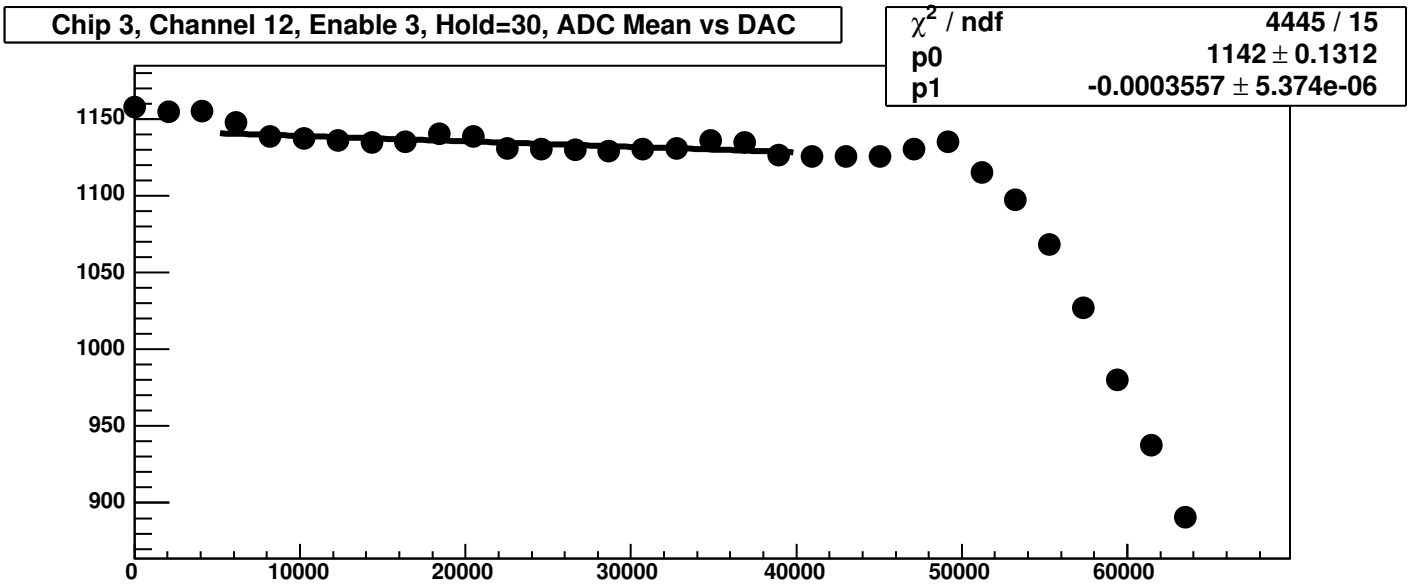

Chip 3, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000

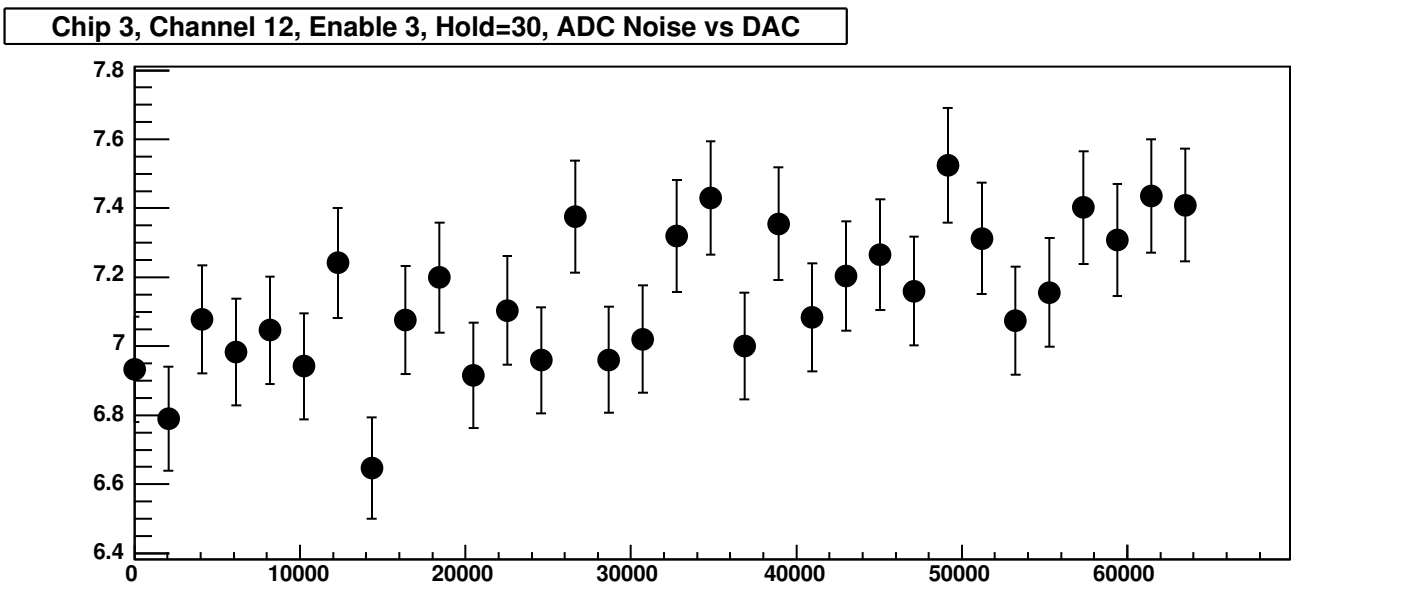

Chip 3, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

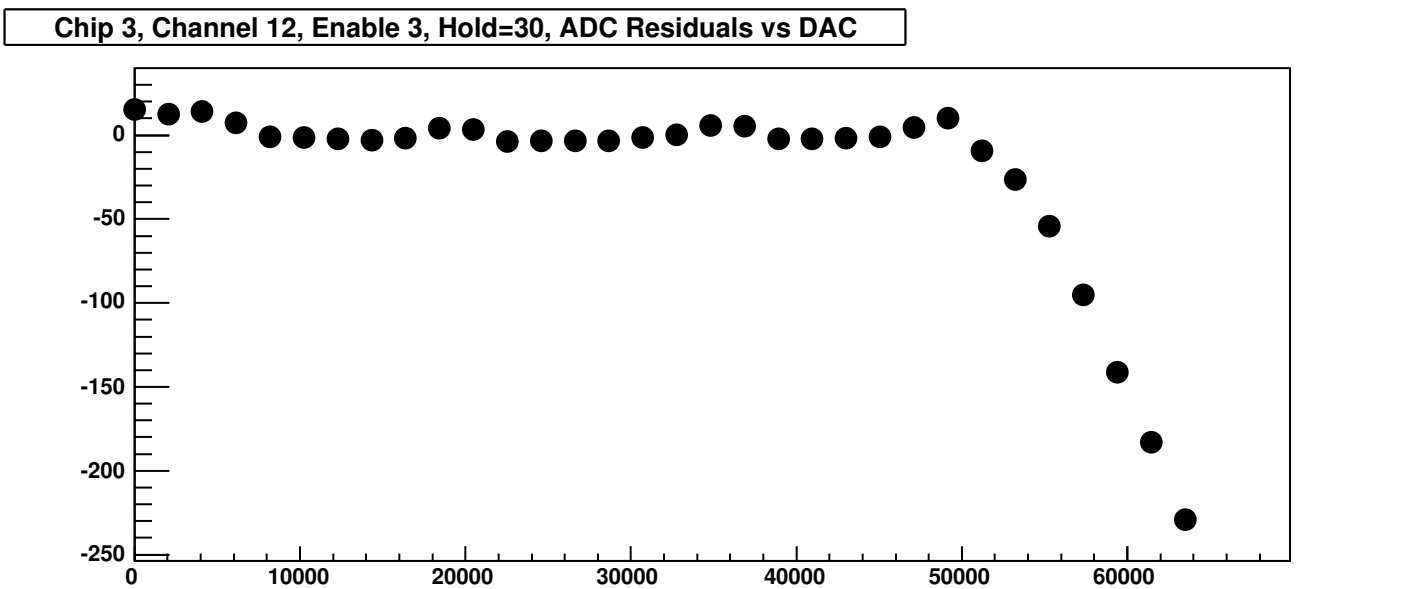

Chip 3, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3650 / 15
p0 1141 ± 0.1277
p1 -0.001057 ± 5.185e-06
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

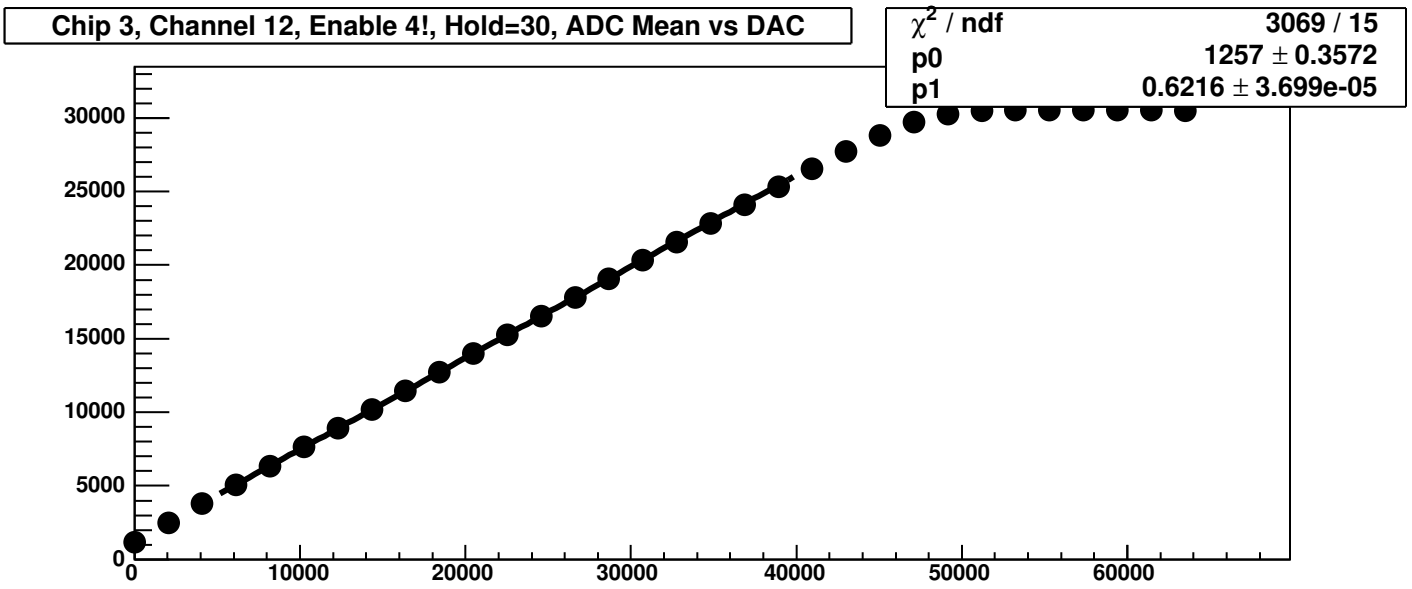

Chip 3, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

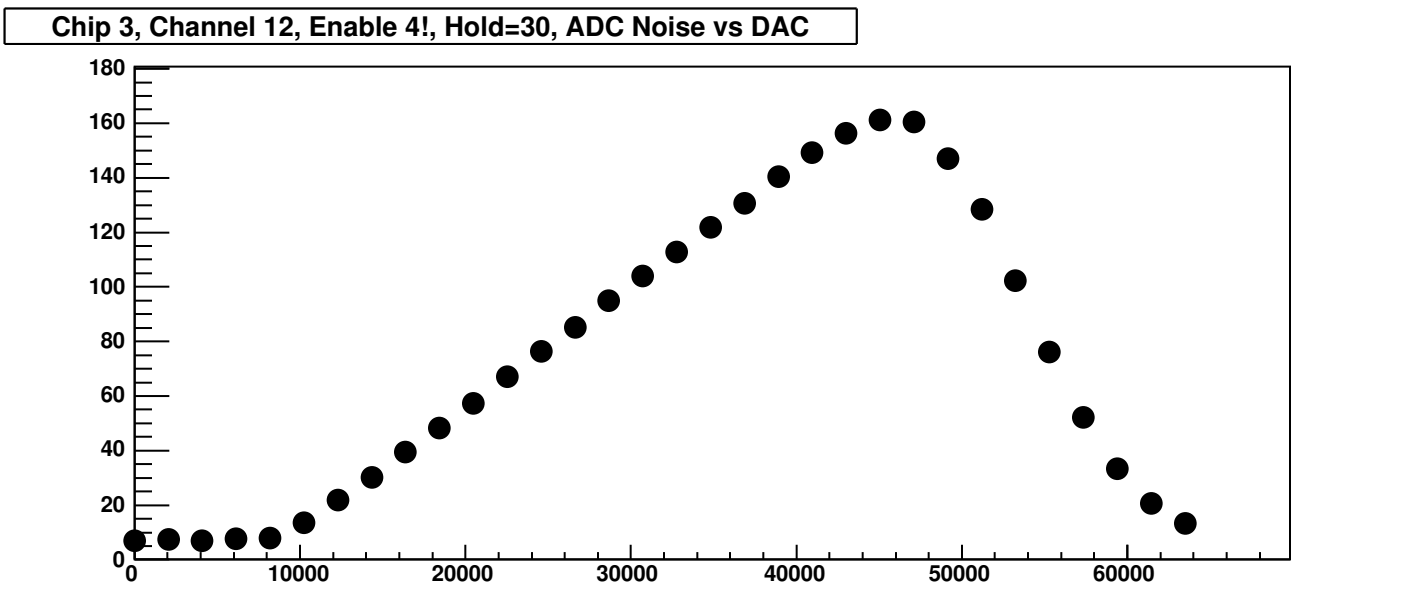

Chip 3, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

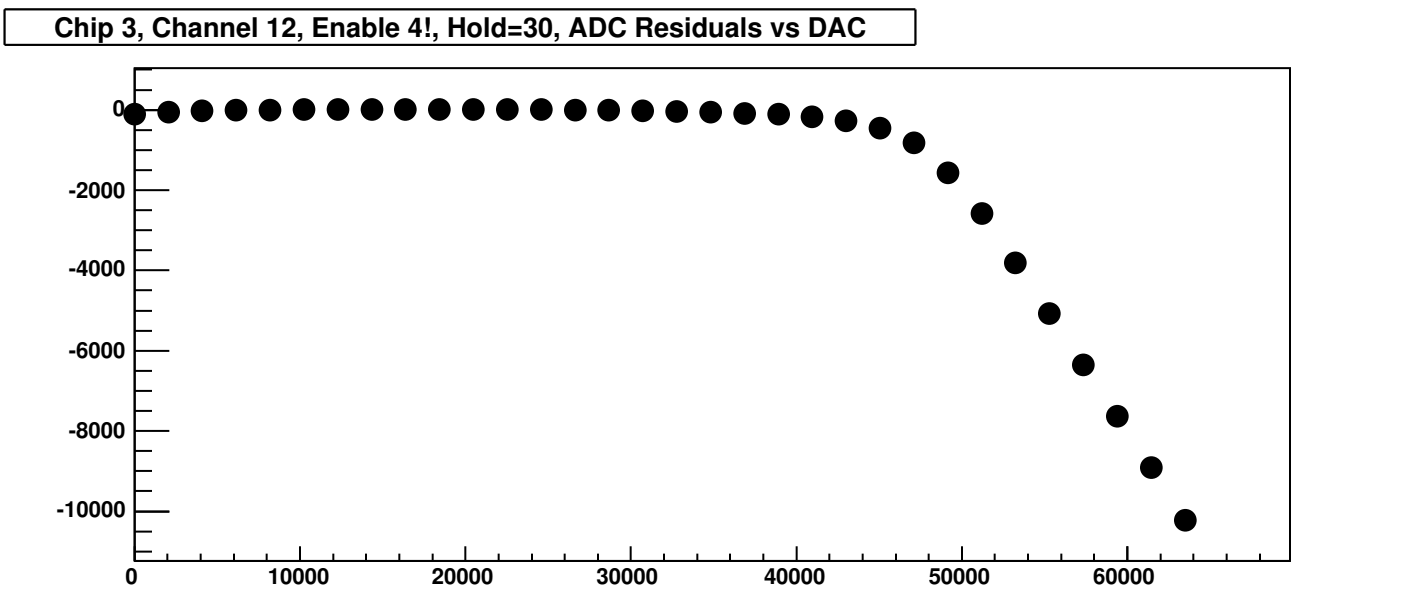

Chip 3, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4445 / 15
p0 1142 ± 0.1312
p1 -0.0003557 ± 5.374e-06
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

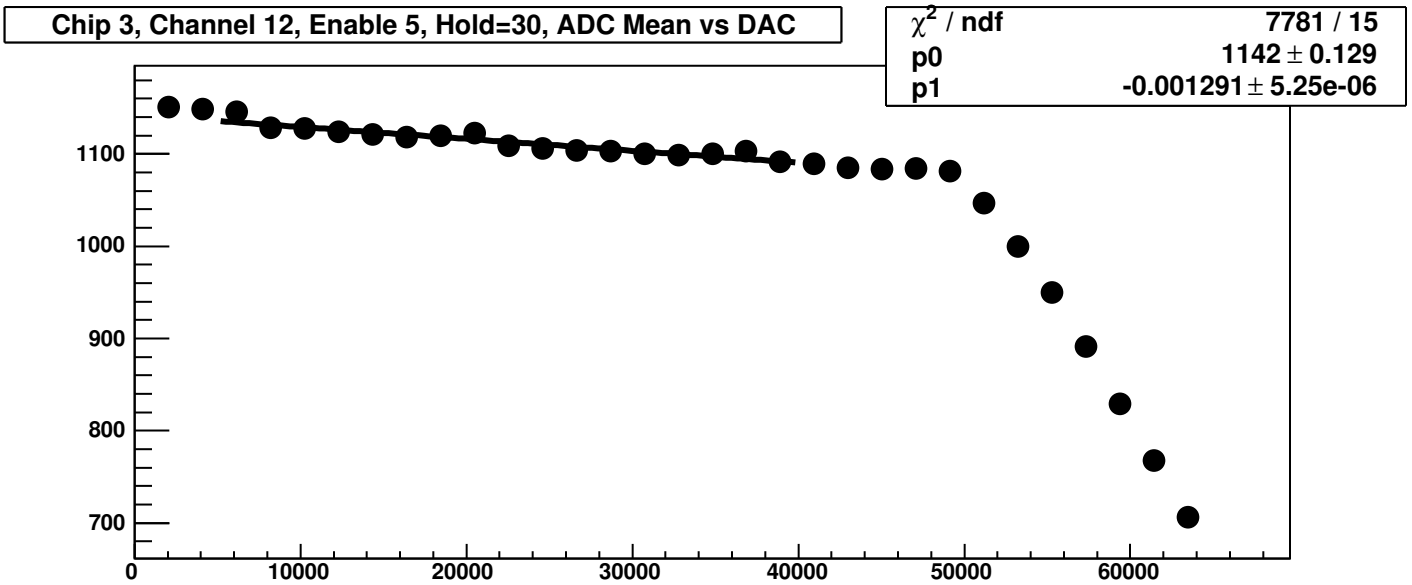

Chip 3, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

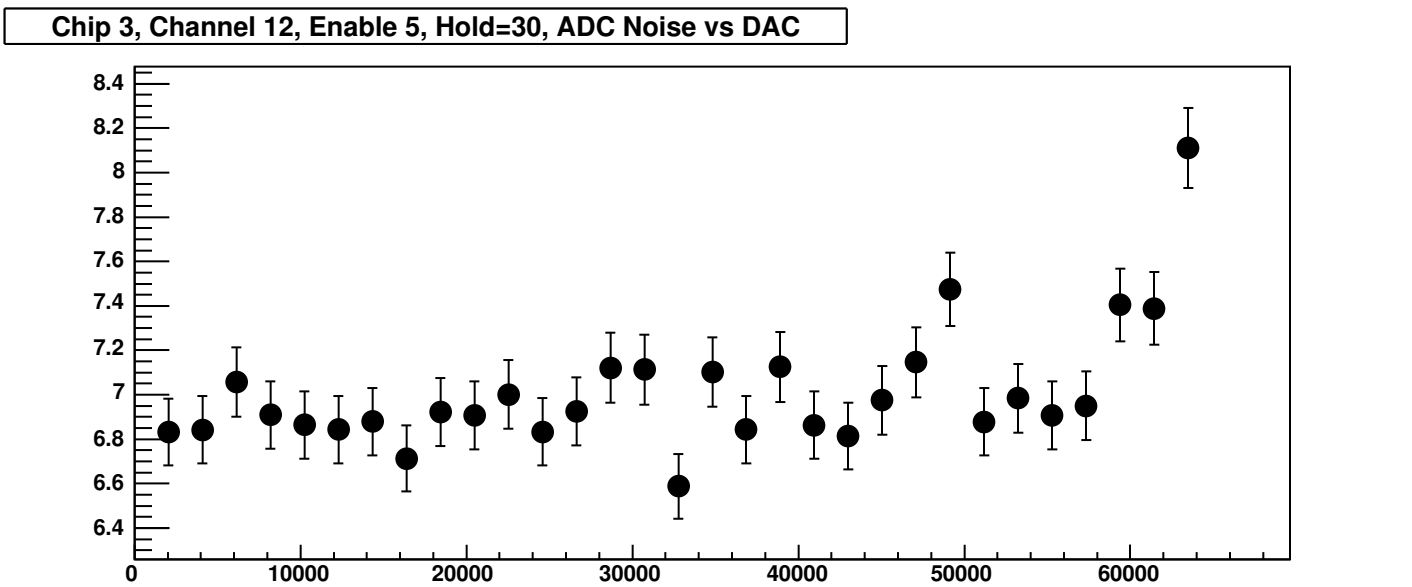

Chip 3, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

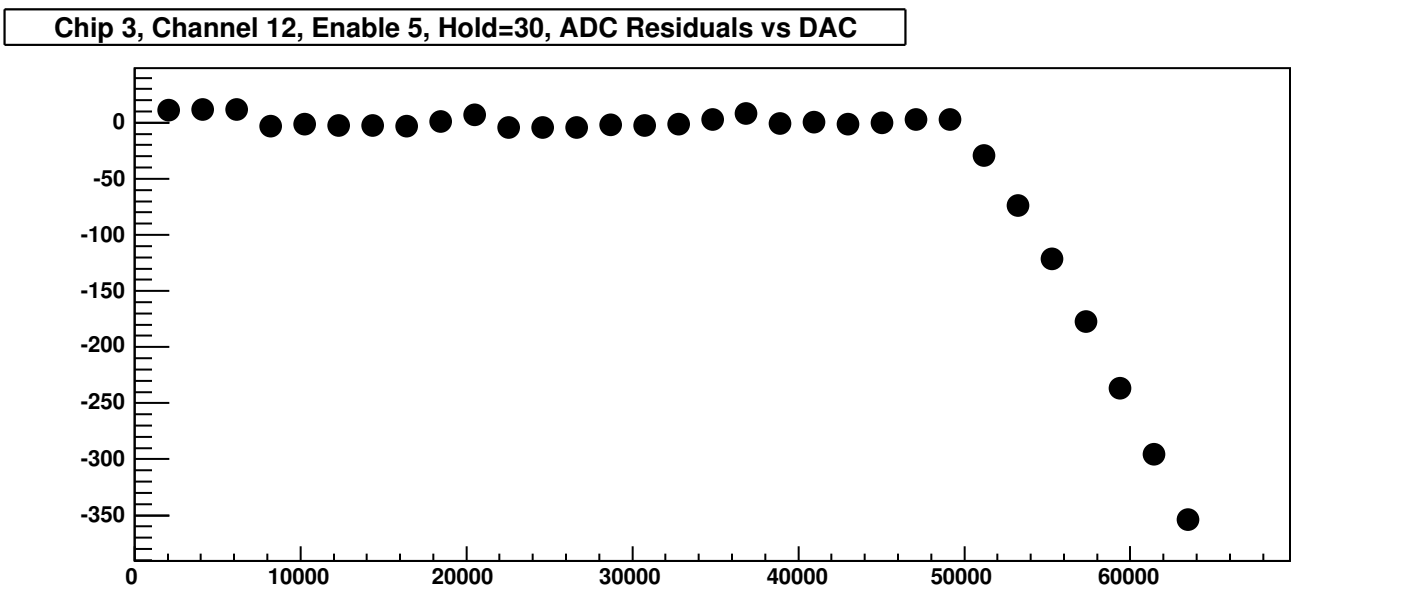

Chip 3, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 3069 / 15
p0 1257 ± 0.3572
p1 0.6216 ± 3.699e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

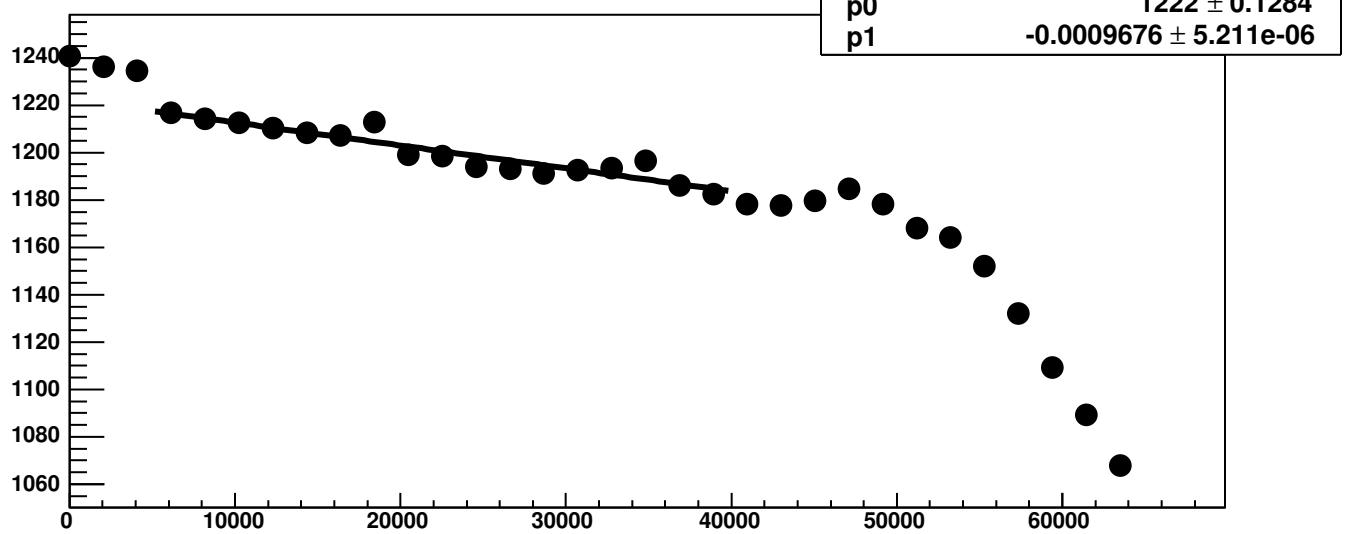

Chip 3, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7781 / 15
p0 1142 ± 0.129
p1 -0.001291 ± 5.25e-06
1100
1000
900
800
700
0
10000
20000
30000
40000
50000
60000

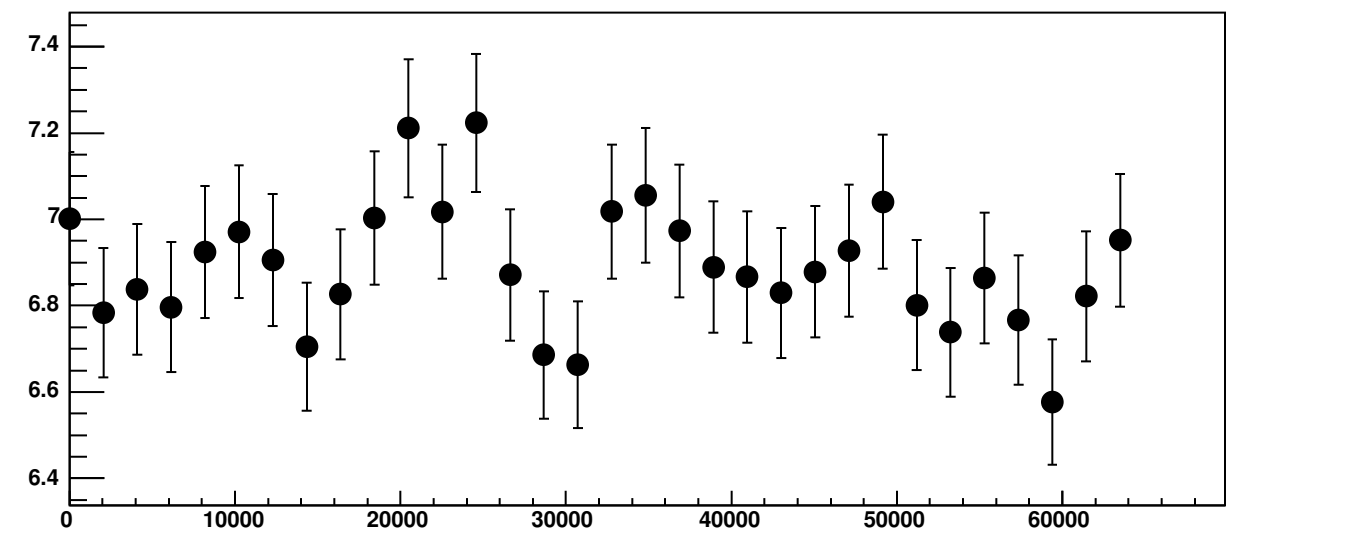

Chip 3, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
8.4
8.2
8
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

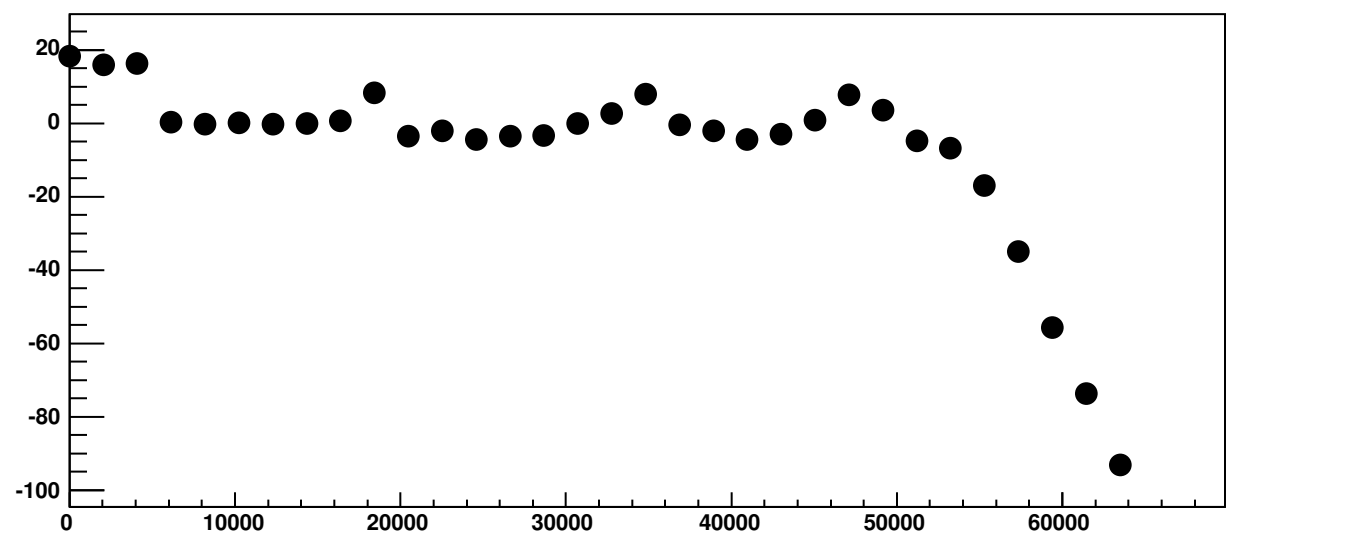
Chip 3, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC

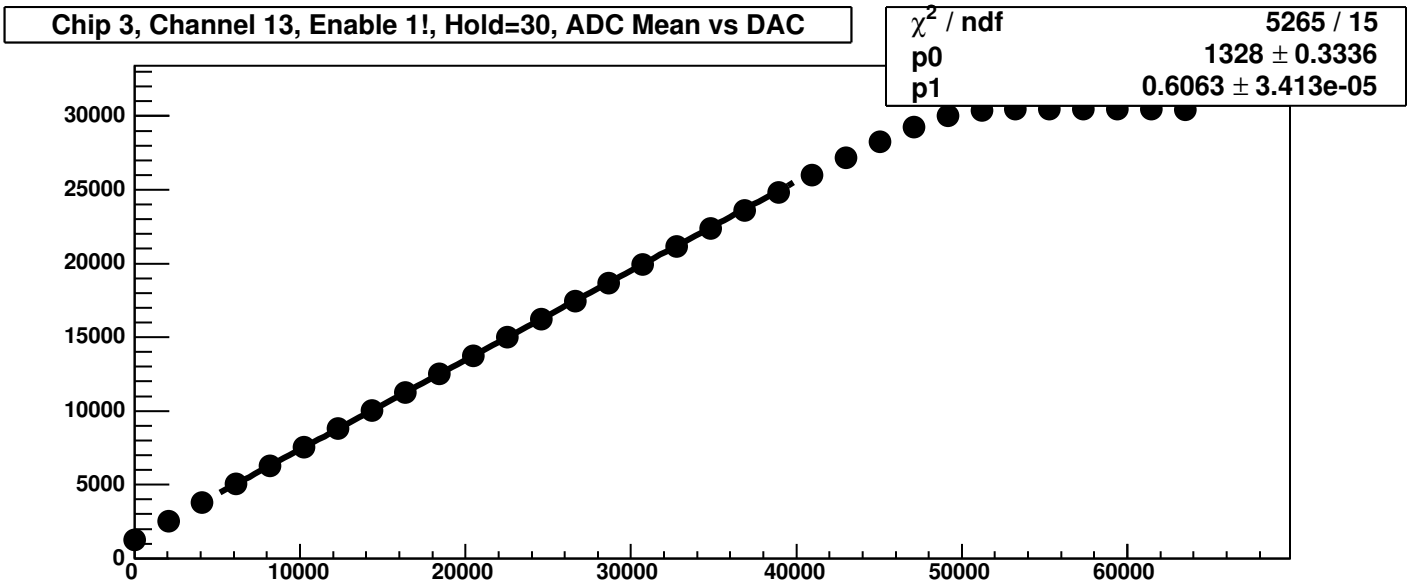


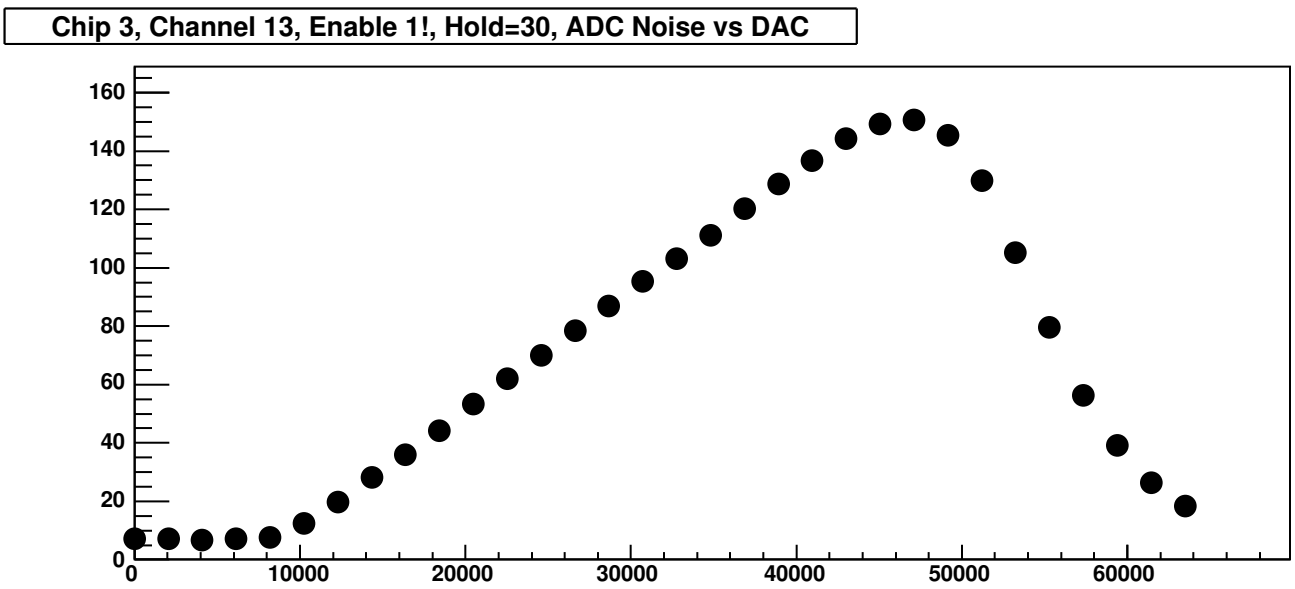
Chip 3, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC

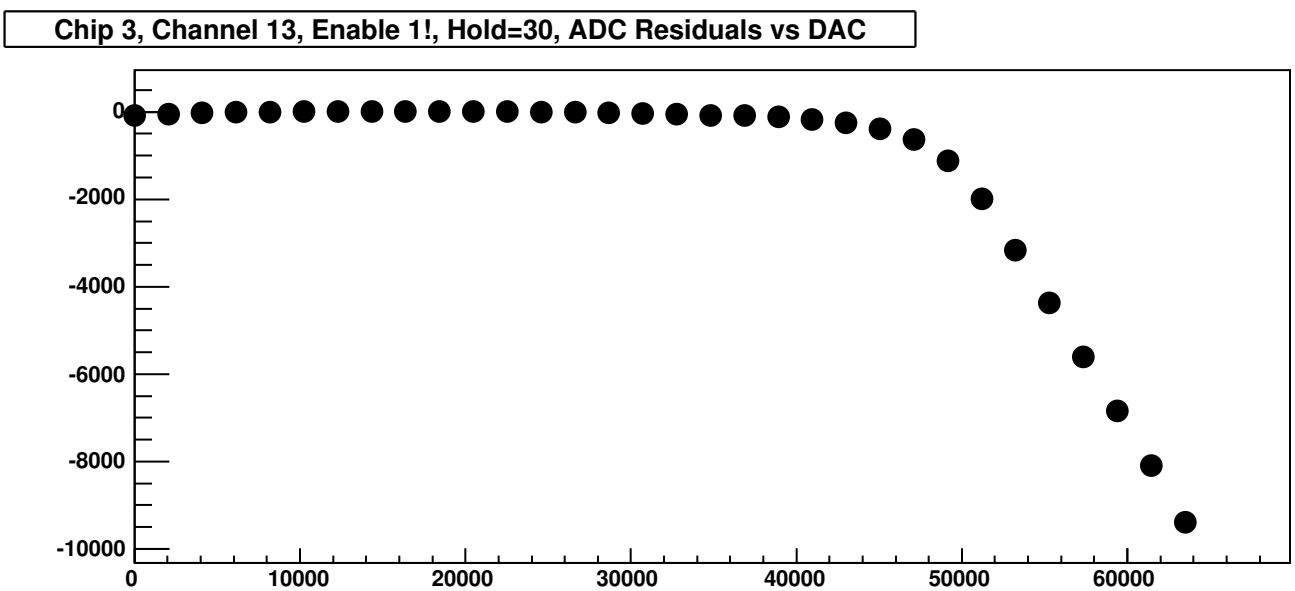


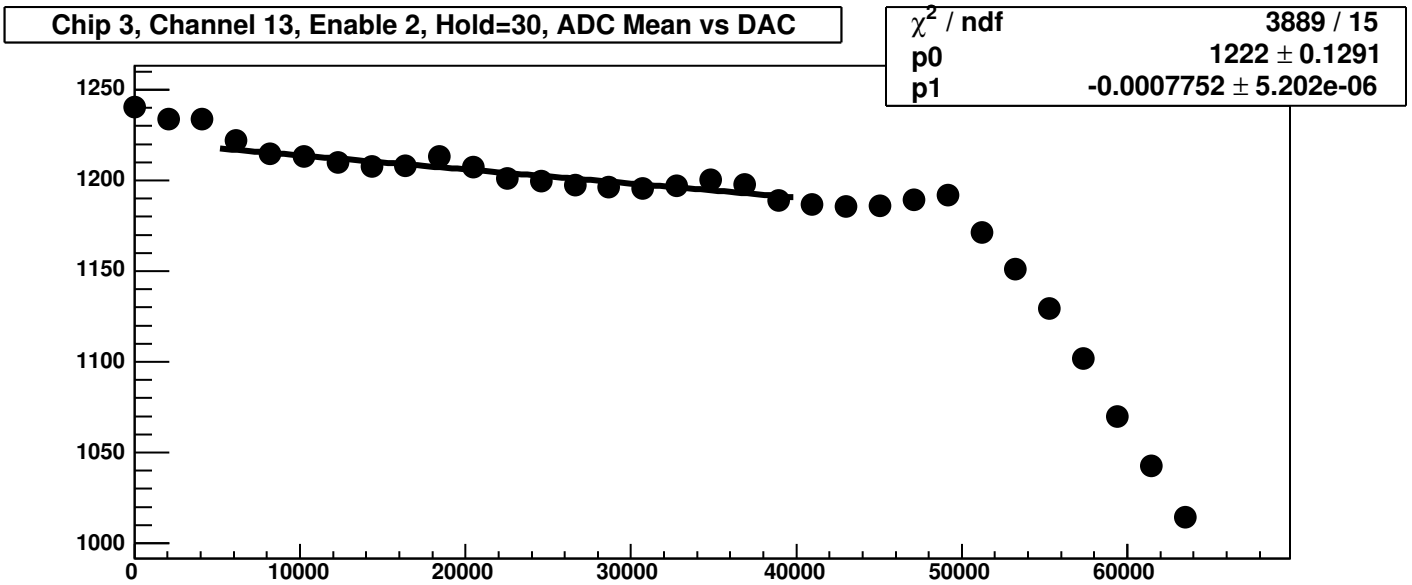
Chip 3, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC

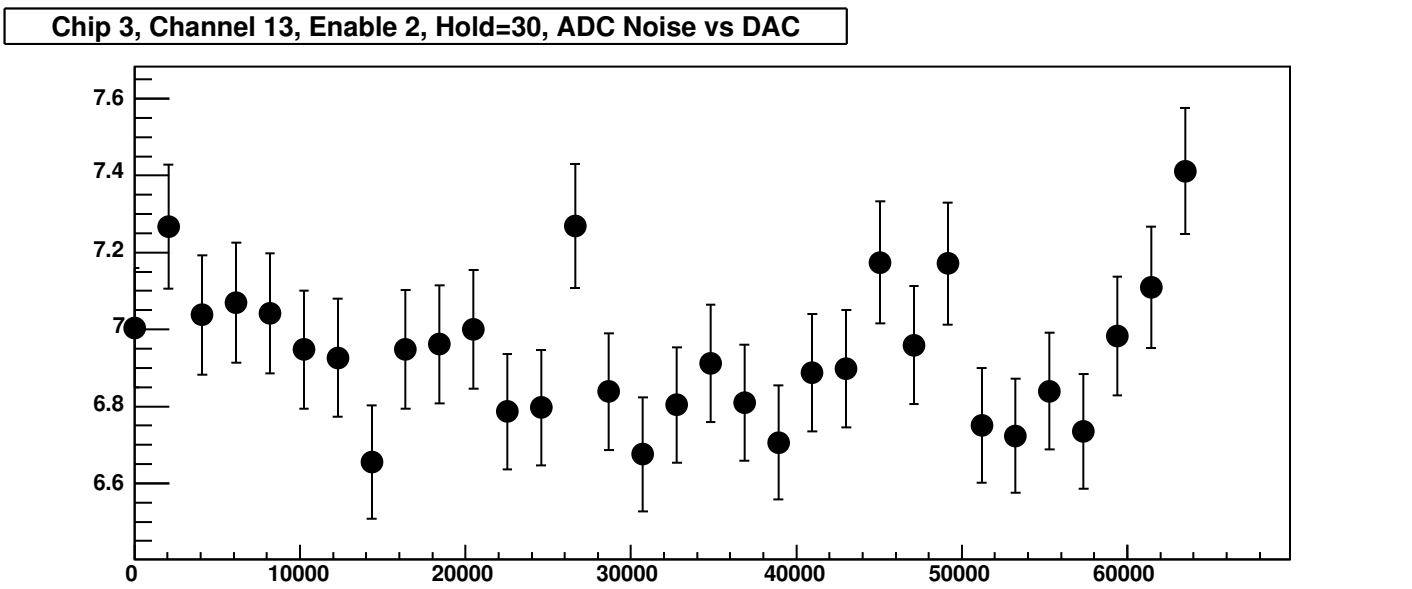


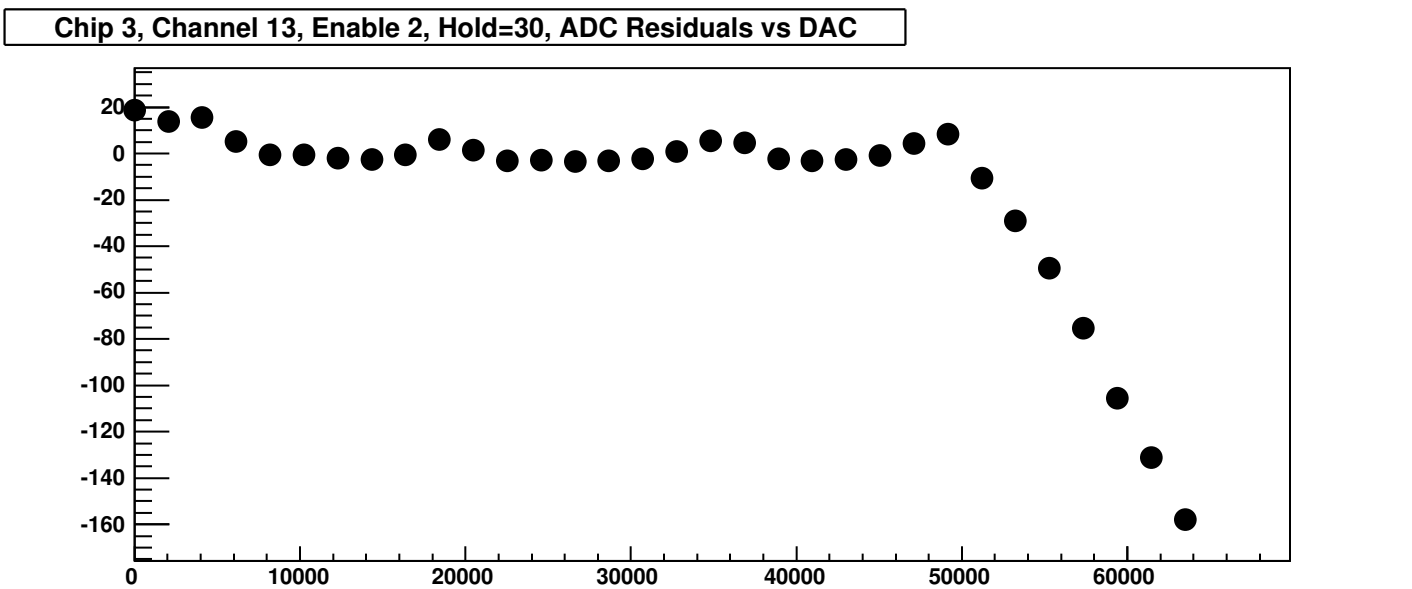

Chip 3, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 5265 / 15
p0 1328 ± 0.3336
p1 0.6063 ± 3.413e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

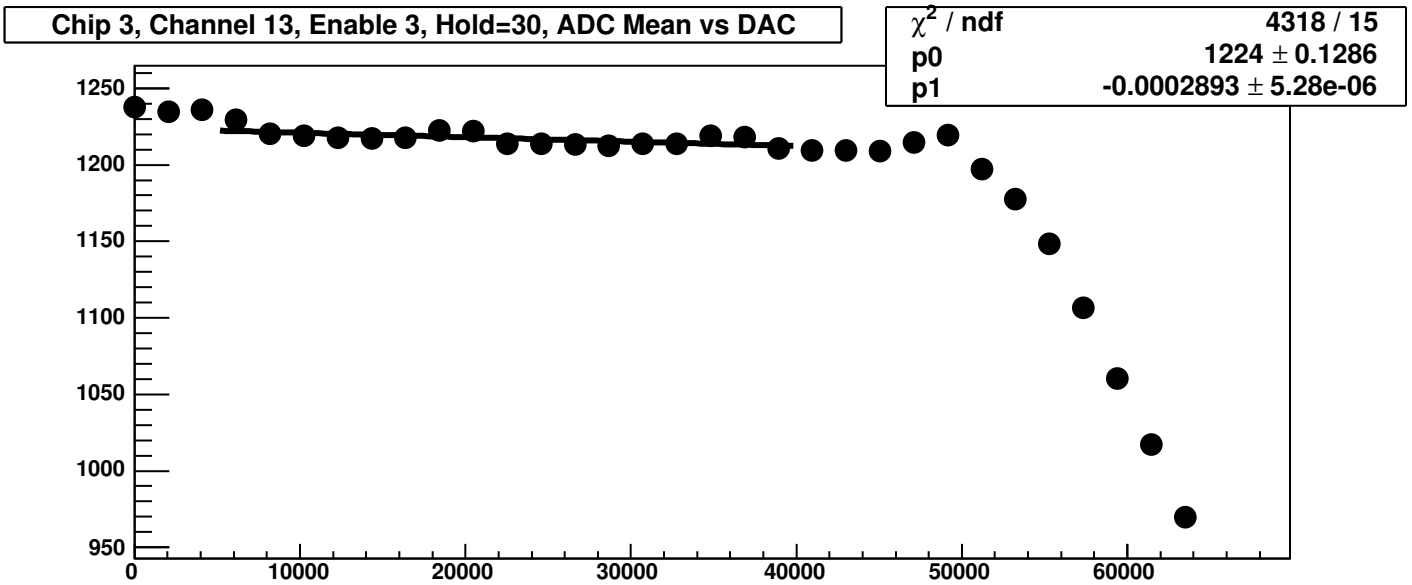

Chip 3, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

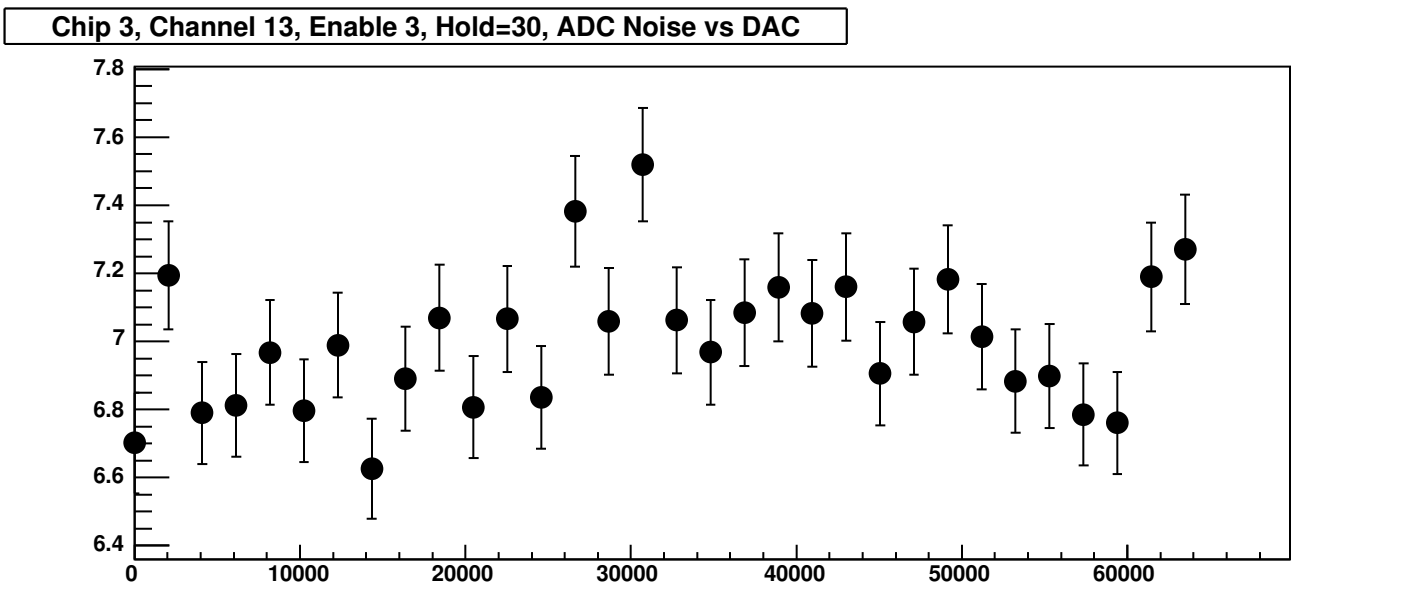

Chip 3, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

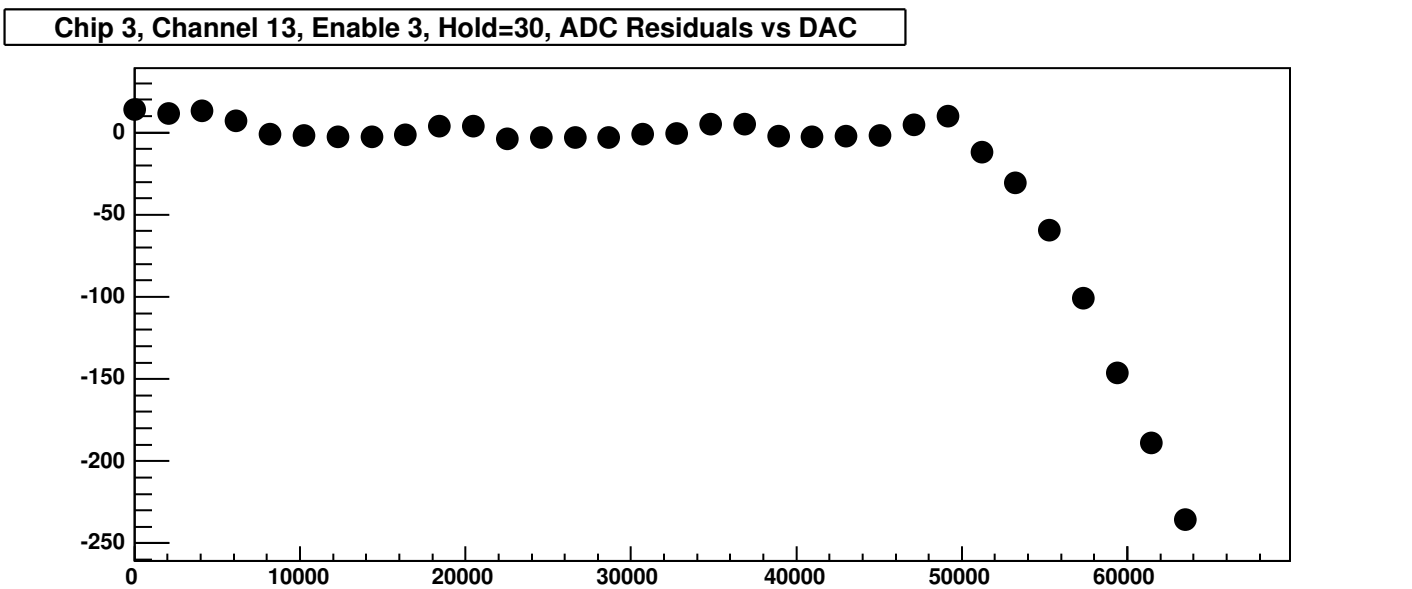

Chip 3, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3889 / 15
p0 1222 ± 0.1291
p1 -0.0007752 ± 5.202e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

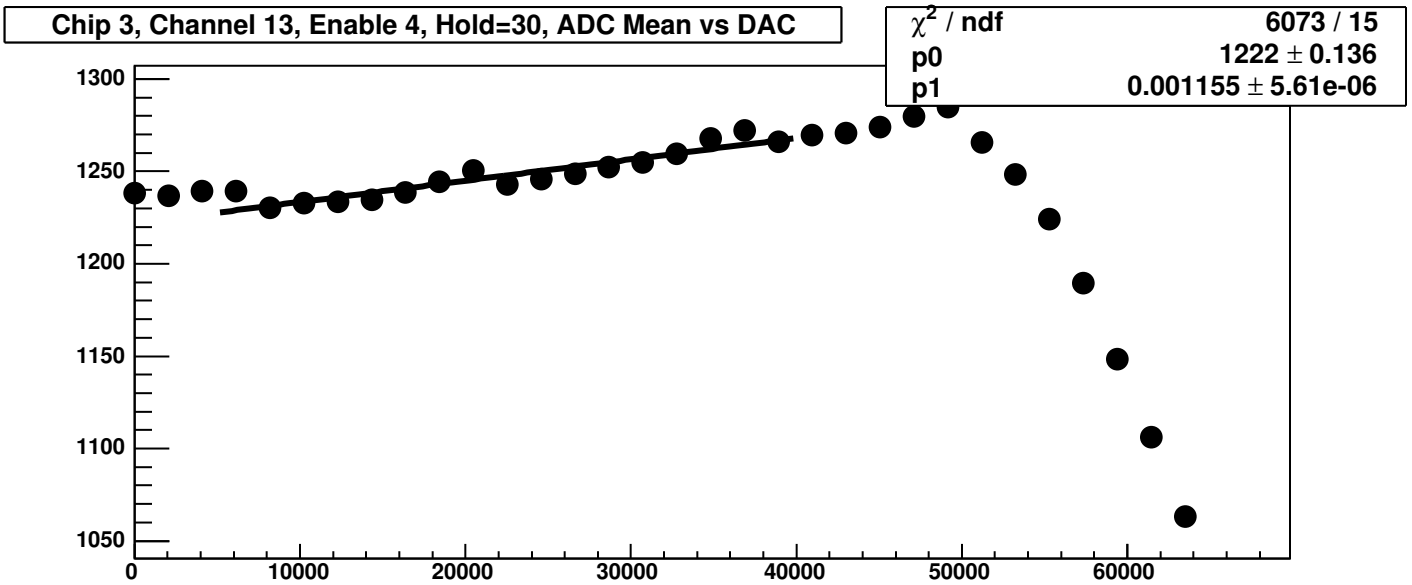

Chip 3, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

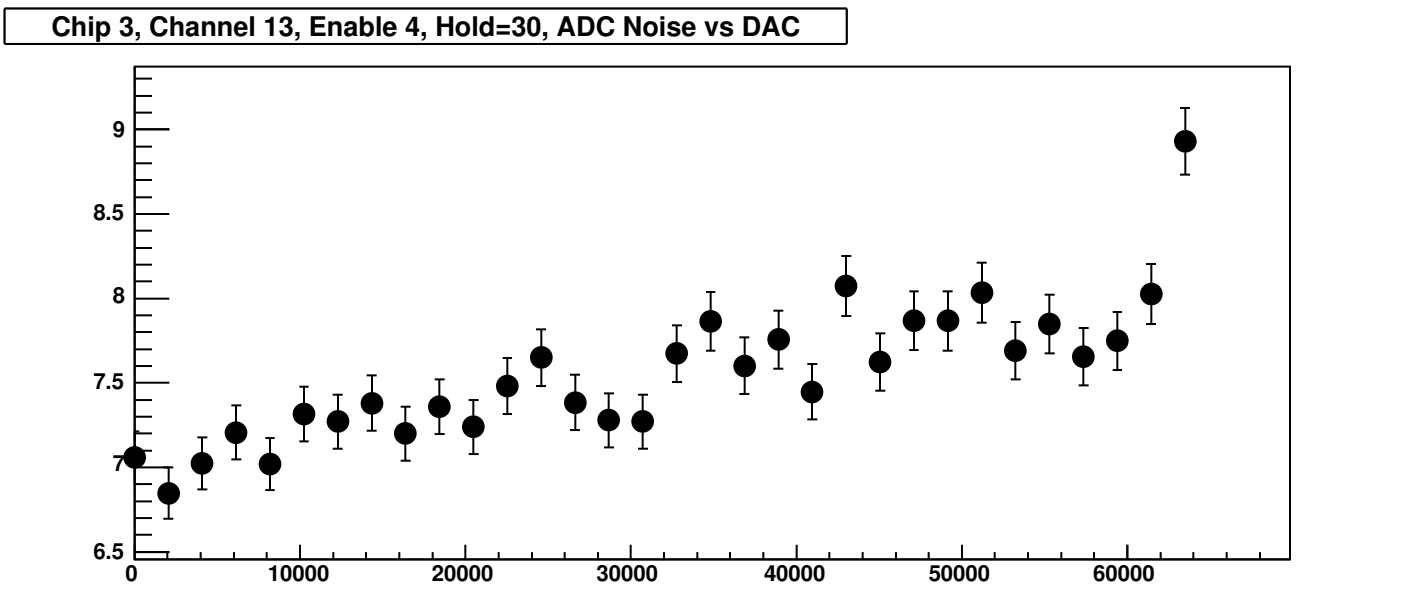

Chip 3, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

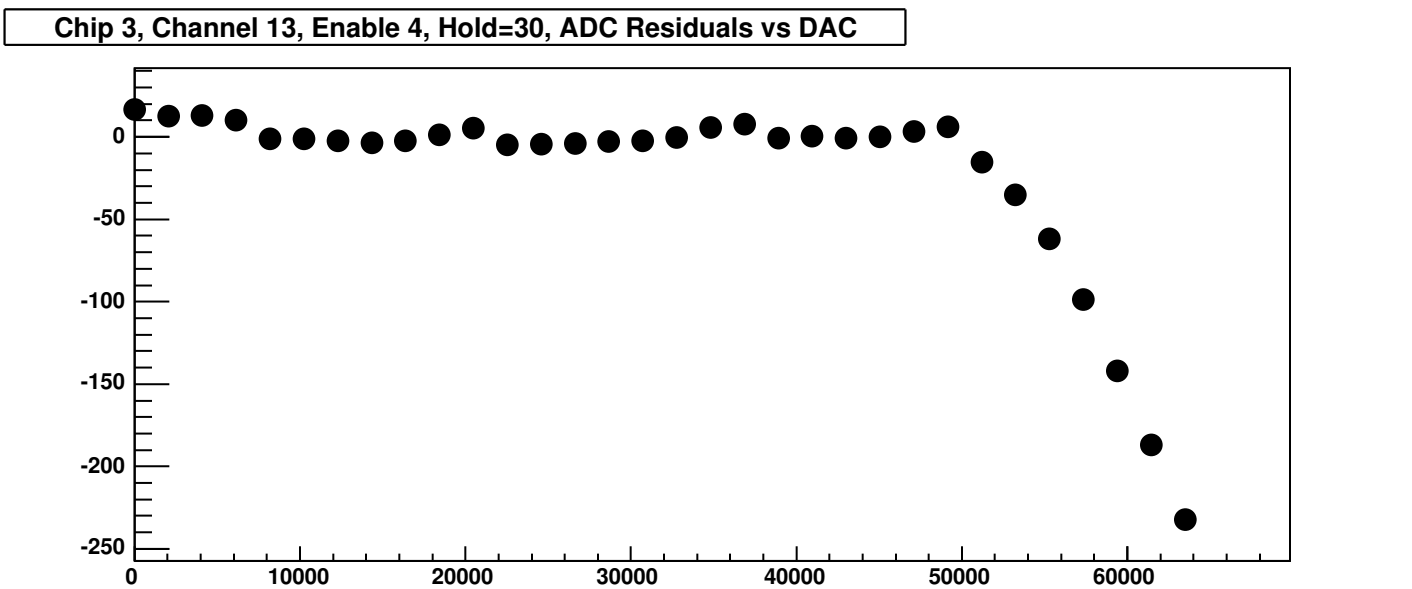

Chip 3, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4318 / 15
p0 1224 ± 0.1286
p1 -0.0002893 ± 5.28e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

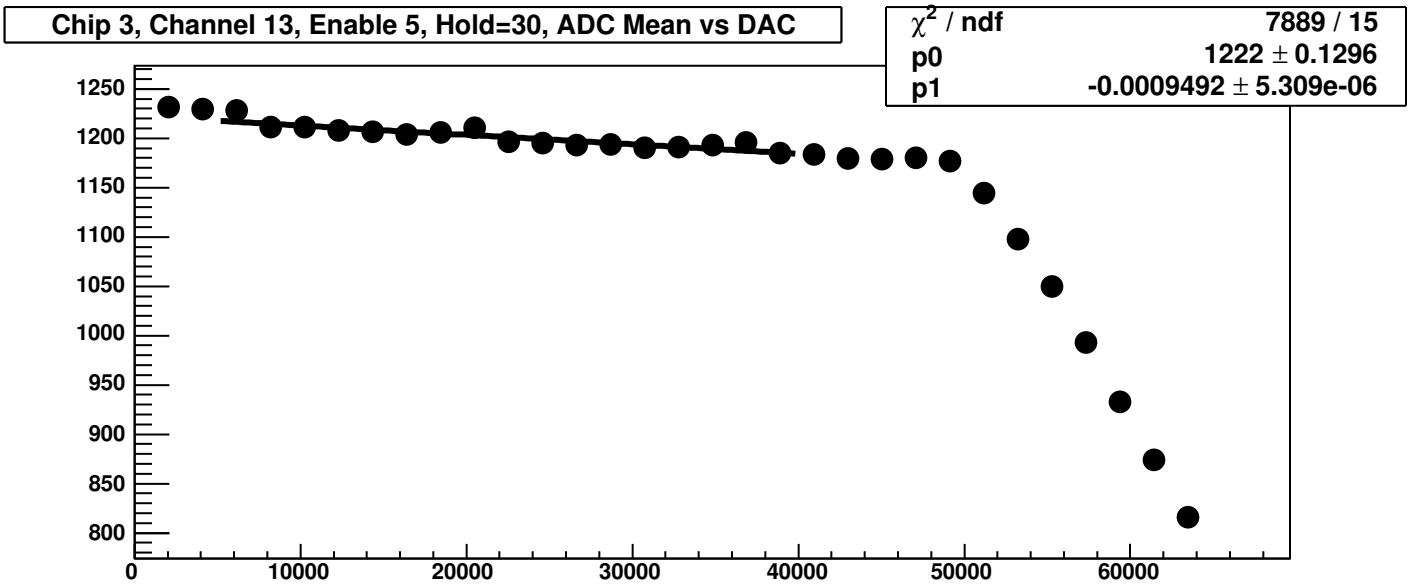

Chip 3, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

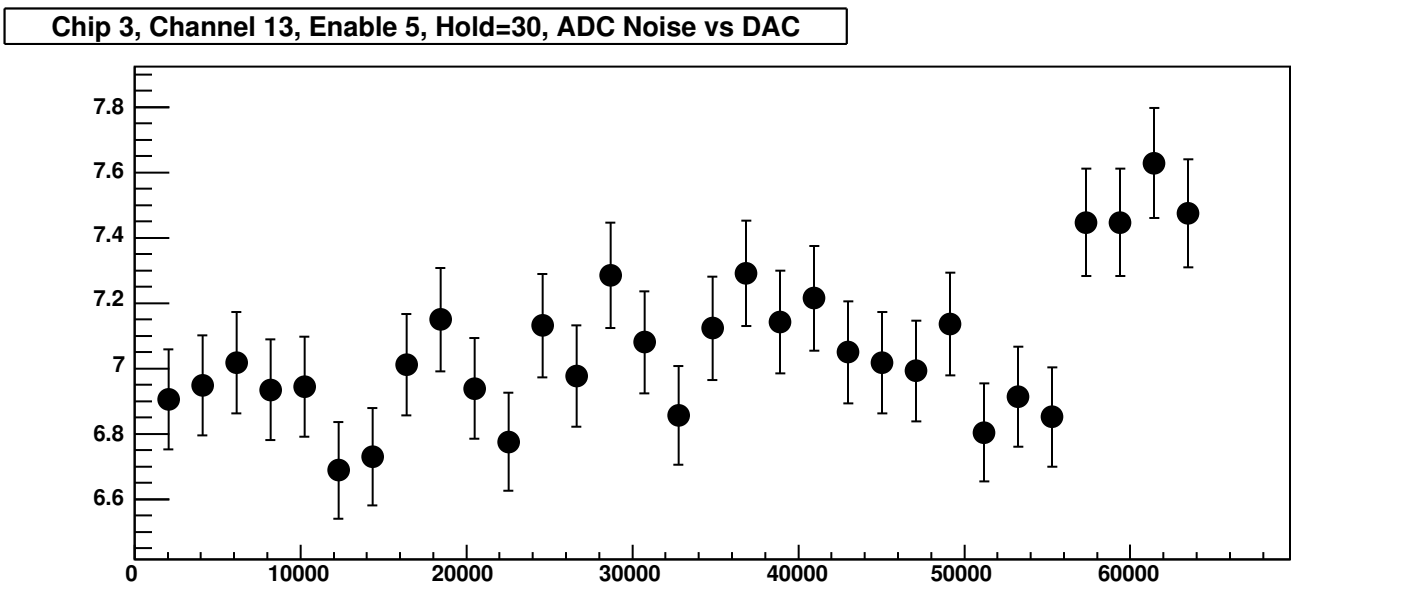

Chip 3, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

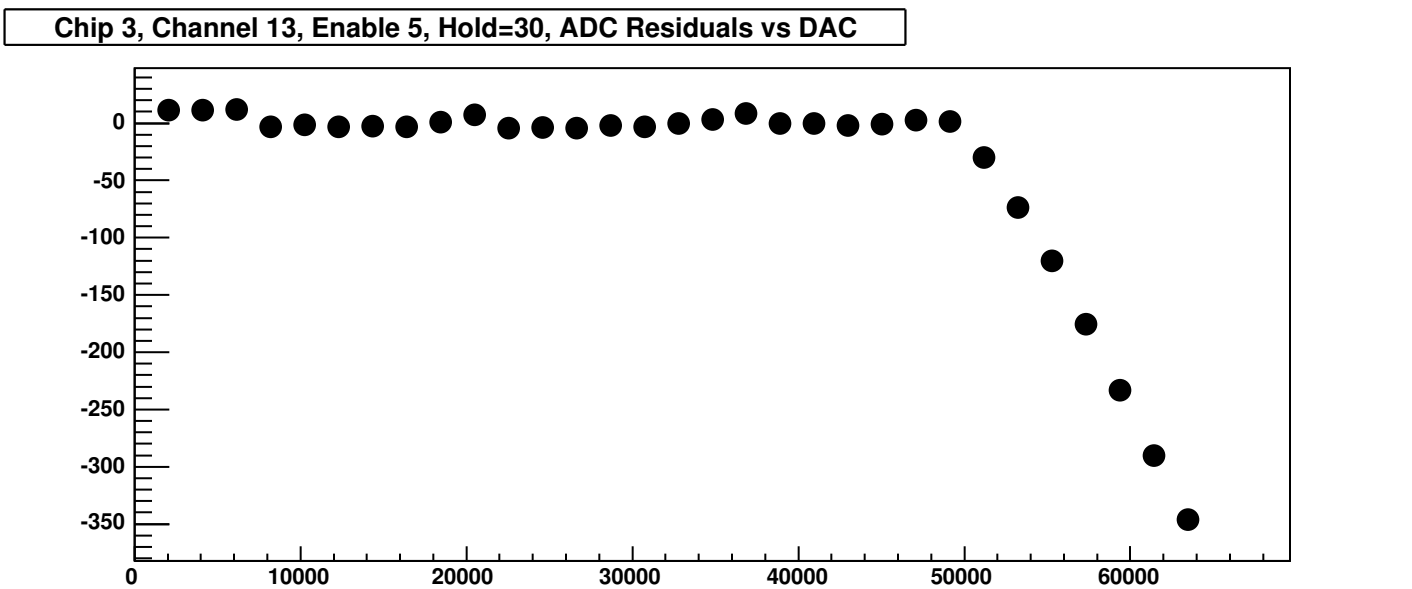

Chip 3, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6073 / 15
p0 1222 ± 0.136
p1 0.001155 ± 5.61e-06
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

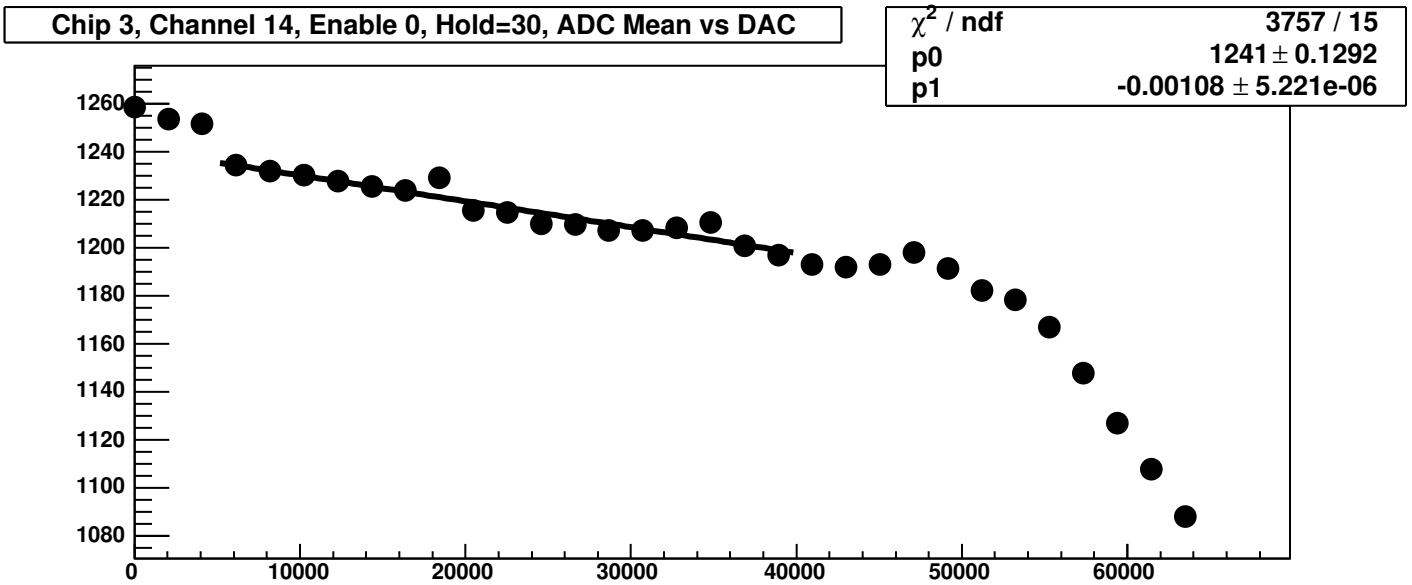

Chip 3, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC
9
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000

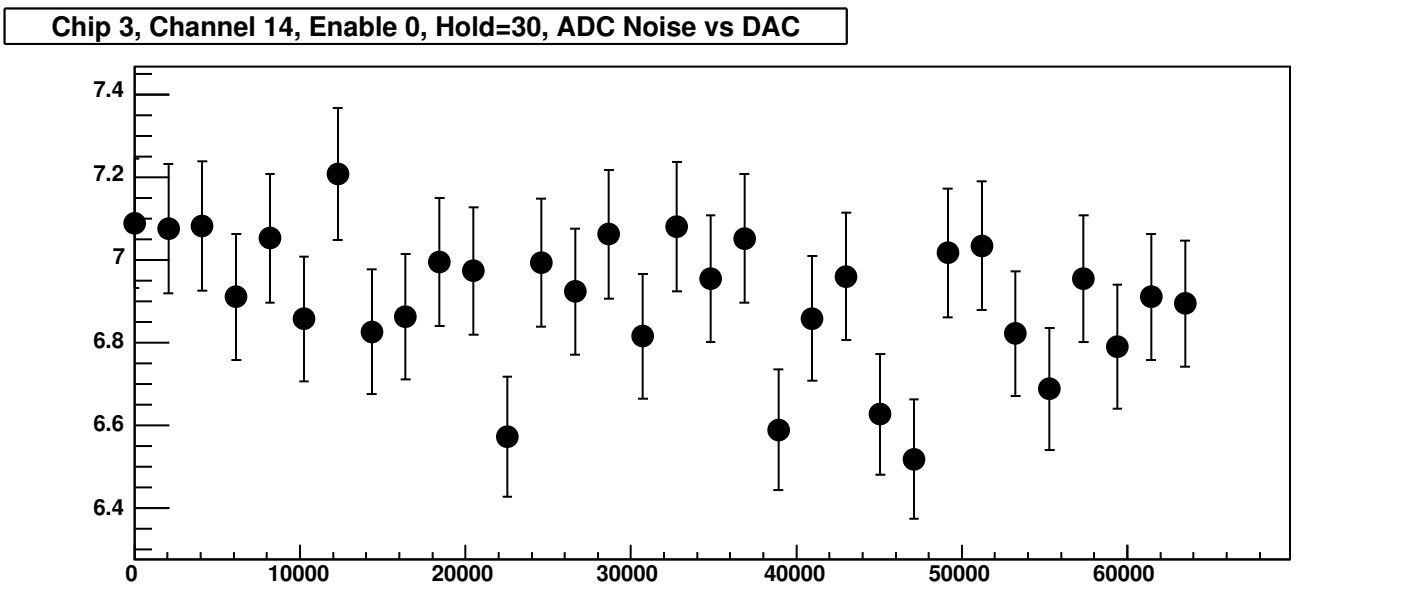

Chip 3, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

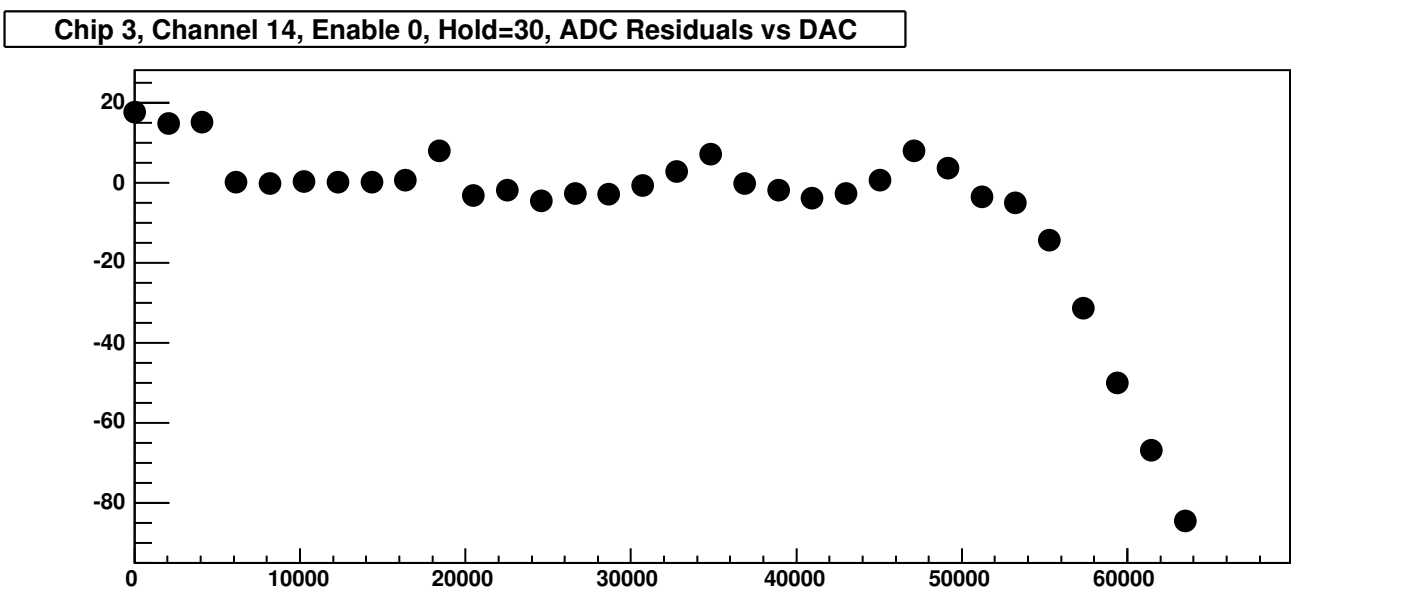

Chip 3, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7889 / 15
p0 1222 ± 0.1296
p1 -0.0009492 ± 5.309e-06
1250
1200
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

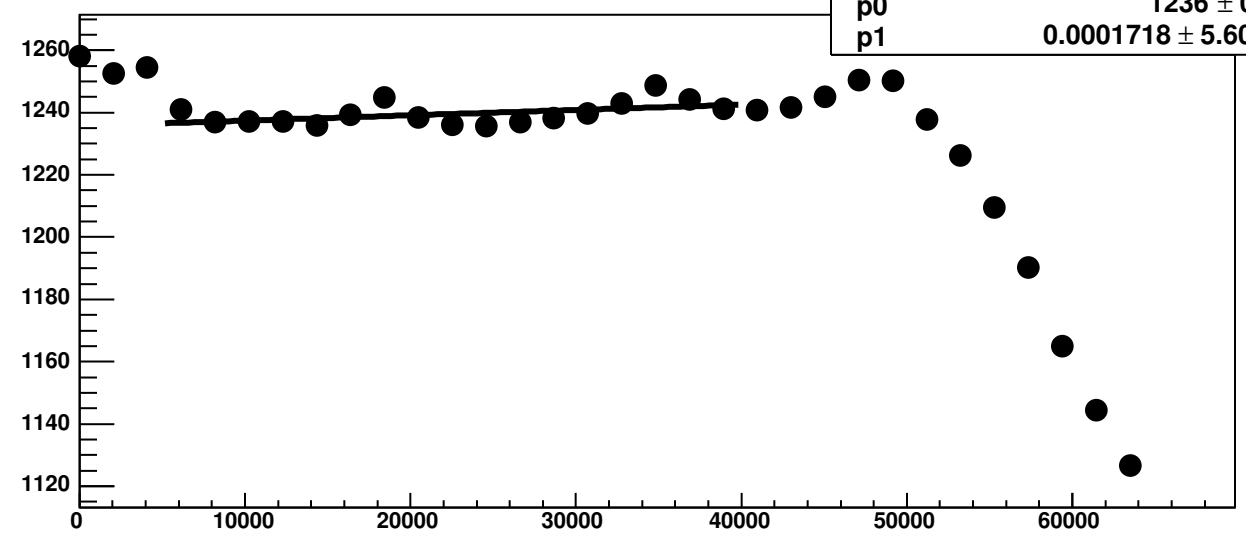

Chip 3, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3757 / 15
p0 1241 ± 0.1292
p1 -0.00108 ± 5.221e-06
1260
1240
1220
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

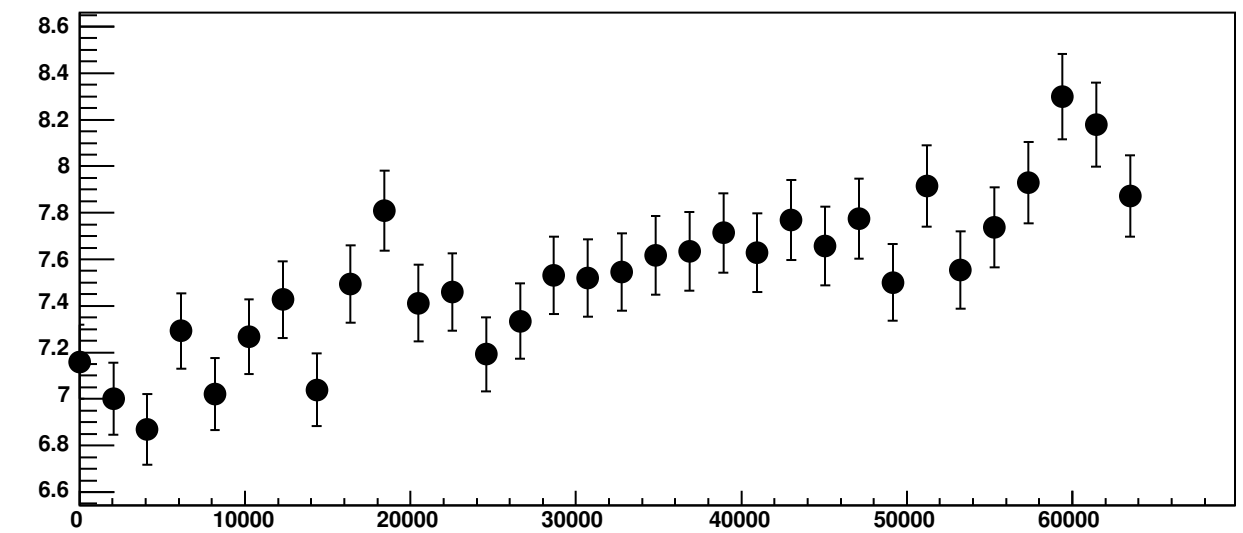

Chip 3, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

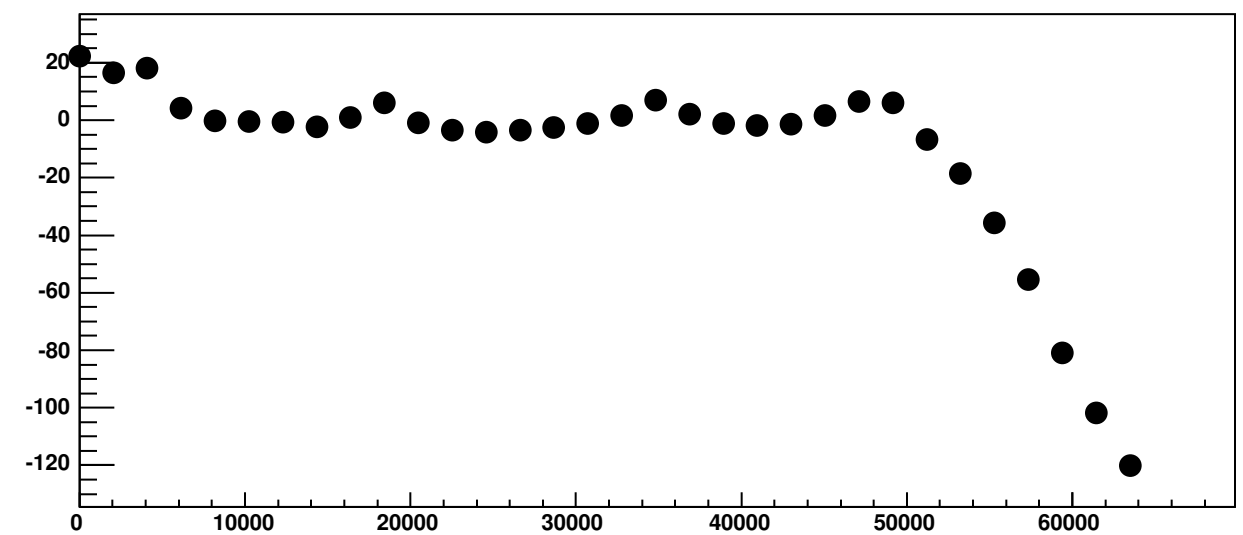
Chip 3, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC

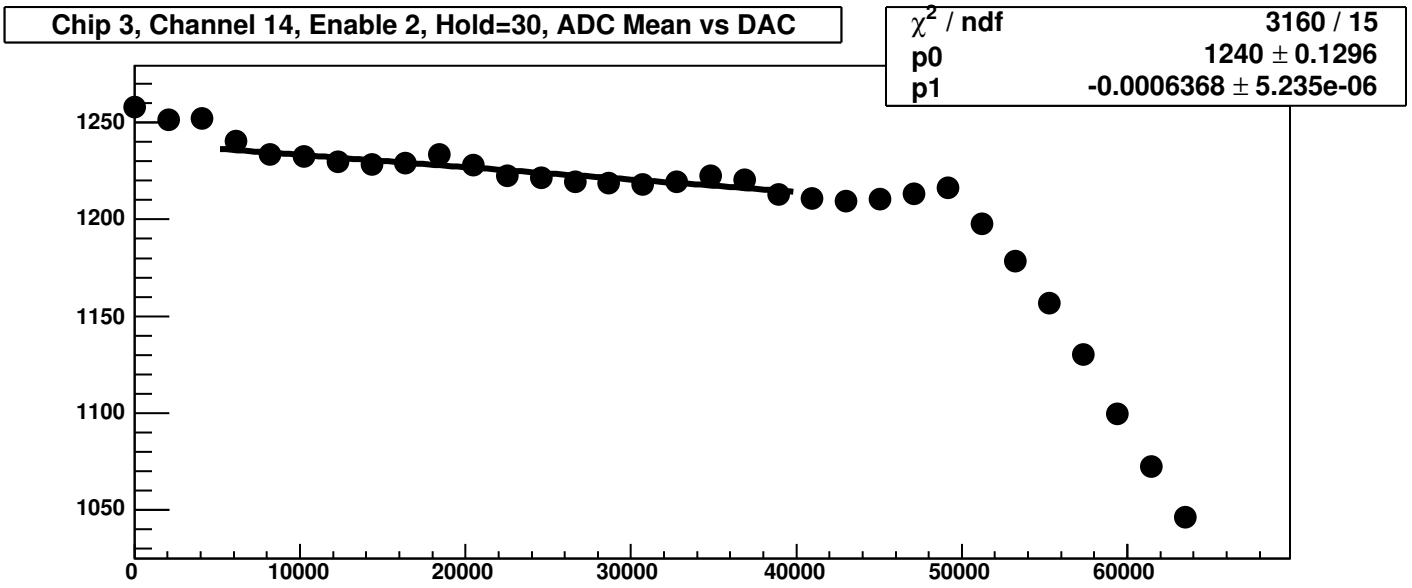


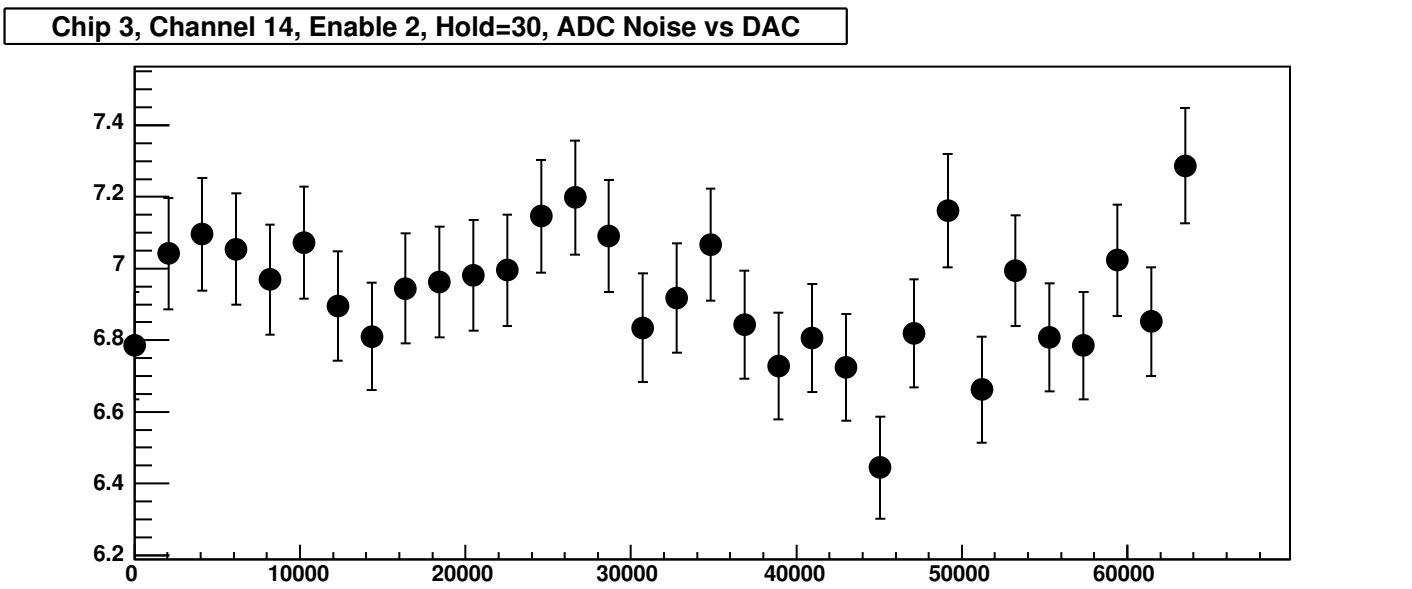
Chip 3, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC

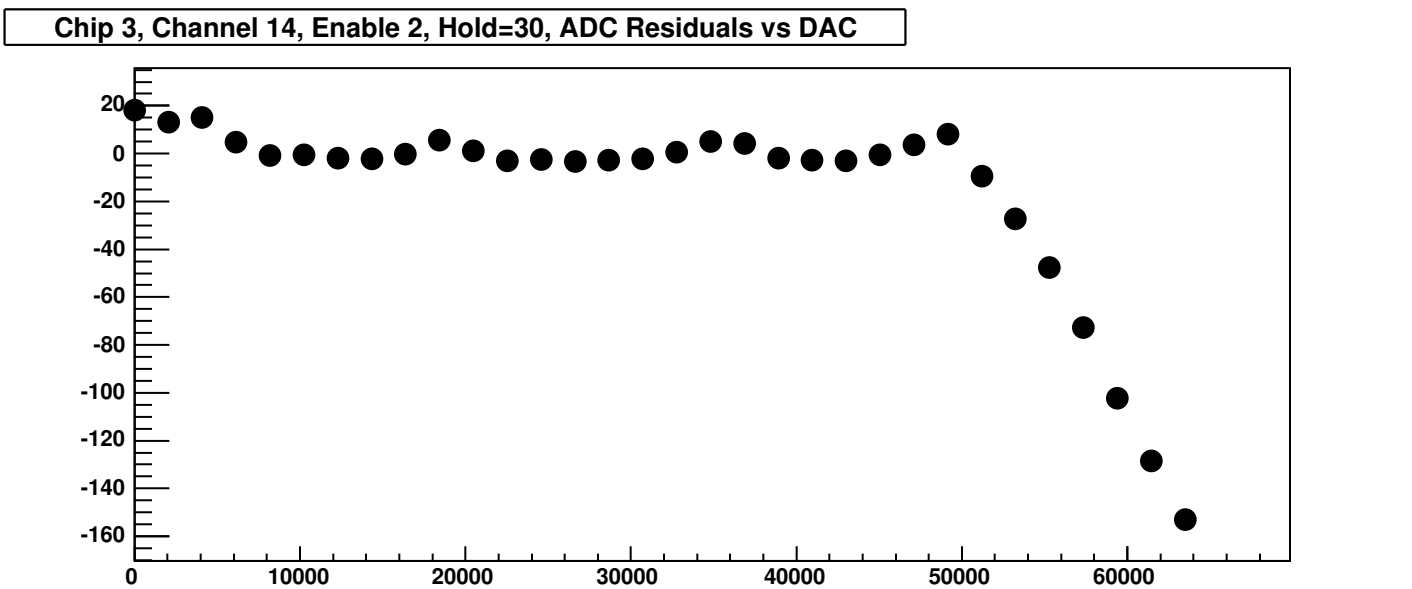


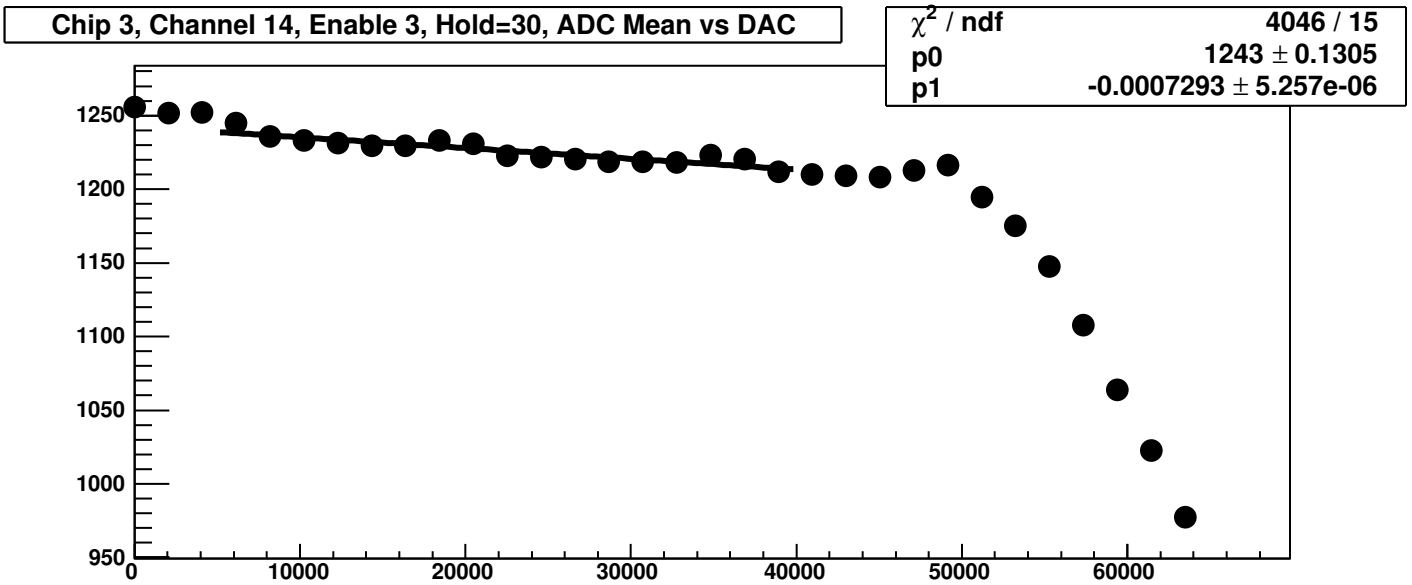
Chip 3, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC

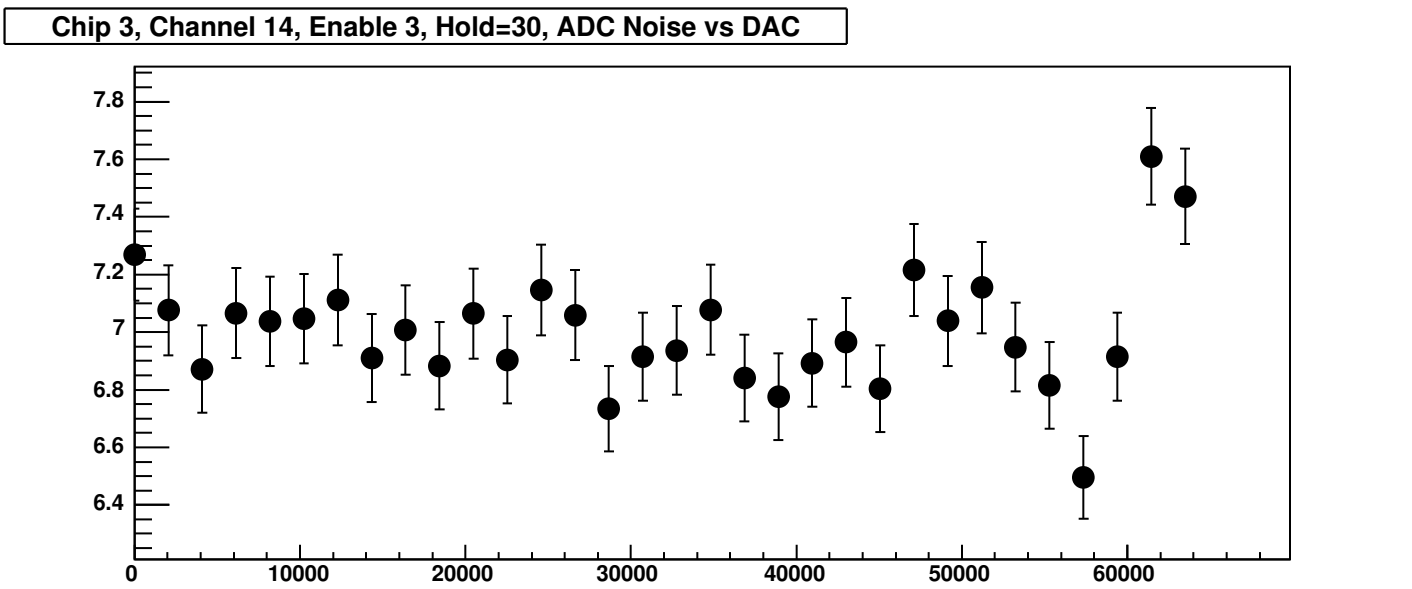


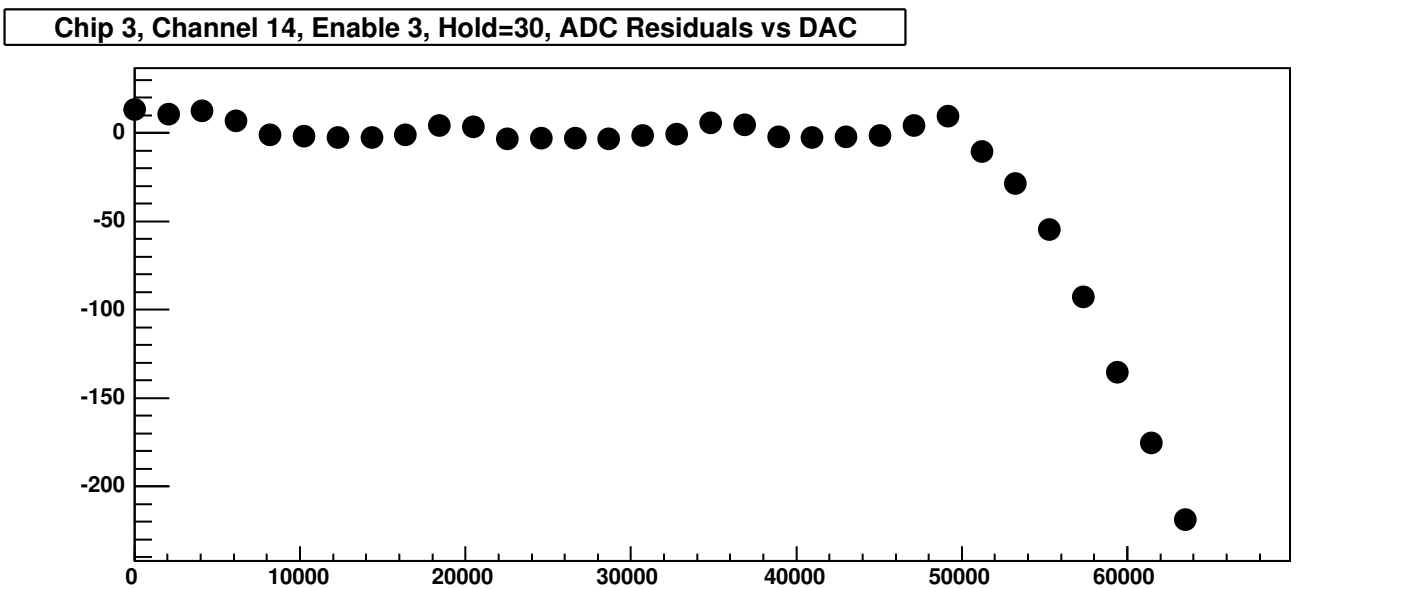

Chip 3, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3160 / 15
p0 1240 ± 0.1296
p1 -0.0006368 ± 5.235e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

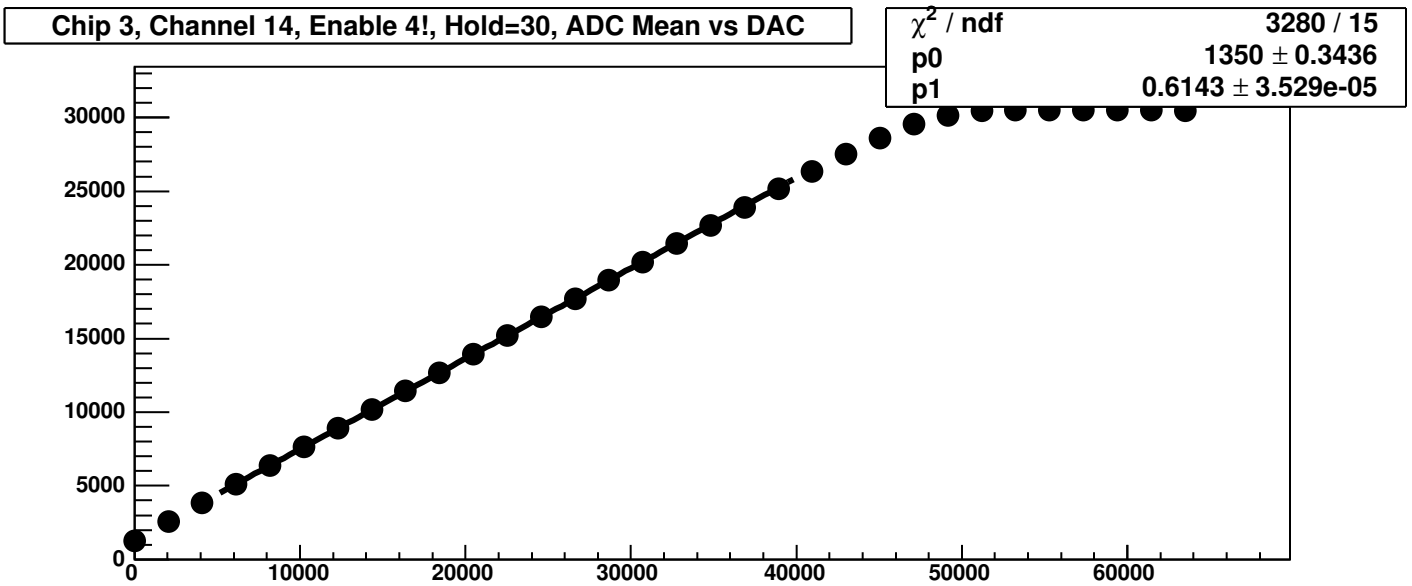

Chip 3, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

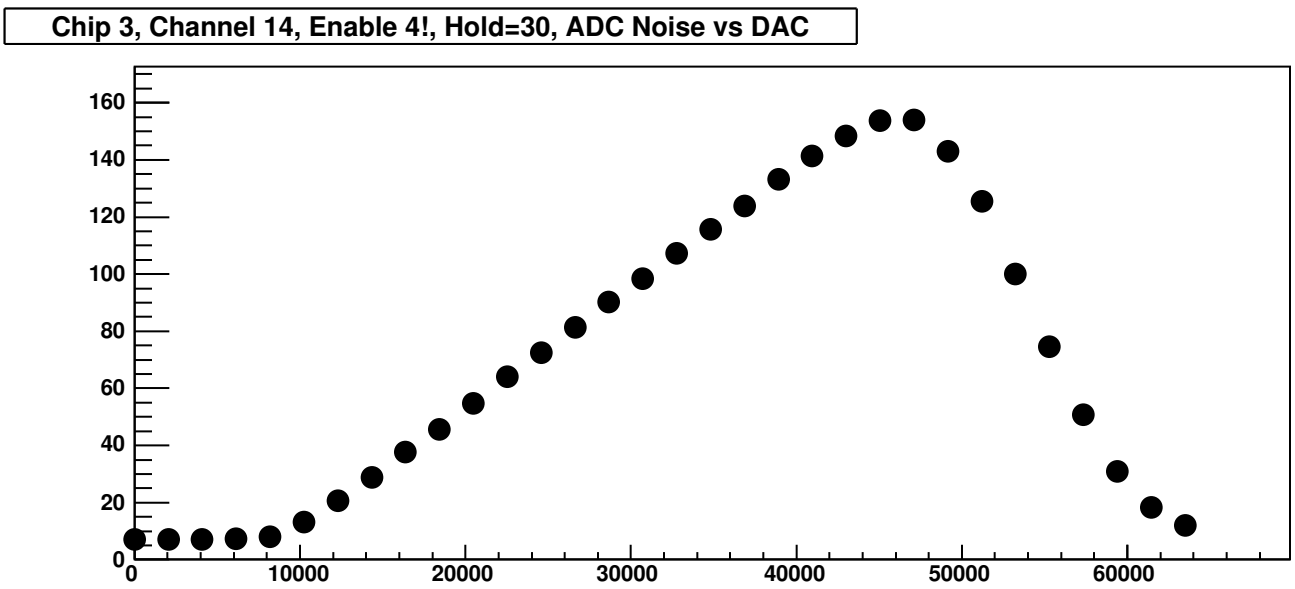

Chip 3, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

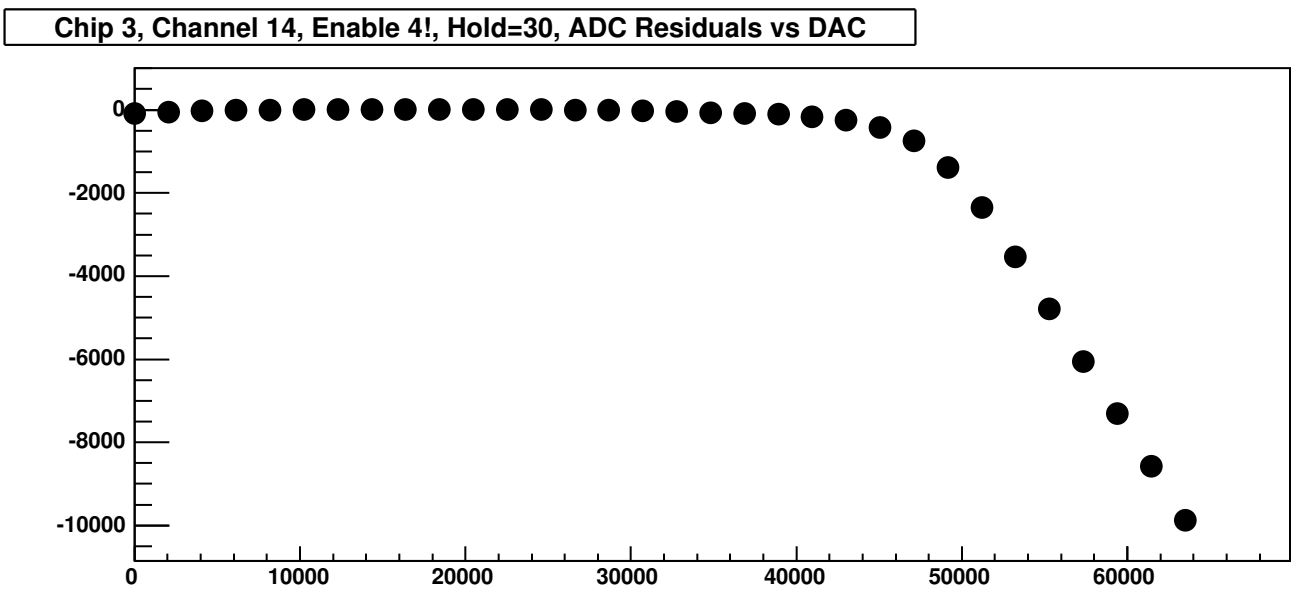

Chip 3, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4046 / 15
p0 1243 ± 0.1305
p1 -0.0007293 ± 5.257e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

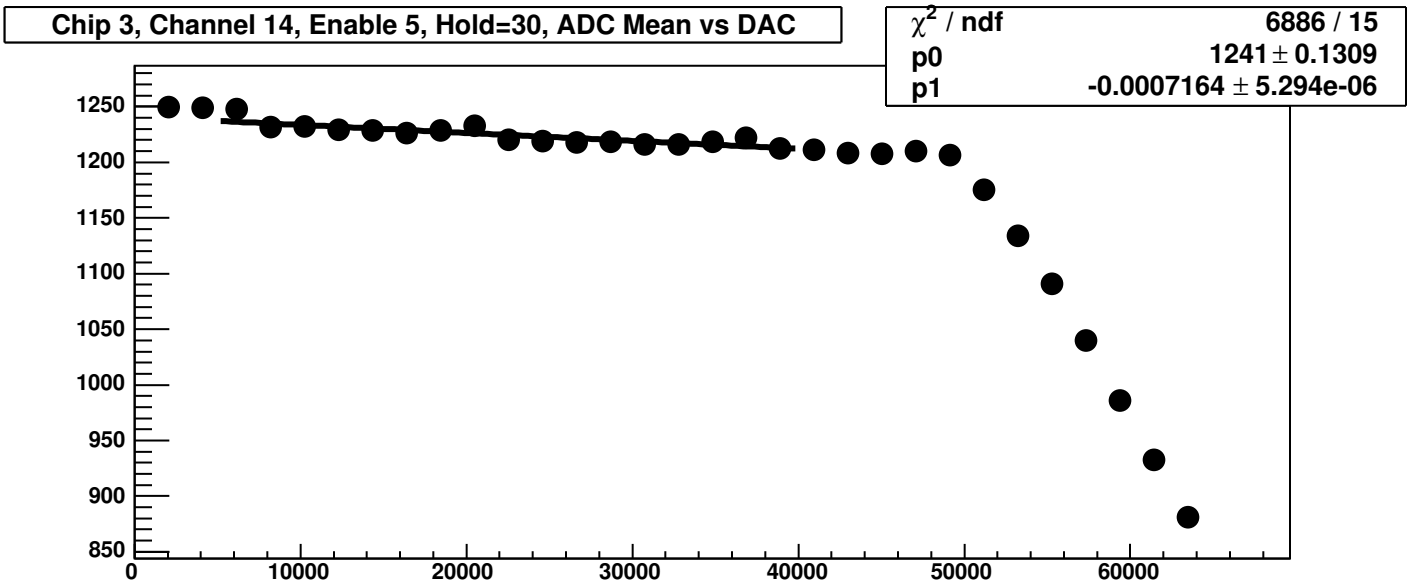

Chip 3, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

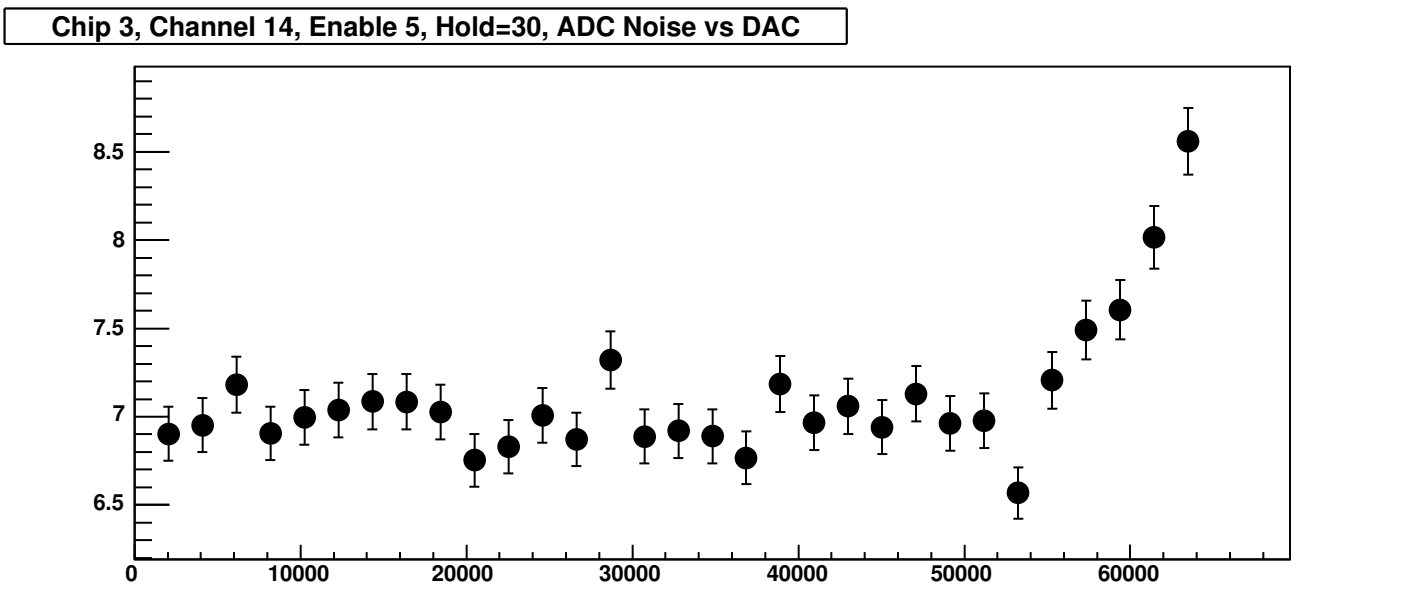

Chip 3, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

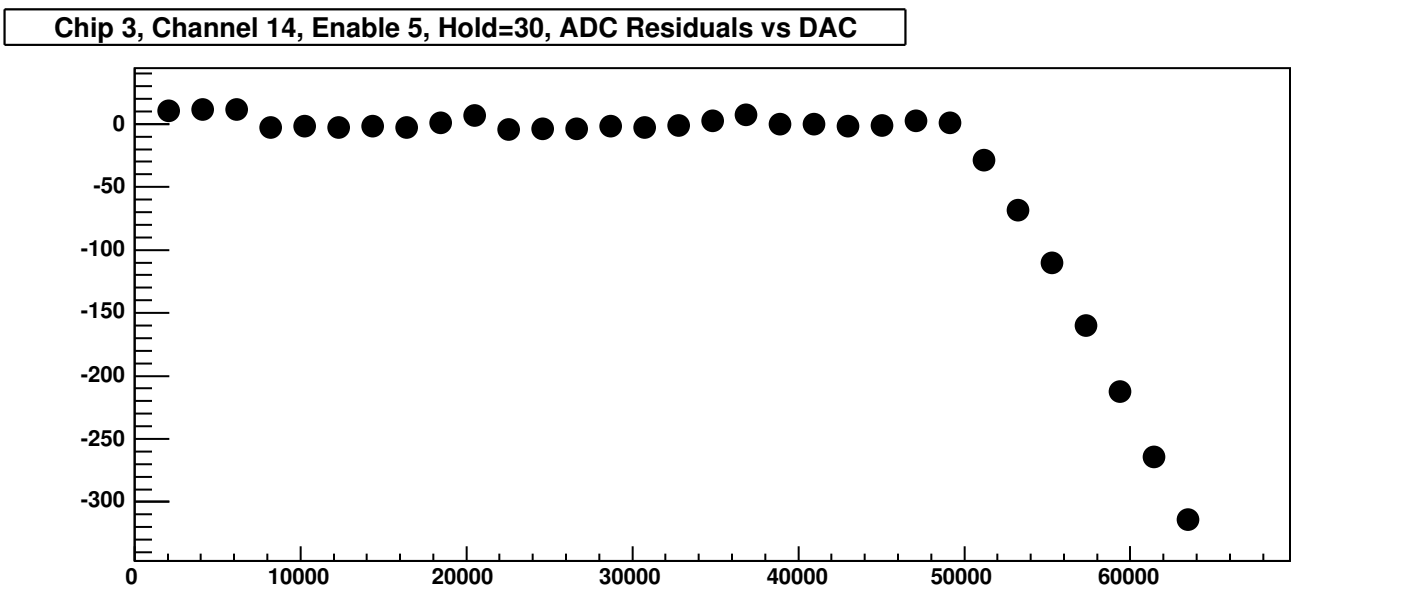

Chip 3, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 3280 / 15
p0 1350 ± 0.3436
p1 0.6143 ± 3.529e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

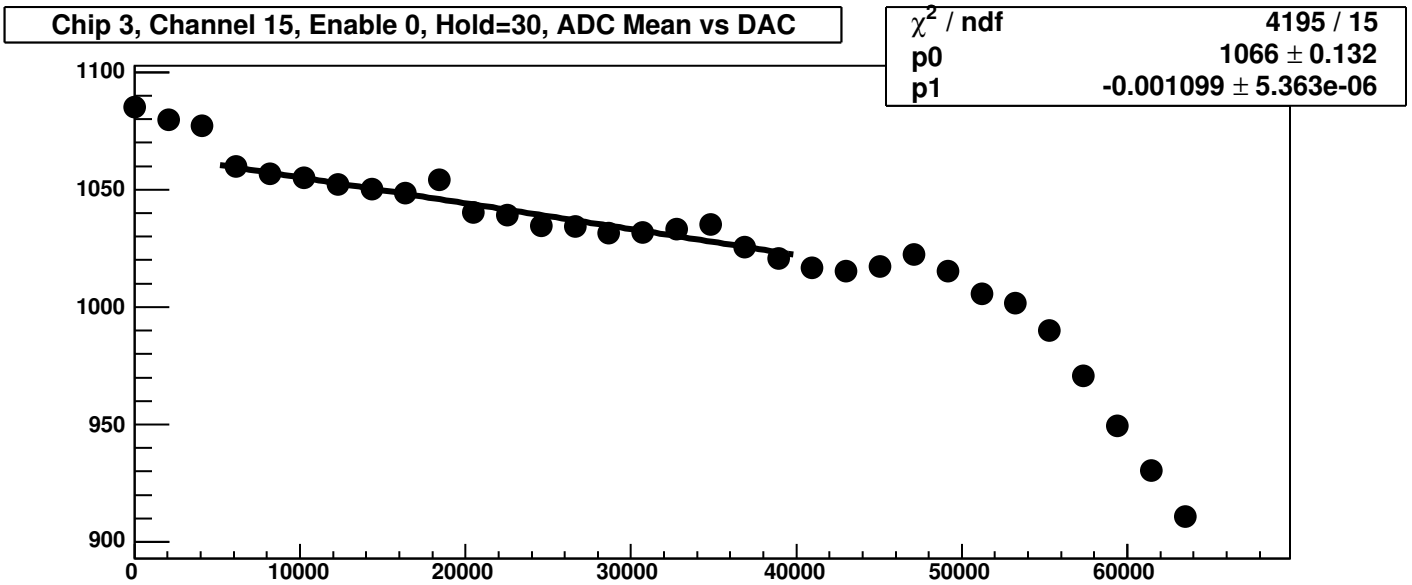

Chip 3, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

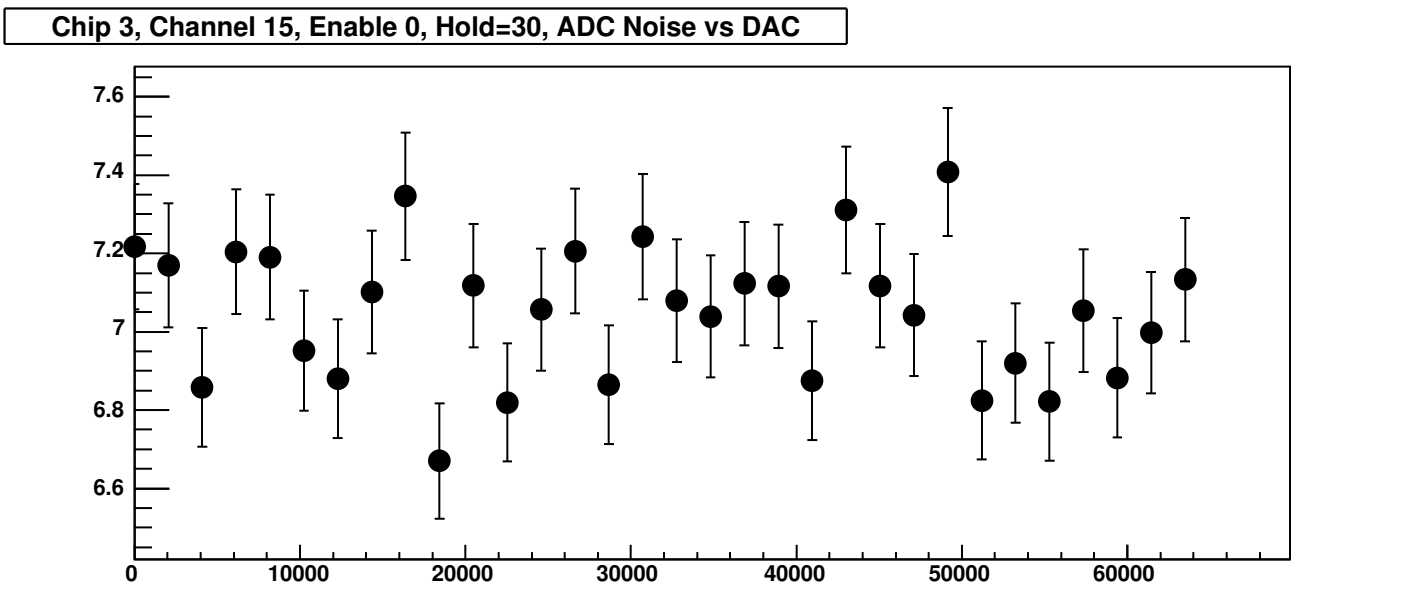

Chip 3, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

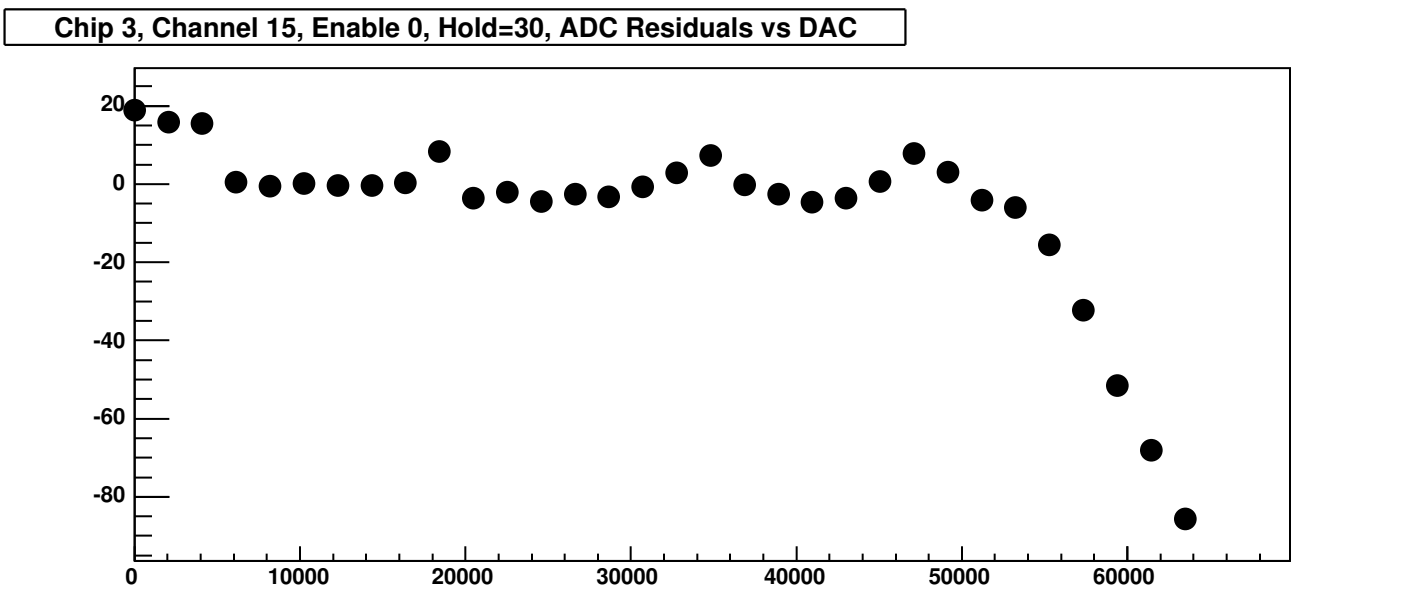

Chip 3, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6886 / 15
p0 1241 ± 0.1309
p1 -0.0007164 ± 5.294e-06
1250
1200
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

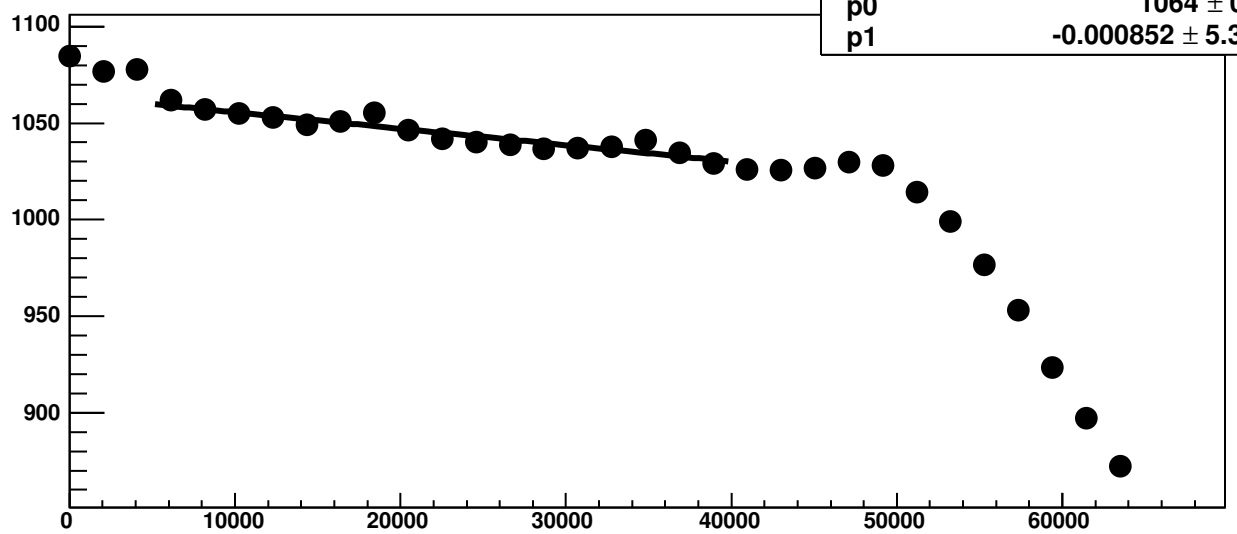

Chip 3, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4195 / 15
p0 1066 ± 0.132
p1 -0.001099 ± 5.363e-06
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

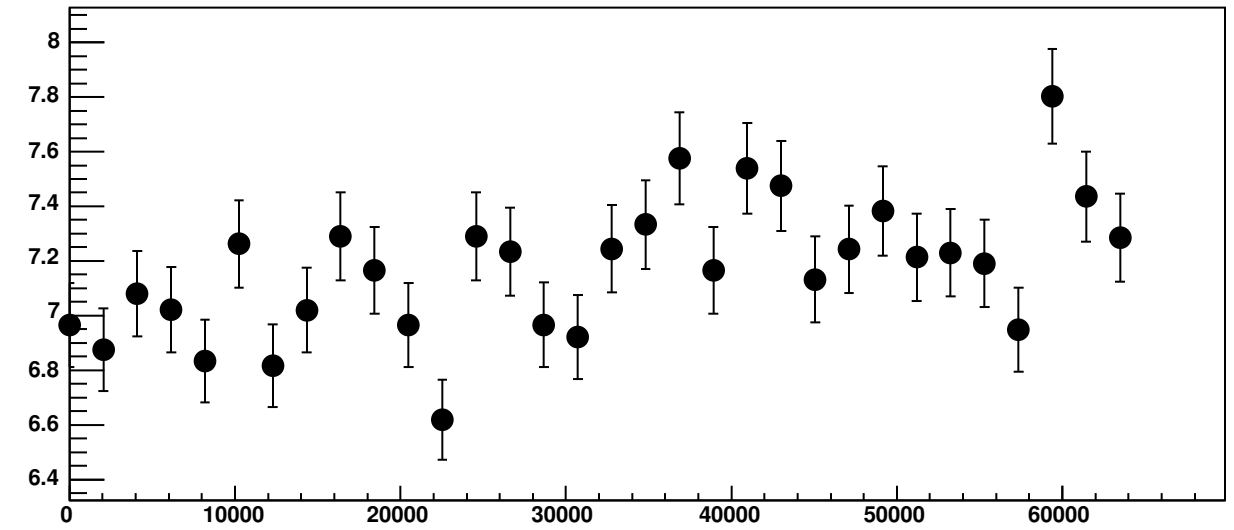

Chip 3, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

Chip 3, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

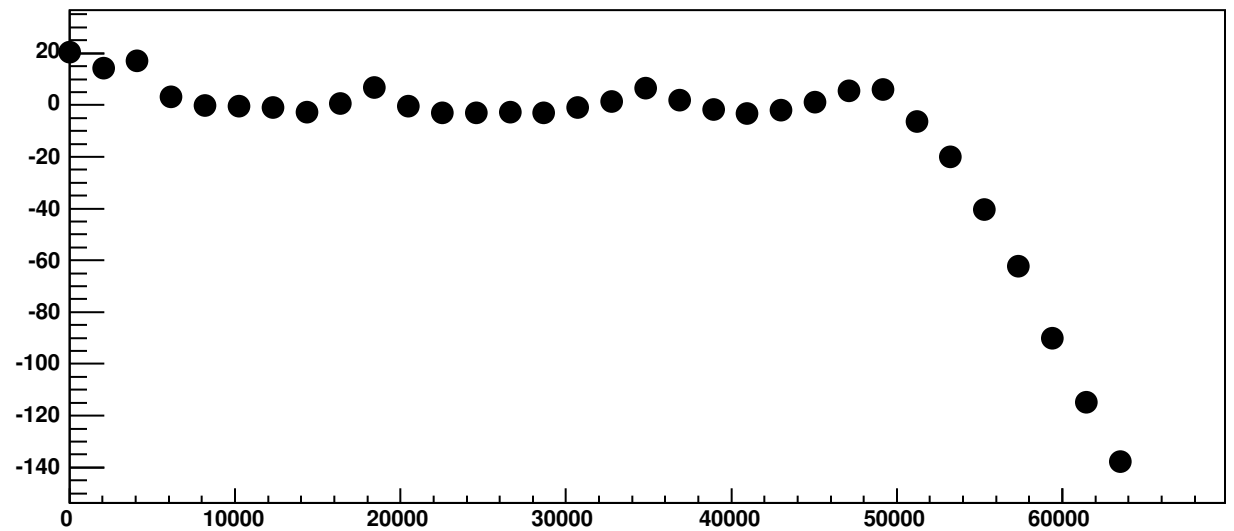
χ^2 / ndf	3078 / 15
p0	1064 ± 0.1315
p1	-0.000852 ± 5.39e-06

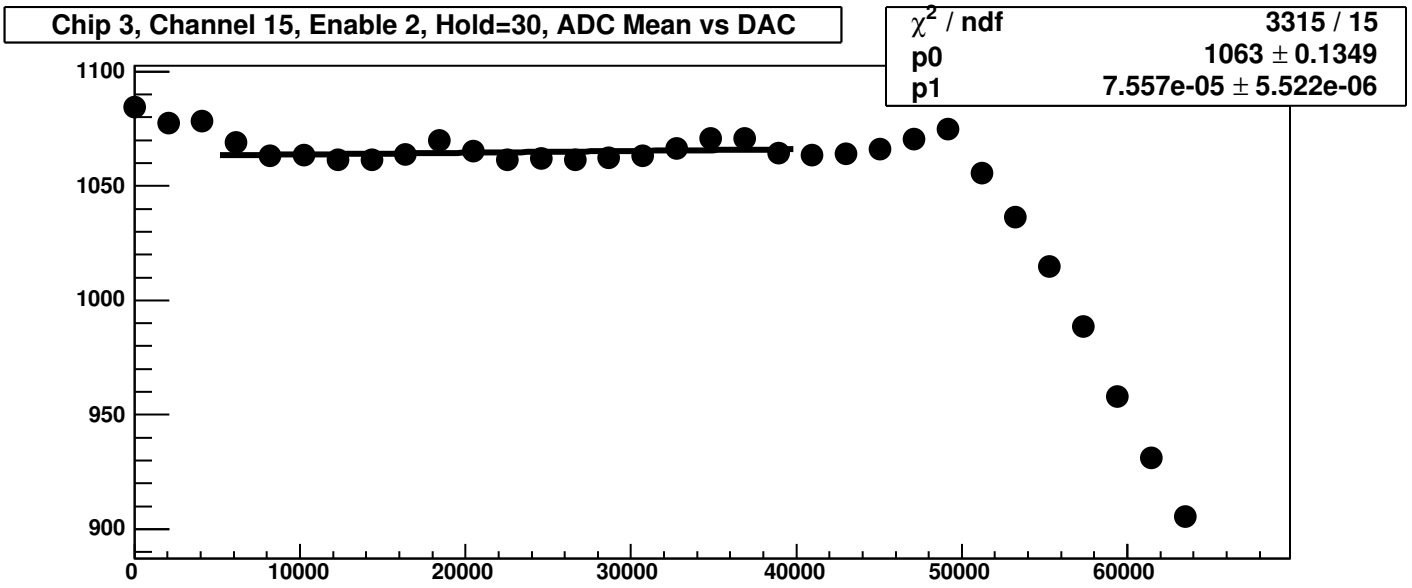


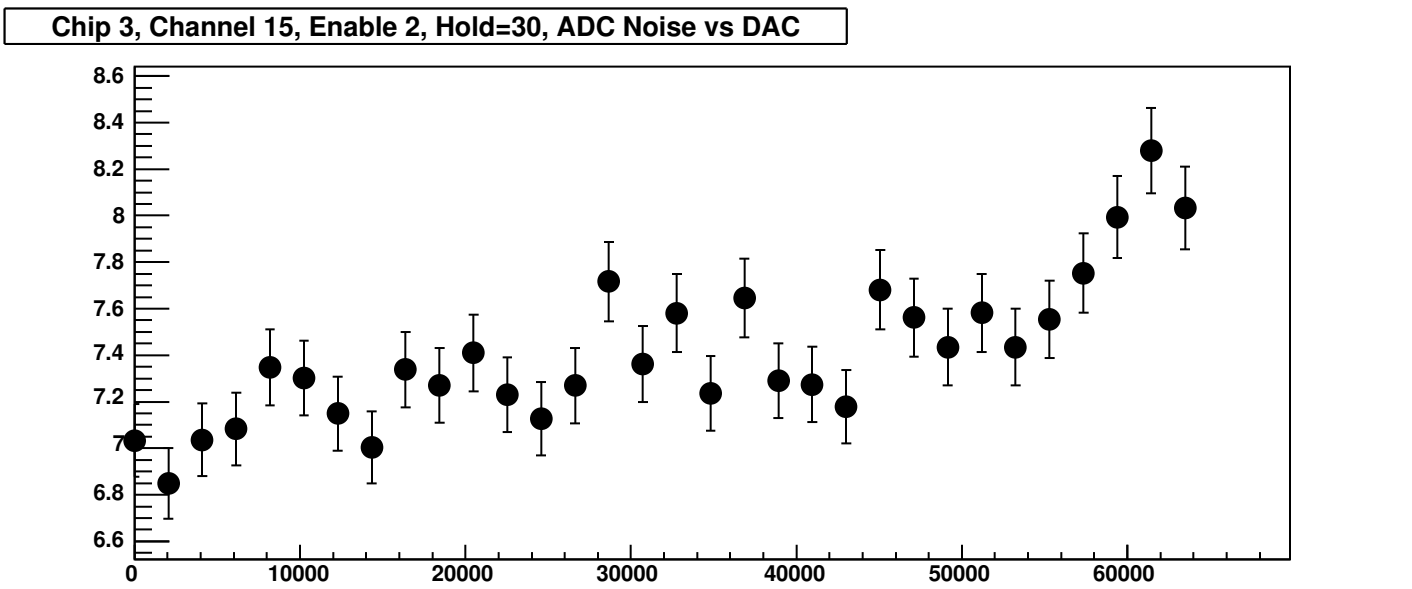
Chip 3, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC

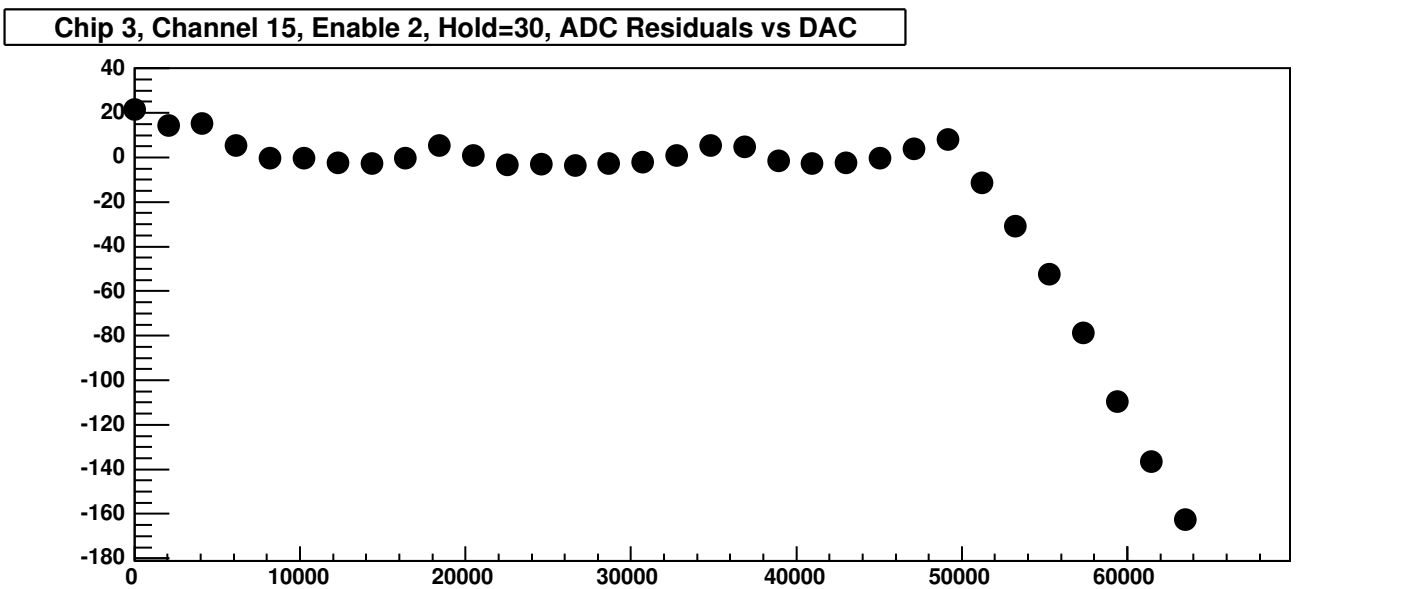


Chip 3, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

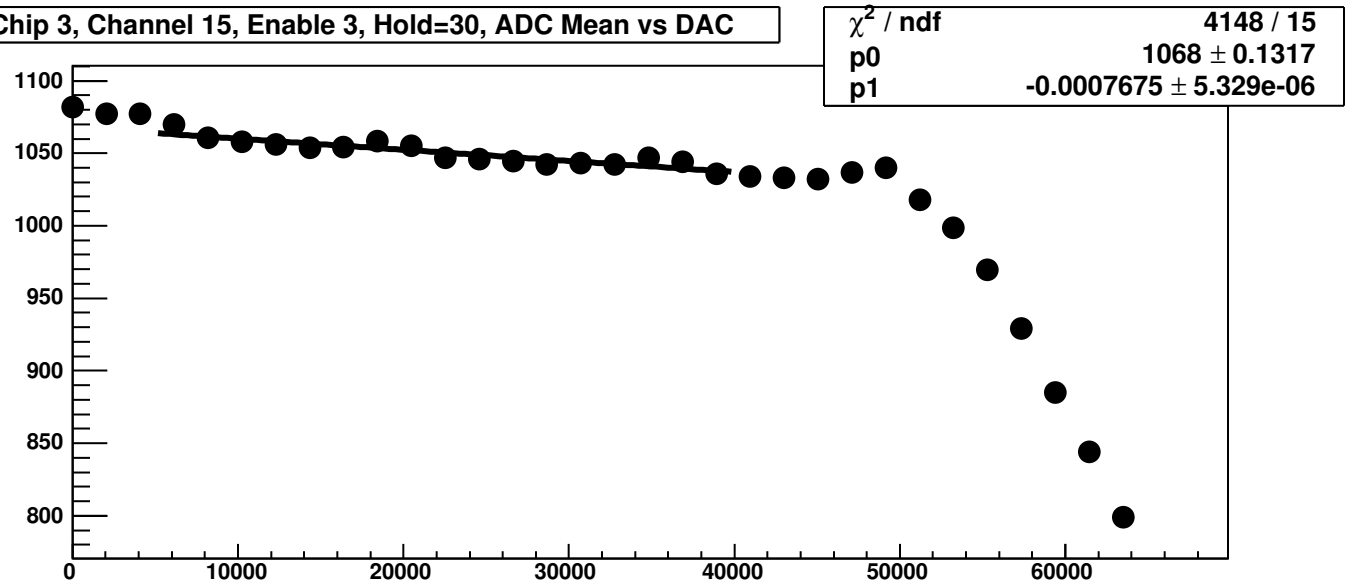



Chip 3, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3315 / 15
p0 1063 ± 0.1349
p1 7.557e-05 ± 5.522e-06
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

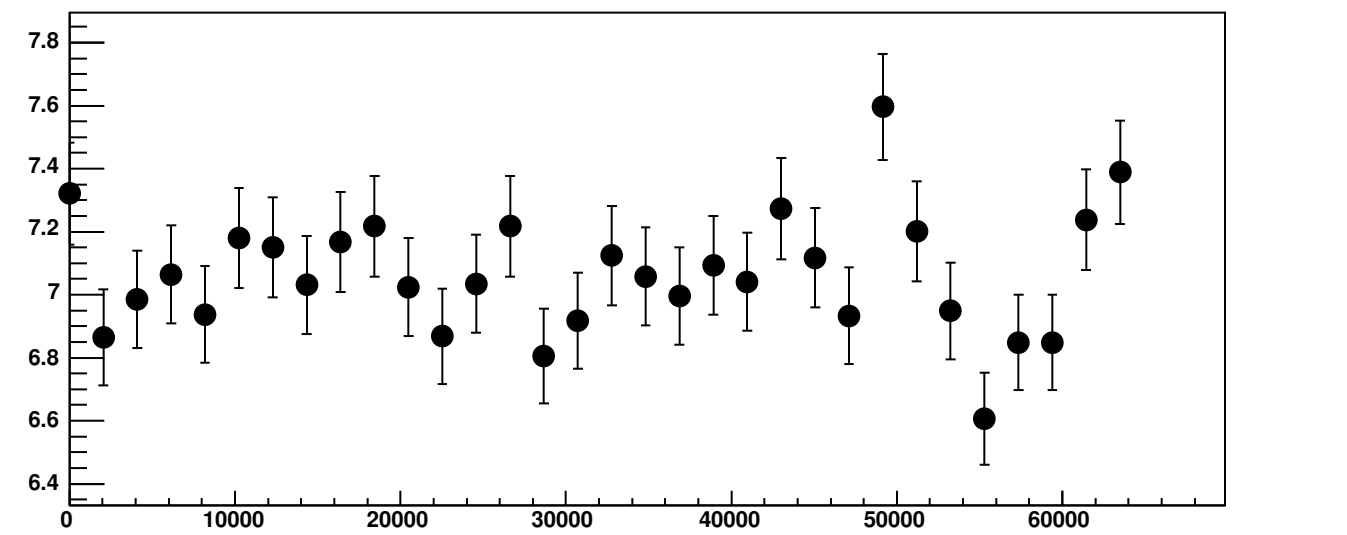

Chip 3, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
8.6
8.4
8.2
8
7.8
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

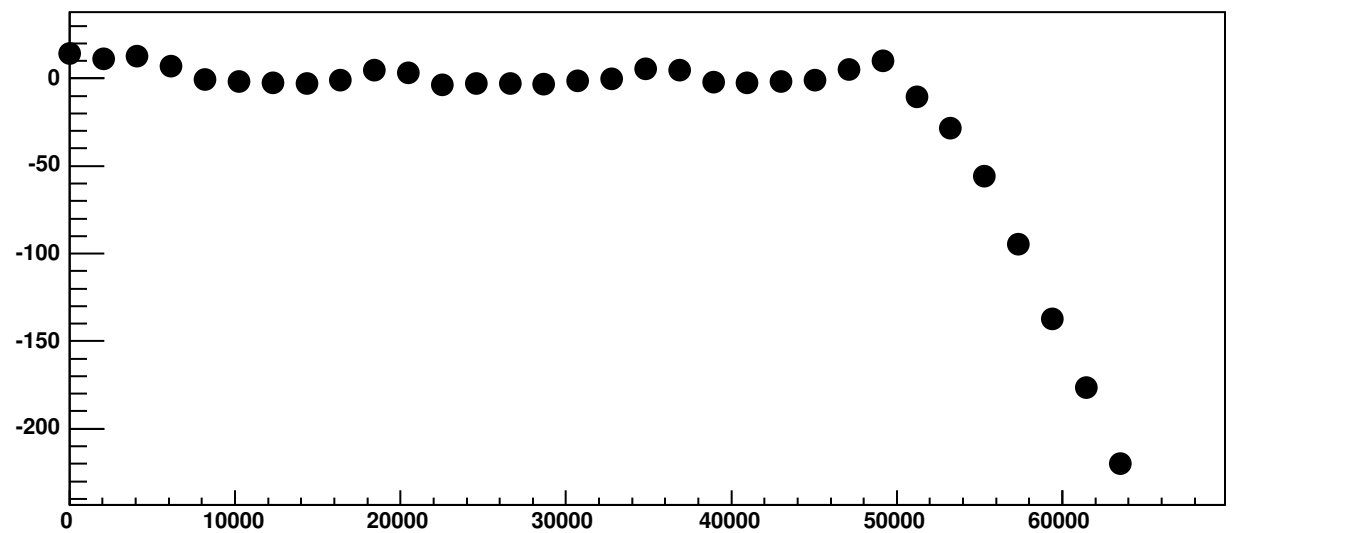
Chip 3, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC

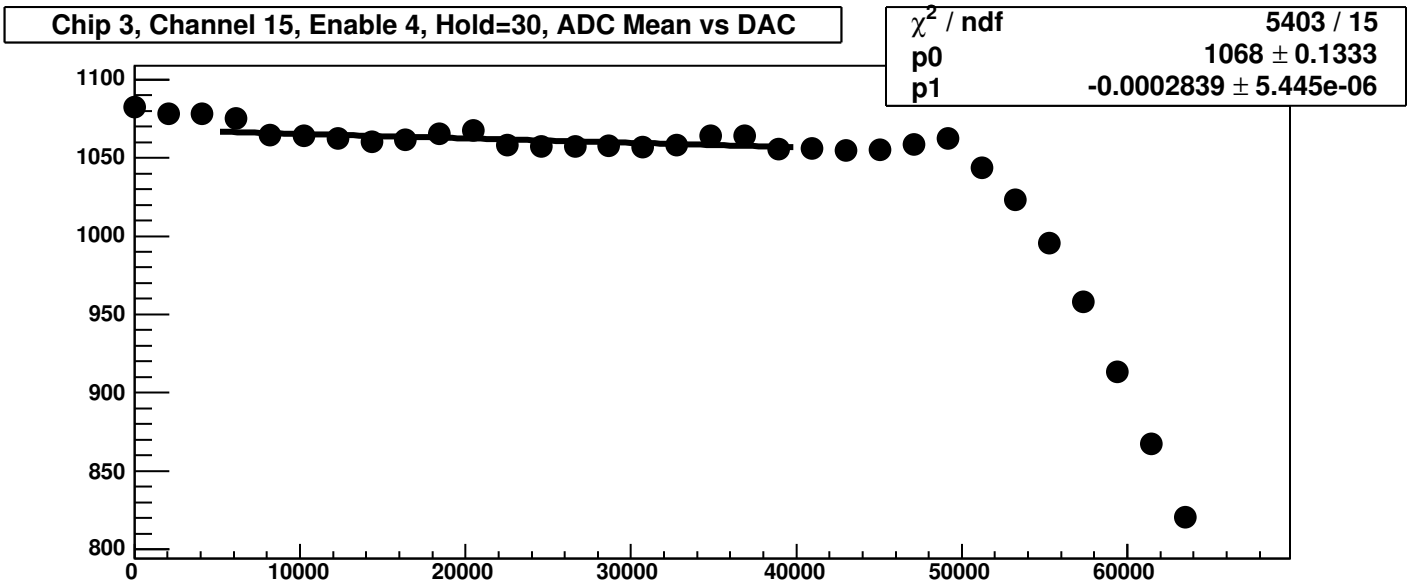


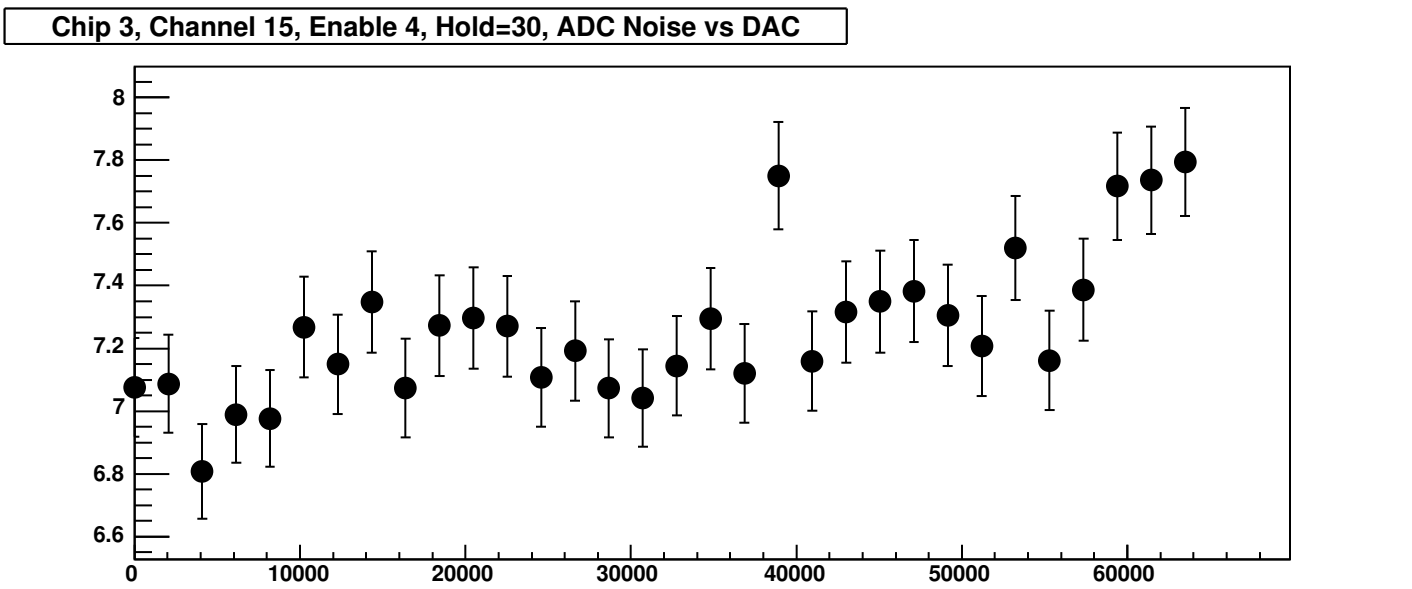
Chip 3, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC

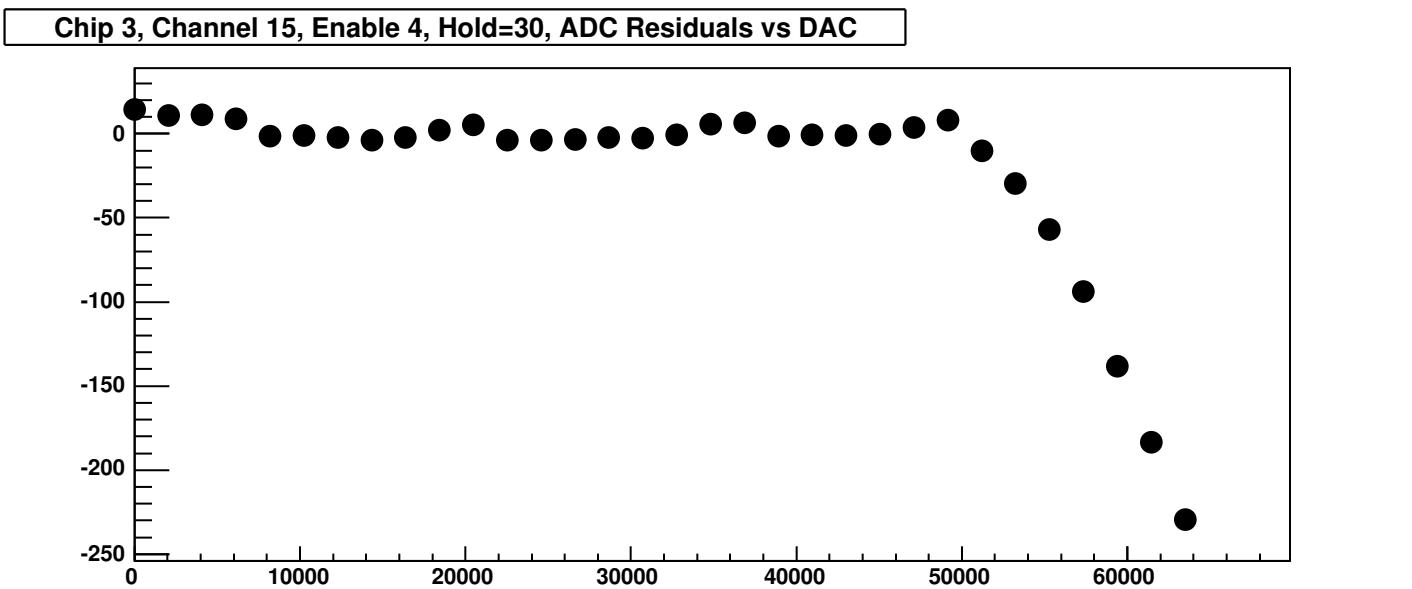


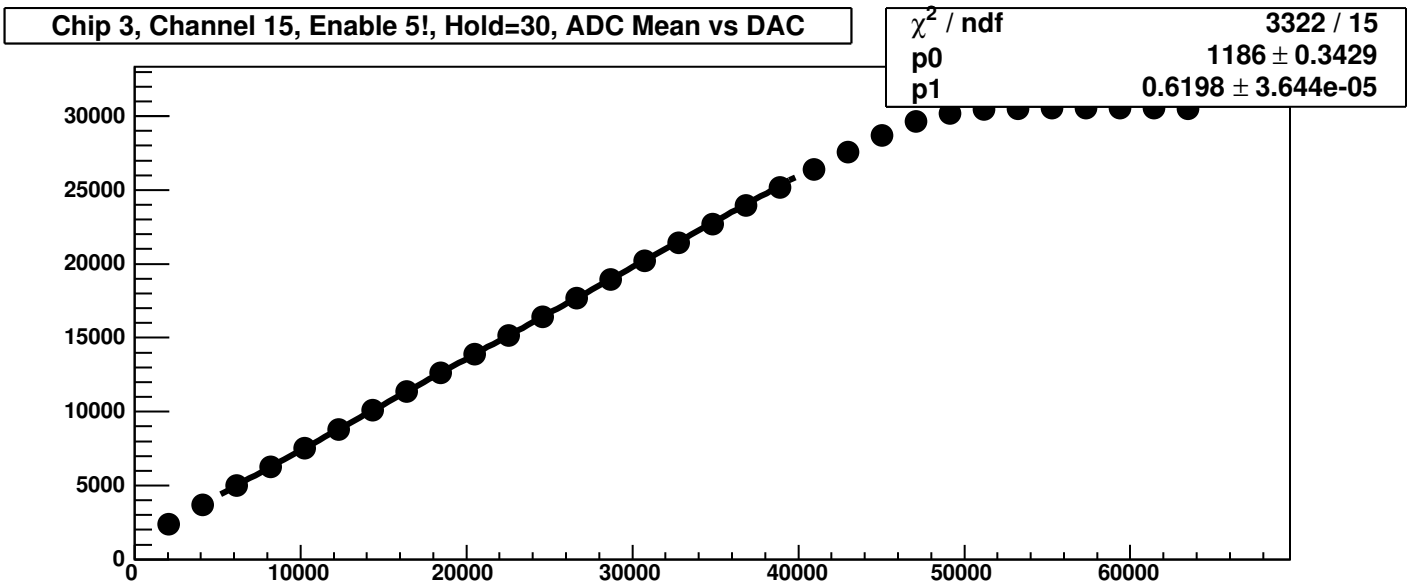
Chip 3, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC

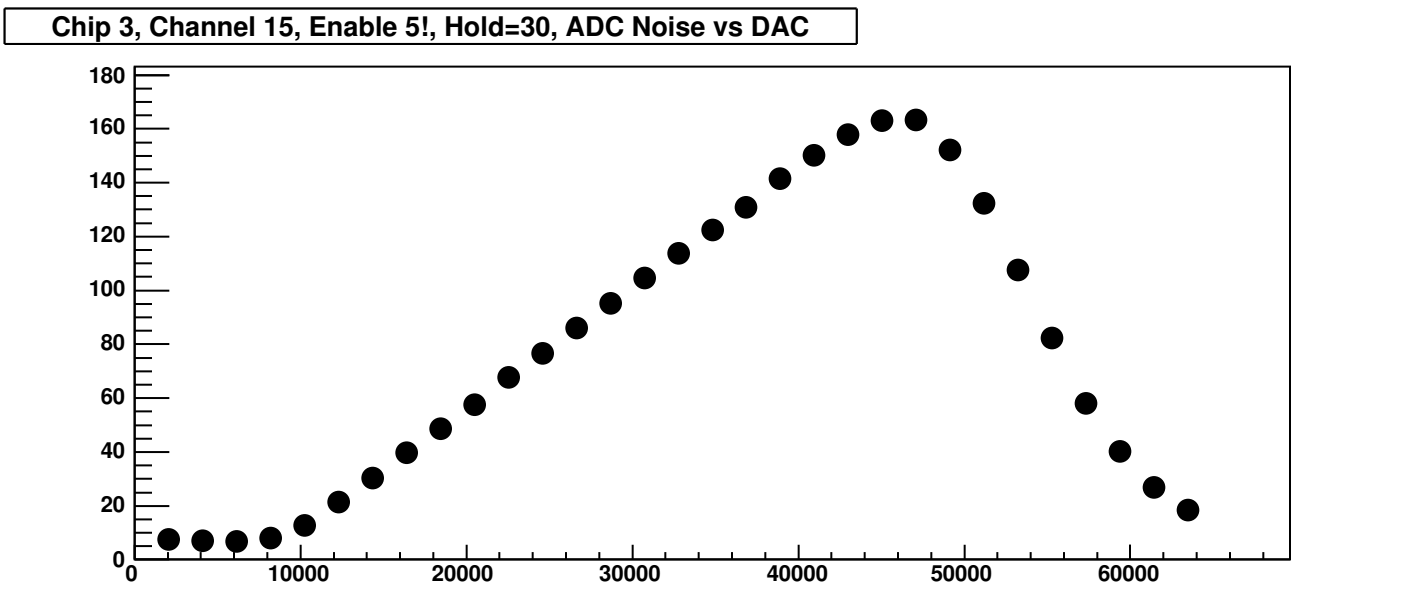


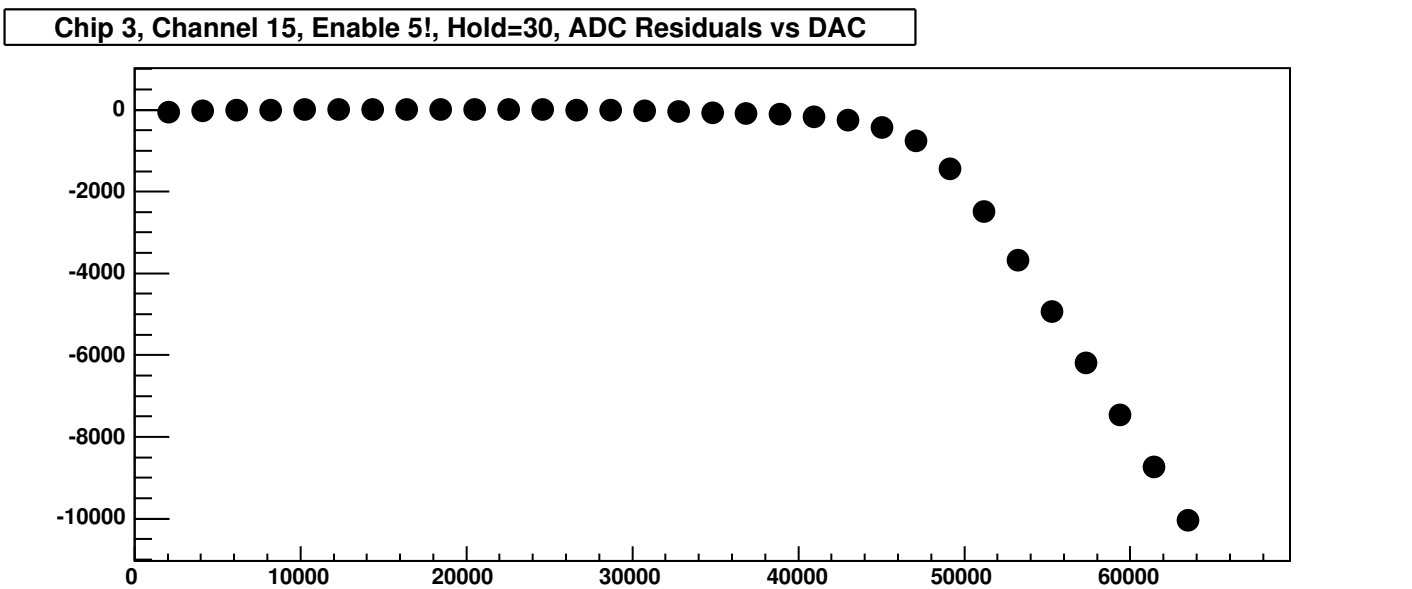

Chip 3, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5403 / 15
p0 1068 ± 0.1333
p1 -0.0002839 ± 5.445e-06
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

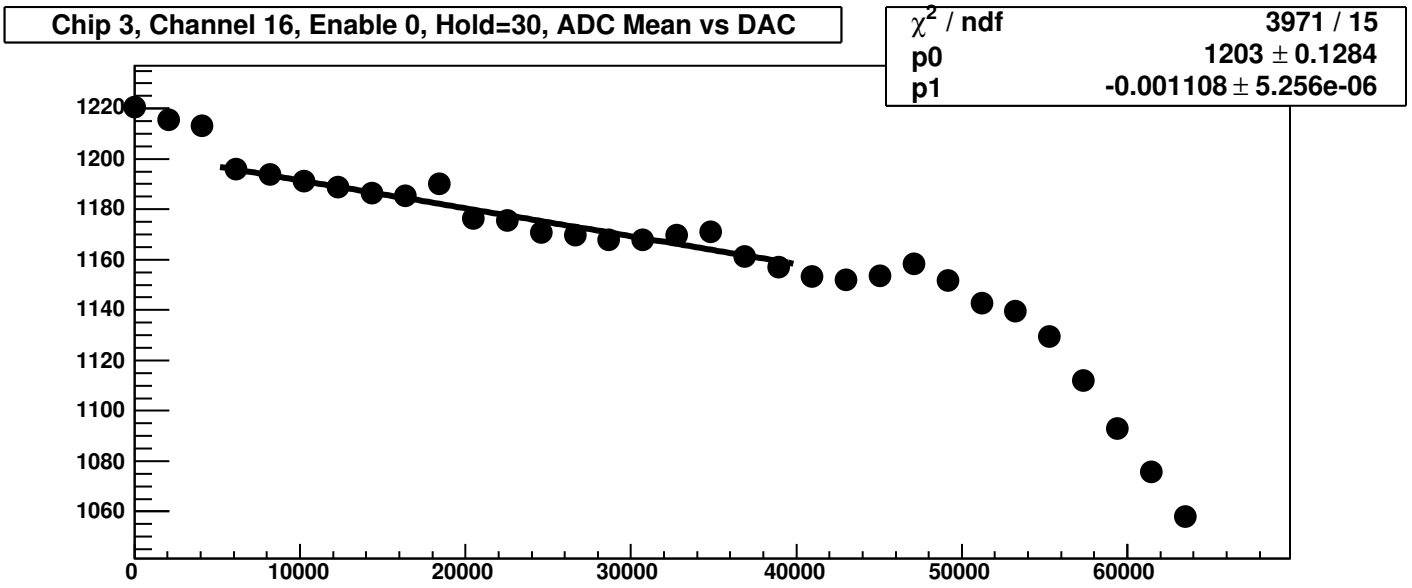

Chip 3, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
8
7.8
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

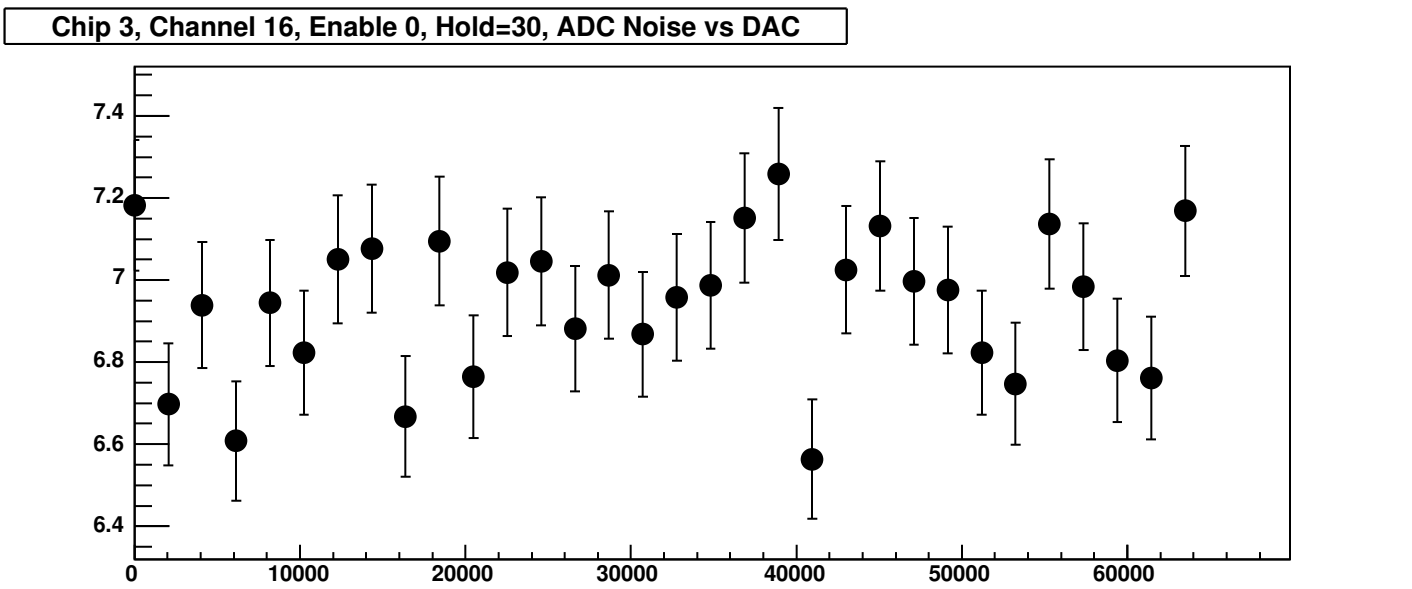

Chip 3, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

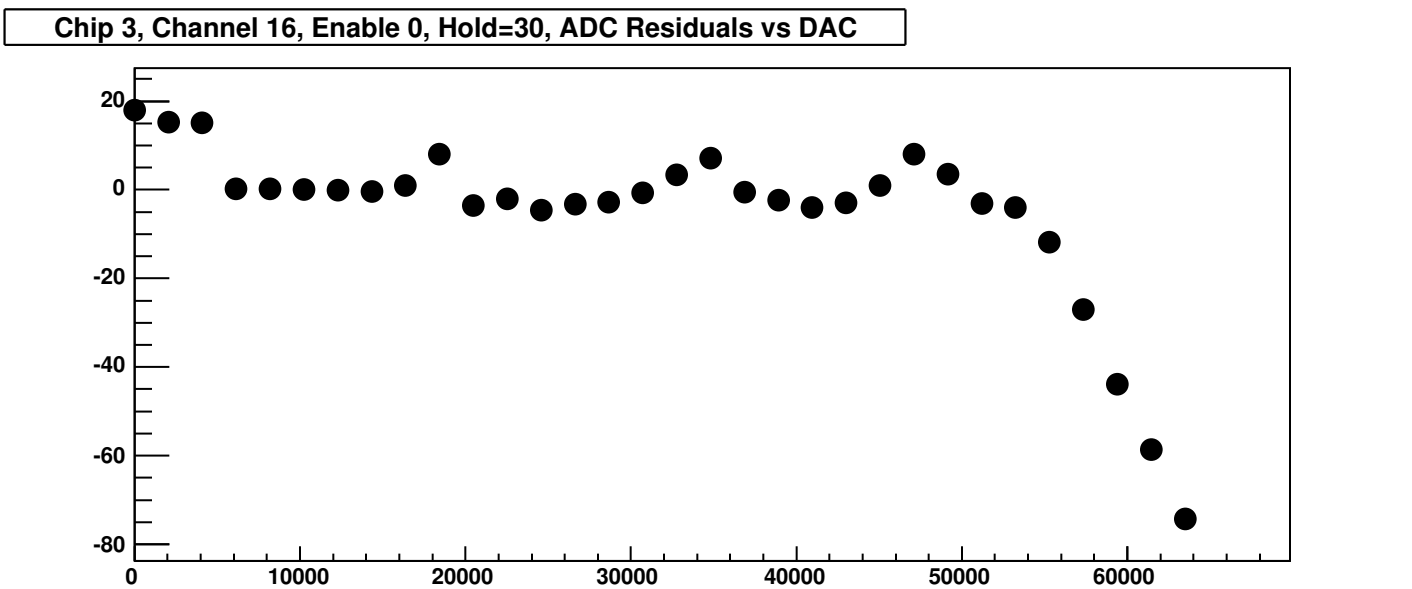

Chip 3, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 3322 / 15
p0 1186 ± 0.3429
p1 0.6198 ± 3.644e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

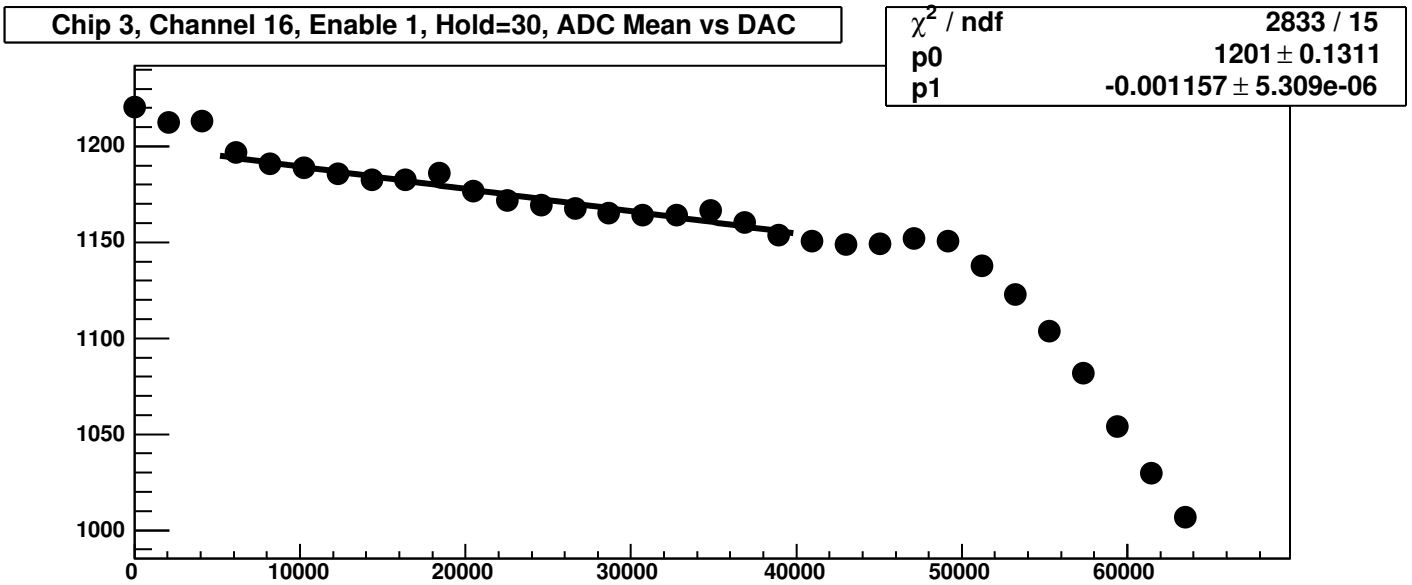

Chip 3, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

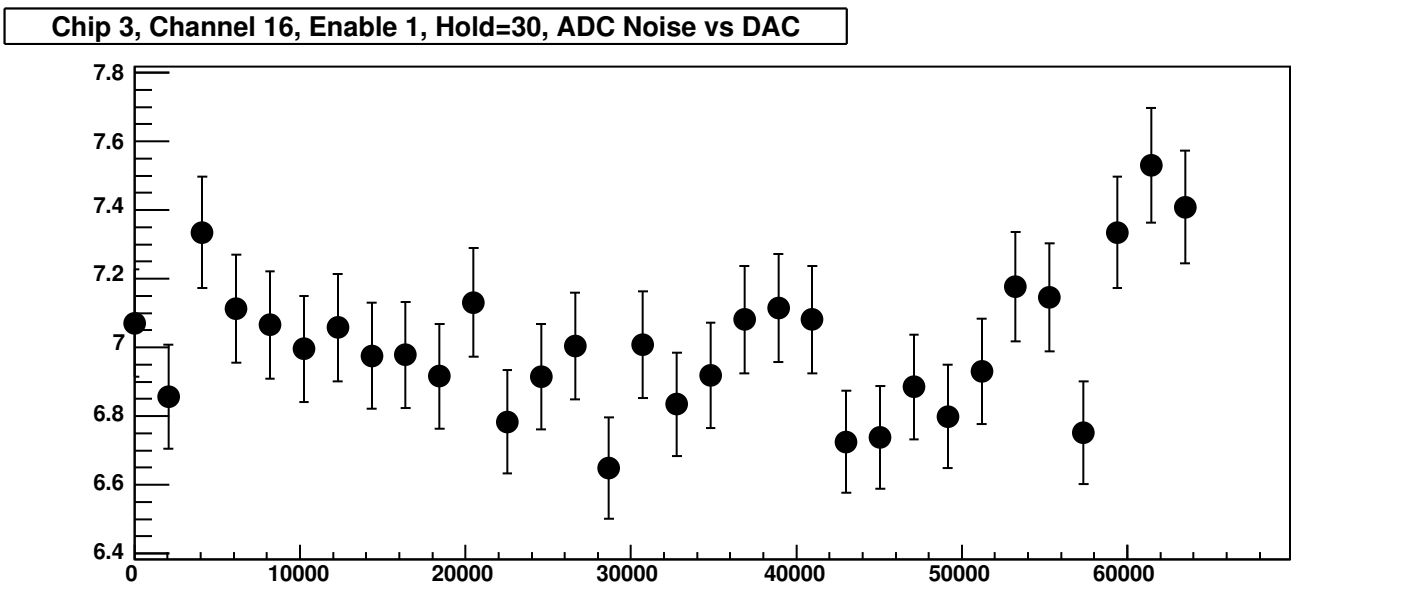

Chip 3, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

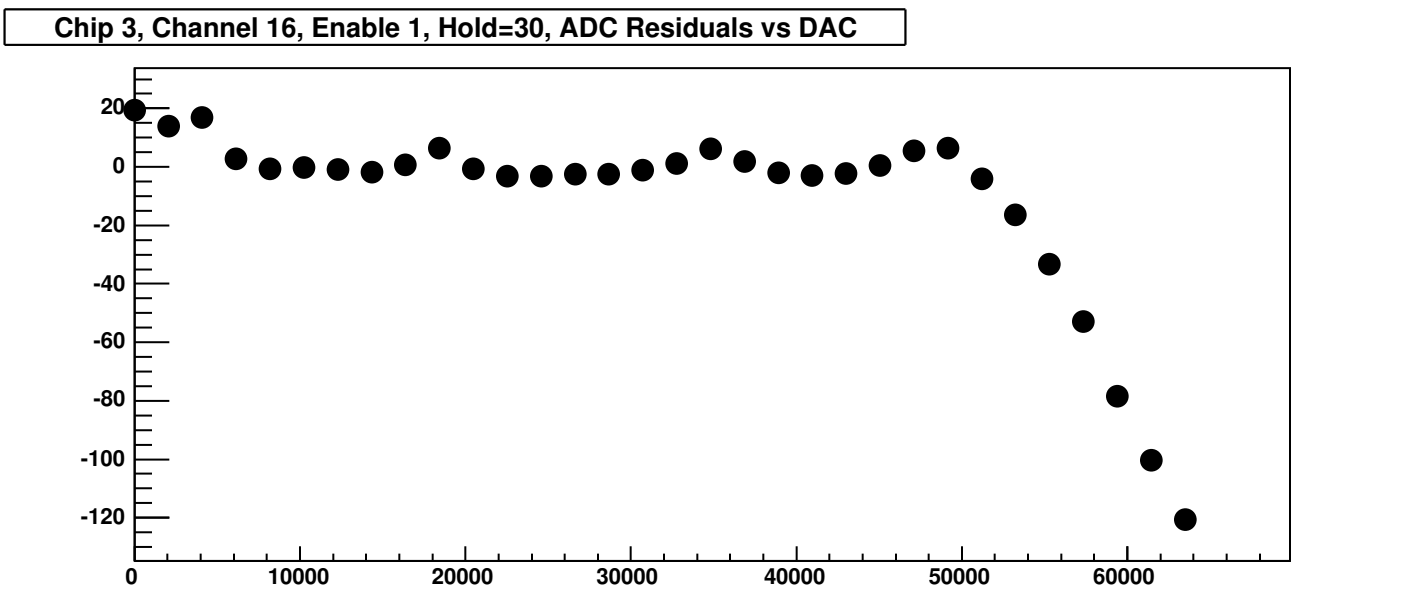

Chip 3, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3971 / 15
p0 1203 ± 0.1284
p1 -0.001108 ± 5.256e-06
1220
1200
1180
1160
1140
1120
1100
1080
1060
0
10000
20000
30000
40000
50000
60000

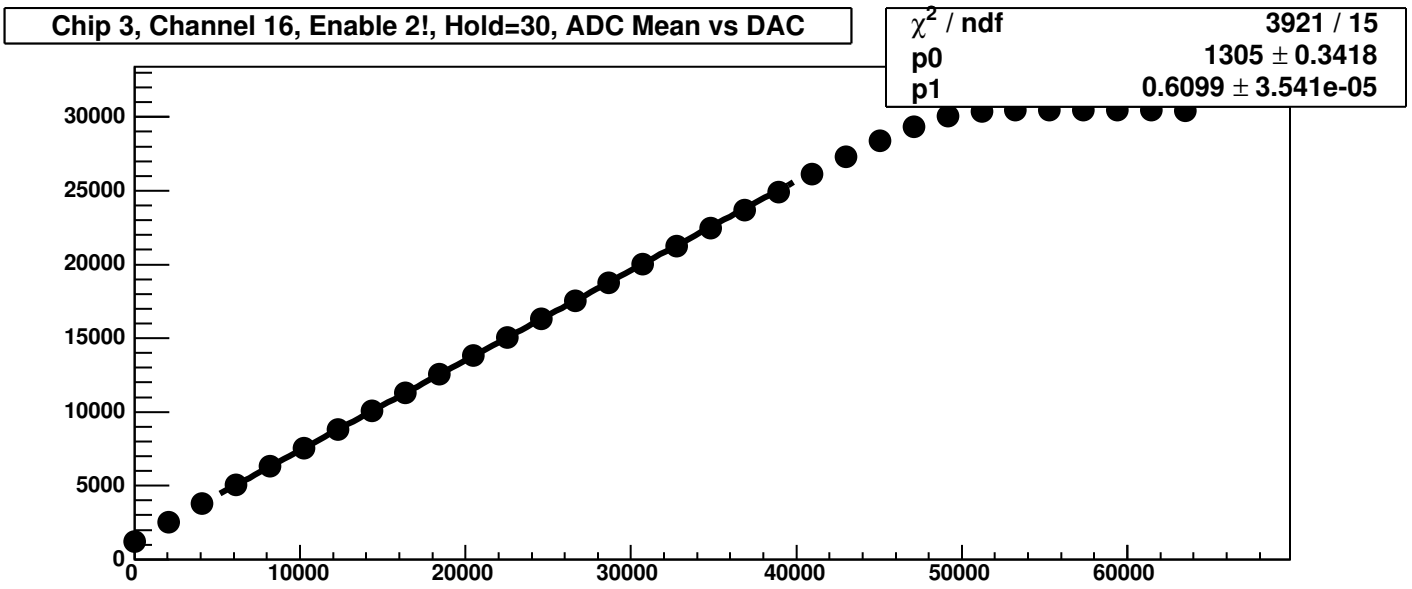

Chip 3, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

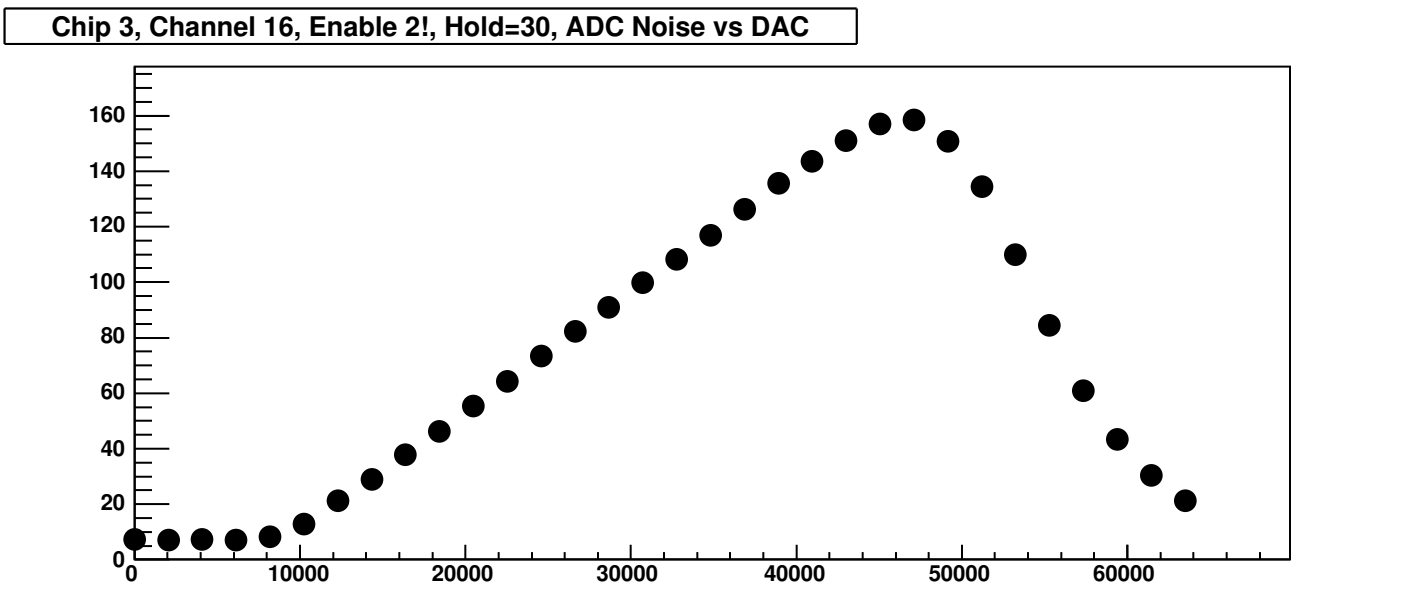

Chip 3, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

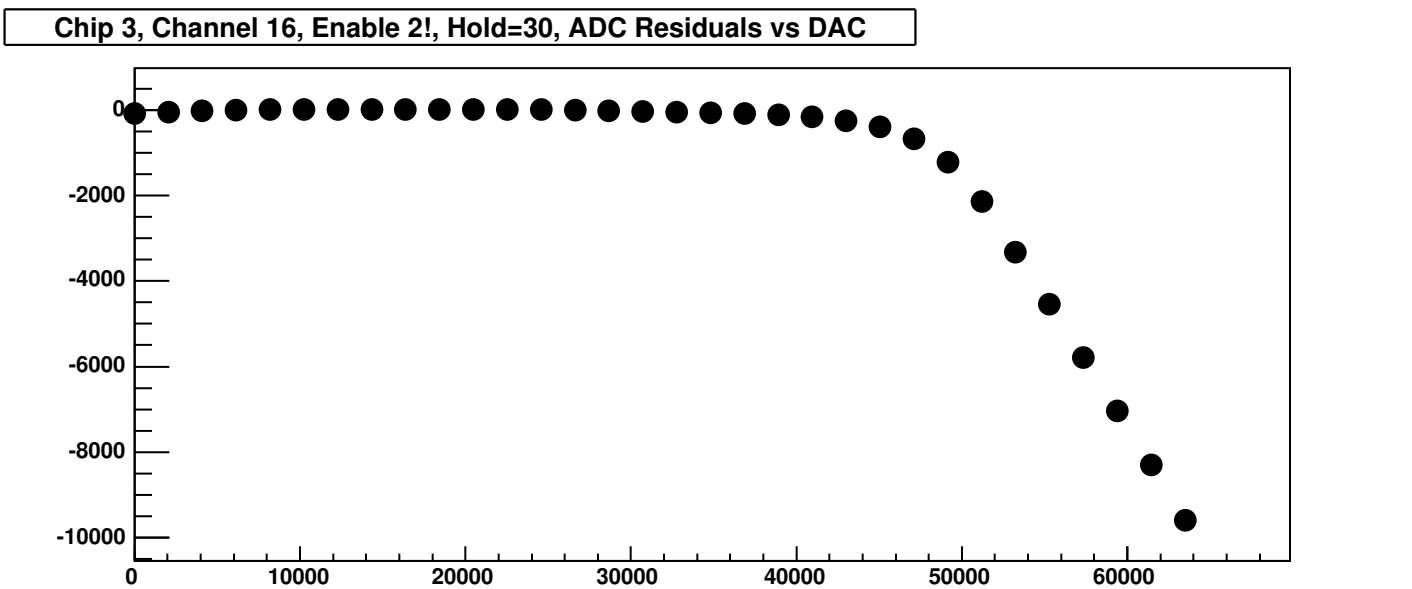

Chip 3, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2833 / 15
p0 1201 ± 0.1311
p1 -0.001157 ± 5.309e-06
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

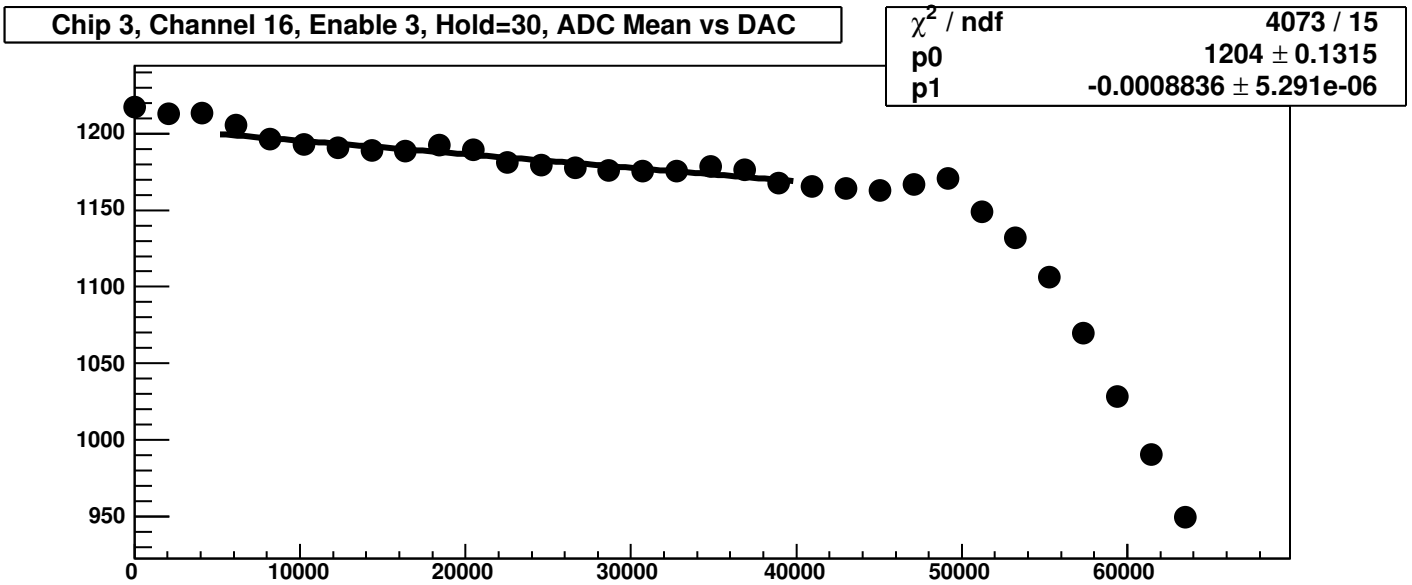

Chip 3, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

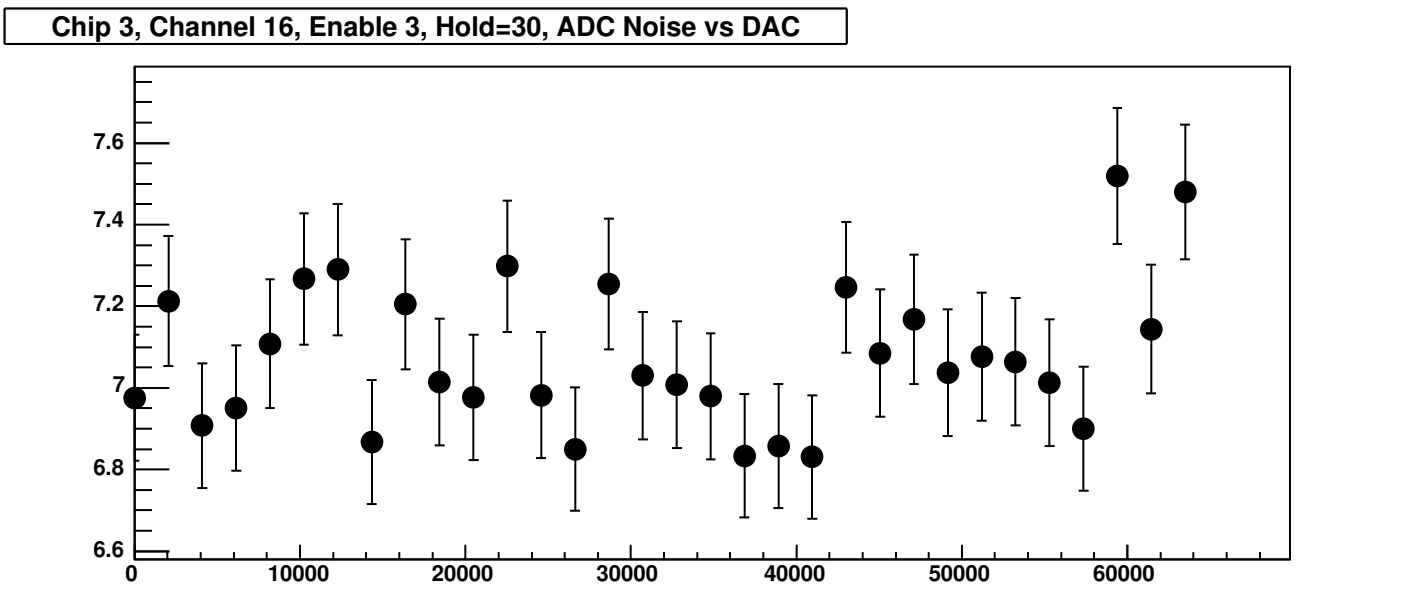

Chip 3, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

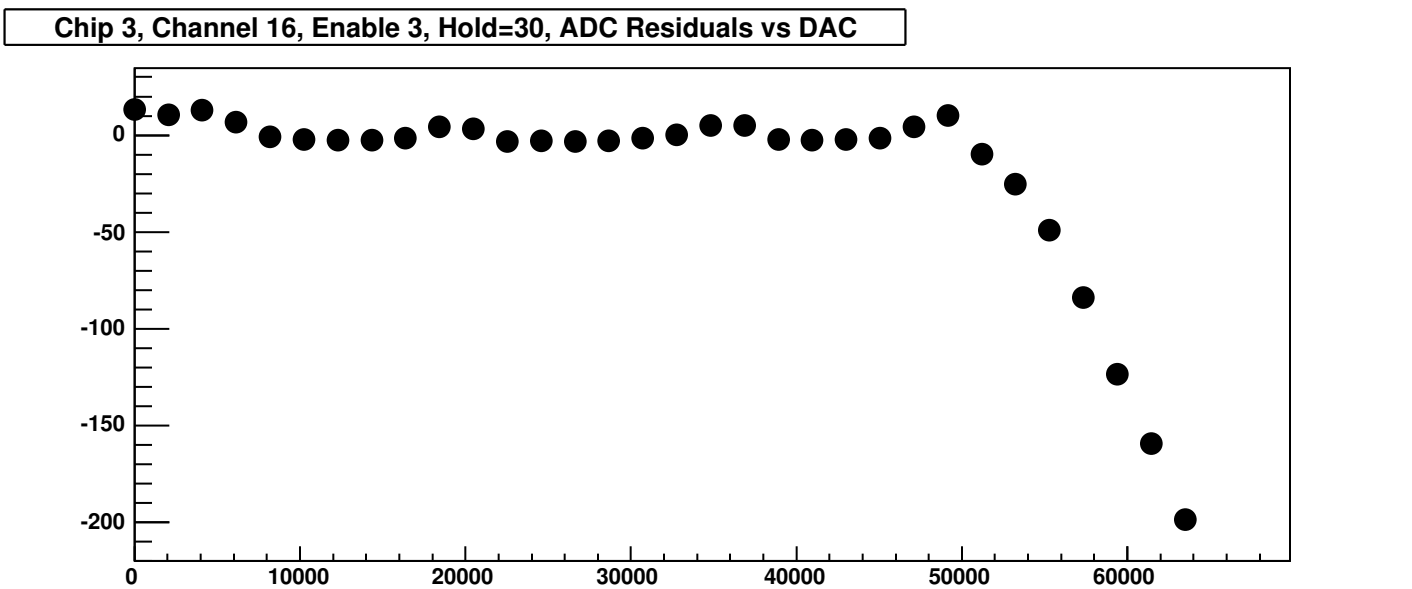

Chip 3, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 3921 / 15
p0 1305 ± 0.3418
p1 0.6099 ± 3.541e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

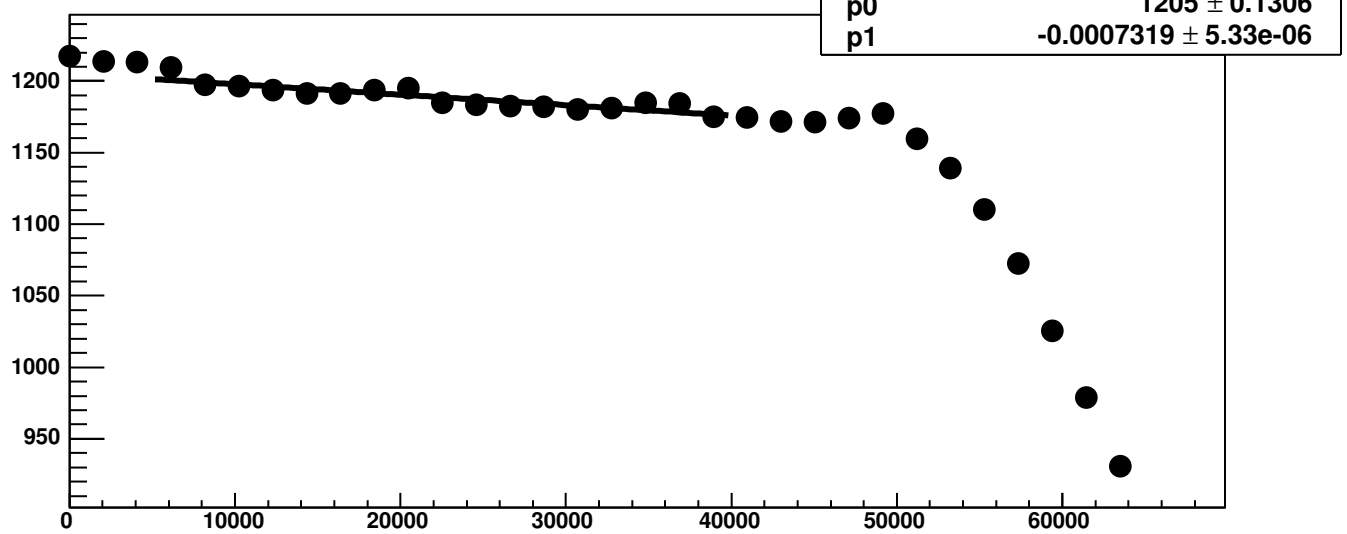

Chip 3, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4073 / 15
p0 1204 ± 0.1315
p1 -0.0008836 ± 5.291e-06
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

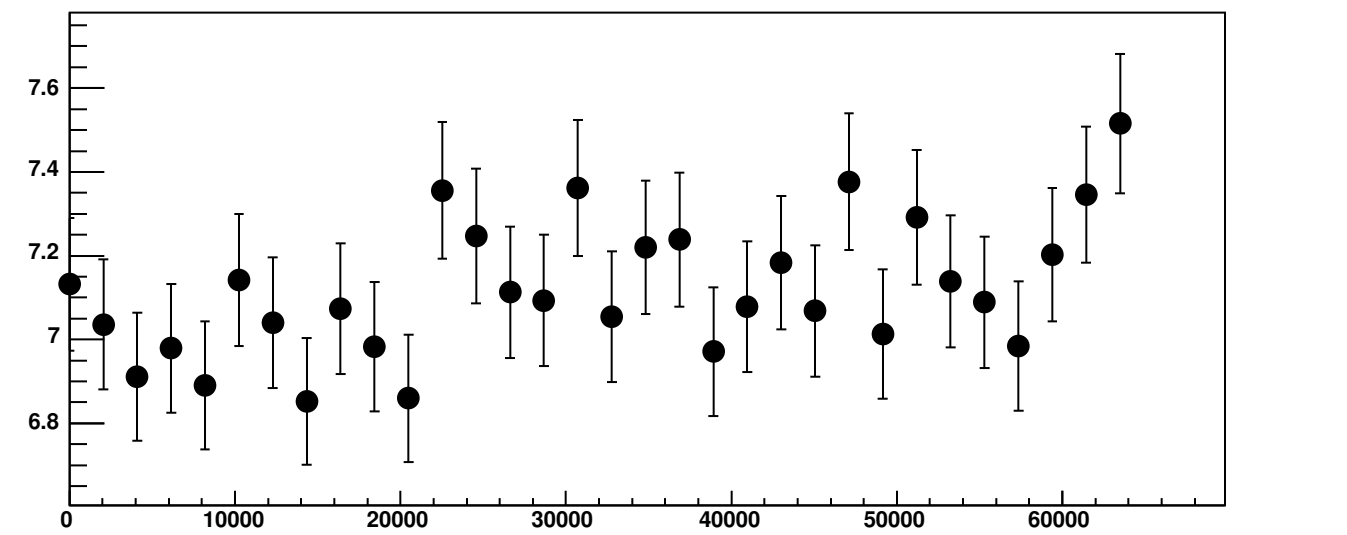

Chip 3, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

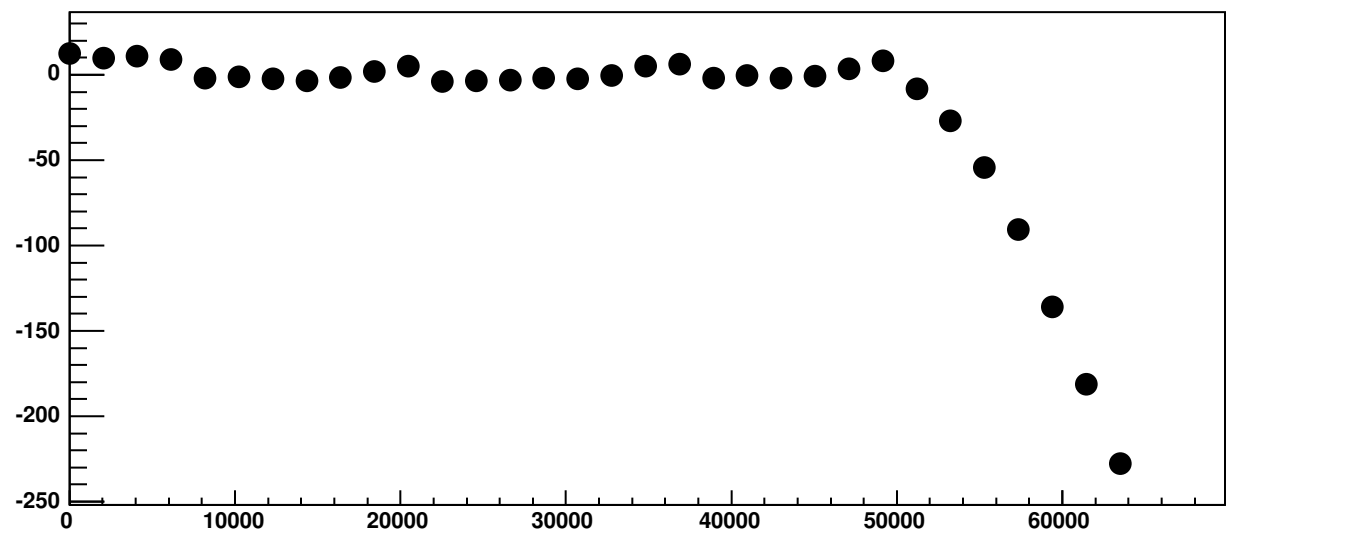
Chip 3, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

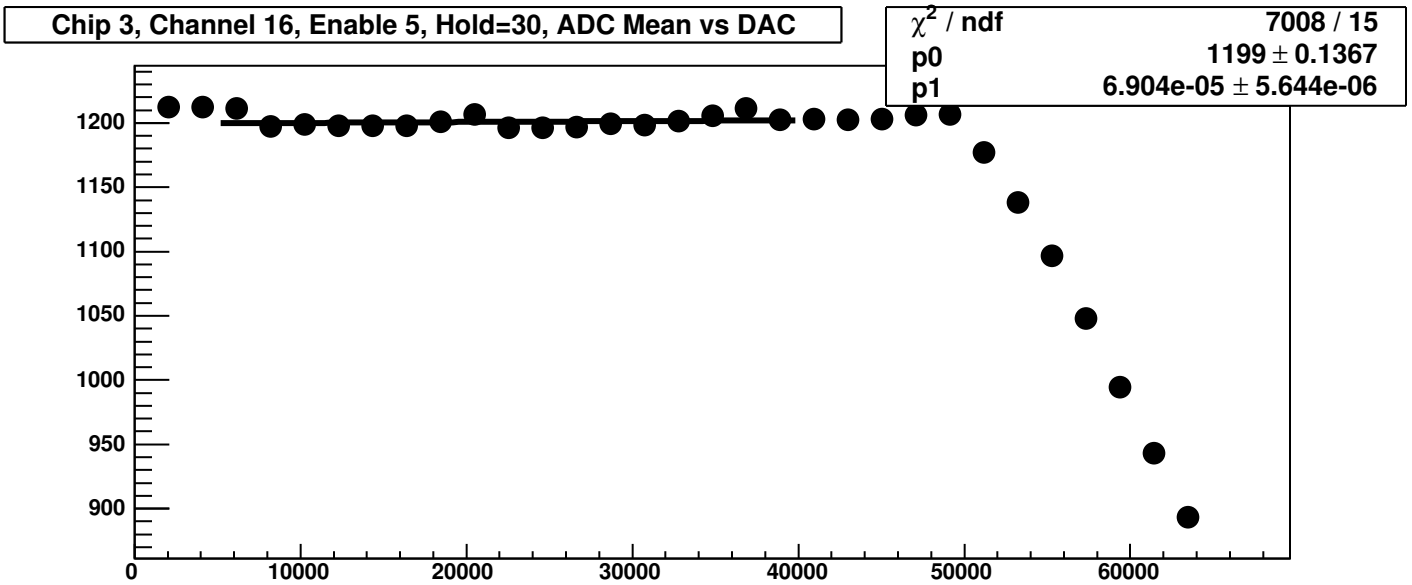


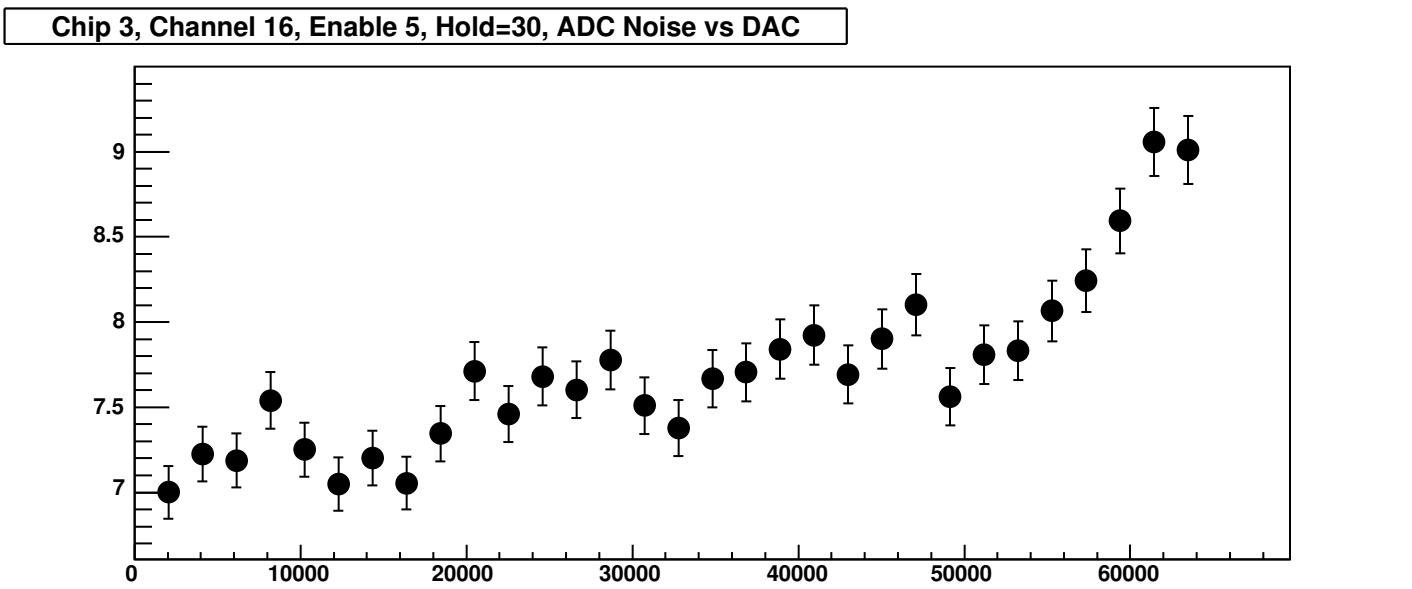
Chip 3, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC

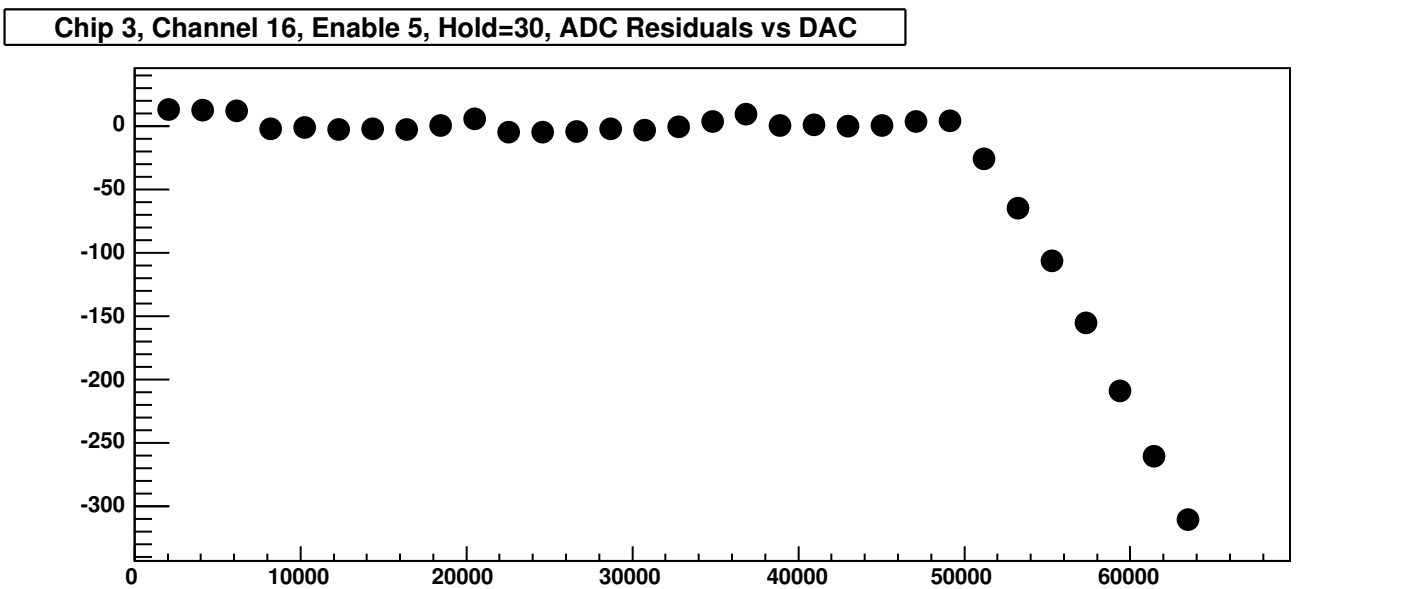


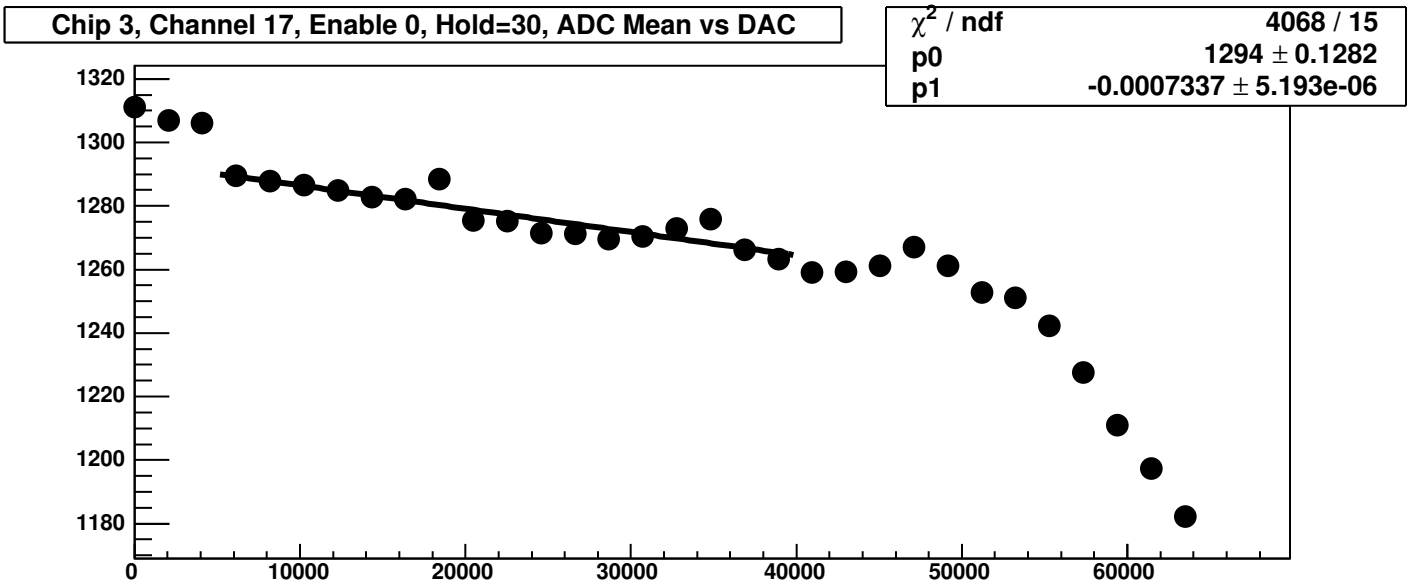
Chip 3, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC

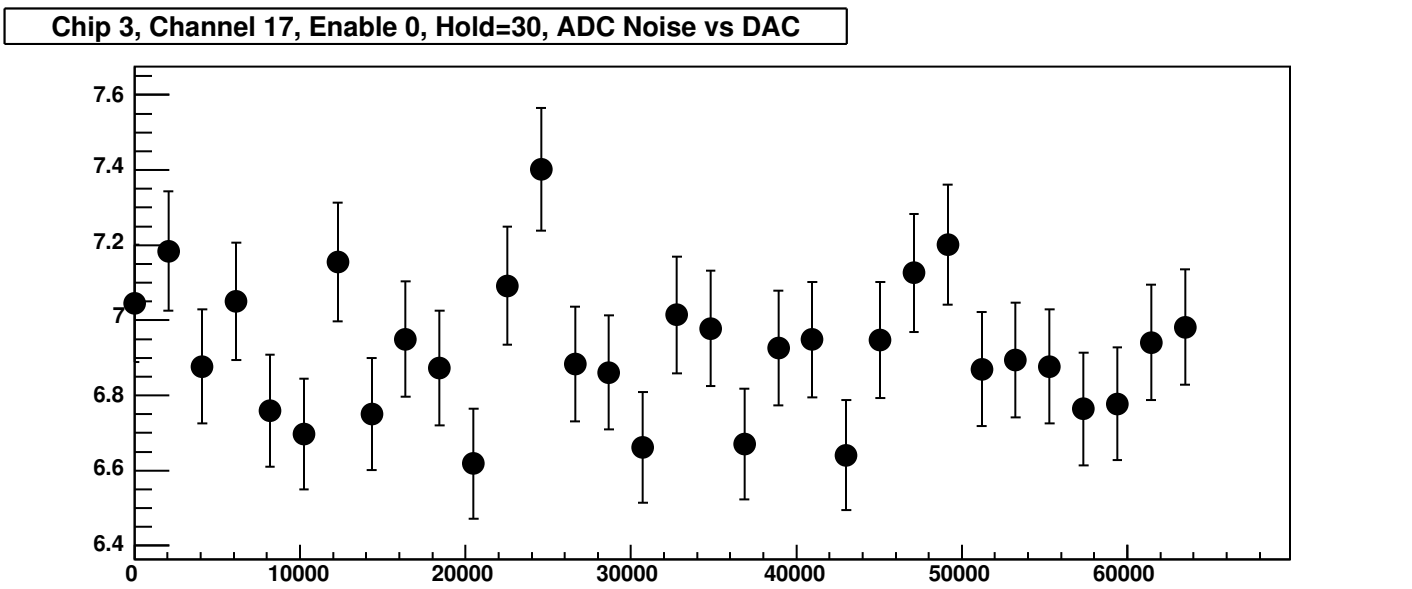


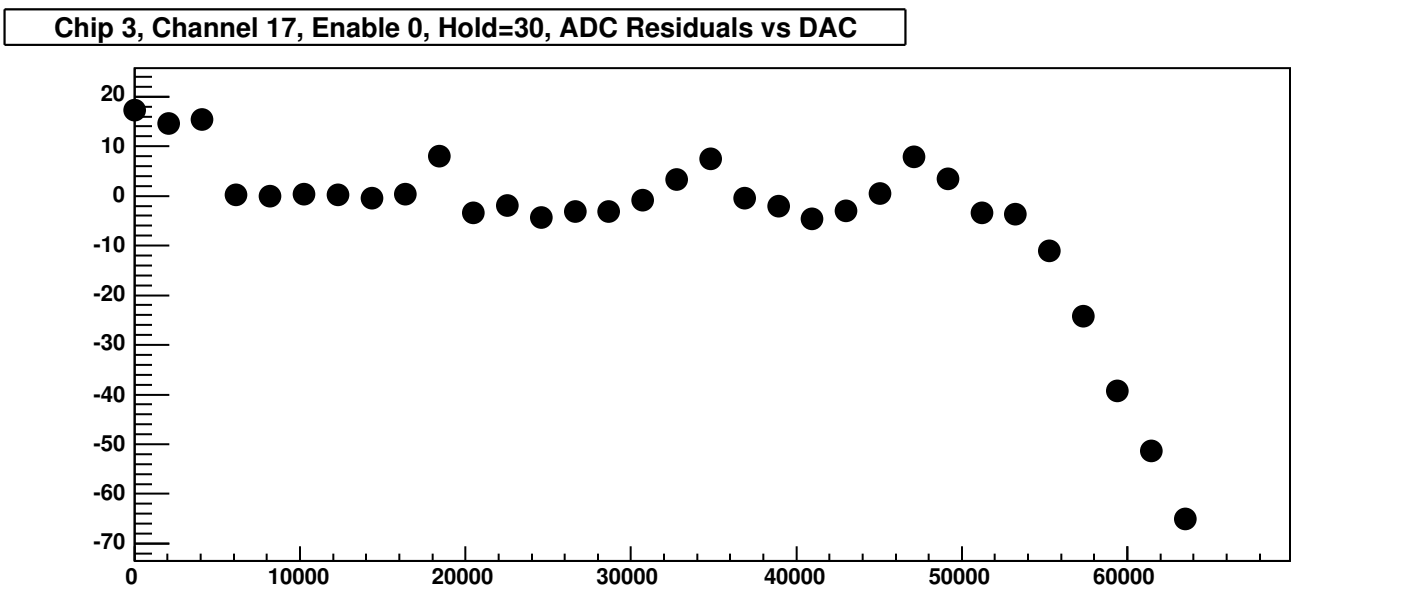

Chip 3, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7008 / 15
p0 1199 ± 0.1367
p1 6.904e-05 ± 5.644e-06
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

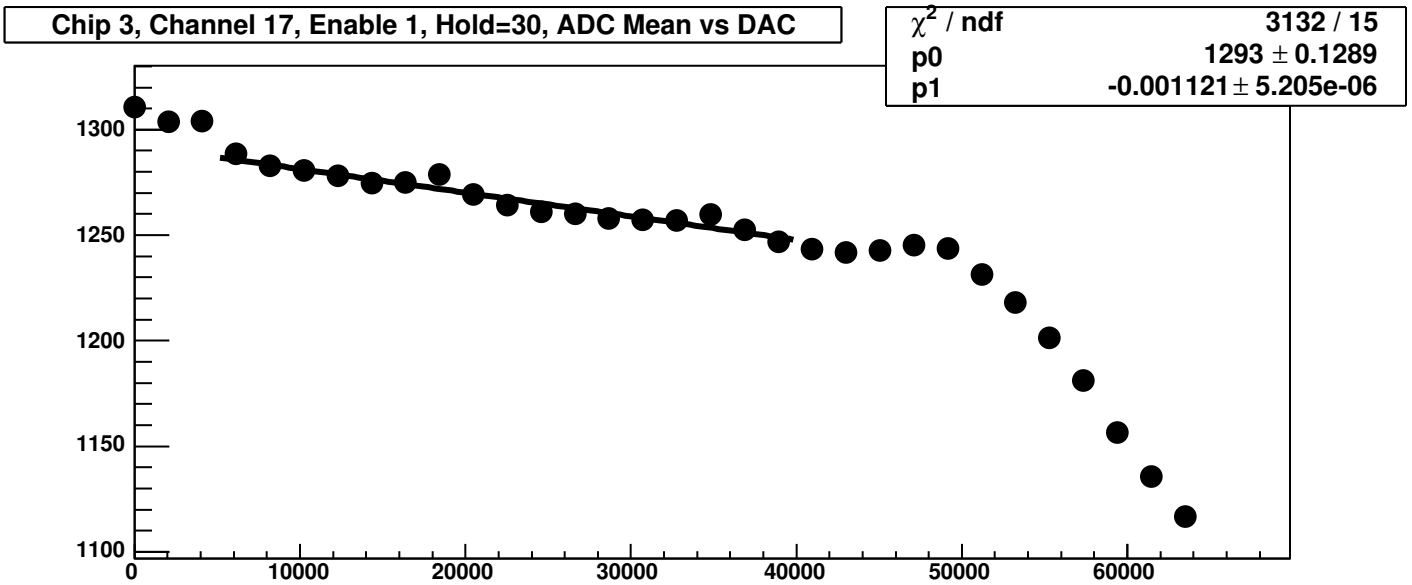

Chip 3, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC
9
8.5
8
7.5
7
0
10000
20000
30000
40000
50000
60000

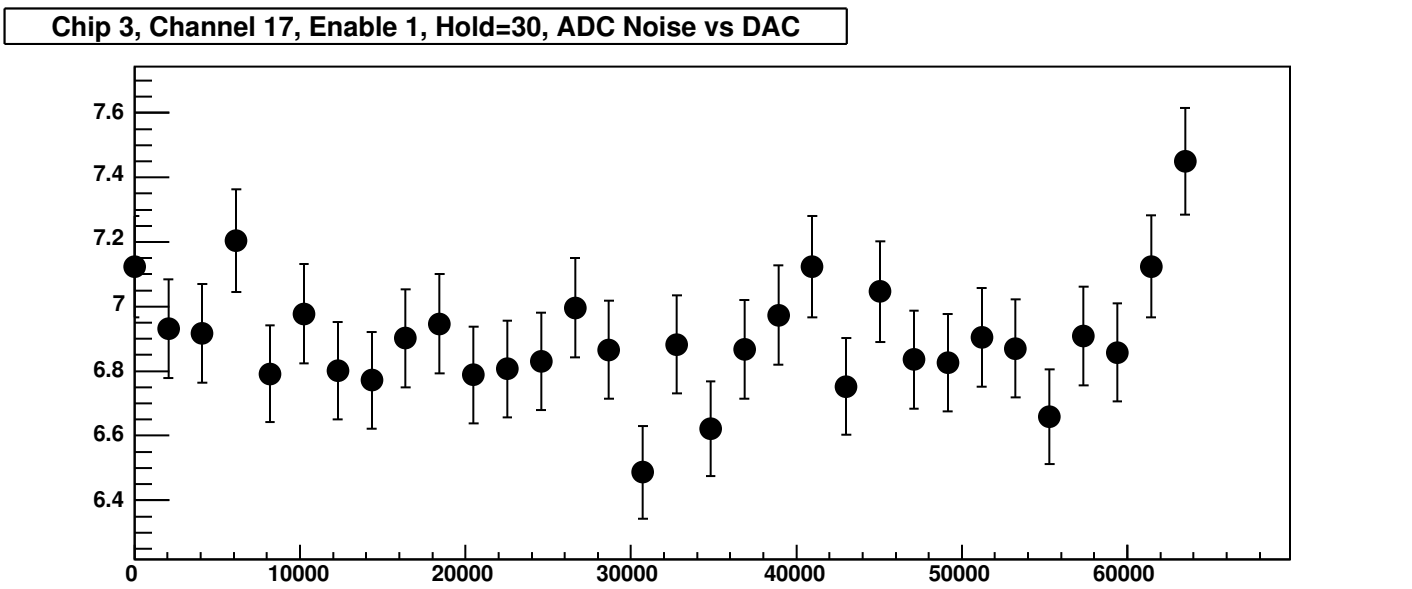

Chip 3, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

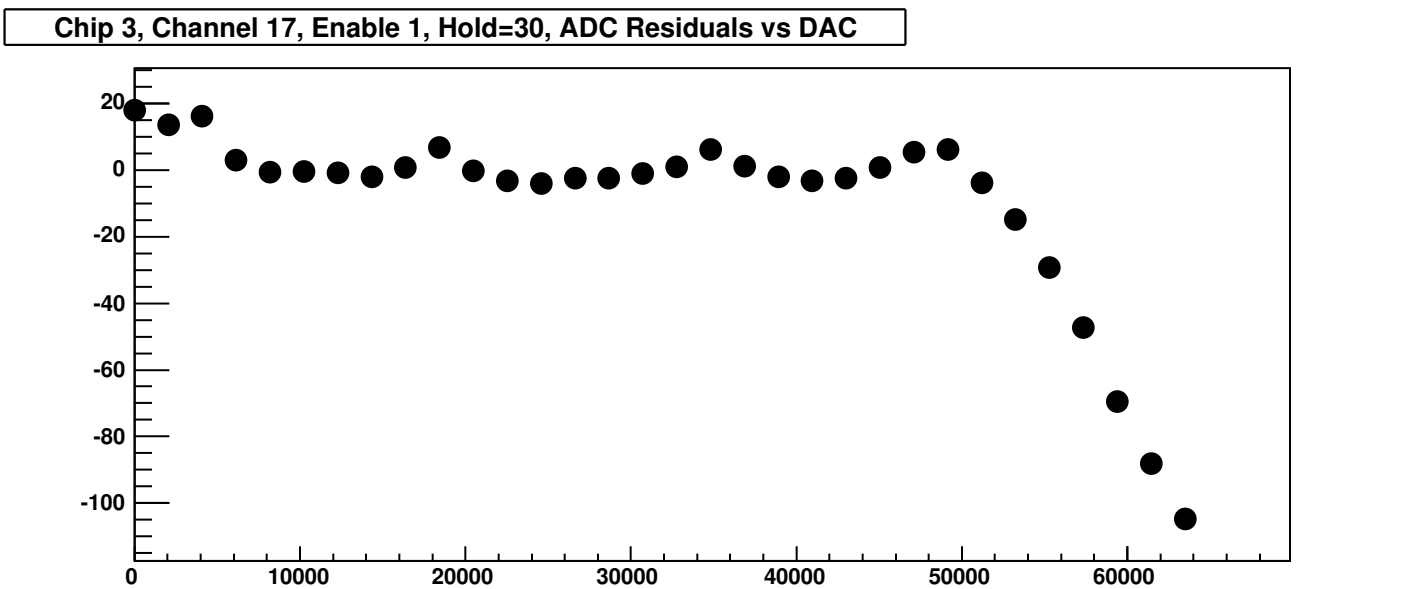

Chip 3, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4068 / 15
p0 1294 ± 0.1282
p1 -0.0007337 ± 5.193e-06
1320
1300
1280
1260
1240
1220
1200
1180
0
10000
20000
30000
40000
50000
60000

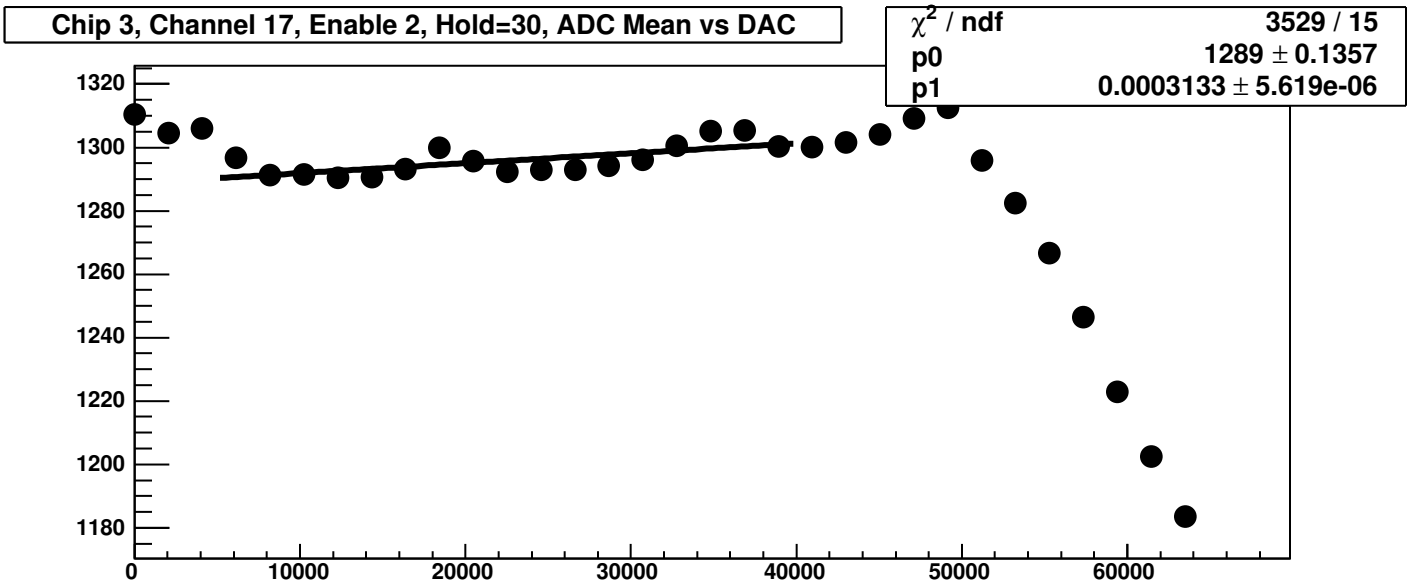

Chip 3, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

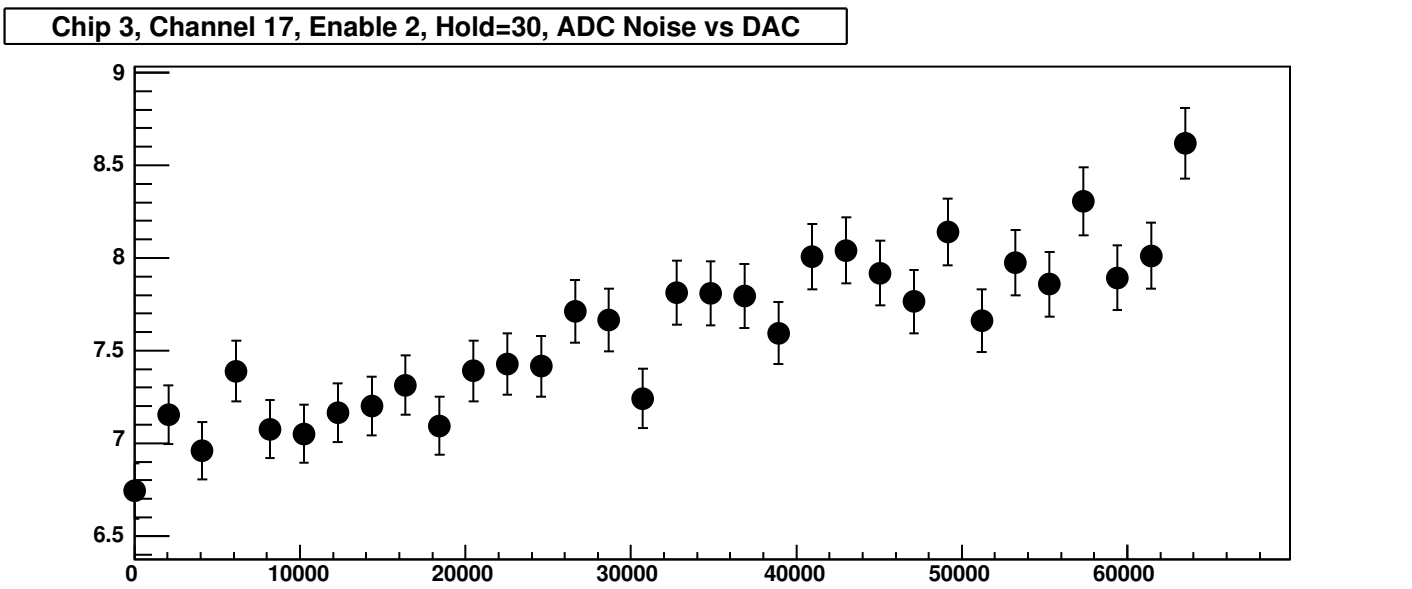

Chip 3, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
-60
-70
0
10000
20000
30000
40000
50000
60000

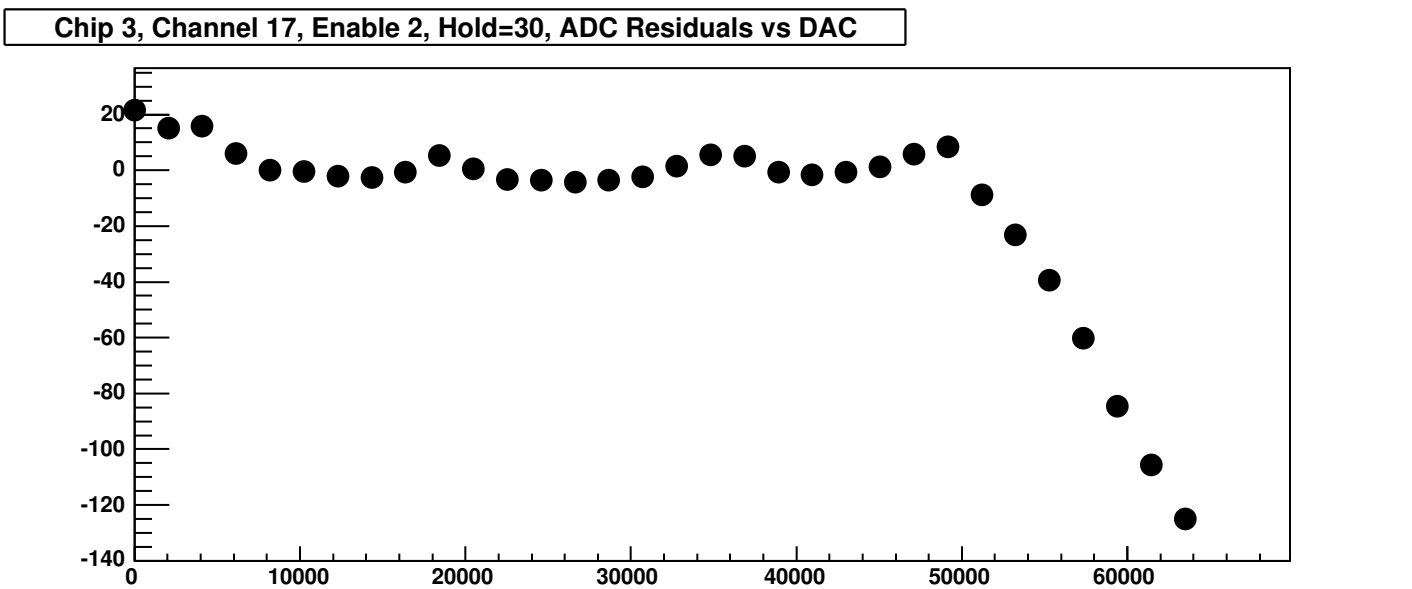

Chip 3, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3132 / 15
p0 1293 ± 0.1289
p1 -0.001121 ± 5.205e-06
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

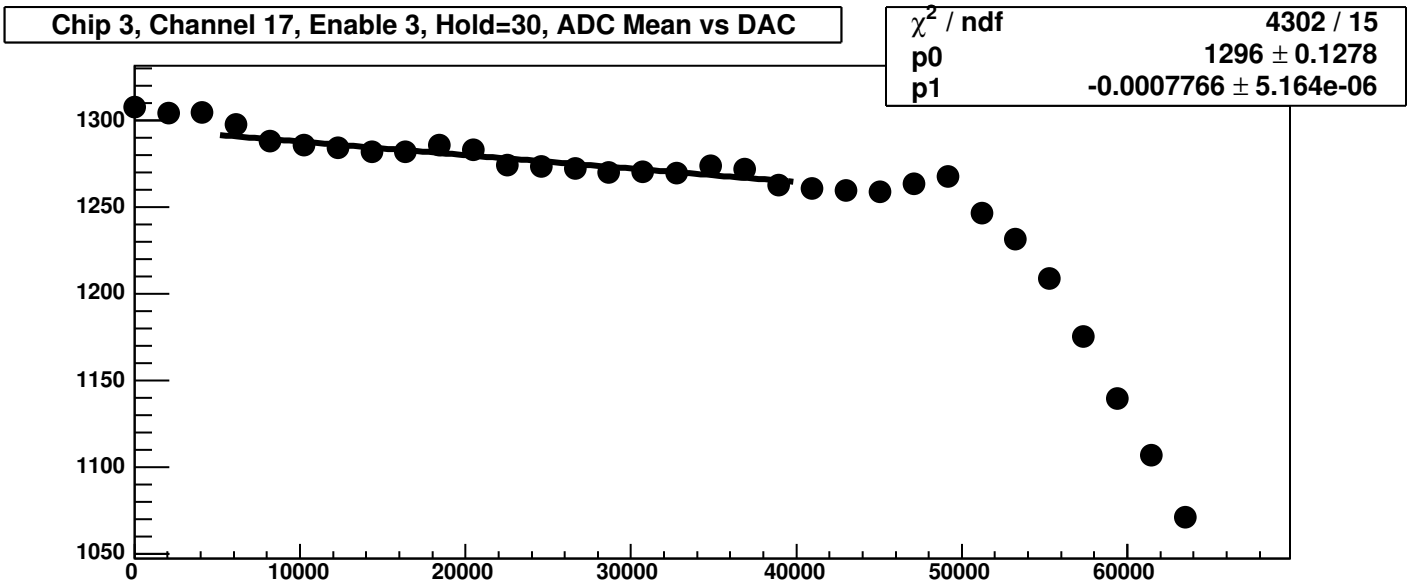

Chip 3, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

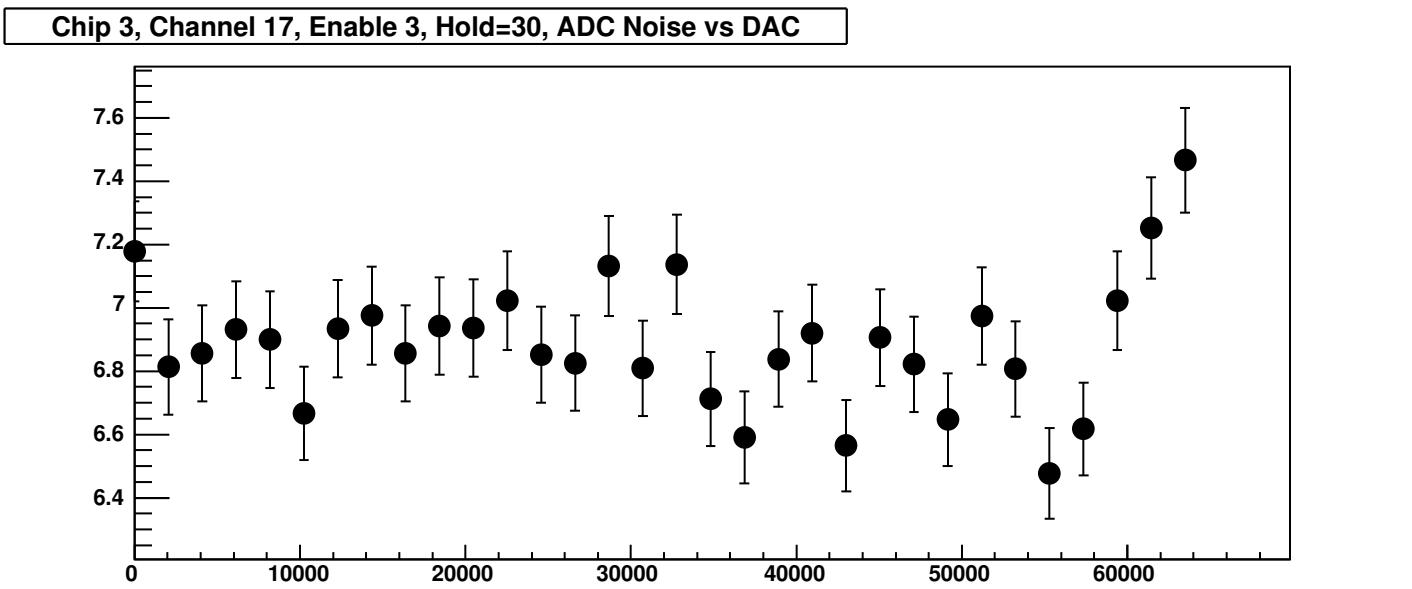

Chip 3, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

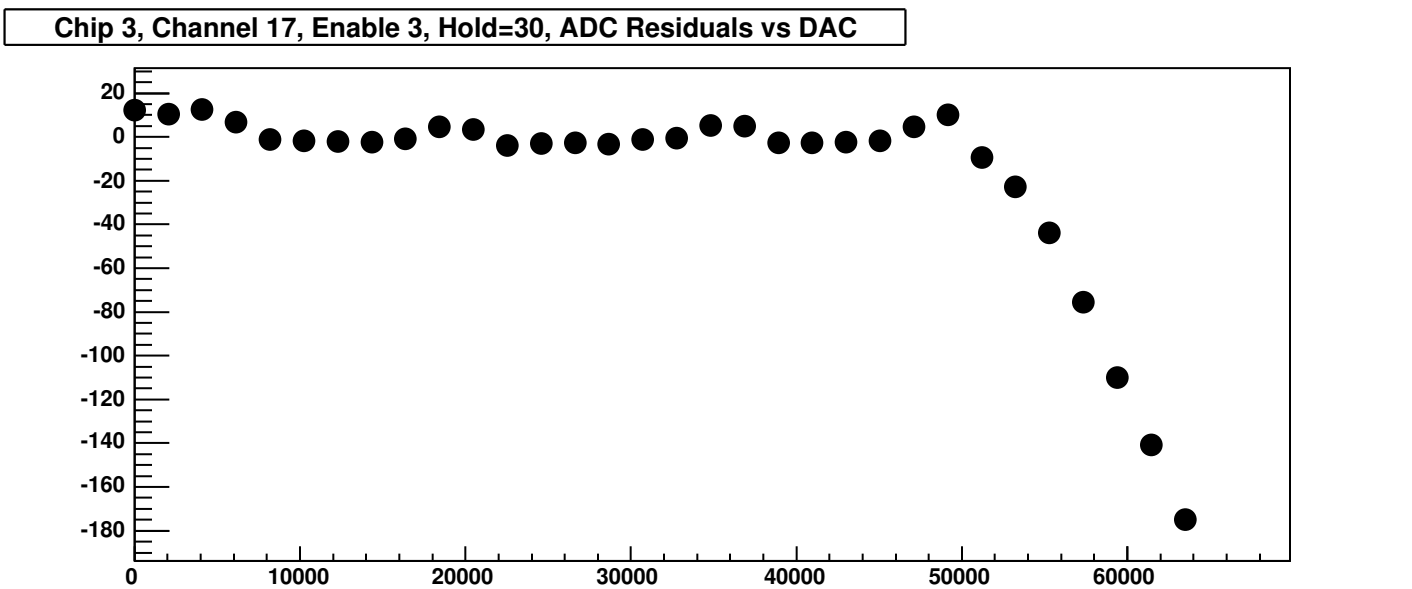

Chip 3, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3529 / 15
p0 1289 ± 0.1357
p1 0.0003133 ± 5.619e-06
1320
1300
1280
1260
1240
1220
1200
1180
0
10000
20000
30000
40000
50000
60000

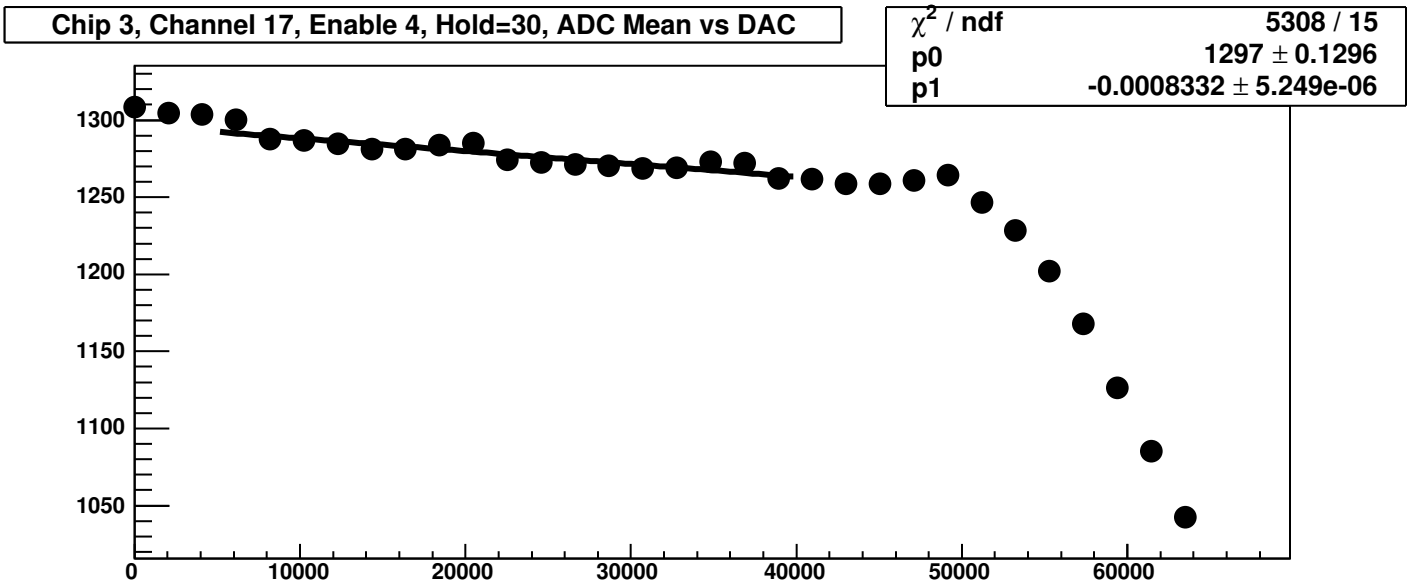

Chip 3, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC
9
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000

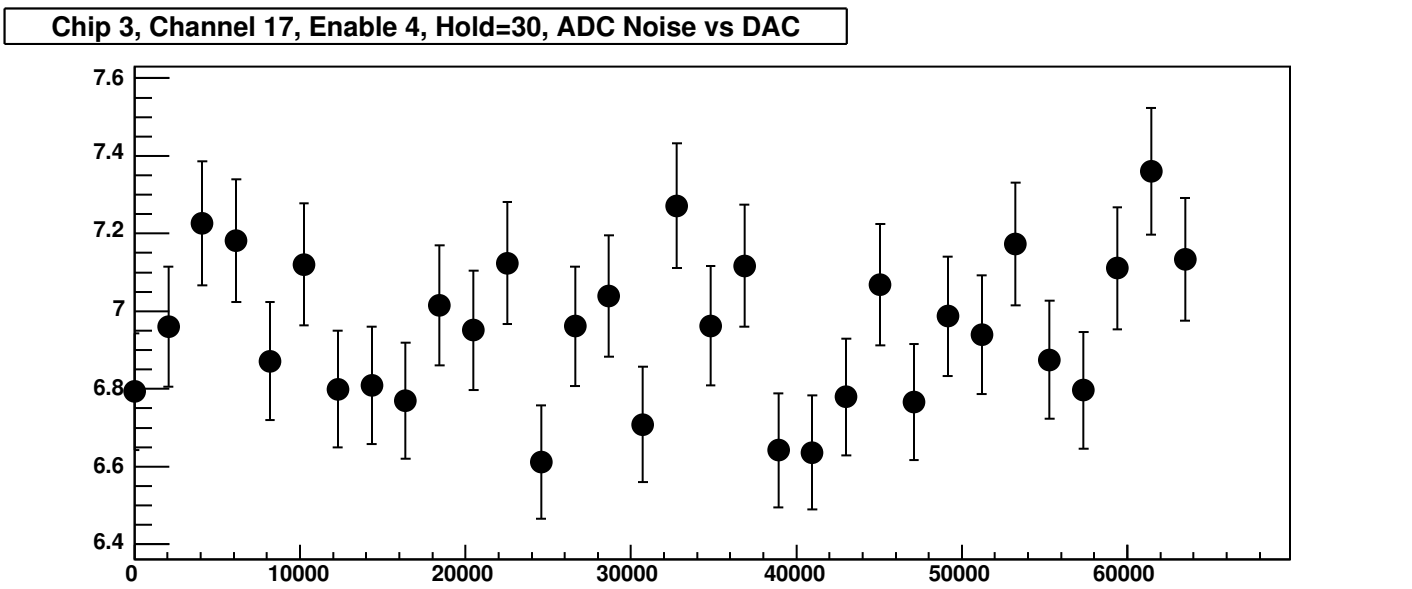

Chip 3, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

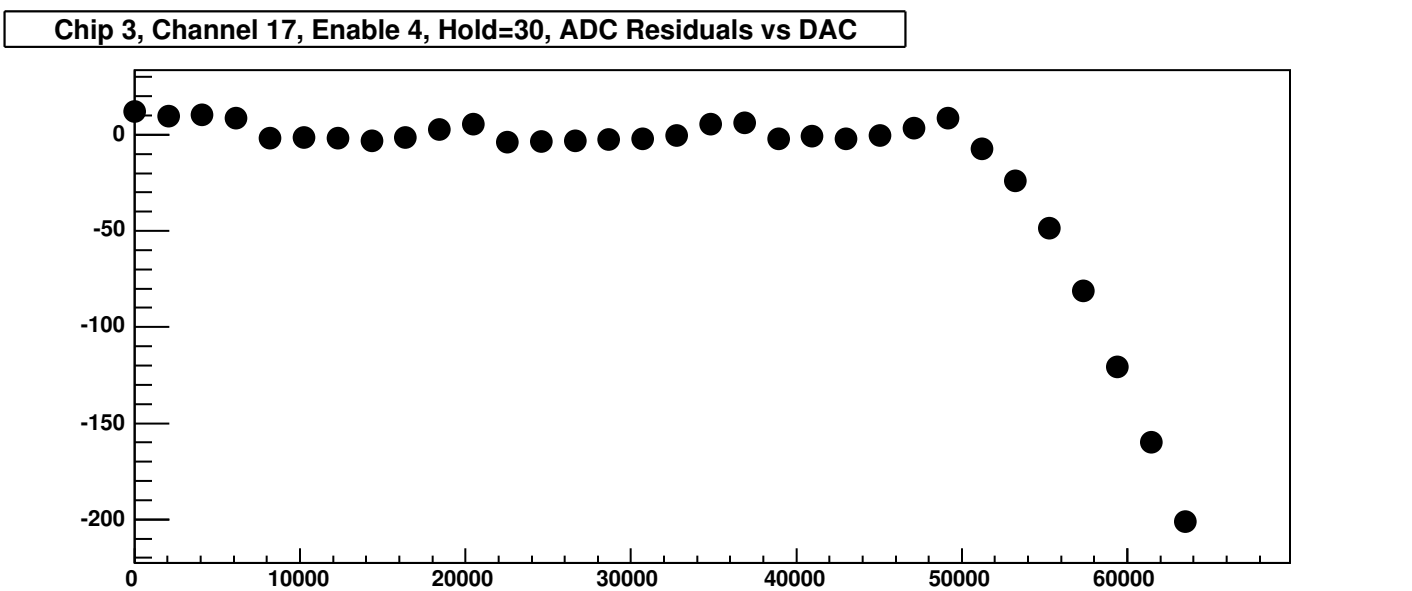

Chip 3, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4302 / 15
p0 1296 ± 0.1278
p1 -0.0007766 ± 5.164e-06
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

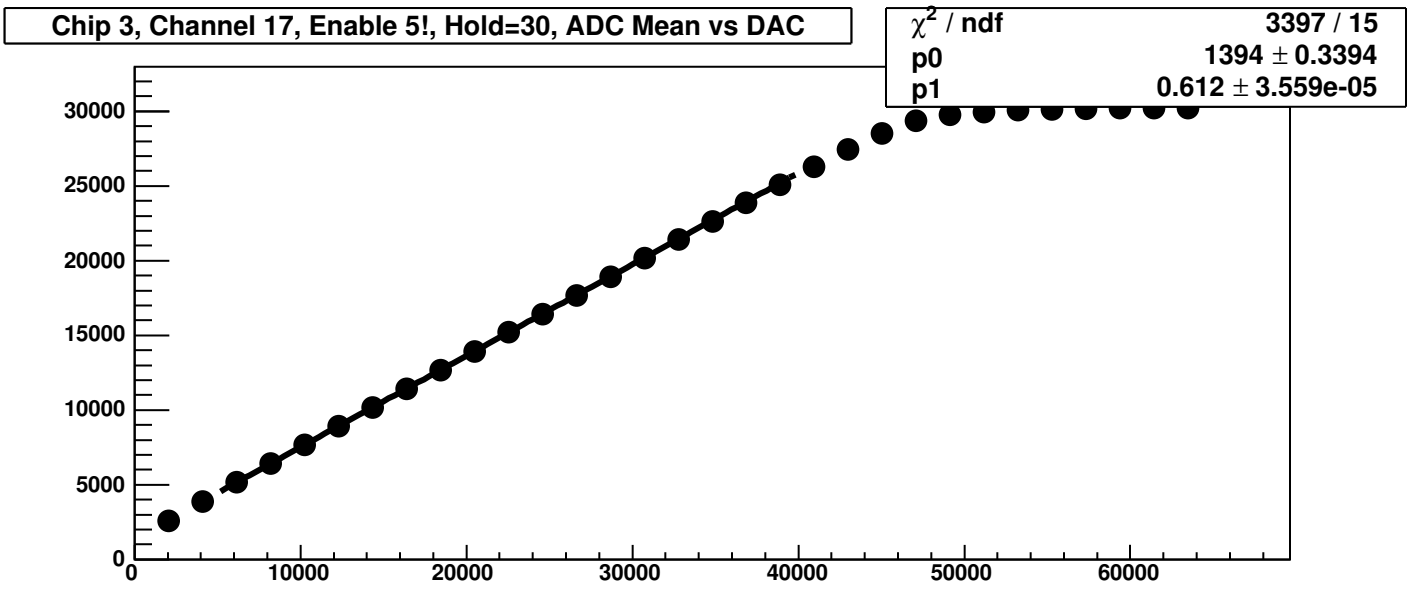

Chip 3, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

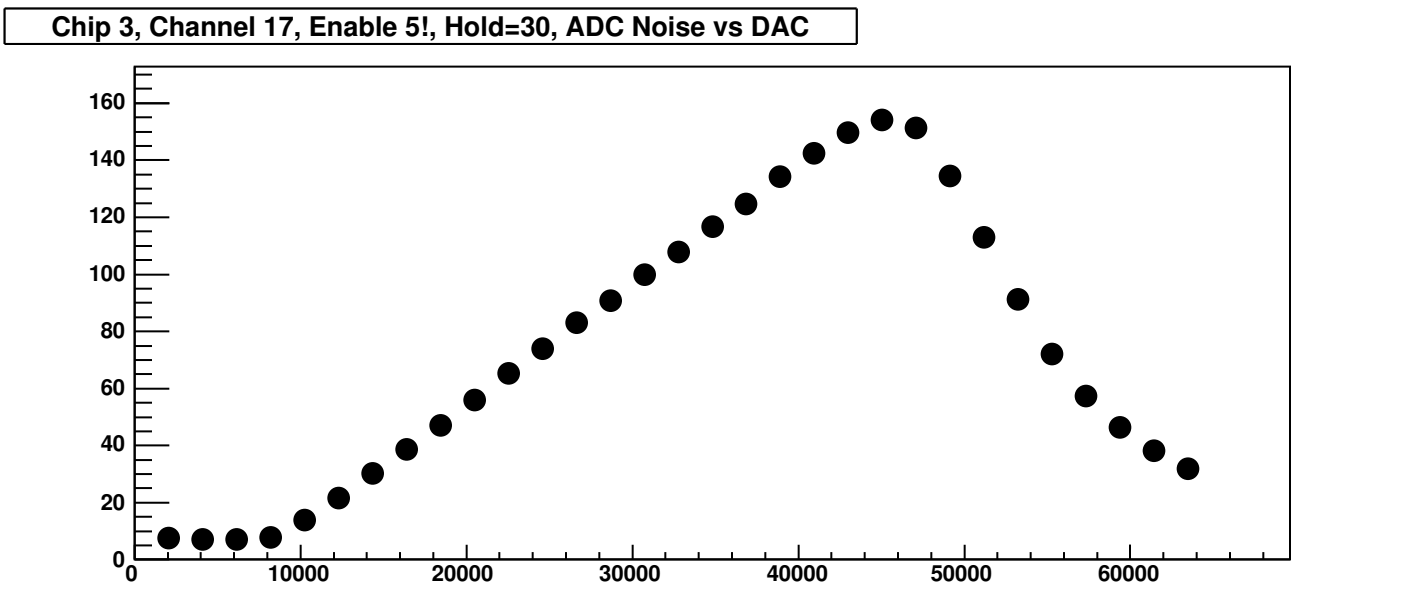

Chip 3, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

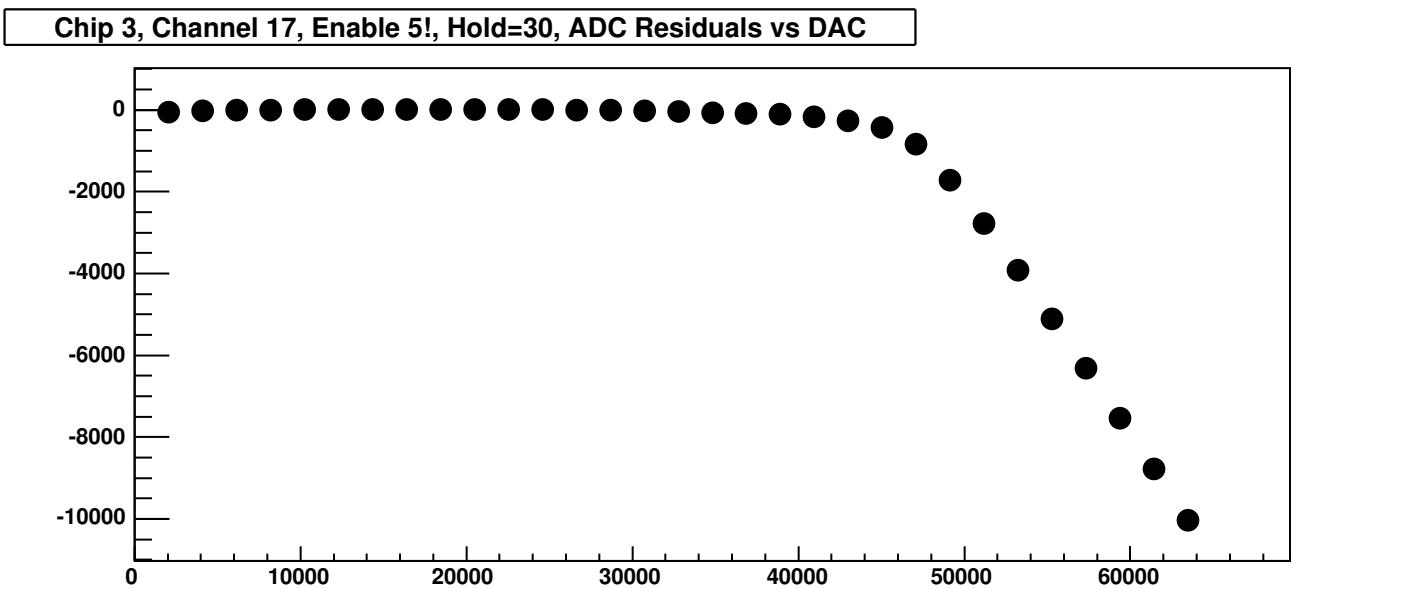

Chip 3, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5308 / 15
p0 1297 ± 0.1296
p1 -0.0008332 ± 5.249e-06
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

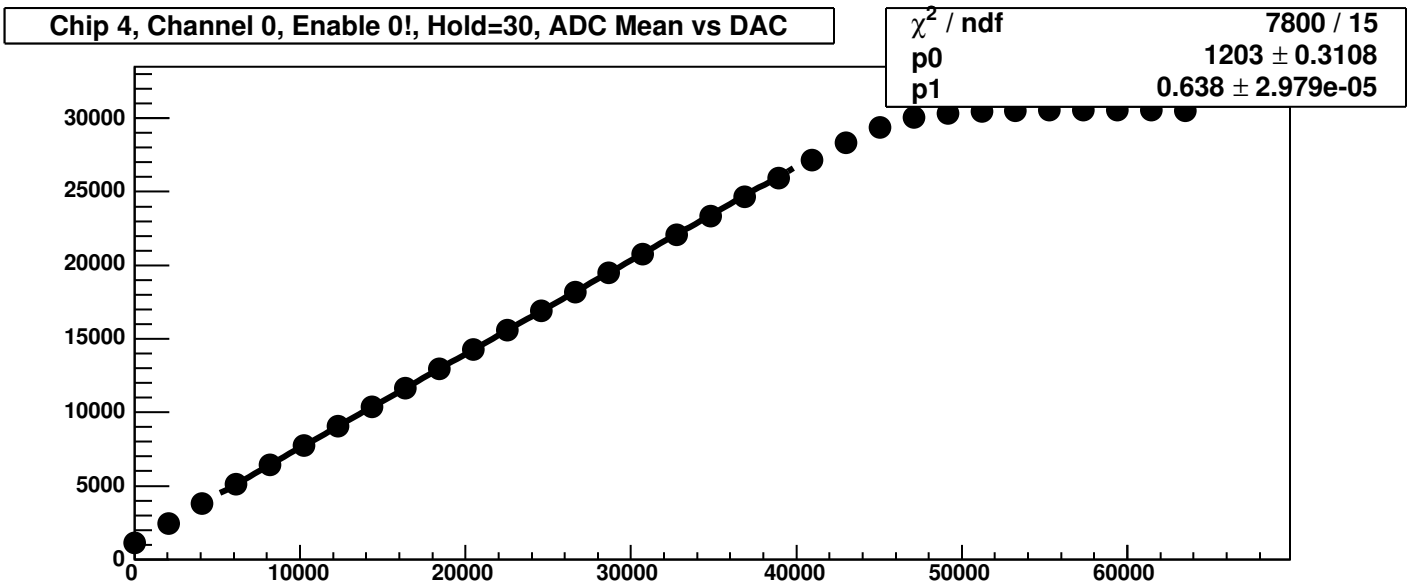

Chip 3, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

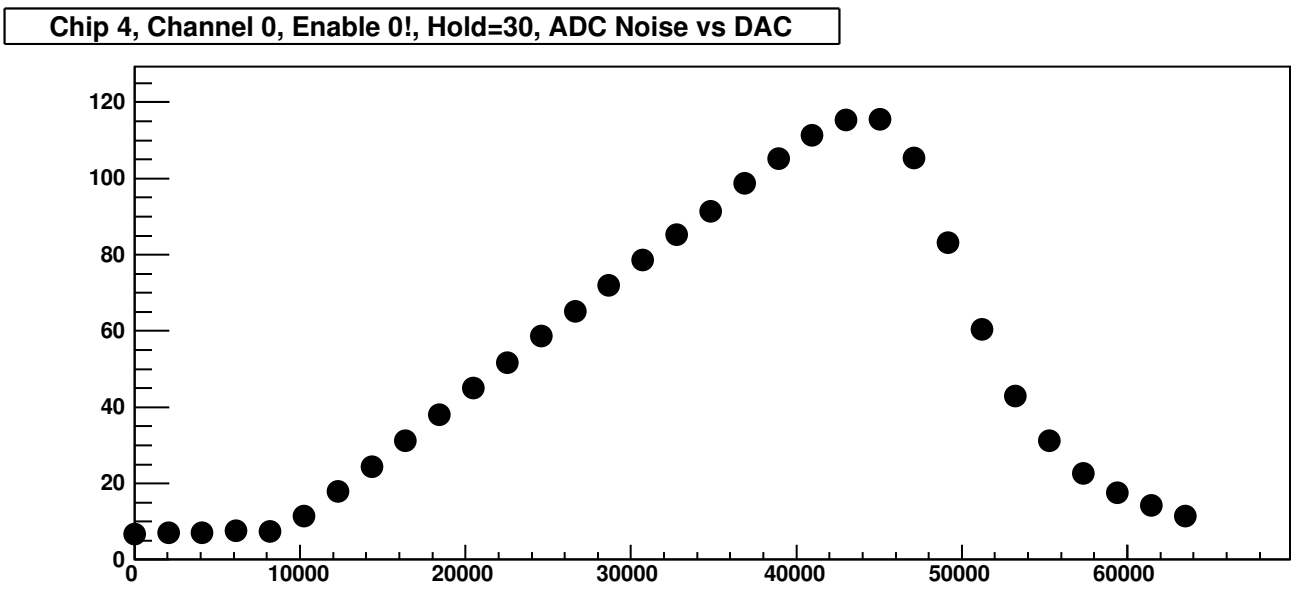

Chip 3, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

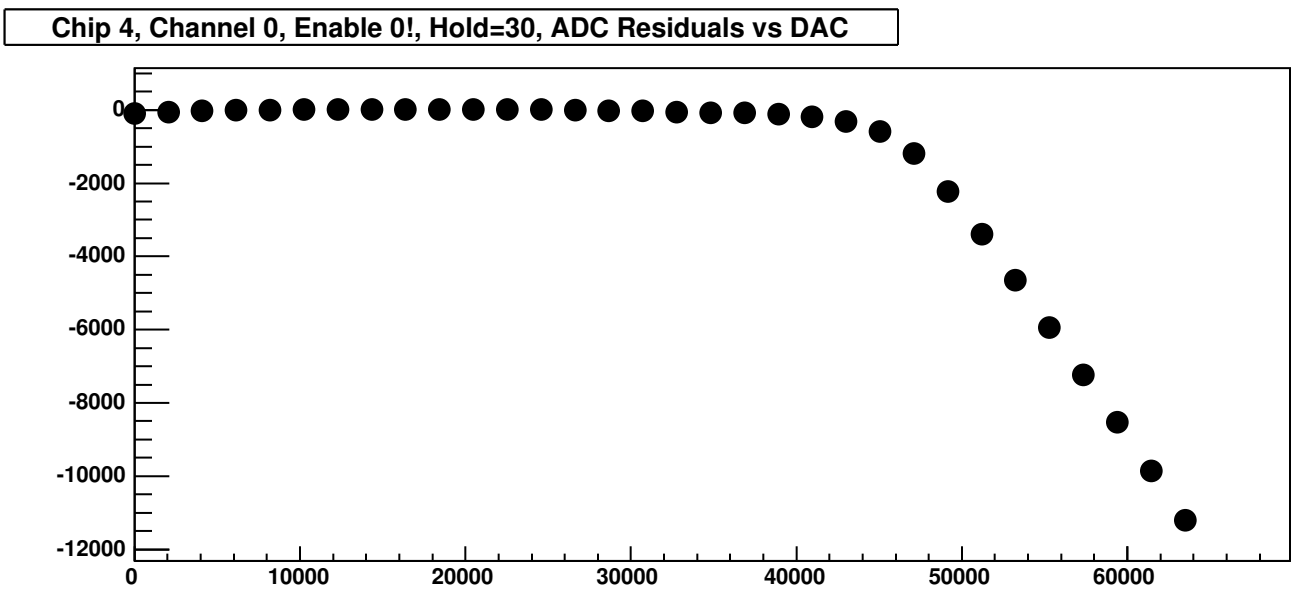

Chip 3, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 3397 / 15
p0 1394 ± 0.3394
p1 0.612 ± 3.559e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

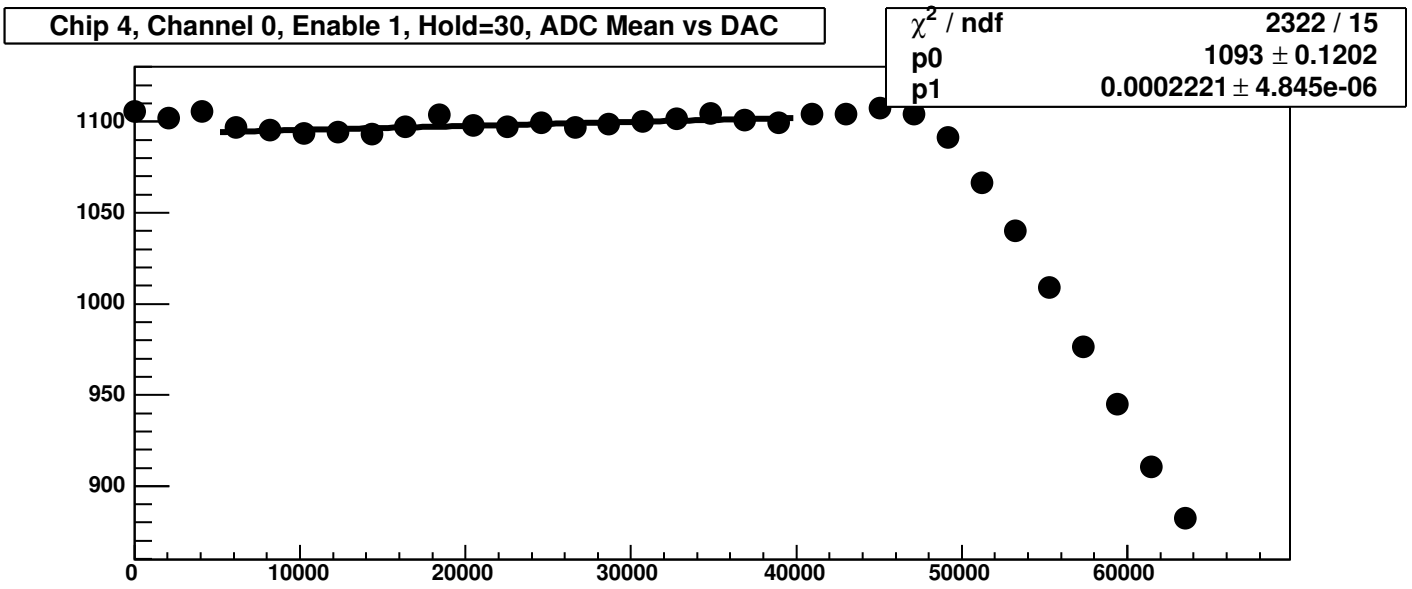

Chip 3, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

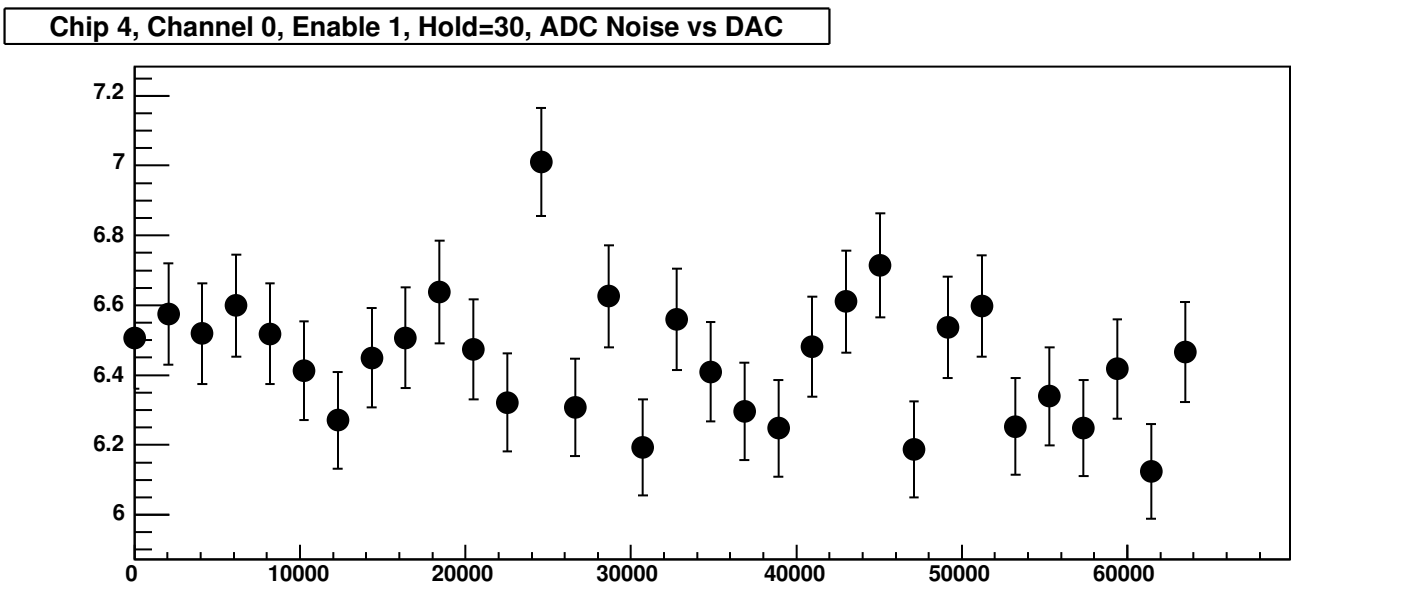

Chip 3, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

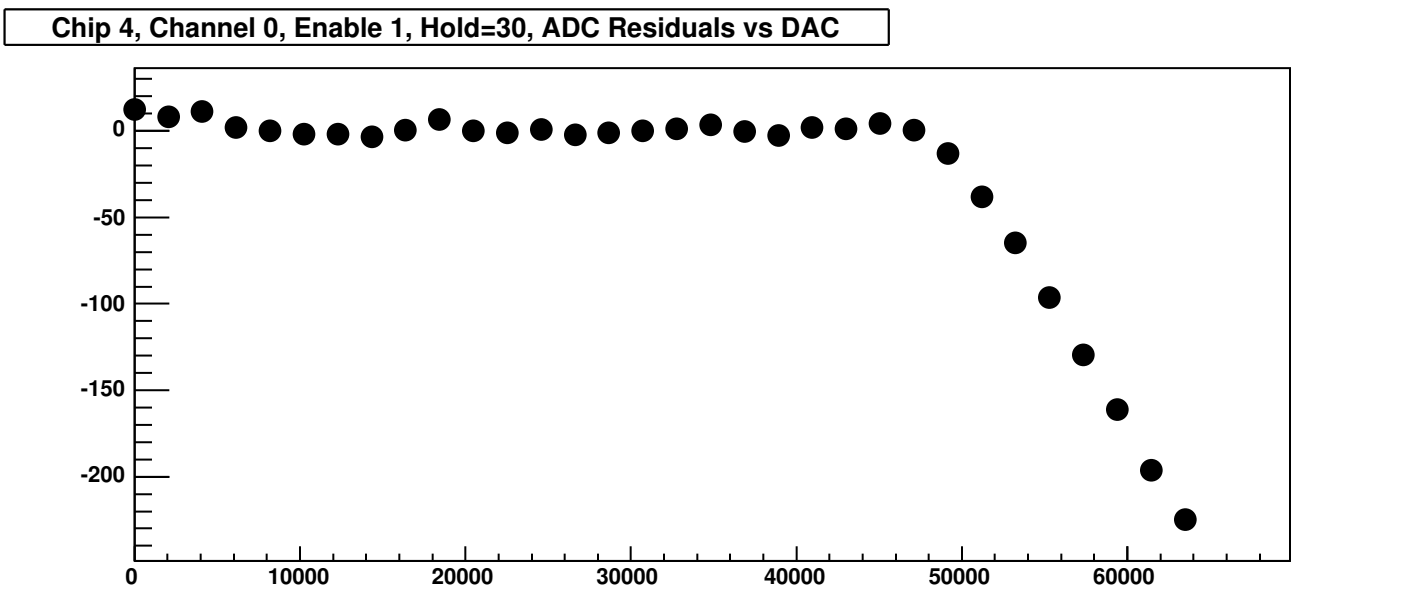

Chip 4, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 7800 / 15
p0 1203 ± 0.3108
p1 0.638 ± 2.979e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

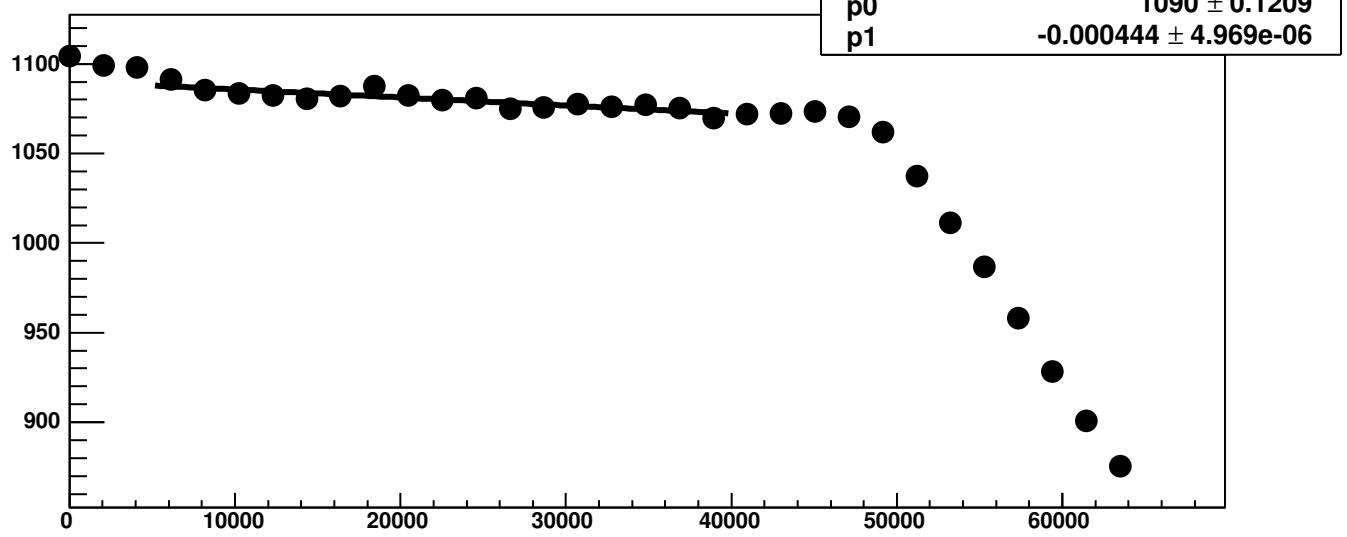

Chip 4, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2322 / 15
p0 1093 ± 0.1202
p1 0.0002221 ± 4.845e-06
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

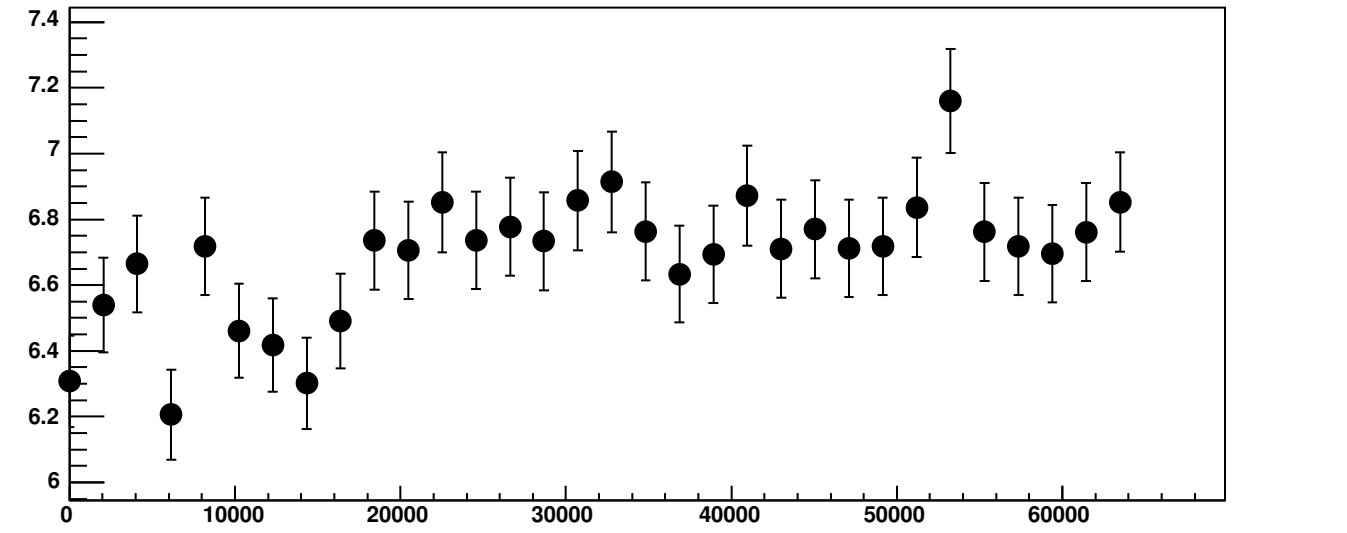

Chip 4, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

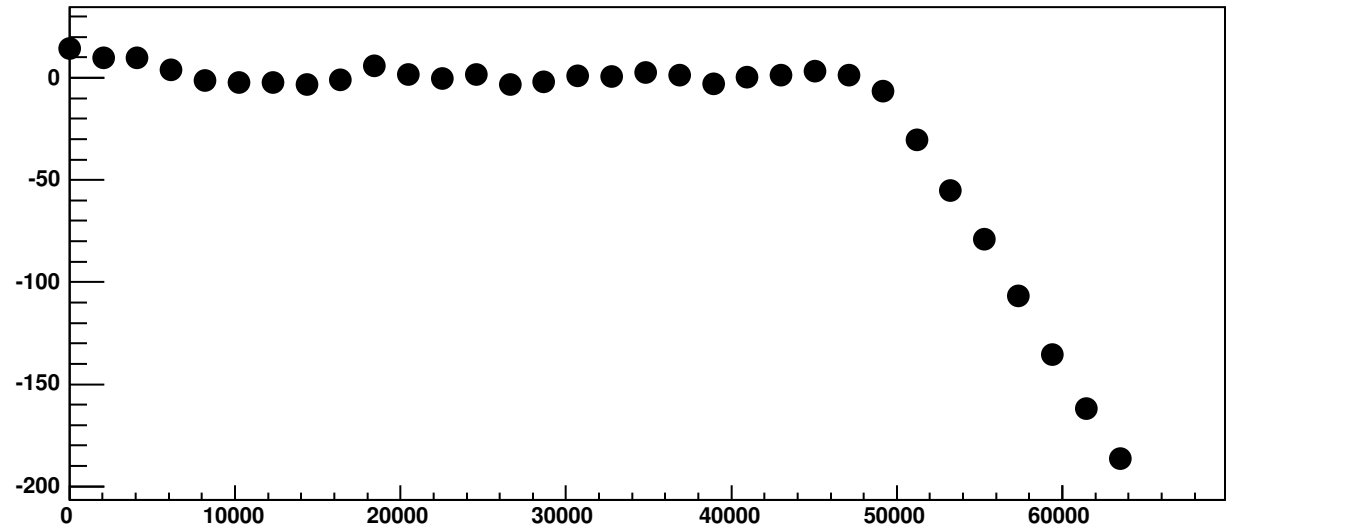
Chip 4, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC

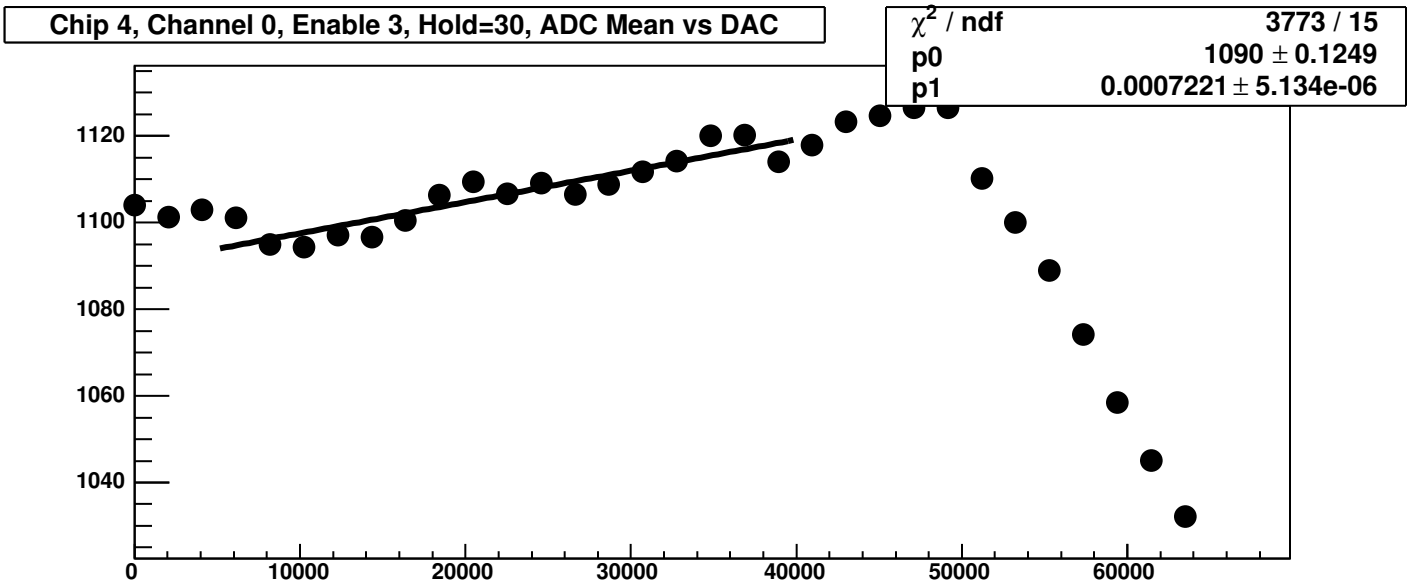


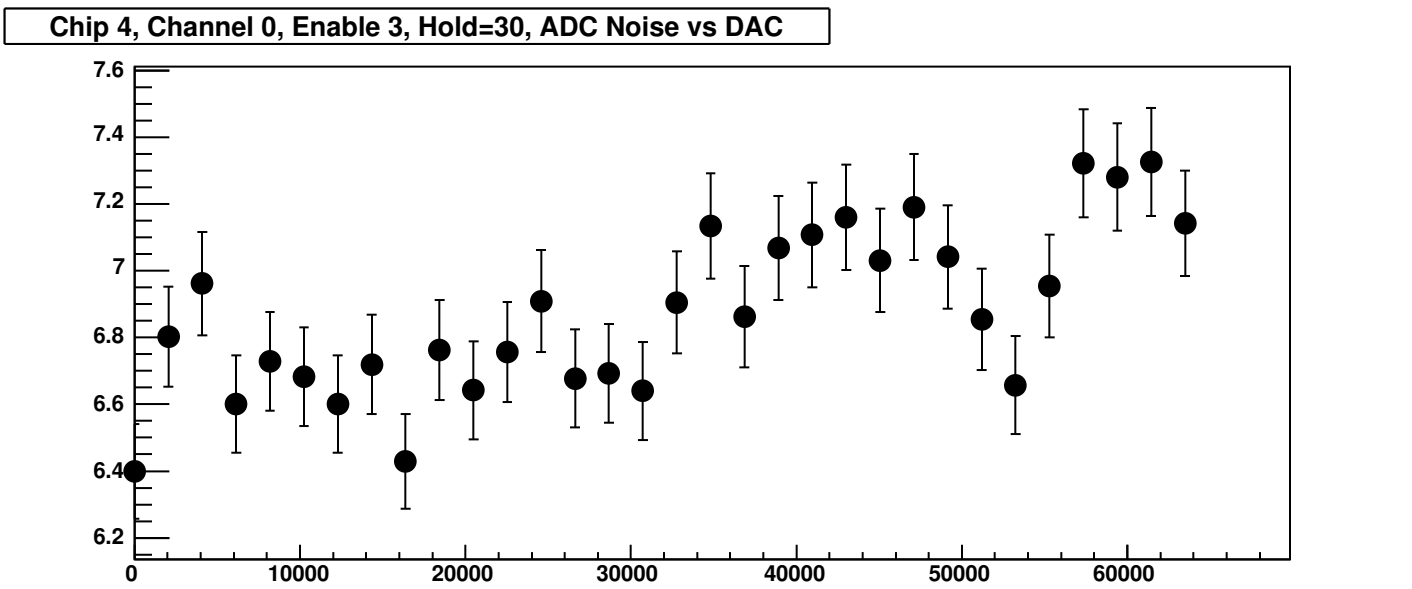
Chip 4, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC

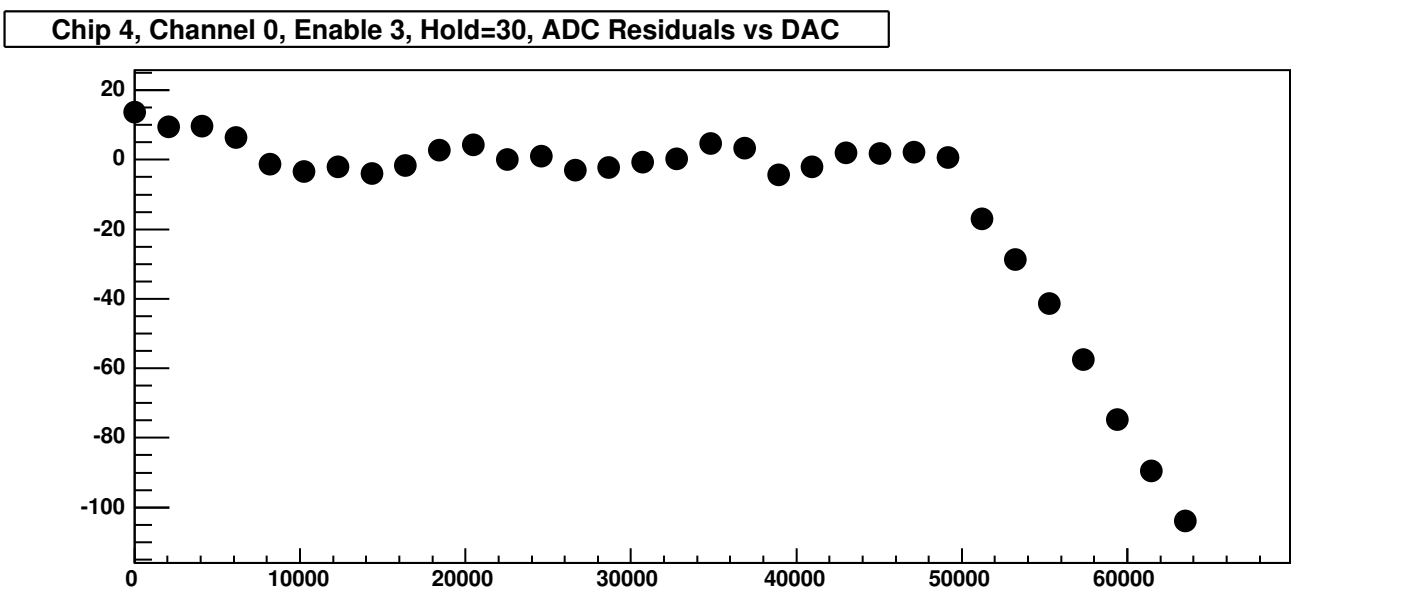


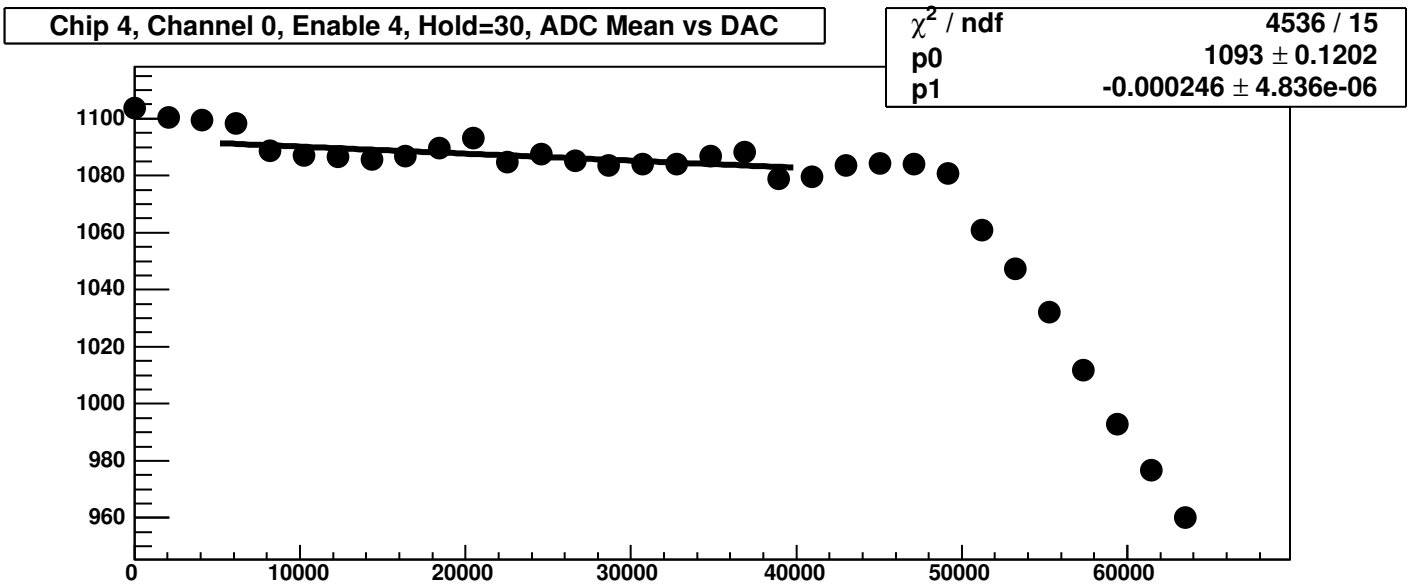
Chip 4, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

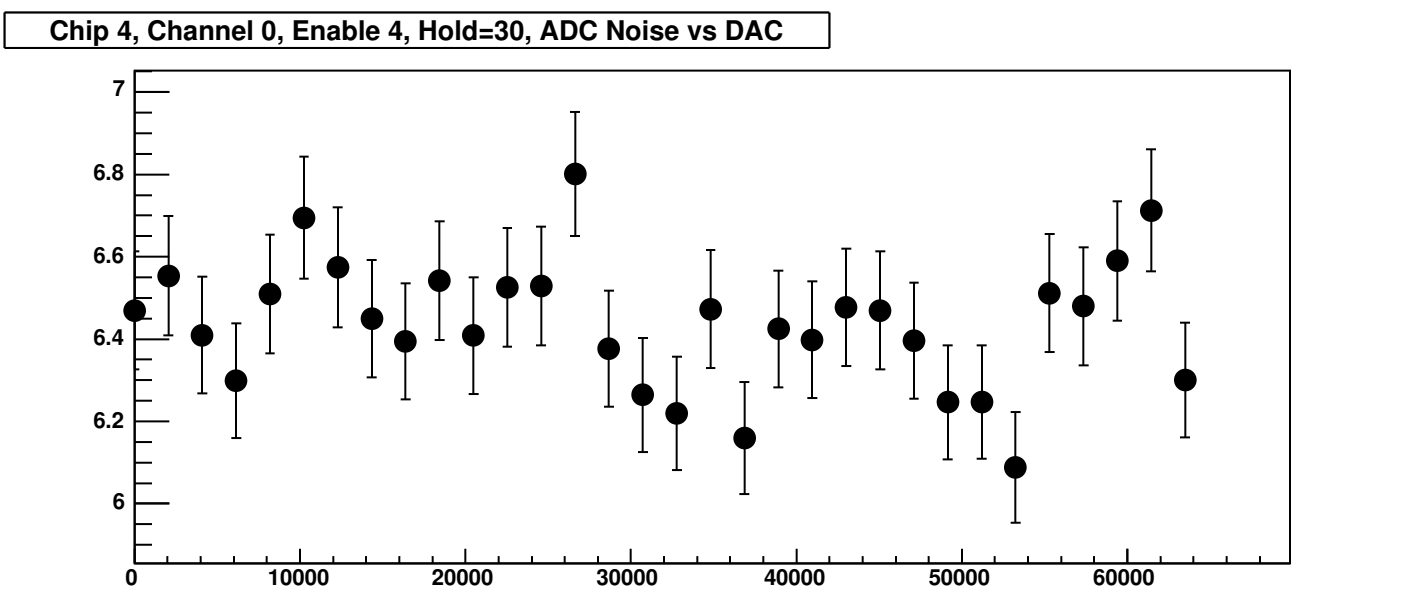


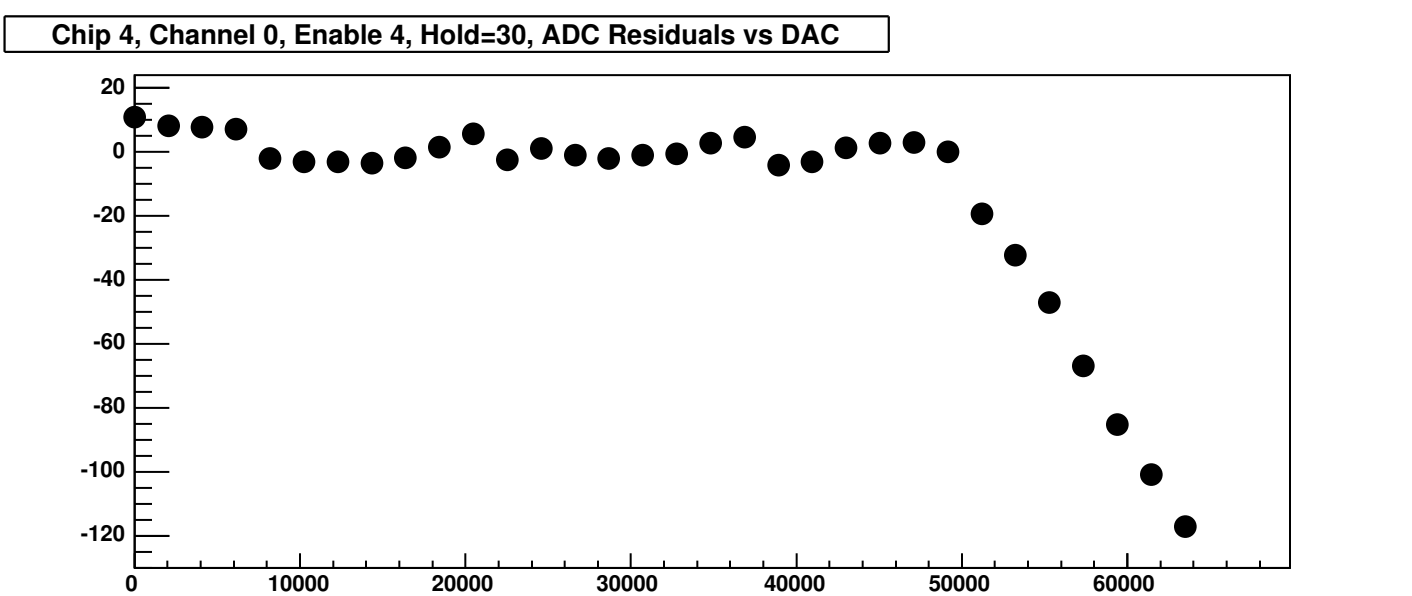

Chip 4, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3773 / 15
p0 1090 ± 0.1249
p1 0.0007221 ± 5.134e-06
1120
1100
1080
1060
1040
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

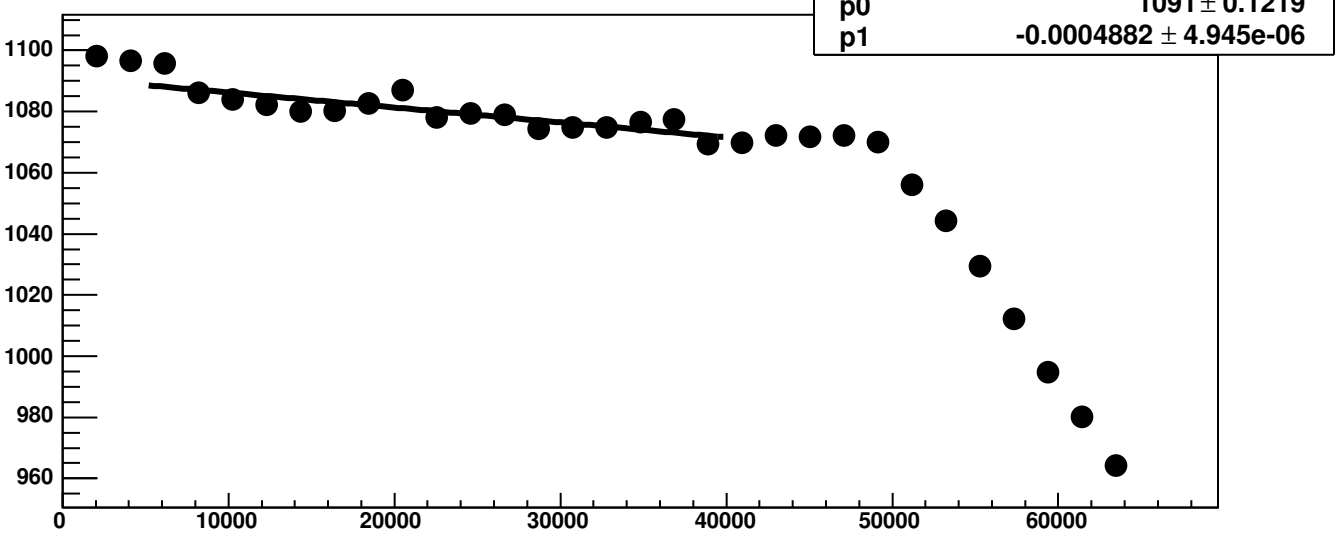

Chip 4, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4536 / 15
p0 1093 ± 0.1202
p1 -0.000246 ± 4.836e-06
1100
1080
1060
1040
1020
1000
980
960
0
10000
20000
30000
40000
50000
60000

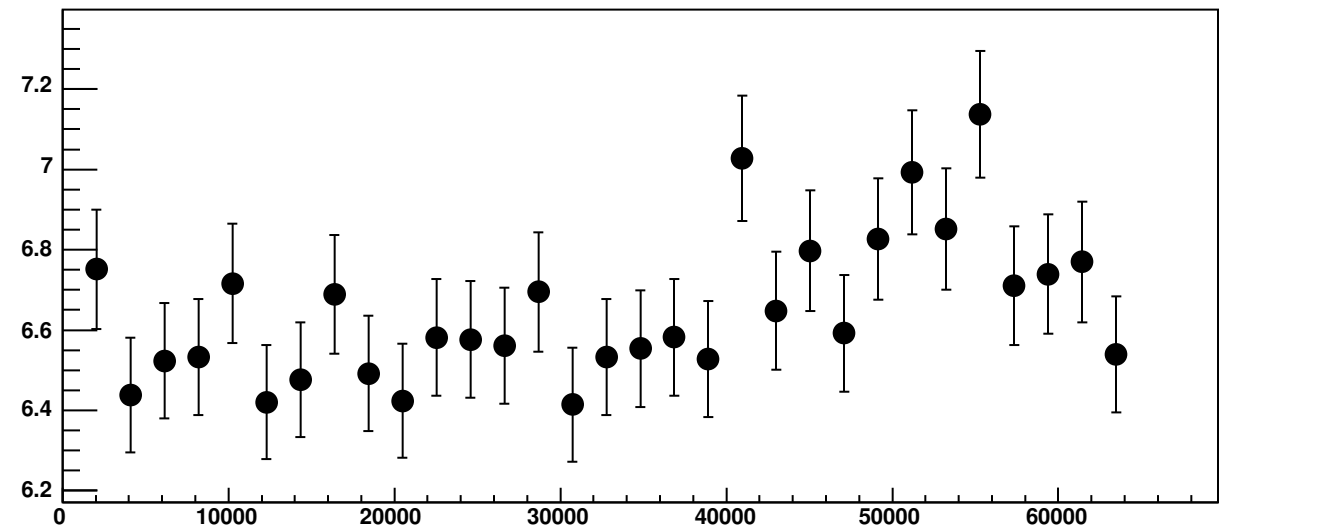

Chip 4, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

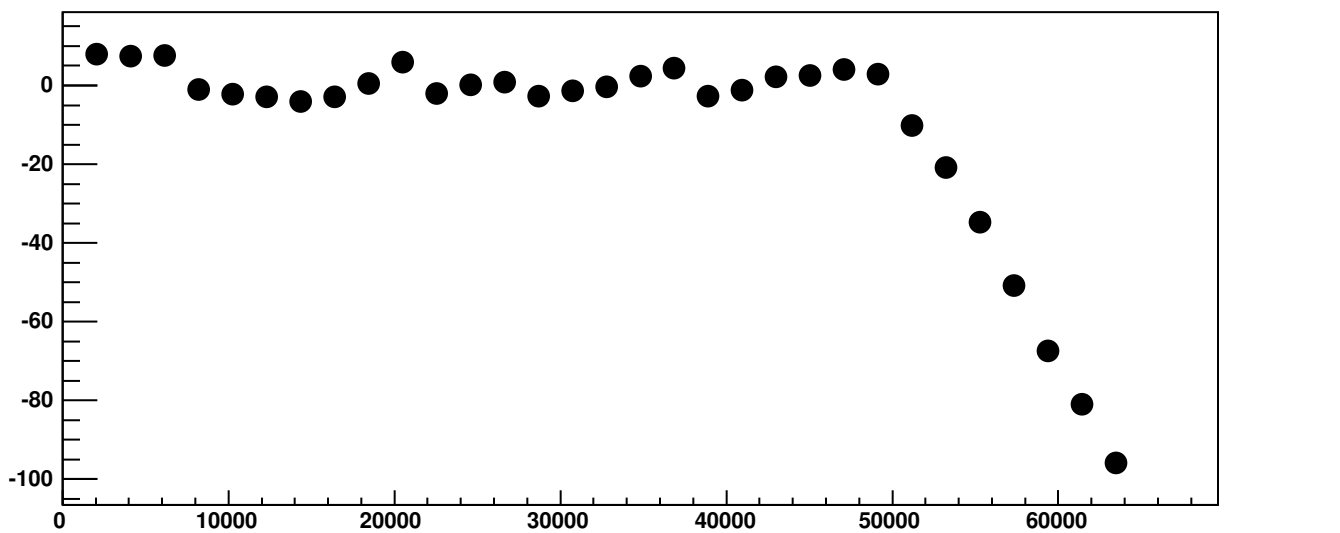
Chip 4, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC

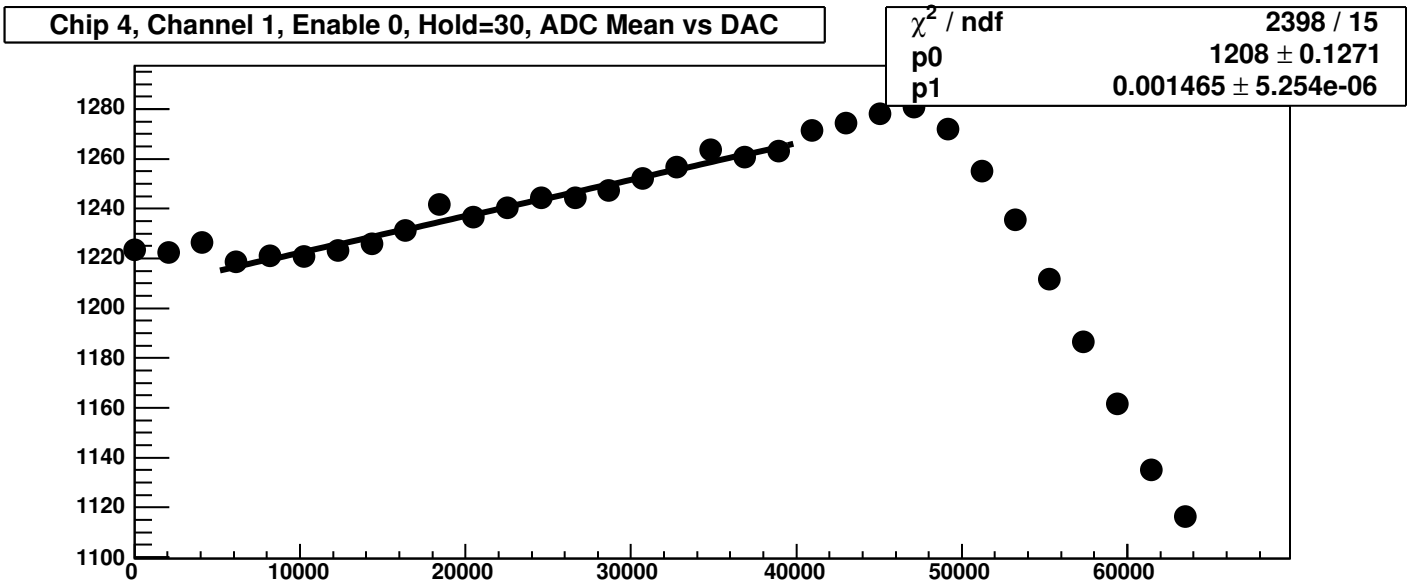


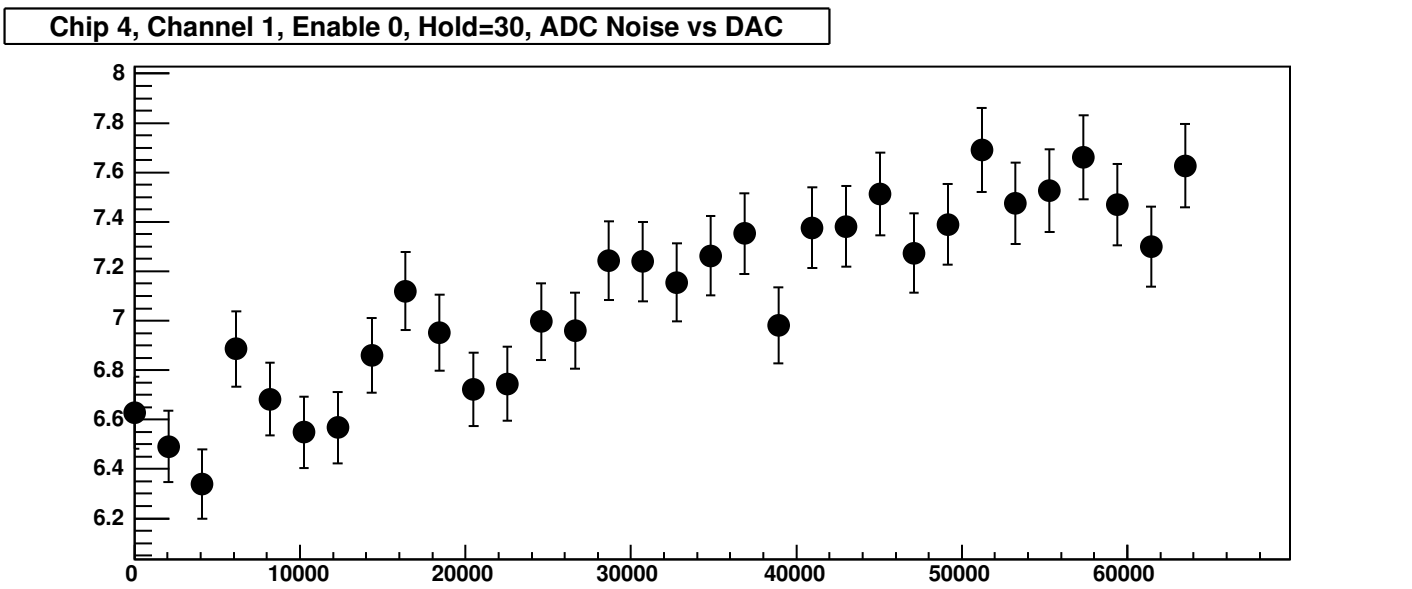
Chip 4, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC

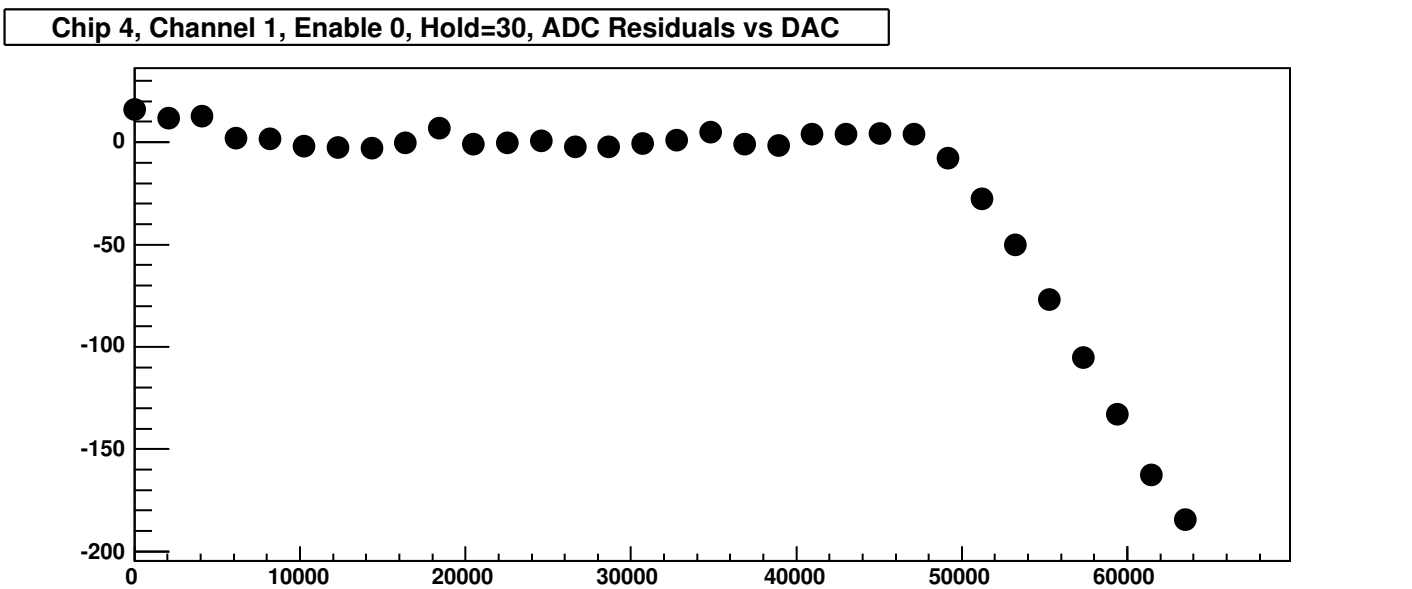


Chip 4, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

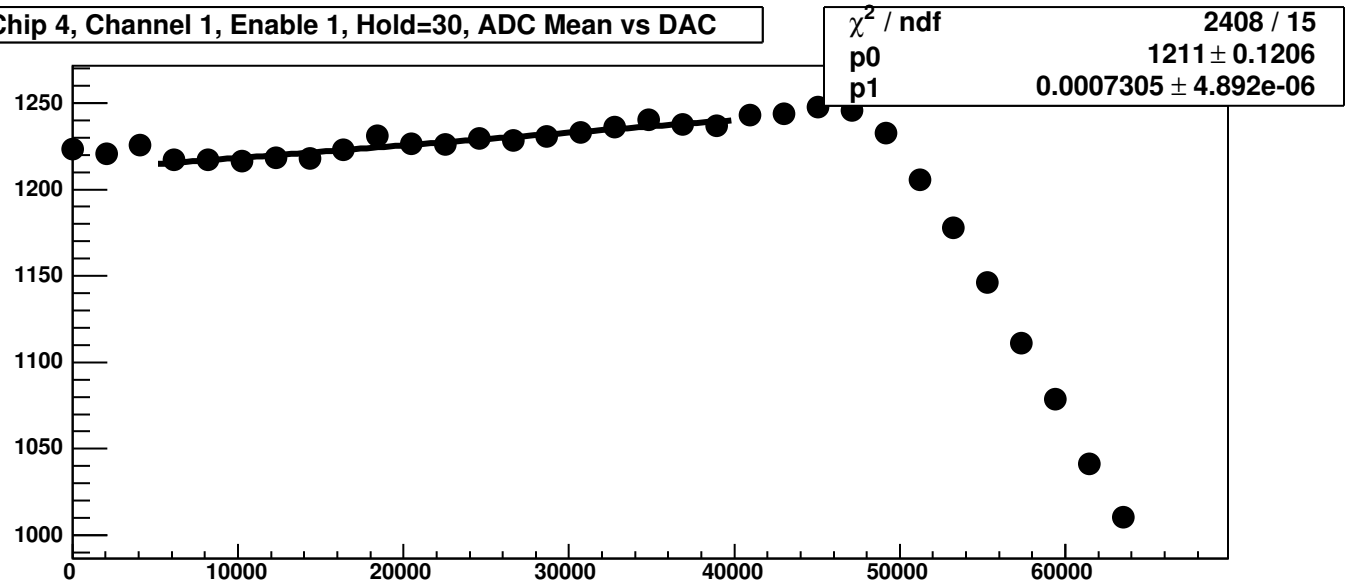



Chip 4, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2398 / 15
p0 1208 ± 0.1271
p1 0.001465 ± 5.254e-06
1280
1260
1240
1220
1200
1180
1160
1140
1120
1100
0
10000
20000
30000
40000
50000
60000

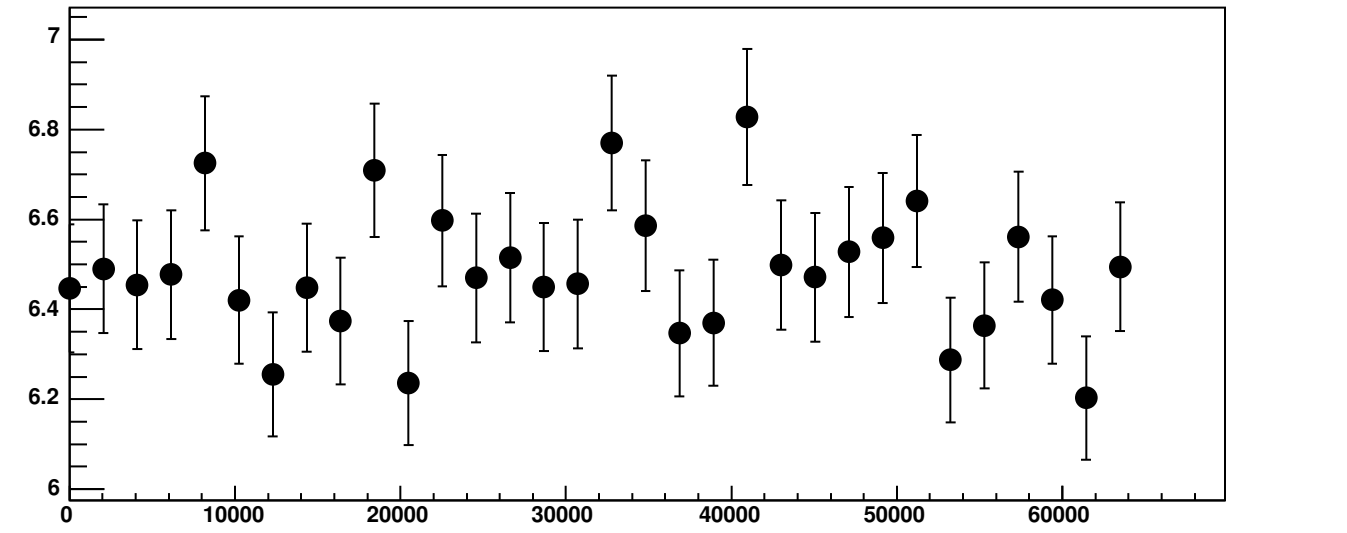

Chip 4, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
8
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

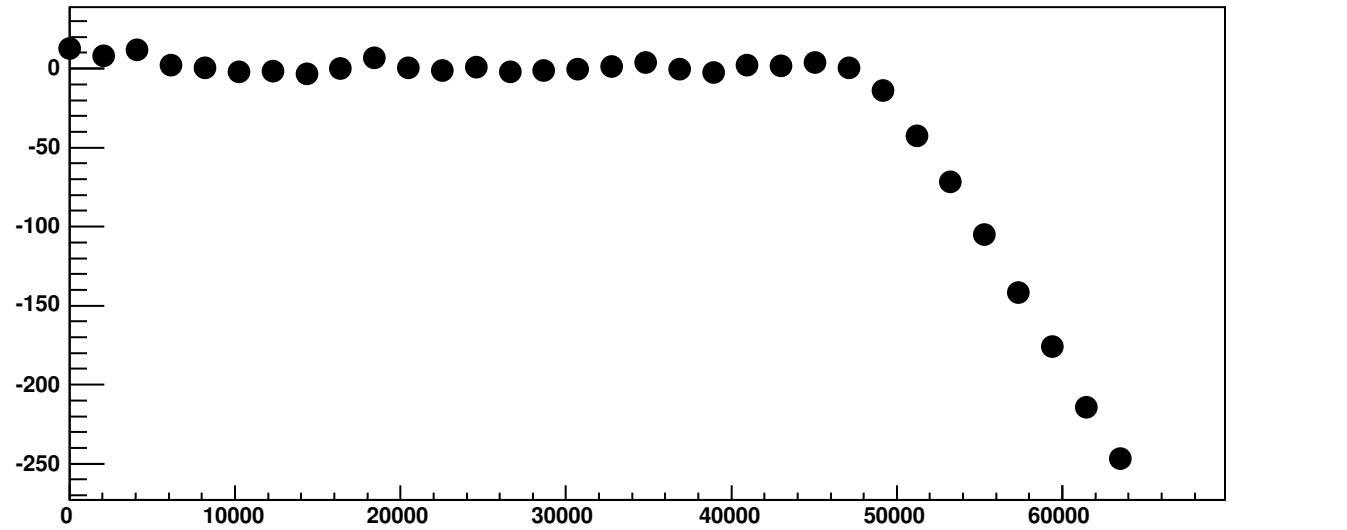
Chip 4, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

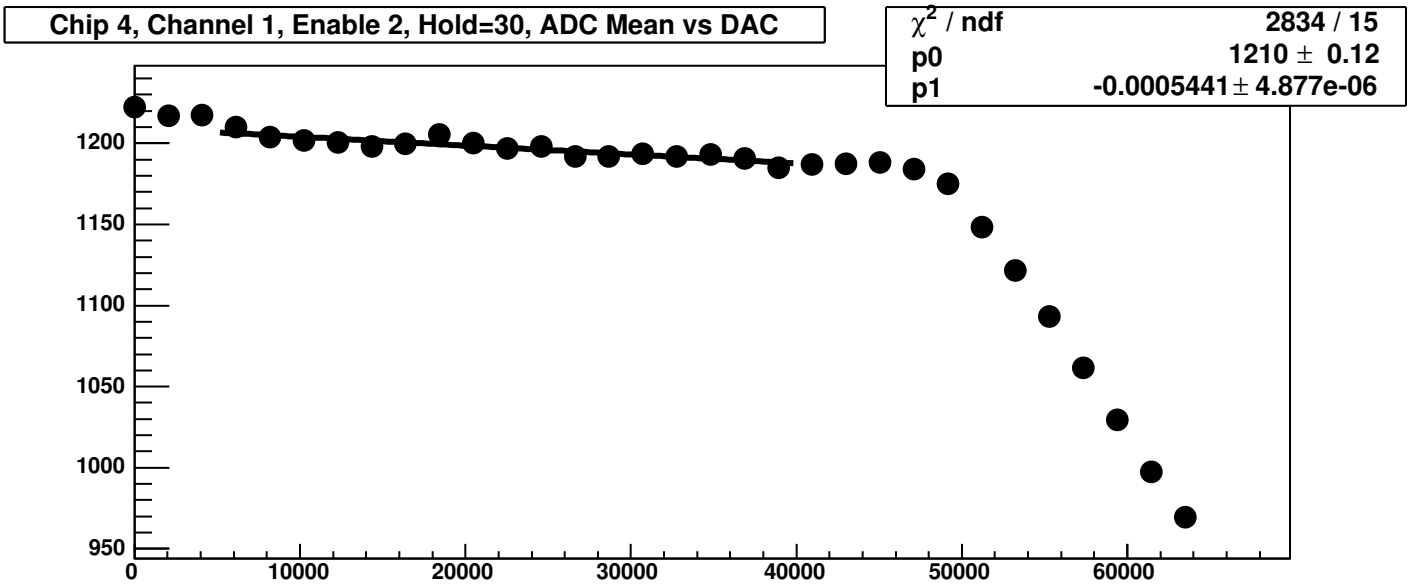


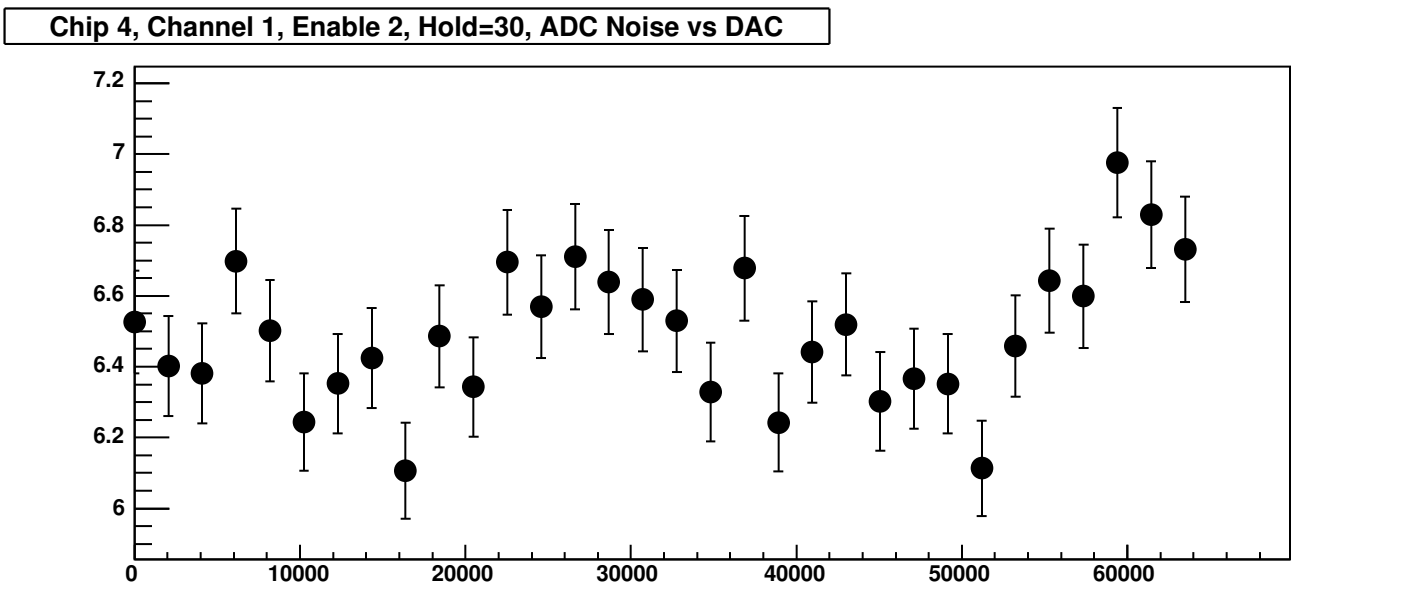
Chip 4, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC

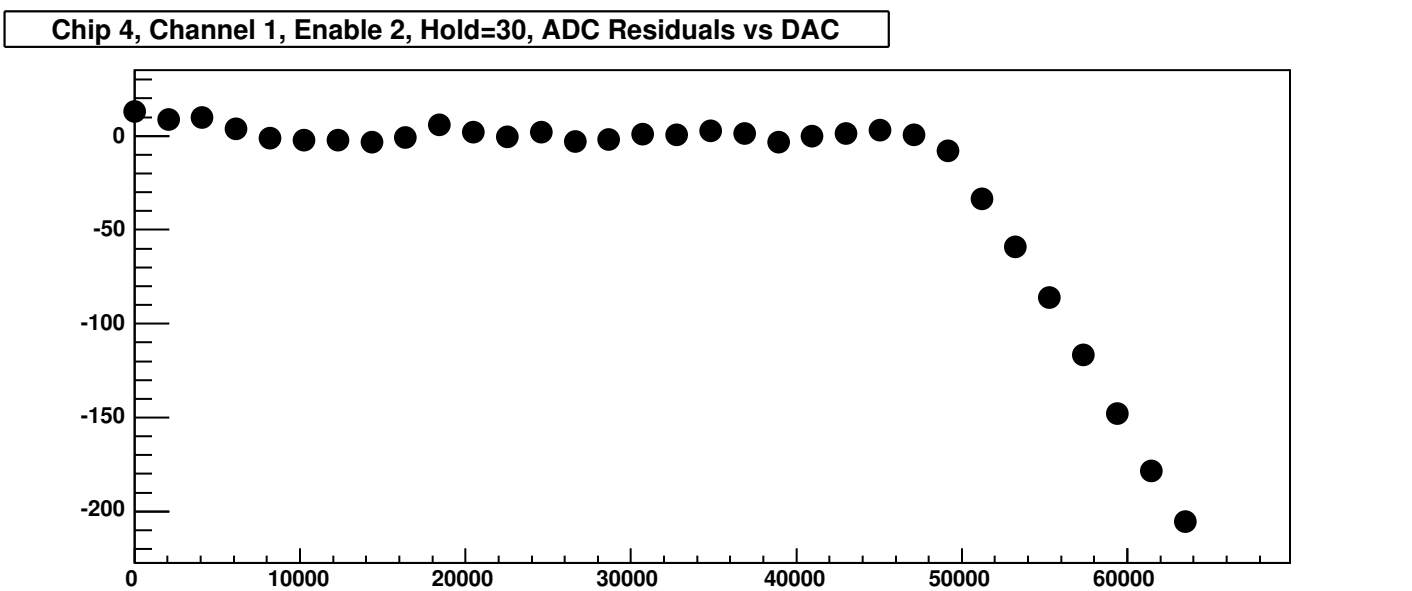


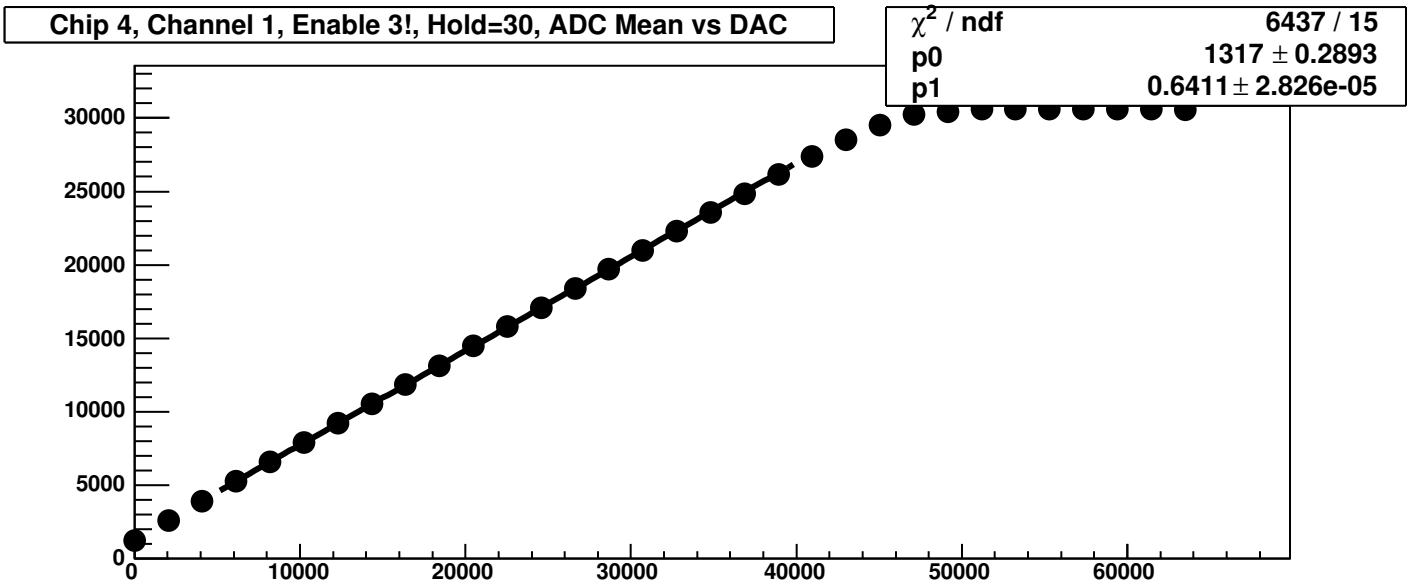
Chip 4, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

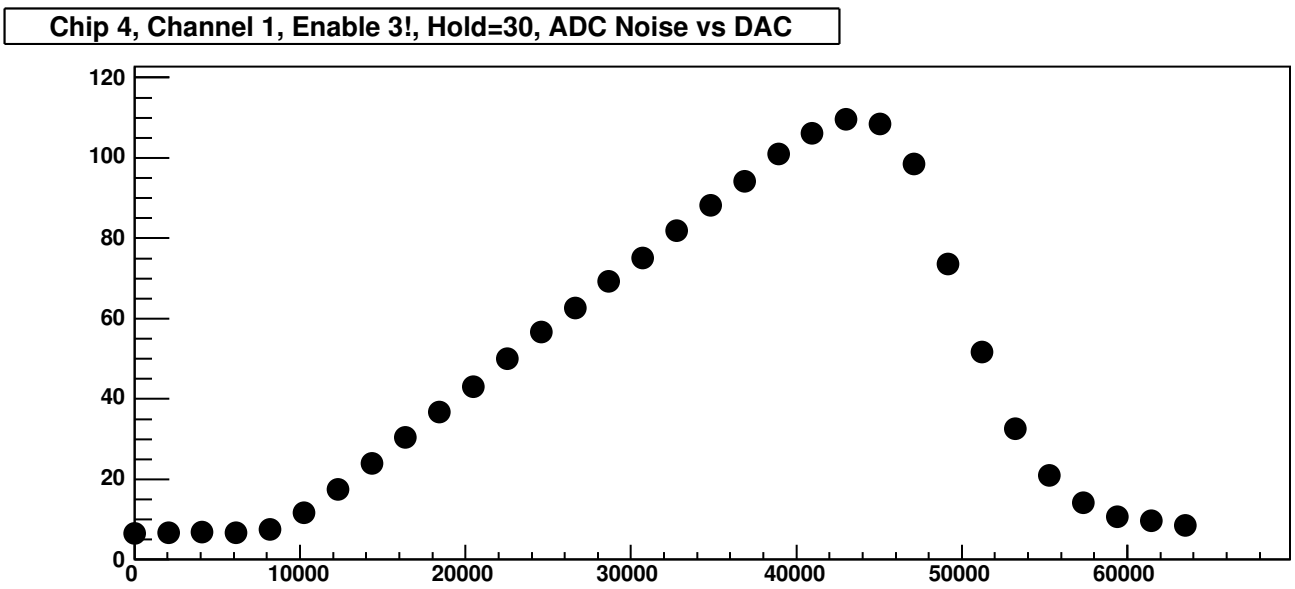


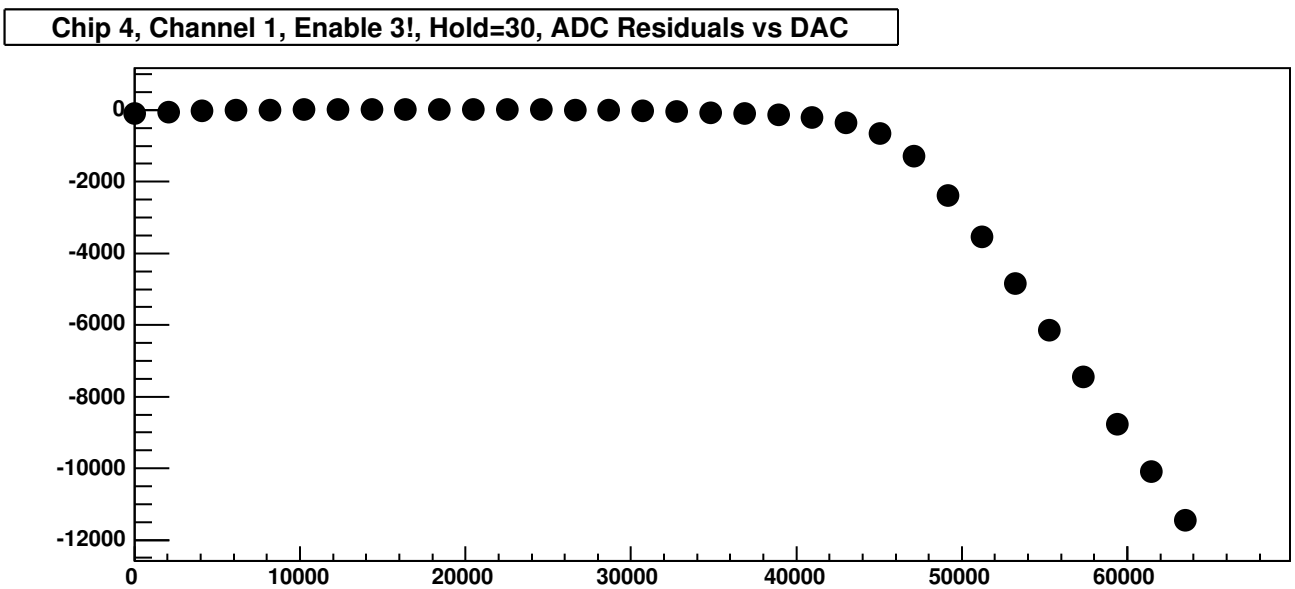

Chip 4, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2834 / 15
p0 1210 ± 0.12
p1 -0.0005441 ± 4.877e-06
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

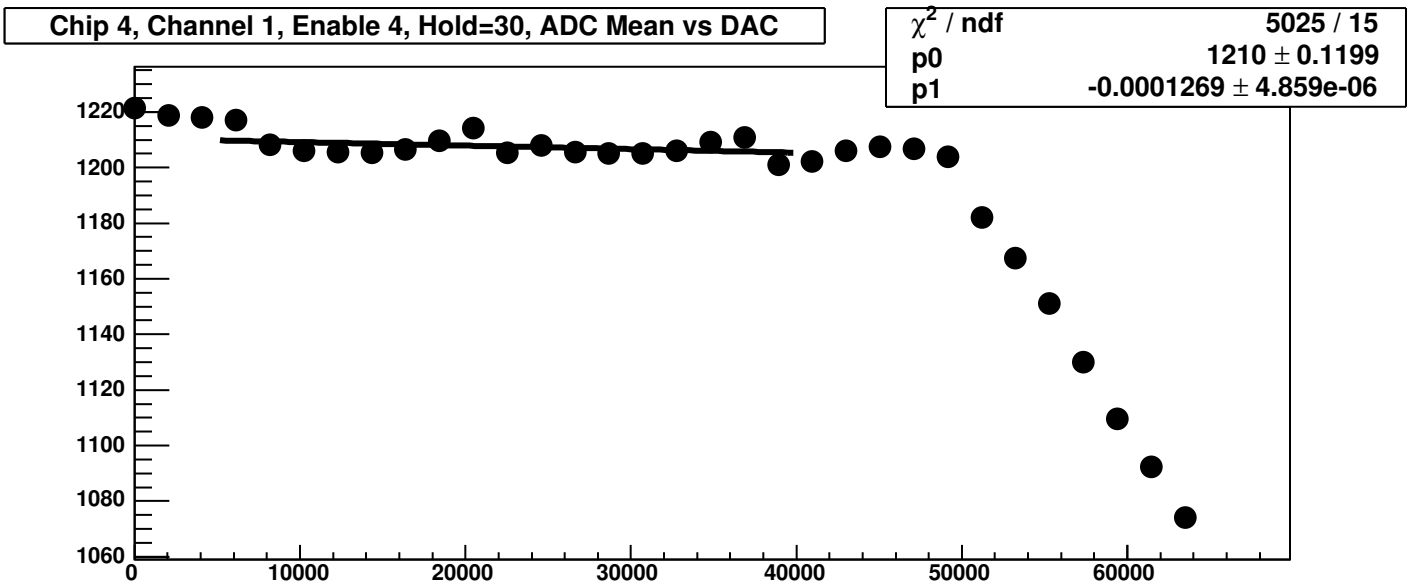

Chip 4, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

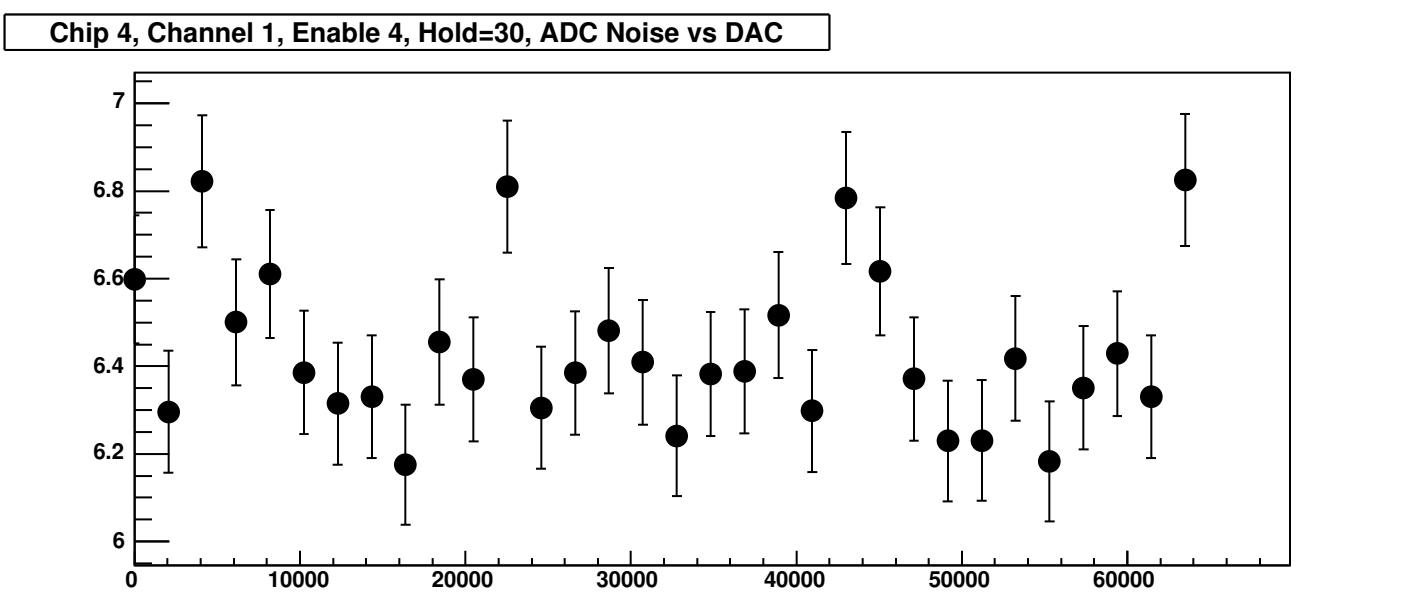

Chip 4, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

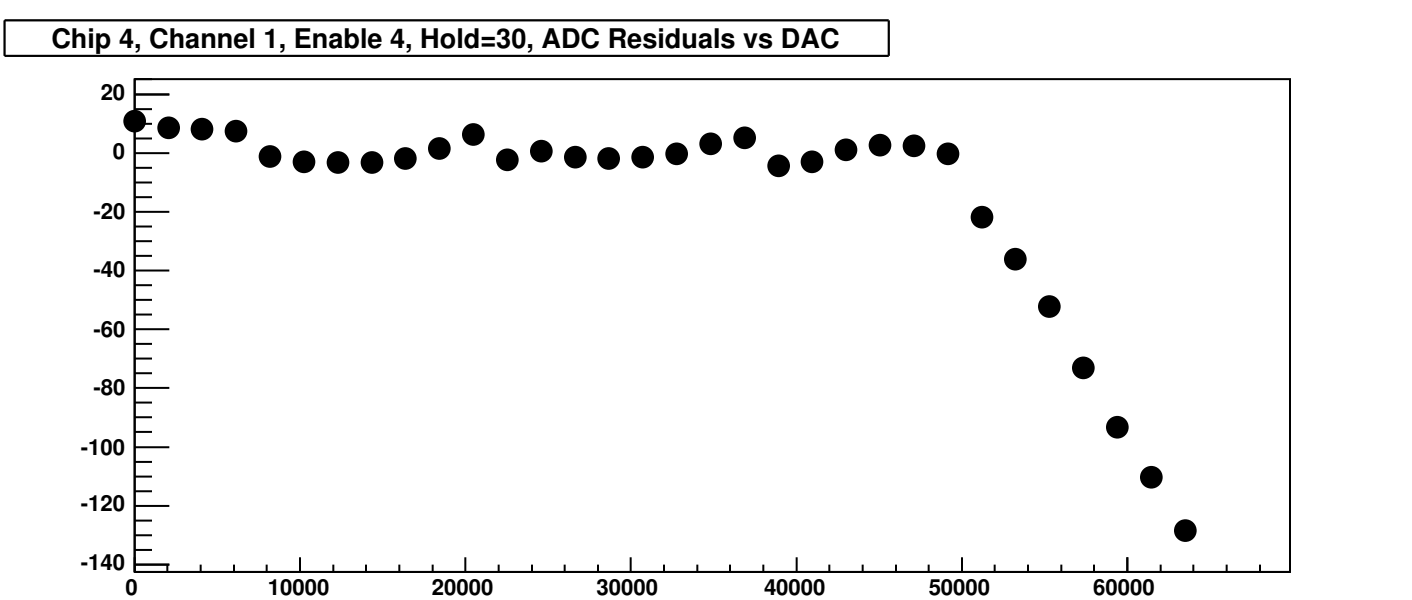

Chip 4, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 6437 / 15
p0 1317 ± 0.2893
p1 0.6411 ± 2.826e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

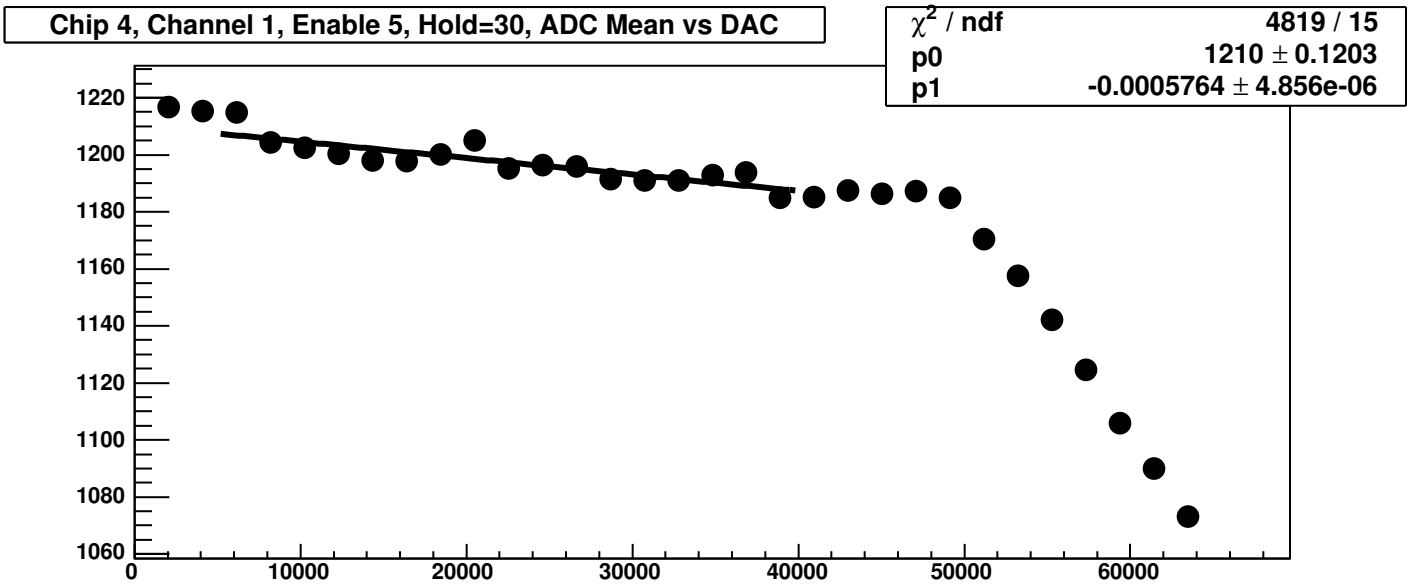

Chip 4, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

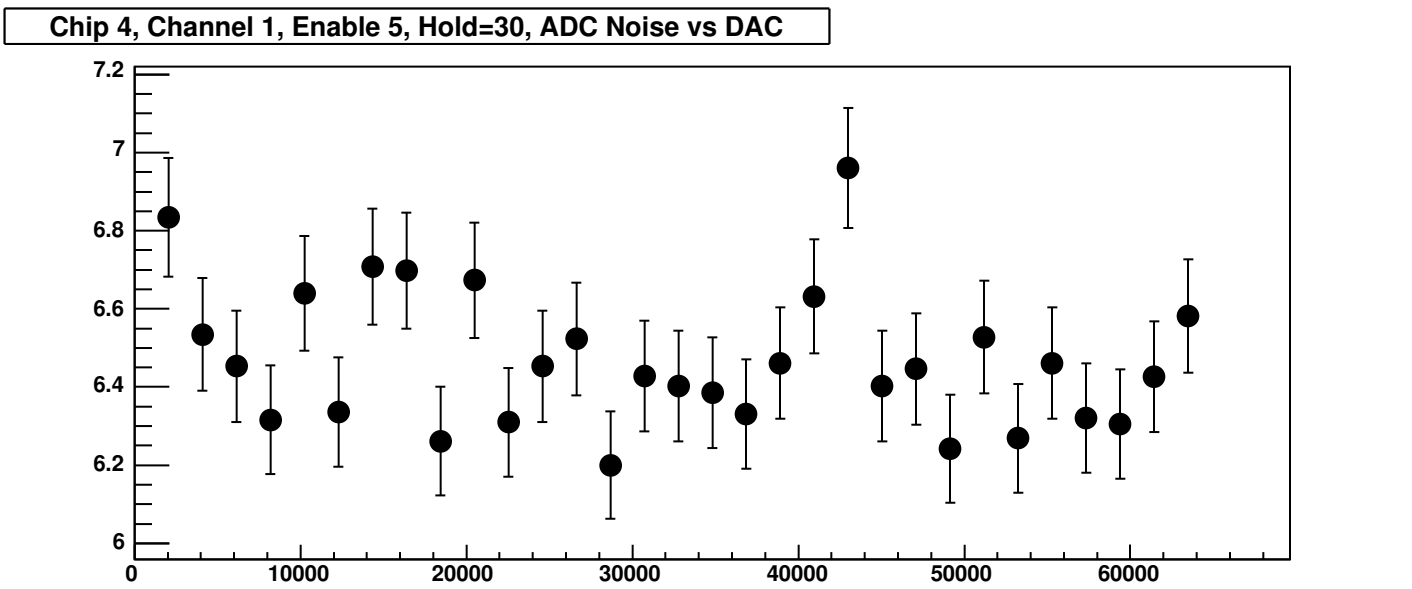

Chip 4, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

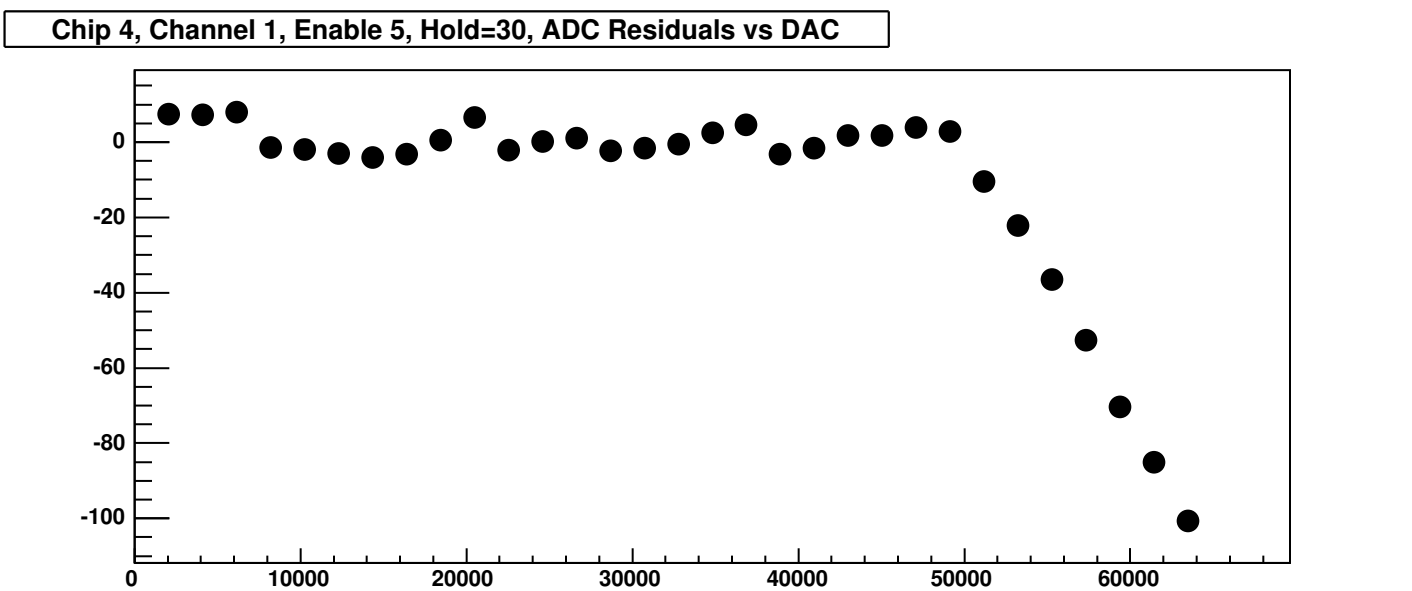

Chip 4, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5025 / 15
p0 1210 ± 0.1199
p1 -0.0001269 ± 4.859e-06
1220
1200
1180
1160
1140
1120
1100
1080
1060
0
10000
20000
30000
40000
50000
60000

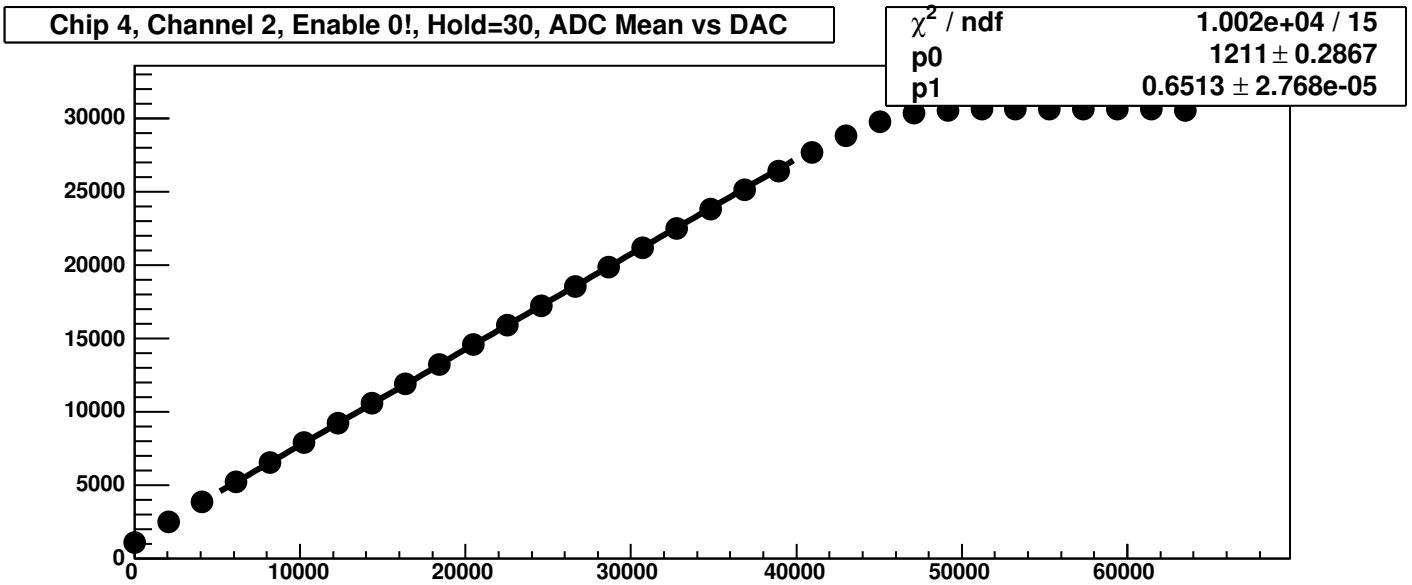

Chip 4, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

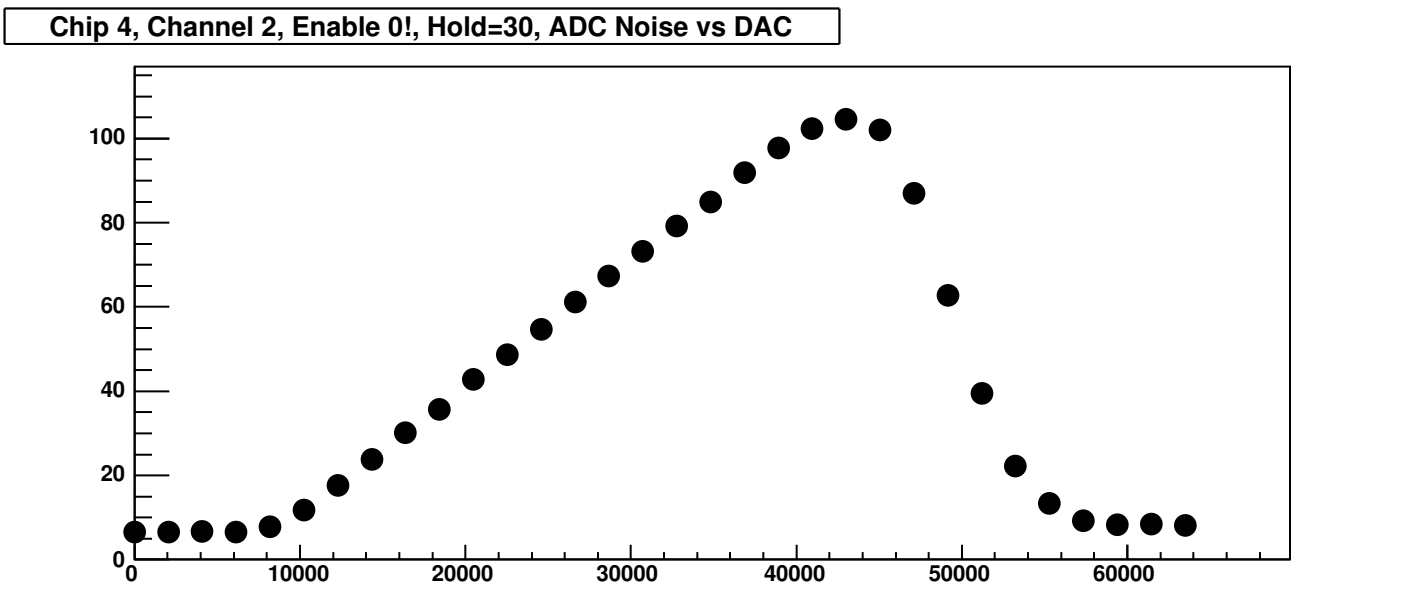

Chip 4, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

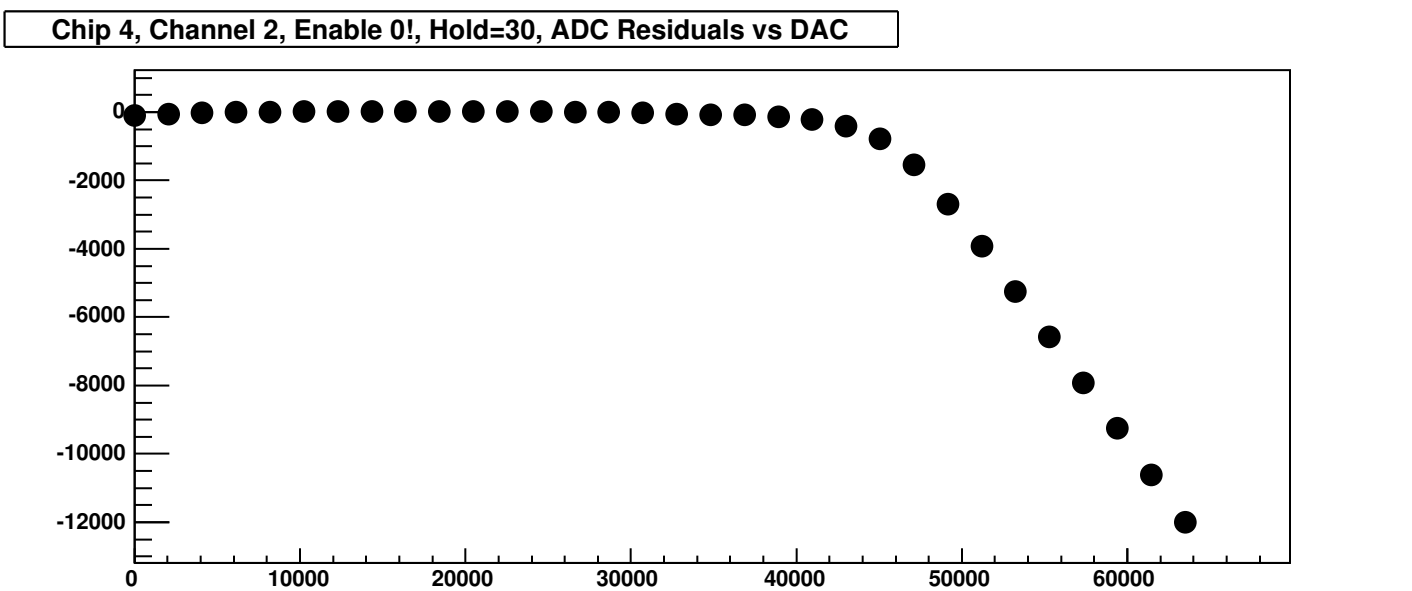

Chip 4, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4819 / 15
p0 1210 ± 0.1203
p1 -0.0005764 ± 4.856e-06
1220
1200
1180
1160
1140
1120
1100
1080
1060
0
10000
20000
30000
40000
50000
60000

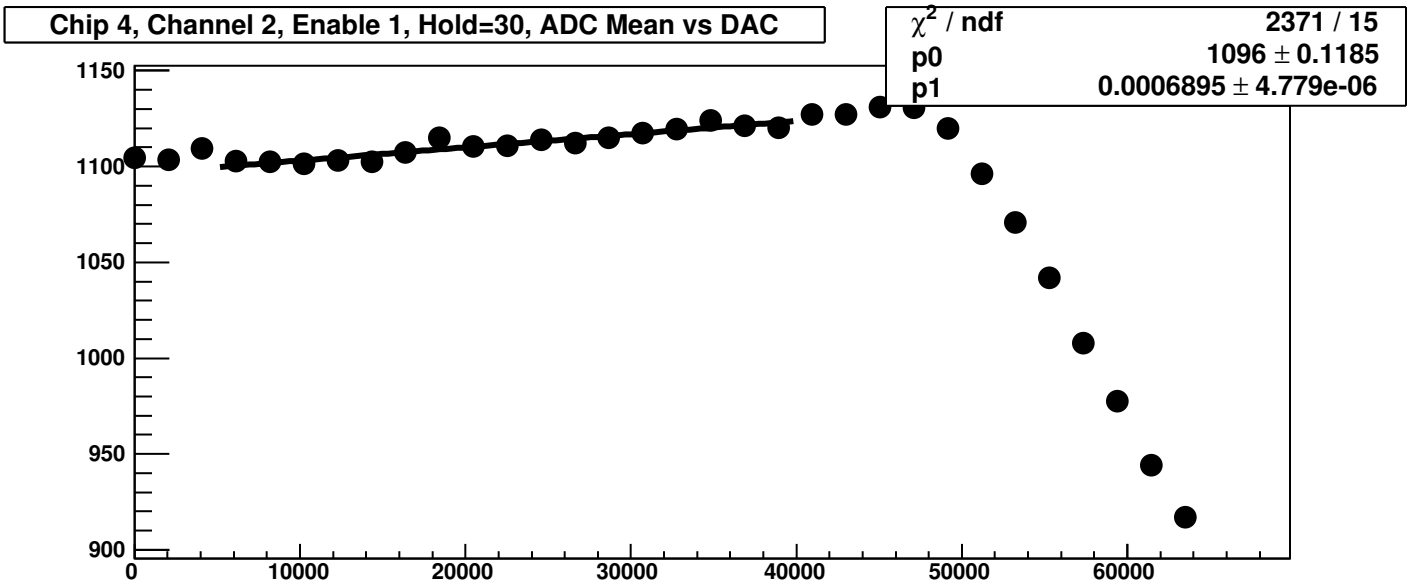

Chip 4, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

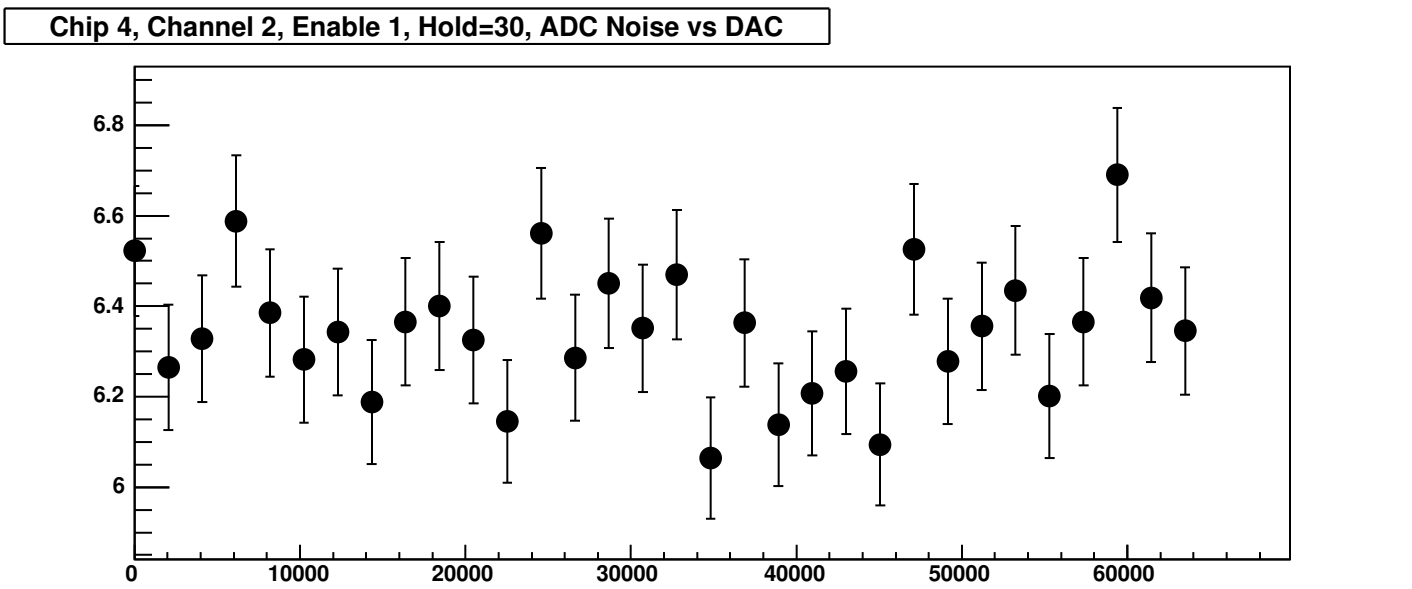

Chip 4, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

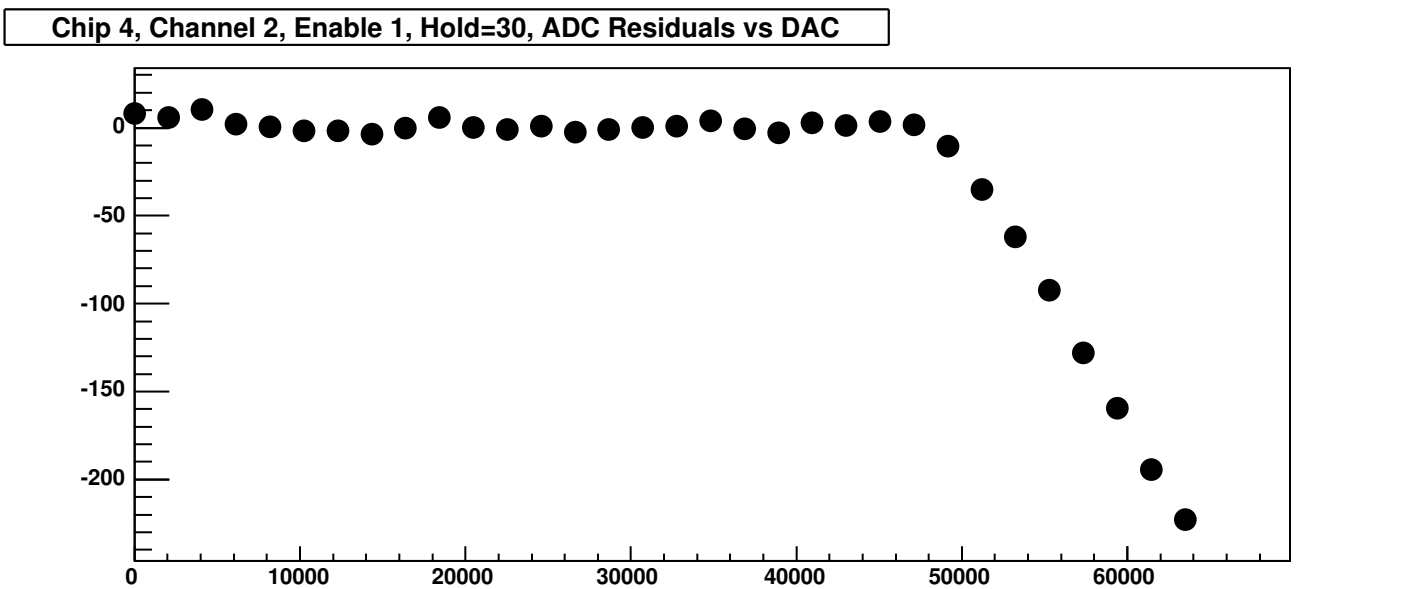

Chip 4, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.002e+04 / 15
p0 1211 ± 0.2867
p1 0.6513 ± 2.768e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

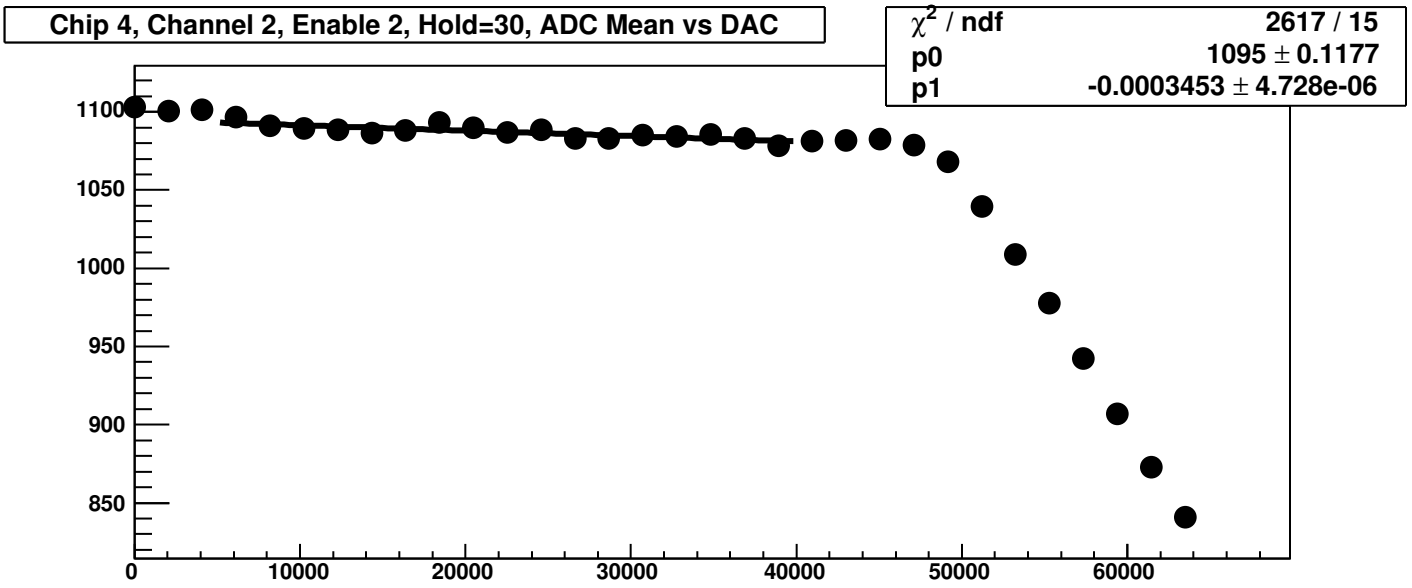

Chip 4, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

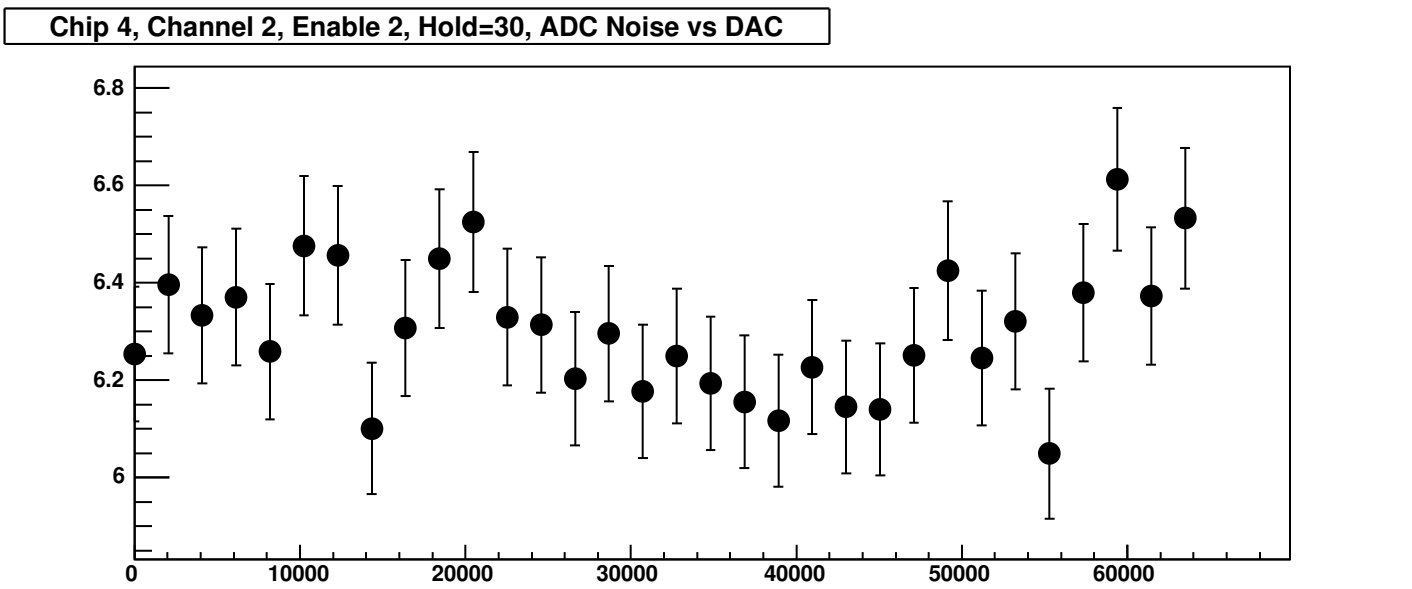

Chip 4, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

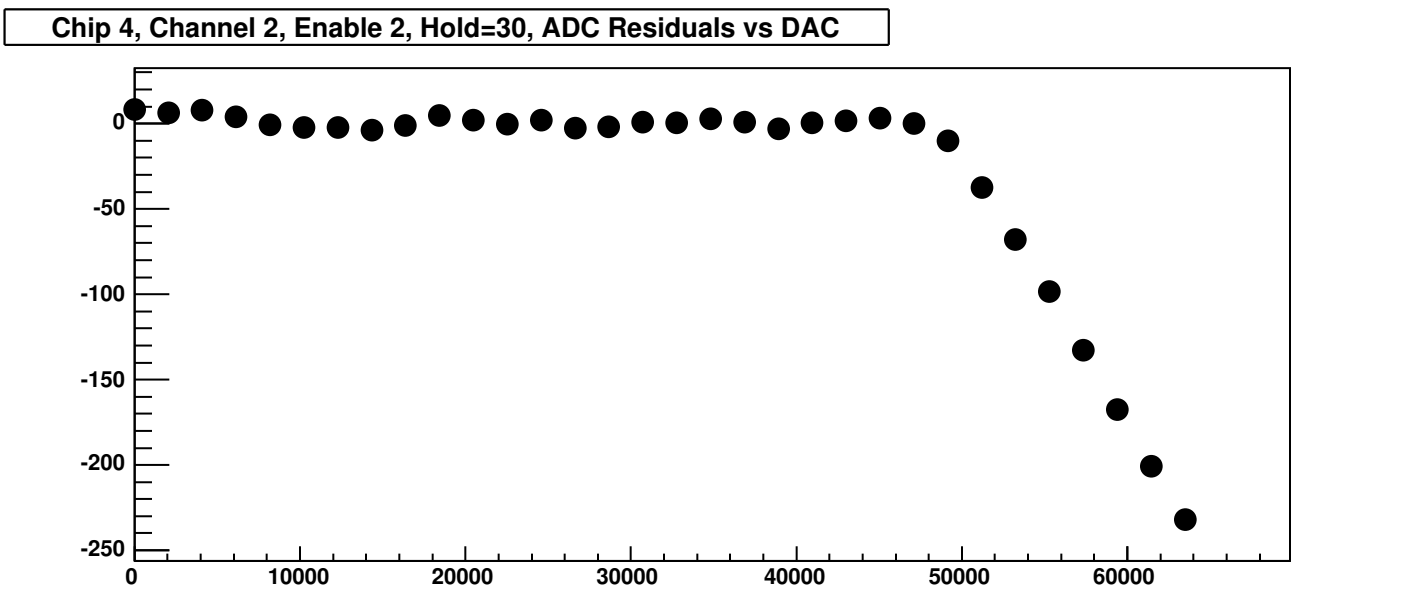

Chip 4, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2371 / 15
p0 1096 ± 0.1185
p1 0.0006895 ± 4.779e-06
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

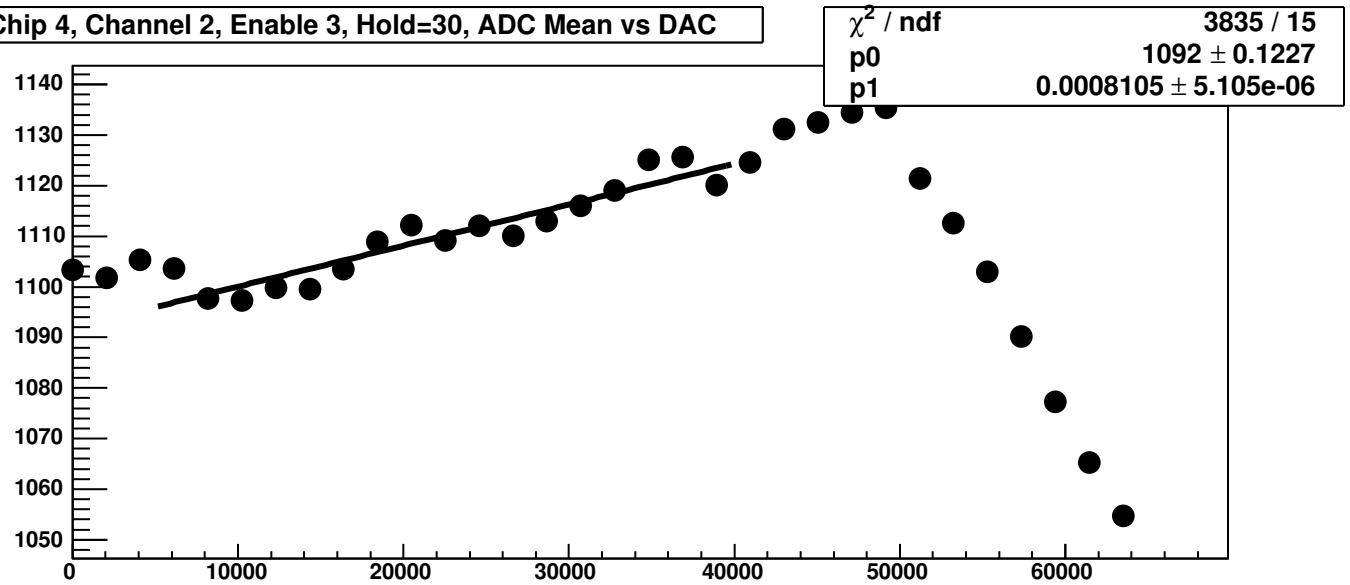

Chip 4, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2617 / 15
p0 1095 ± 0.1177
p1 -0.0003453 ± 4.728e-06
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

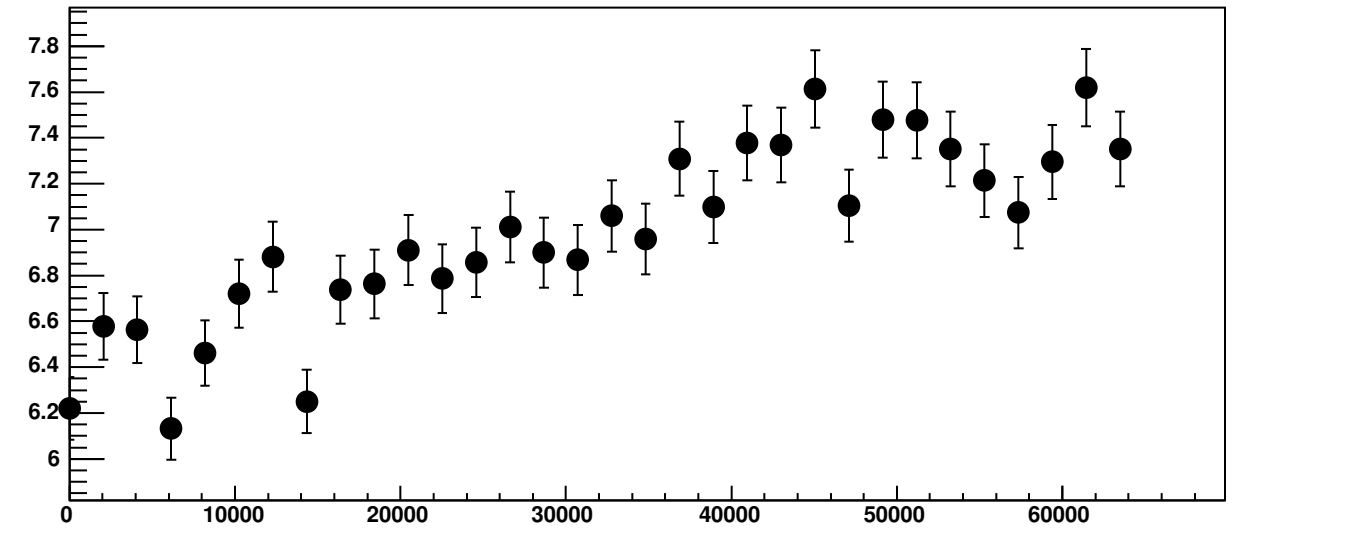

Chip 4, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

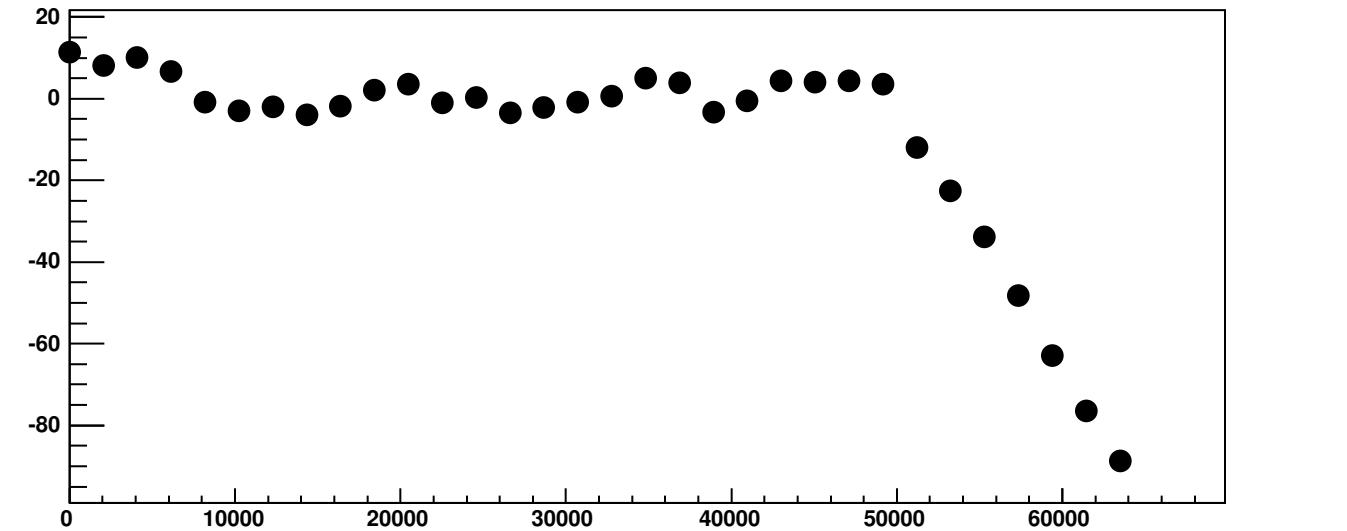
Chip 4, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC

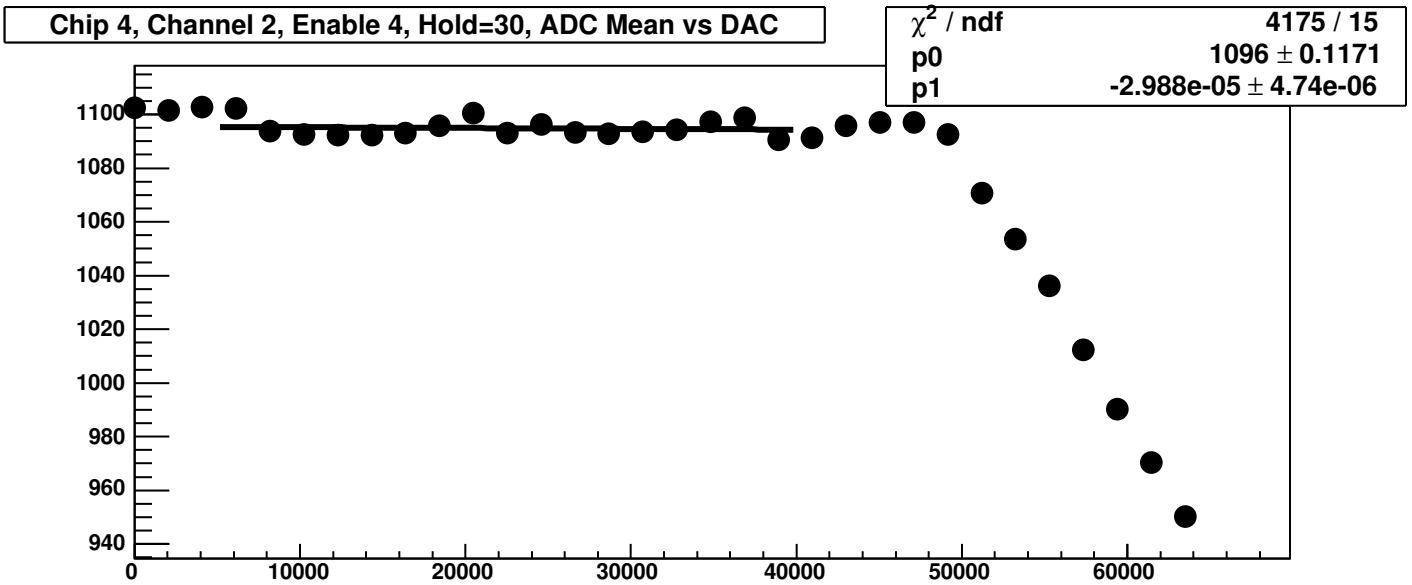


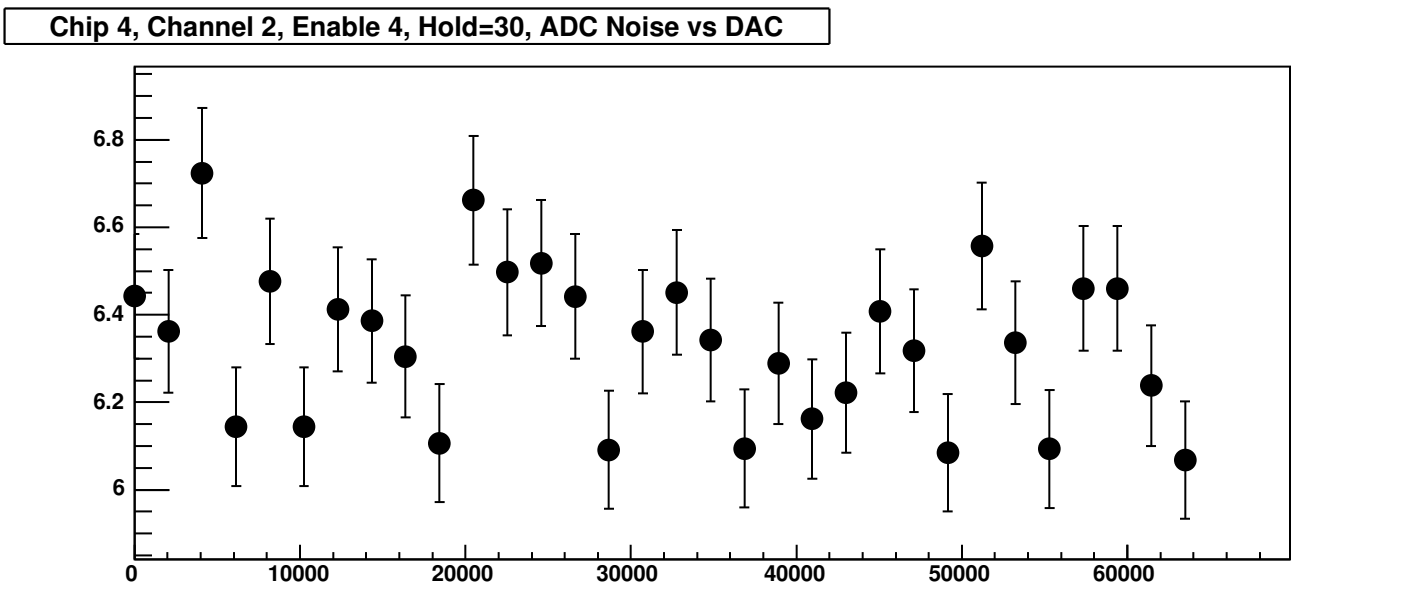
Chip 4, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC

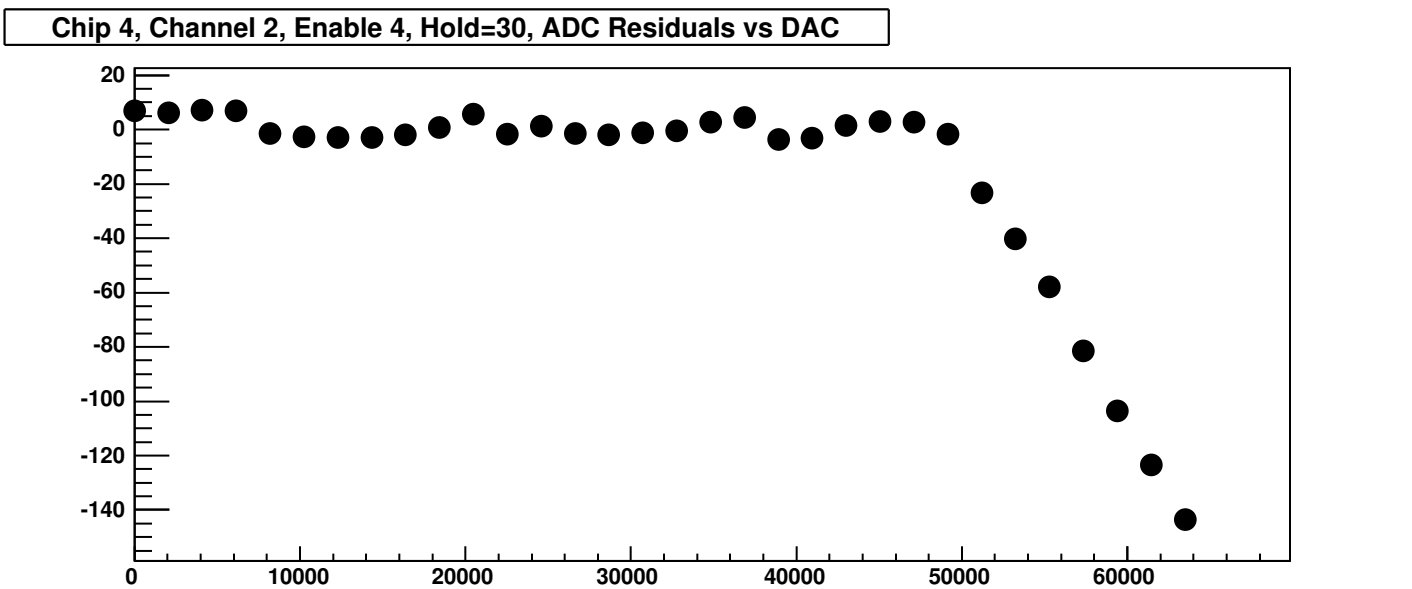


Chip 4, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC

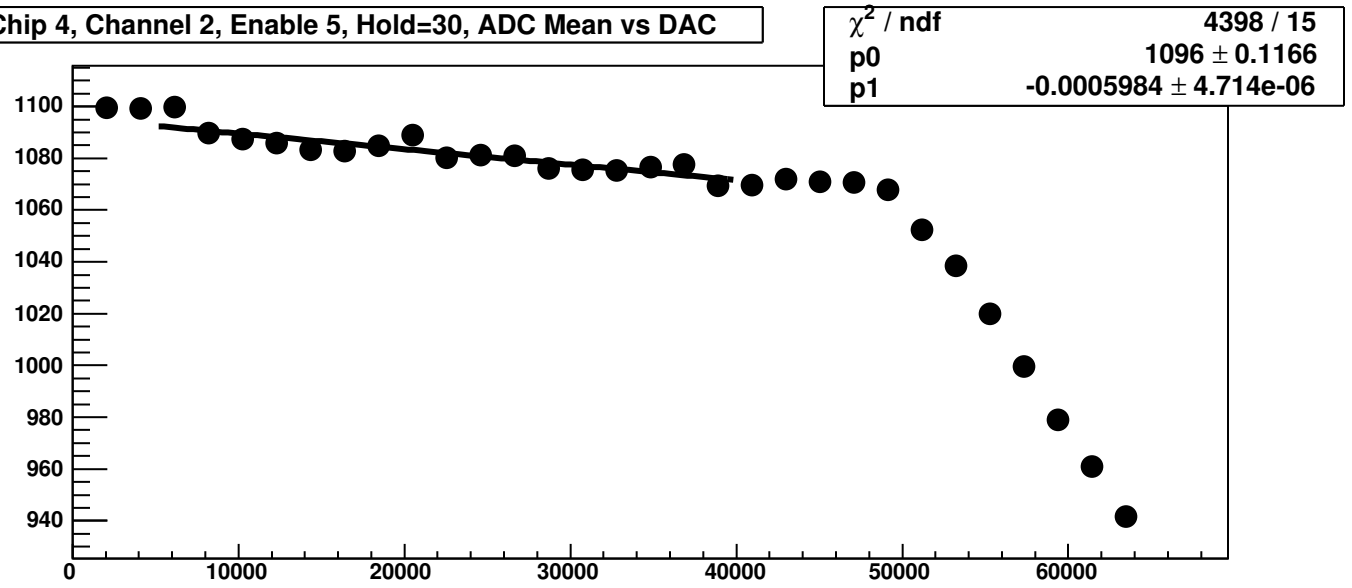



Chip 4, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4175 / 15
p0 1096 ± 0.1171
p1 -2.988e-05 ± 4.74e-06
1100
1080
1060
1040
1020
1000
980
960
940
0
10000
20000
30000
40000
50000
60000

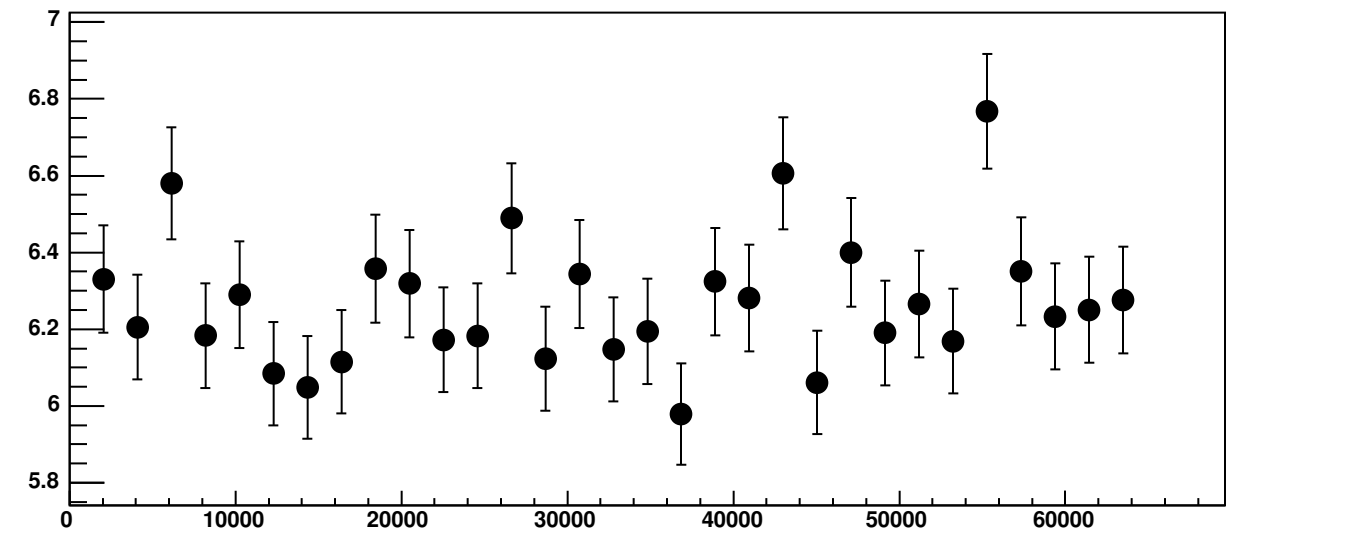

Chip 4, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

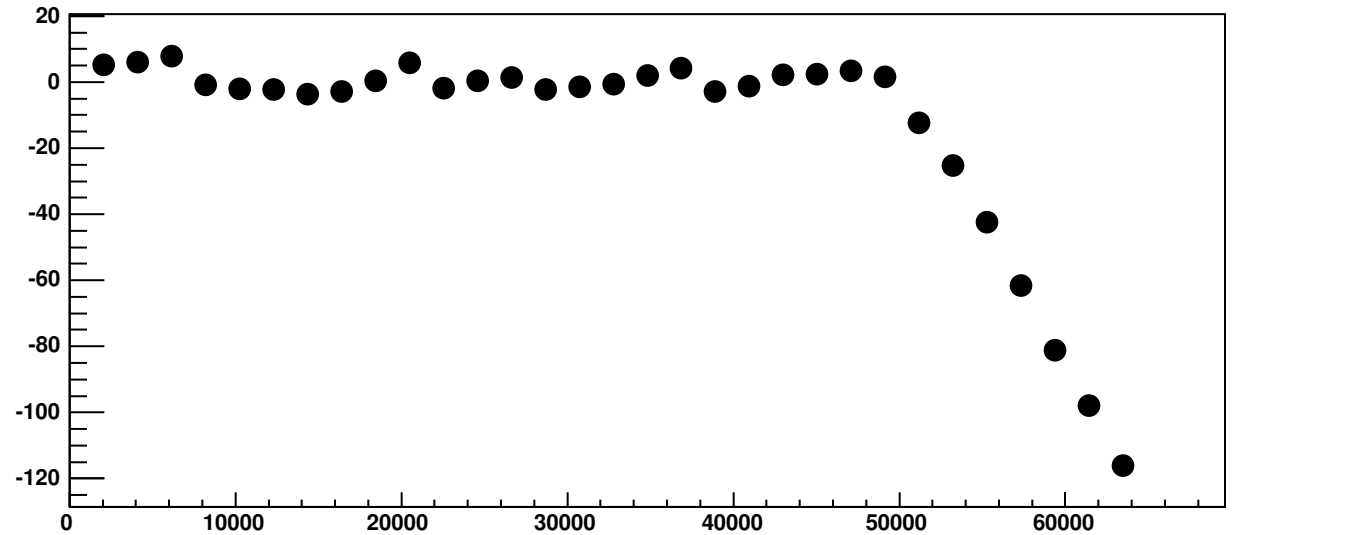
Chip 4, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC



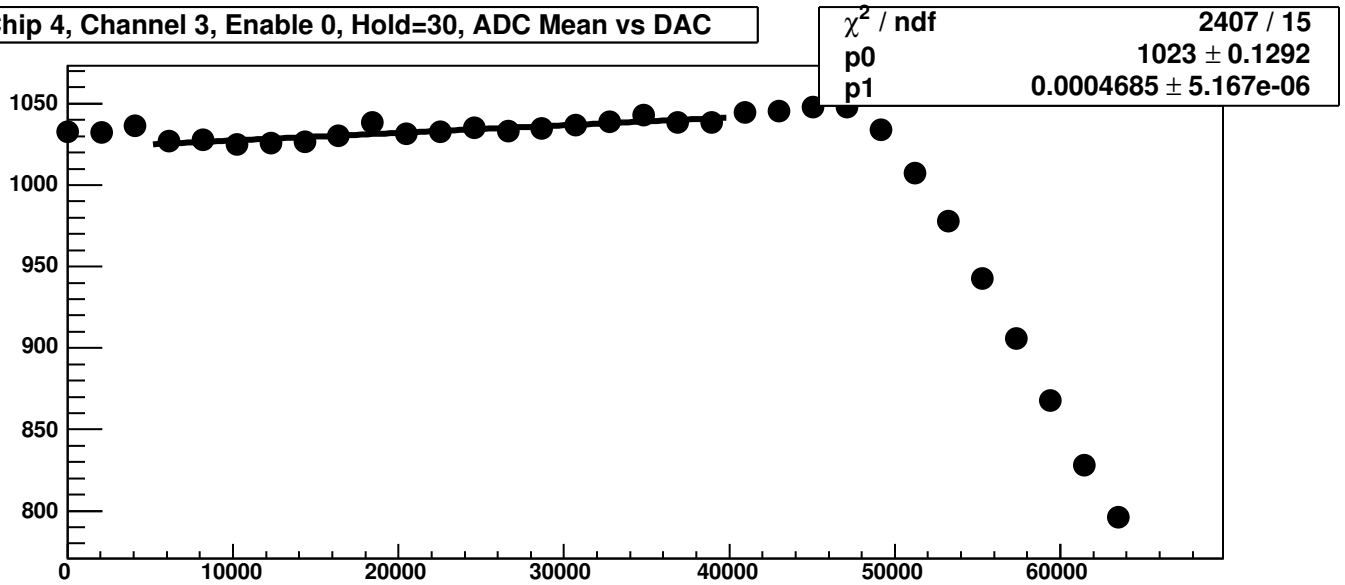
Chip 4, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC



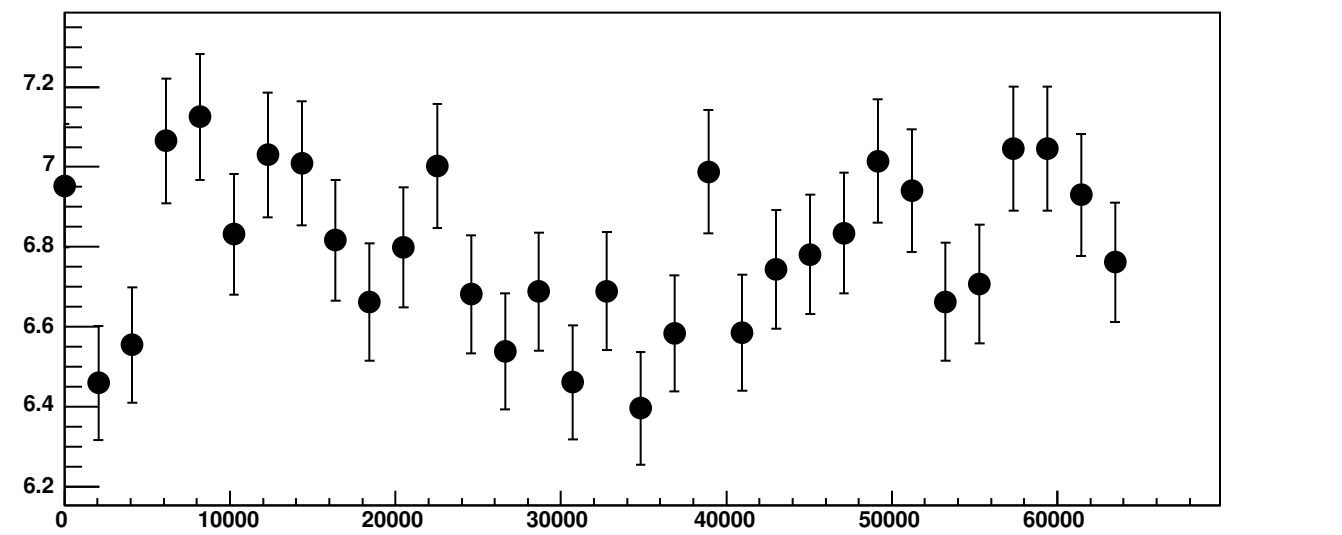
Chip 4, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC



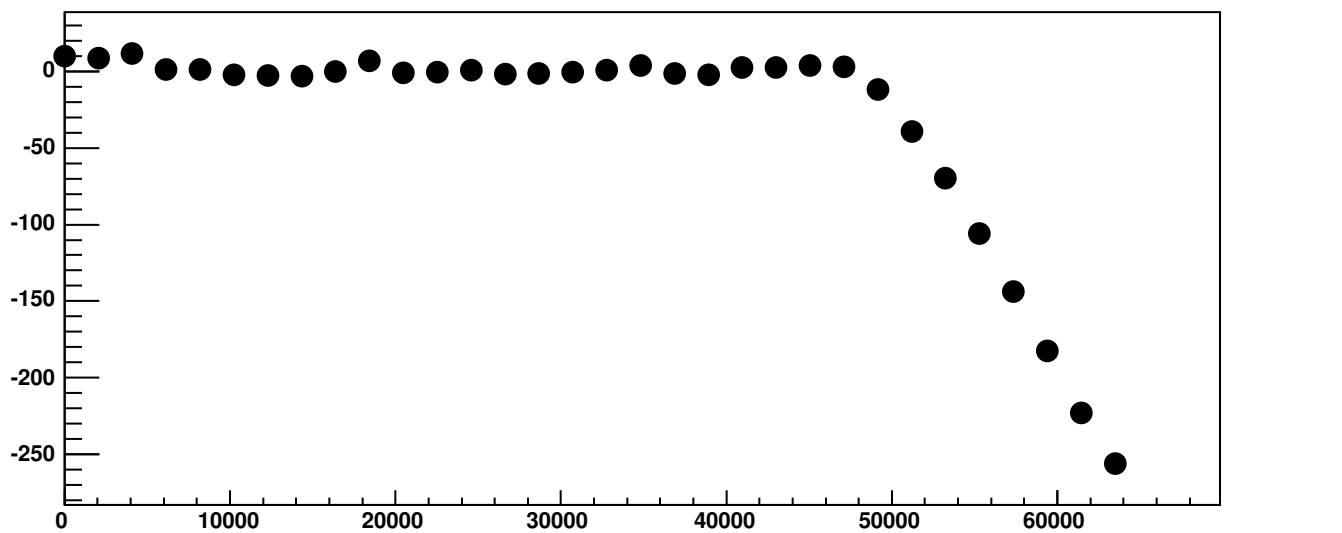
Chip 4, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC

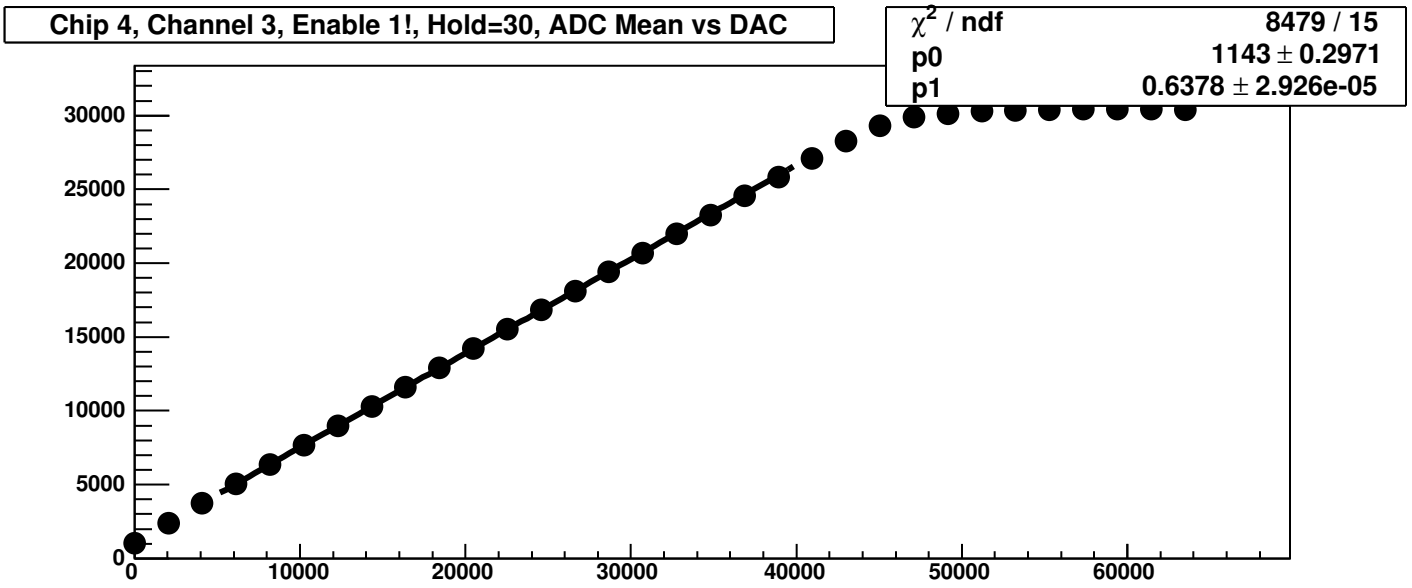


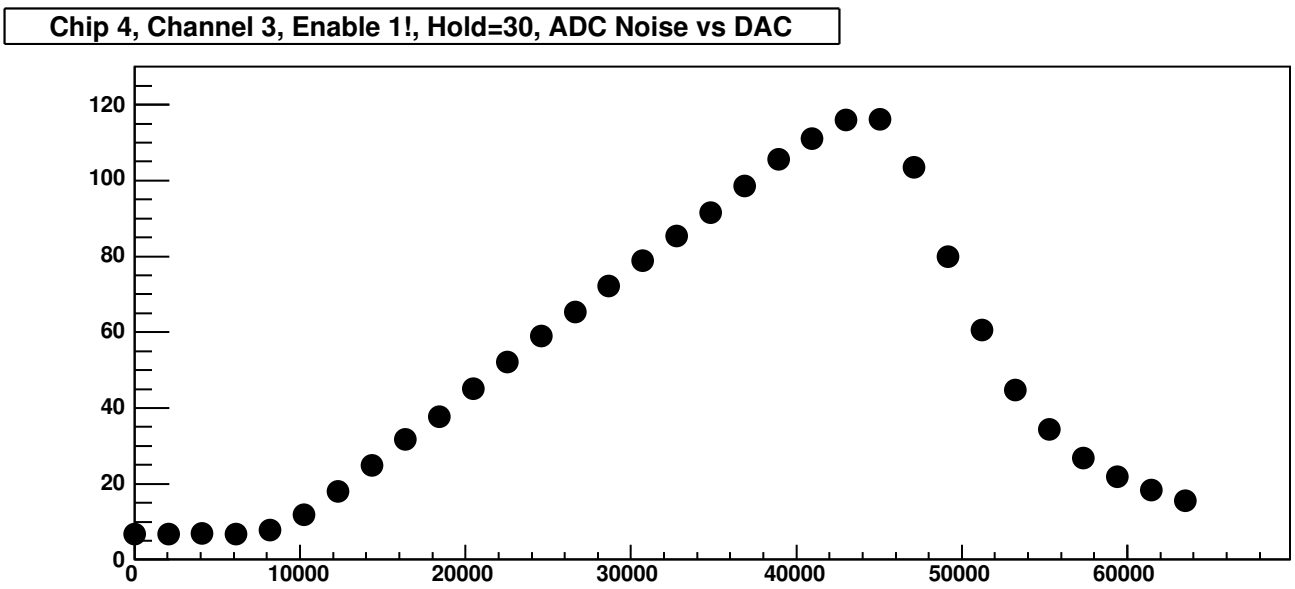
Chip 4, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC

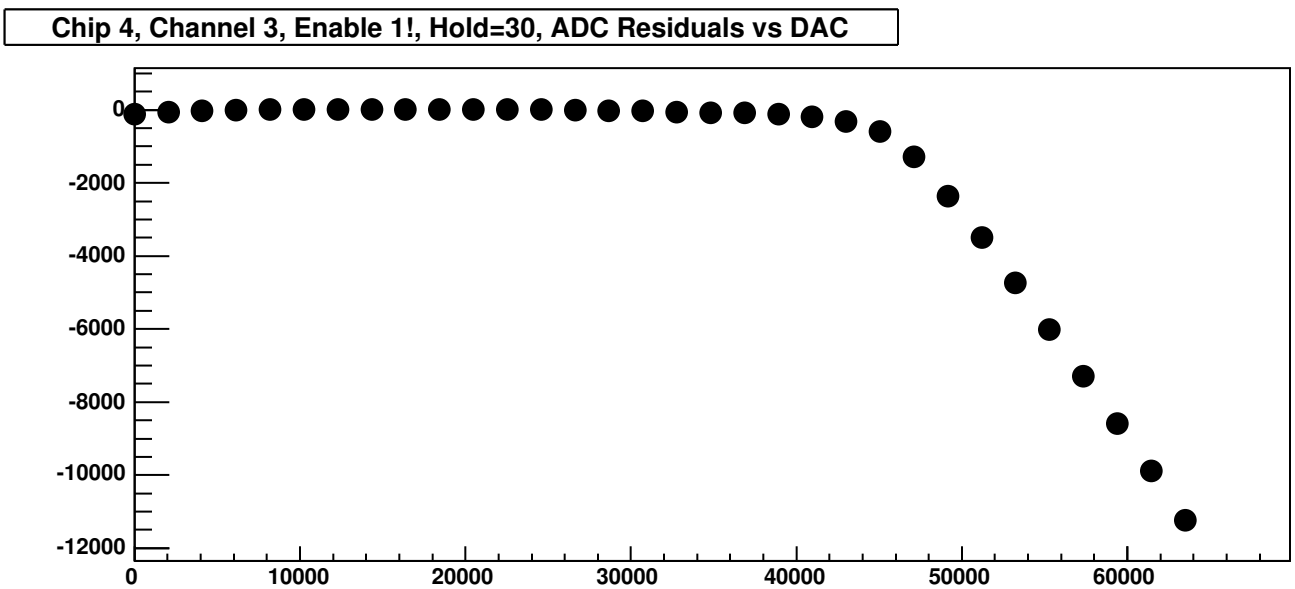


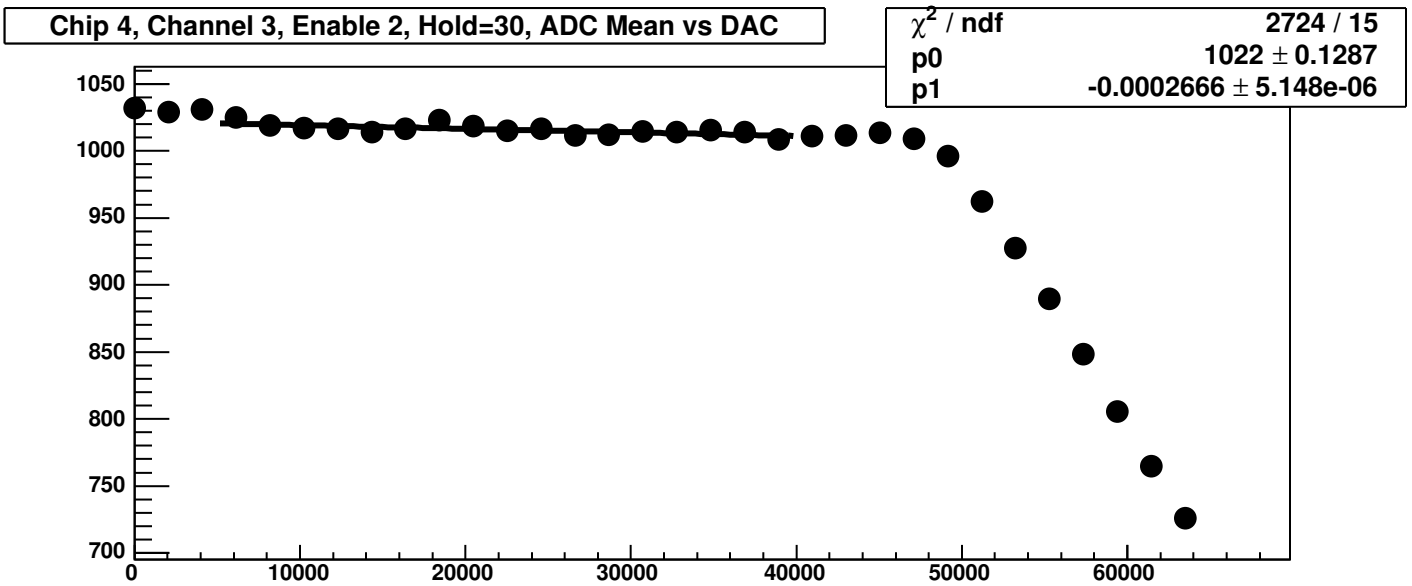
Chip 4, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

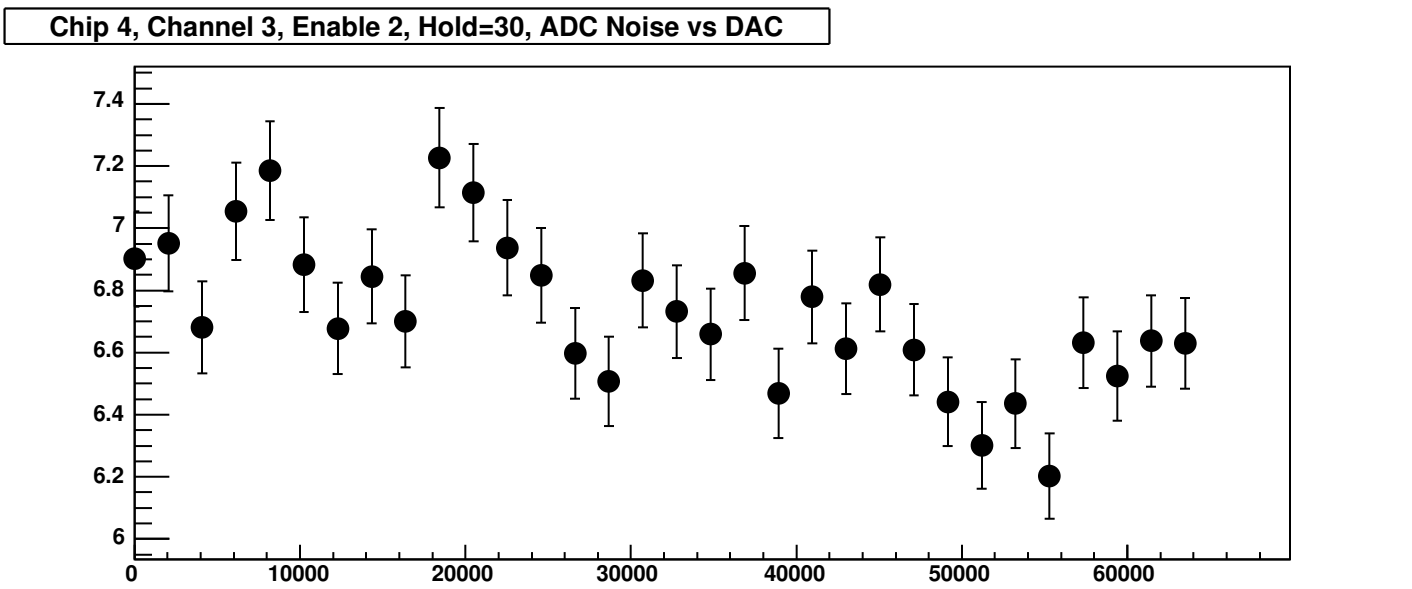


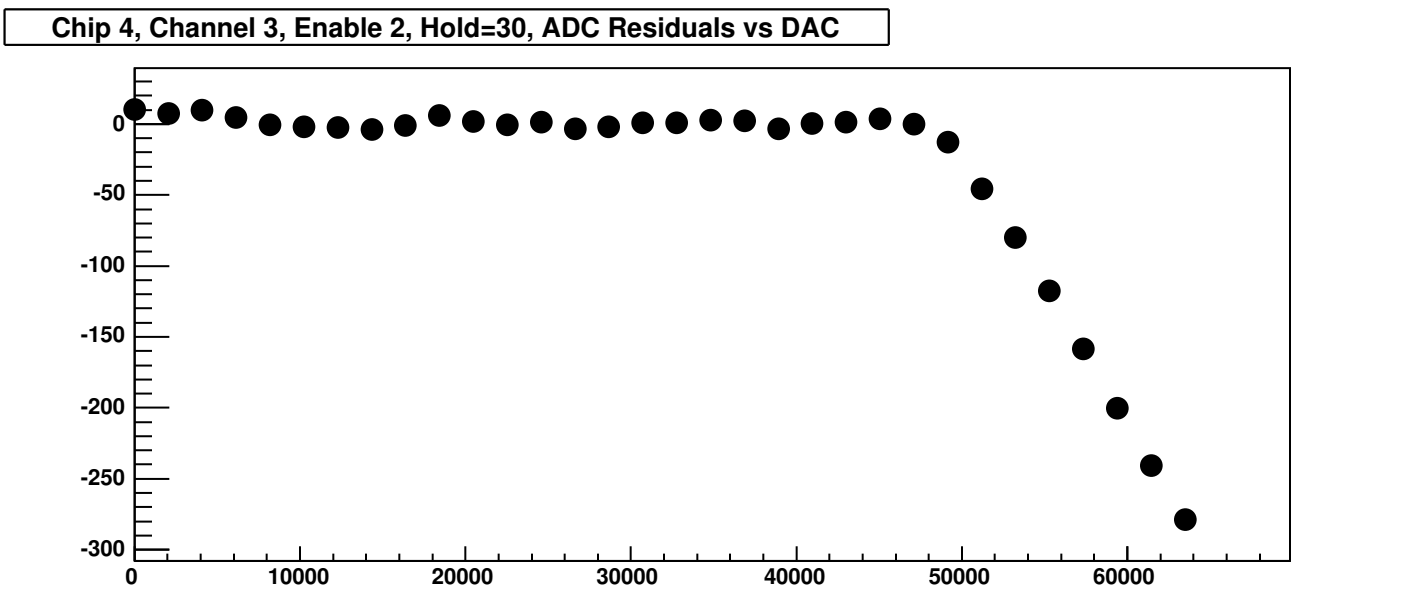

Chip 4, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 8479 / 15
p0 1143 ± 0.2971
p1 0.6378 ± 2.926e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

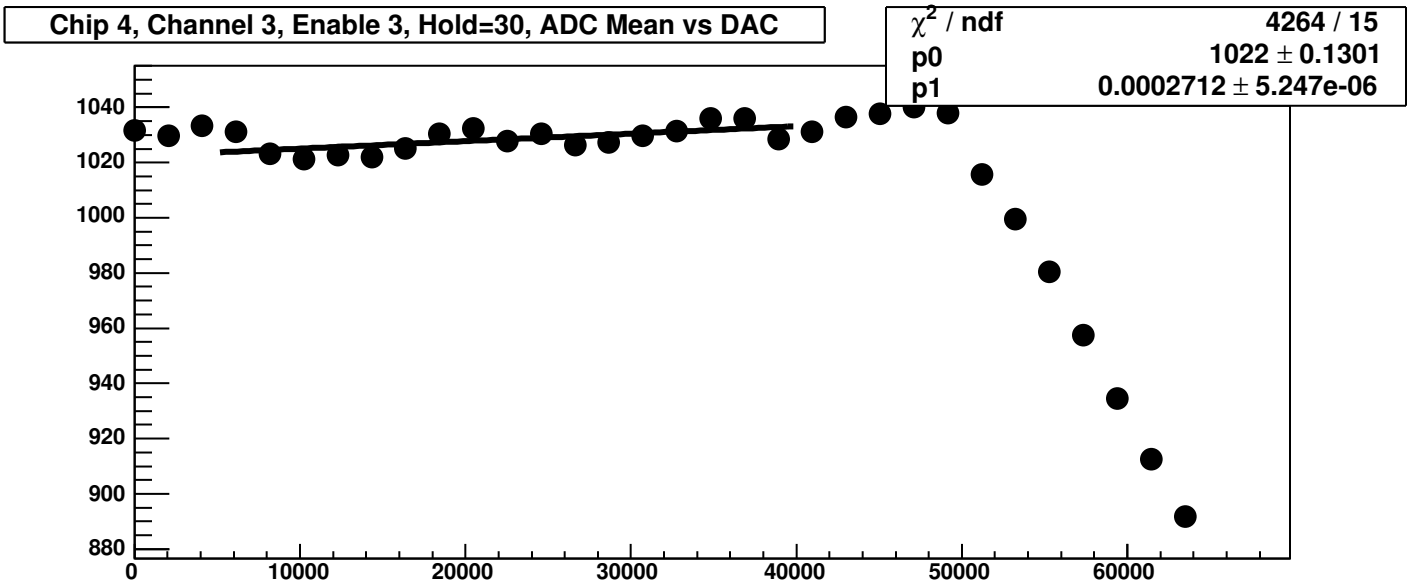

Chip 4, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

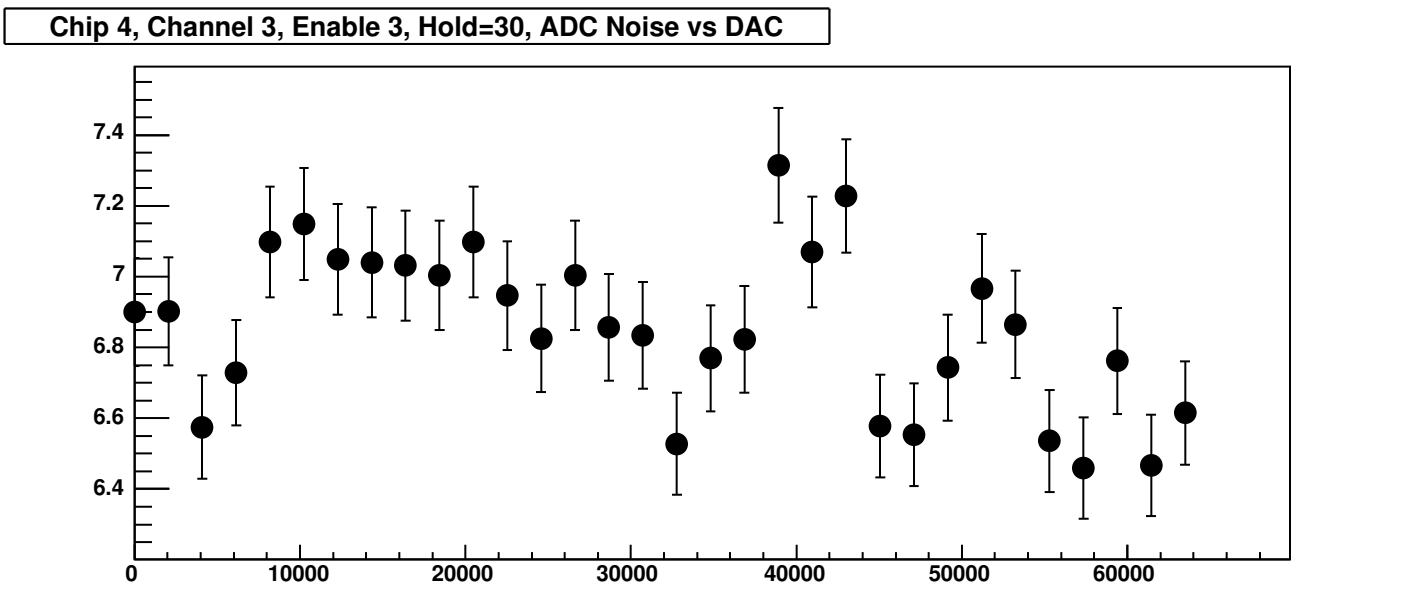

Chip 4, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

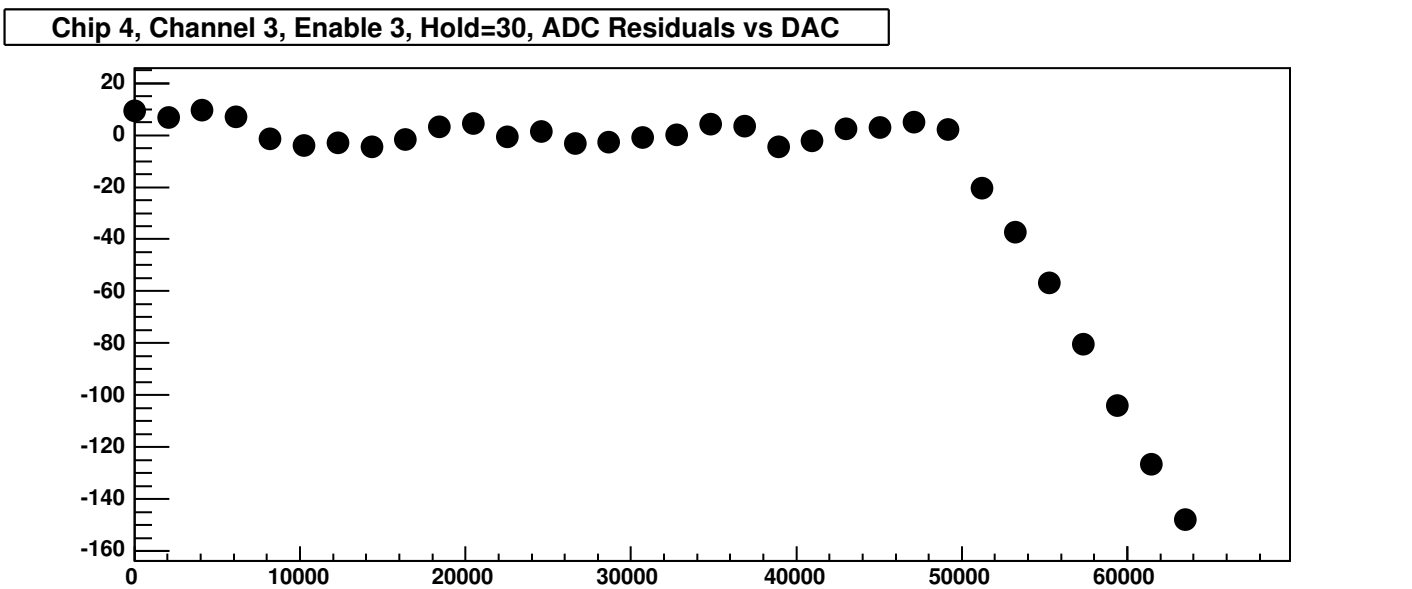

Chip 4, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2724 / 15
p0 1022 ± 0.1287
p1 -0.0002666 ± 5.148e-06
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

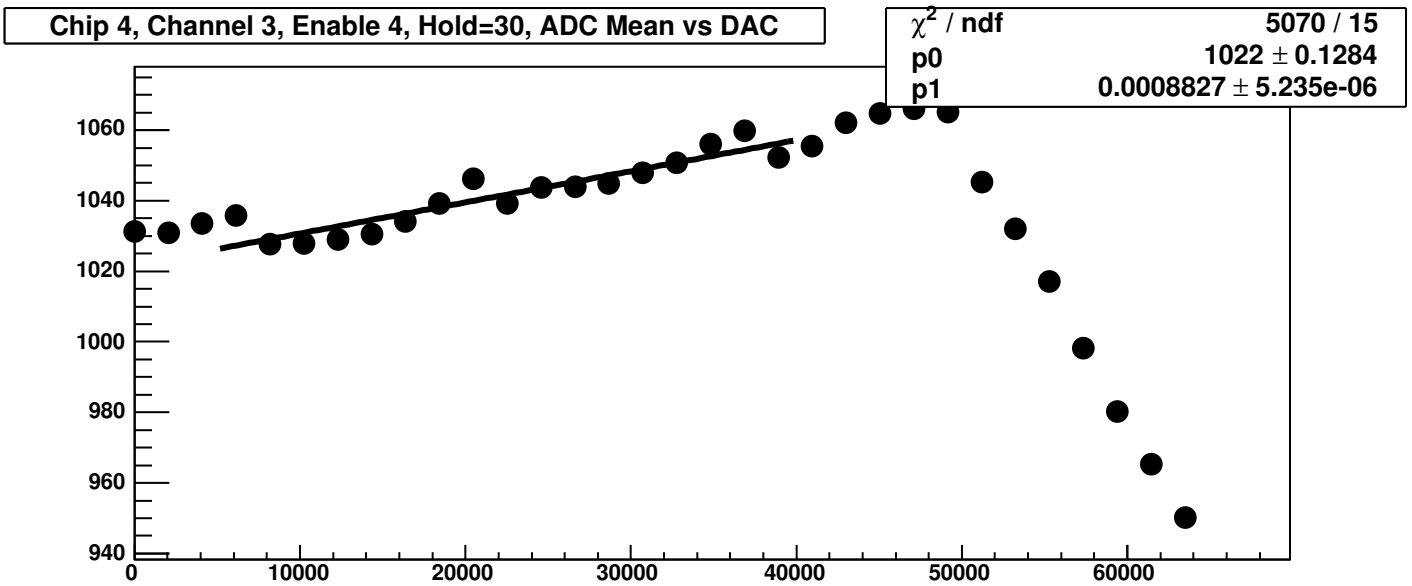

Chip 4, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

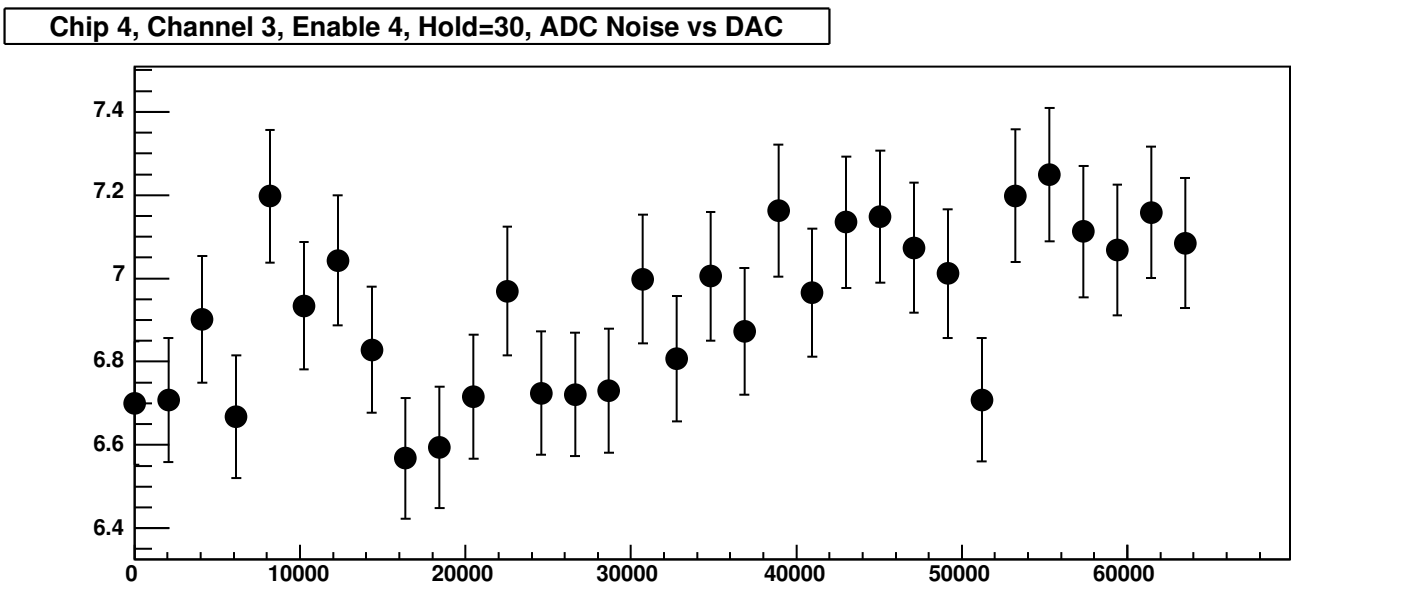

Chip 4, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

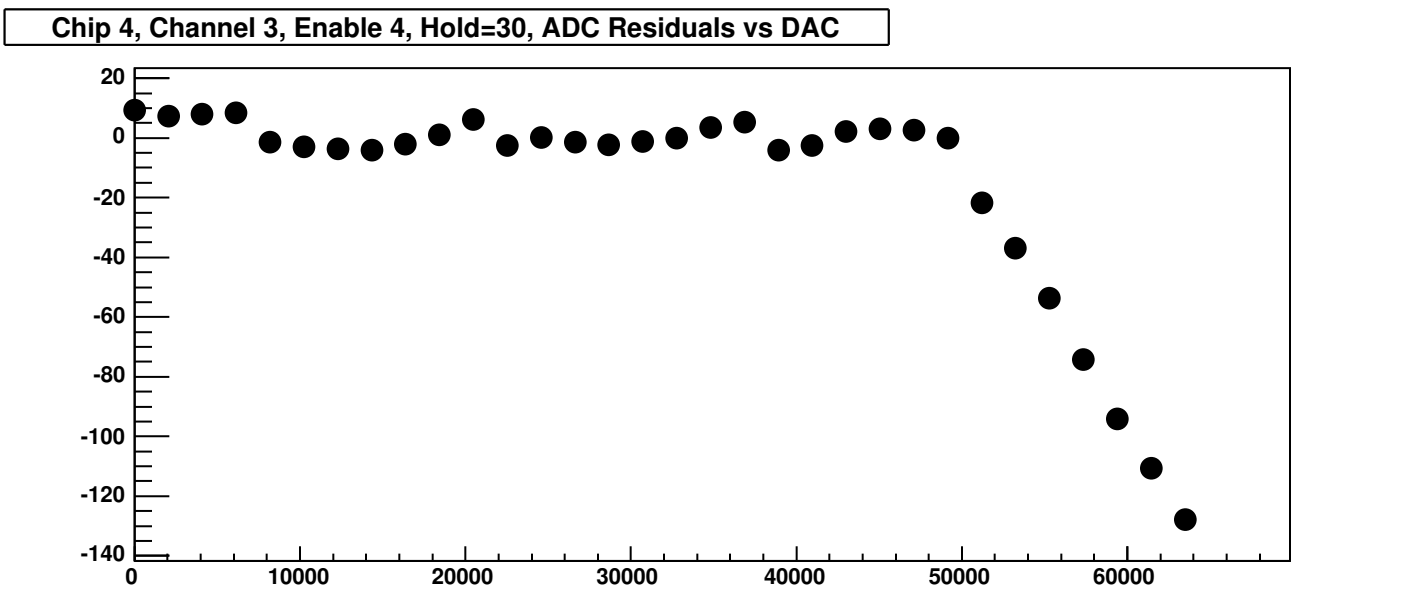

Chip 4, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4264 / 15
p0 1022 ± 0.1301
p1 0.0002712 ± 5.247e-06
1040
1020
1000
980
960
940
920
900
880
0
10000
20000
30000
40000
50000
60000

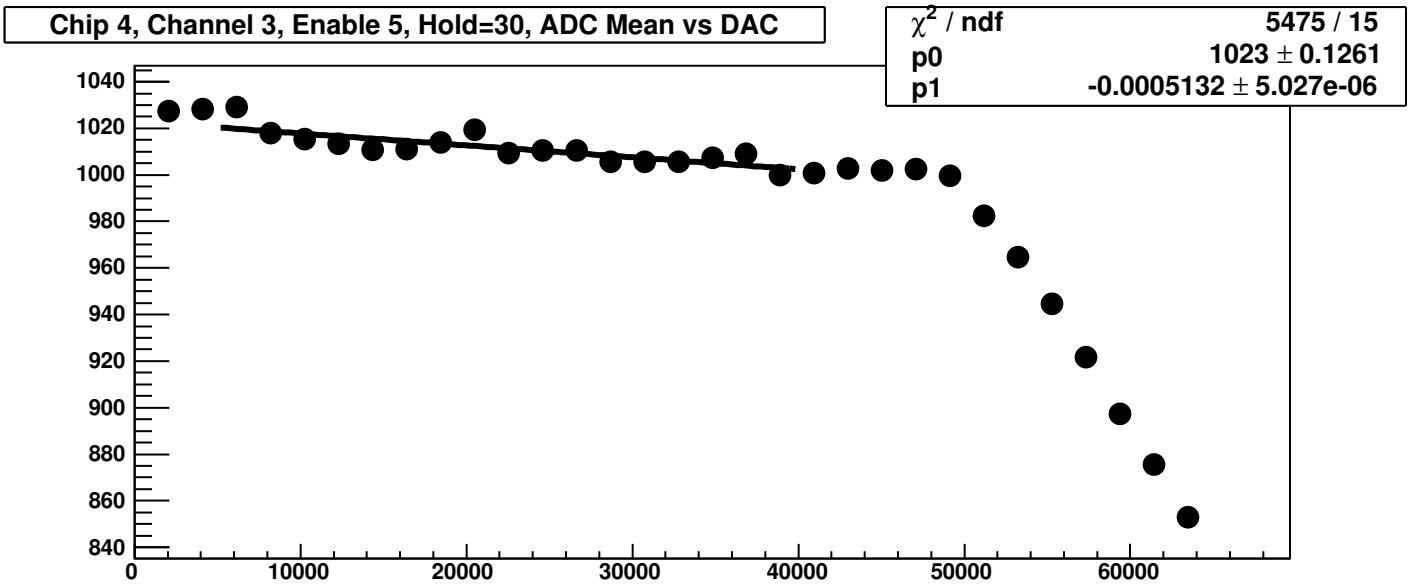

Chip 4, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

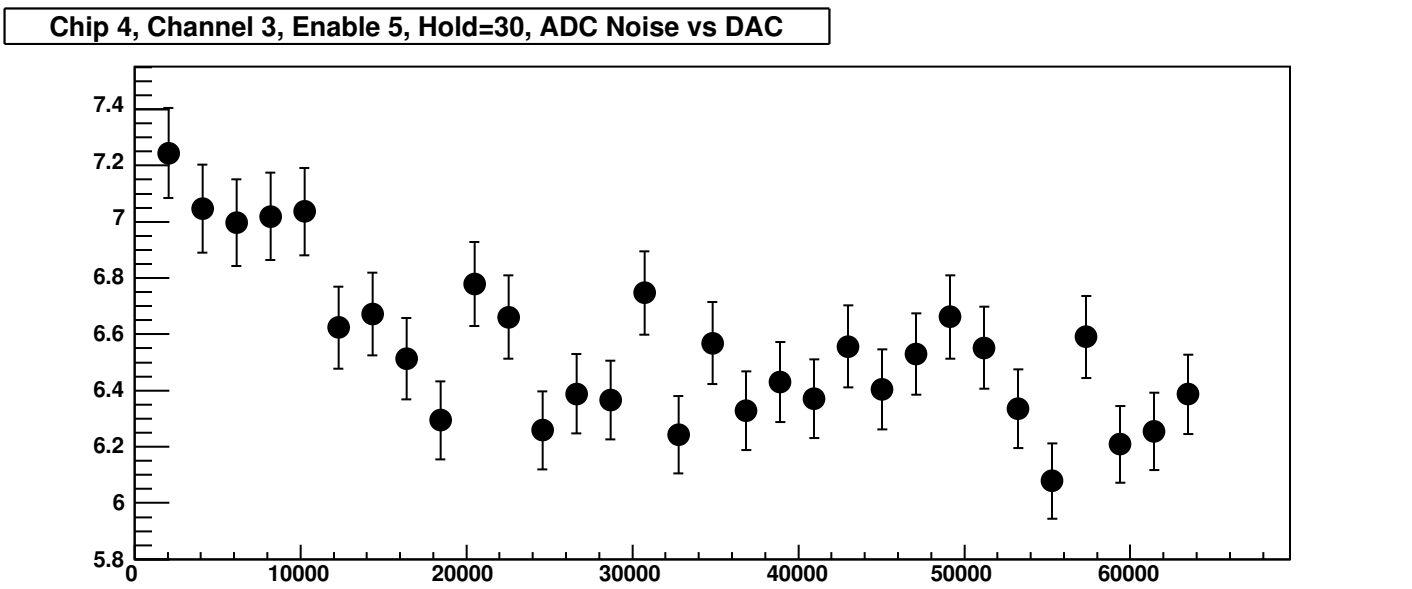

Chip 4, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

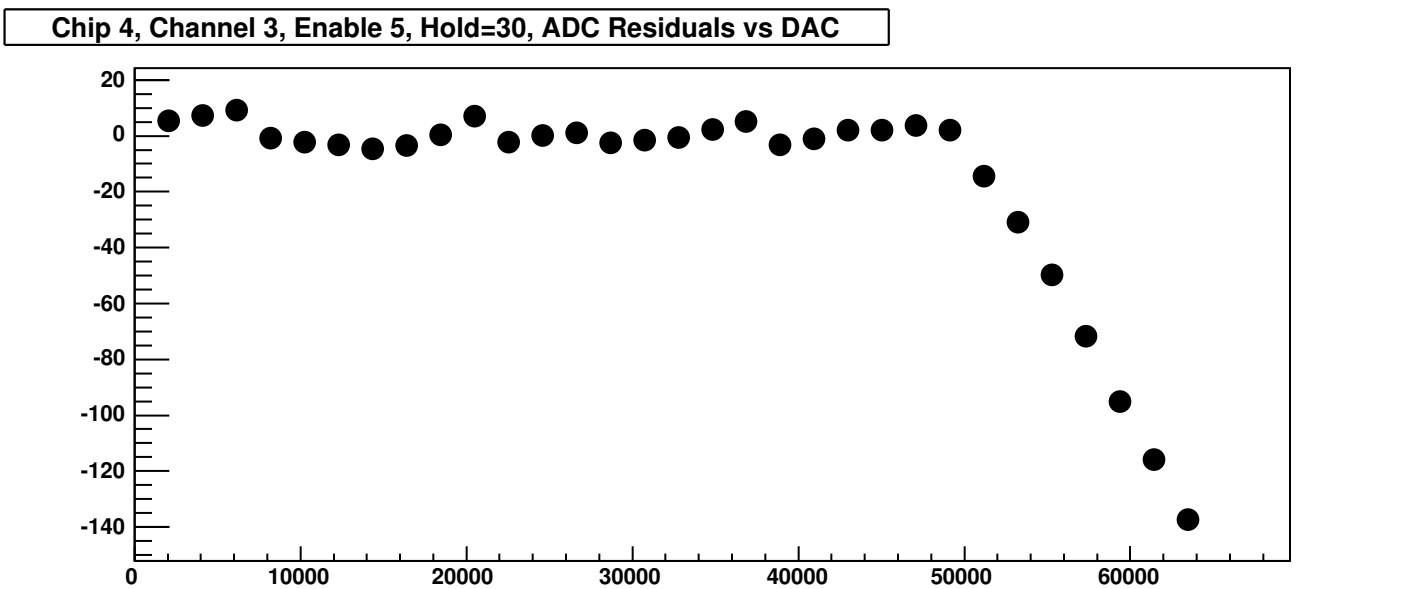

Chip 4, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5070 / 15
p0 1022 ± 0.1284
p1 0.0008827 ± 5.235e-06
1060
1040
1020
1000
980
960
940
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

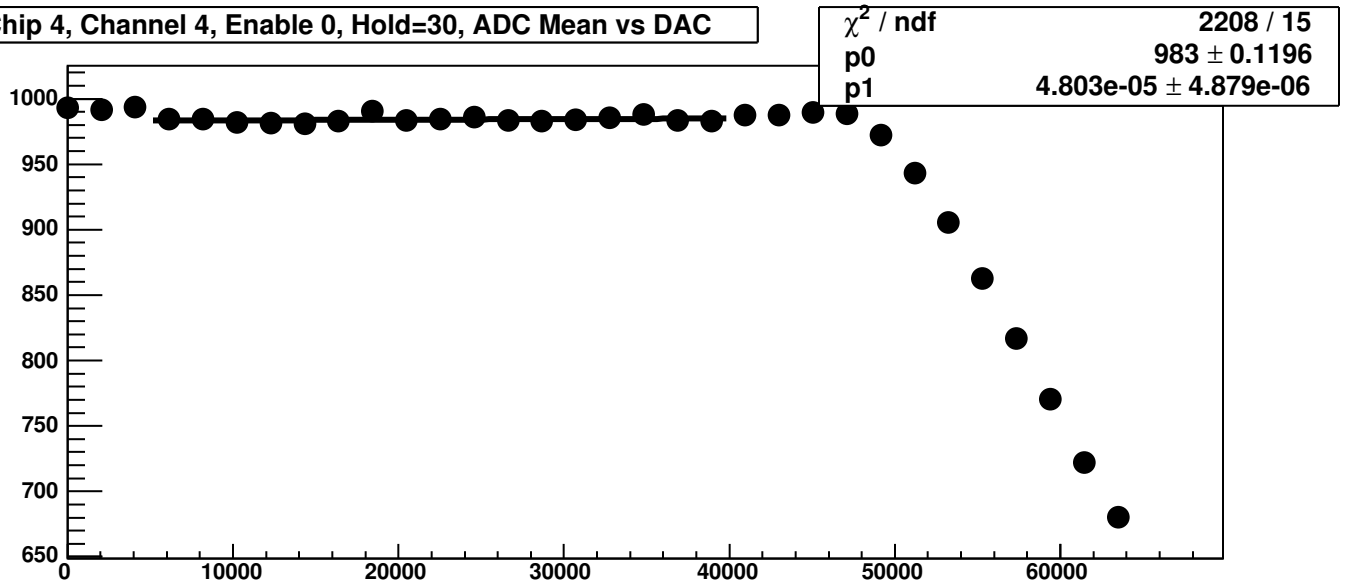

Chip 4, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5475 / 15
p0 1023 ± 0.1261
p1 -0.0005132 ± 5.027e-06
1040
1020
1000
980
960
940
920
900
880
860
840
0
10000
20000
30000
40000
50000
60000

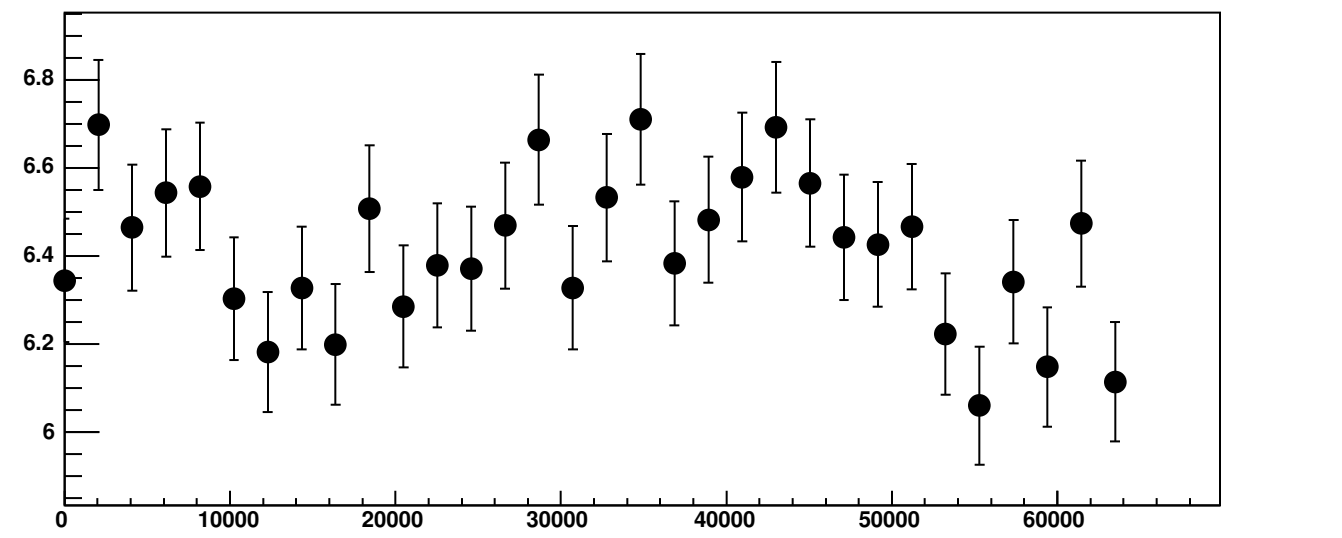

Chip 4, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

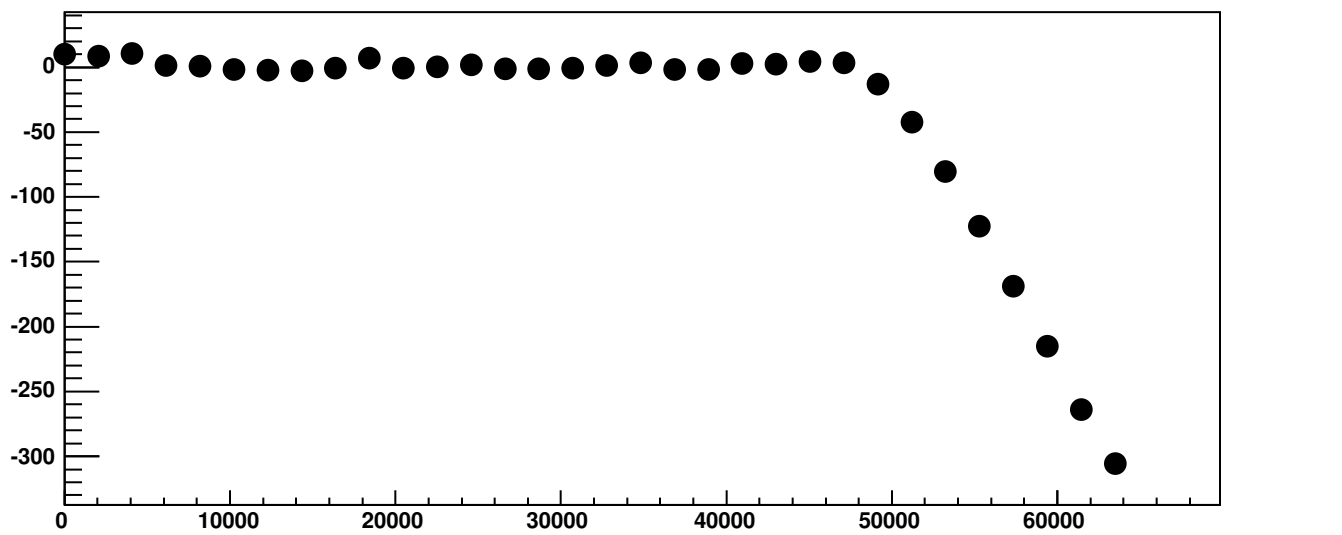
Chip 4, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

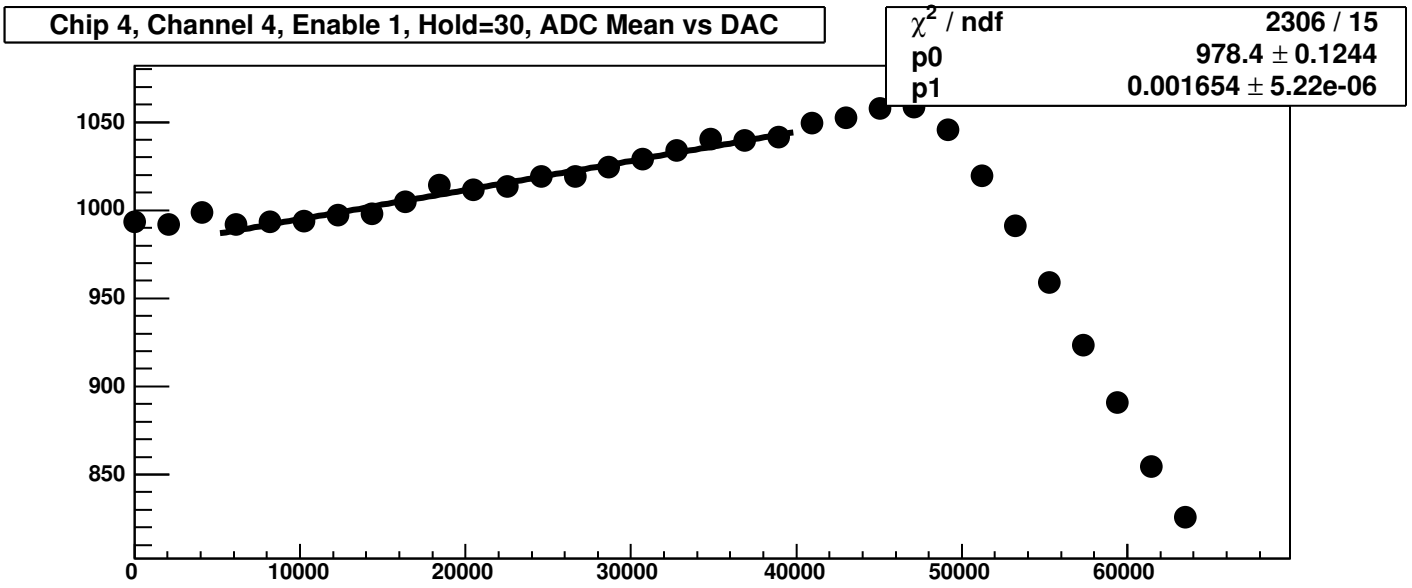


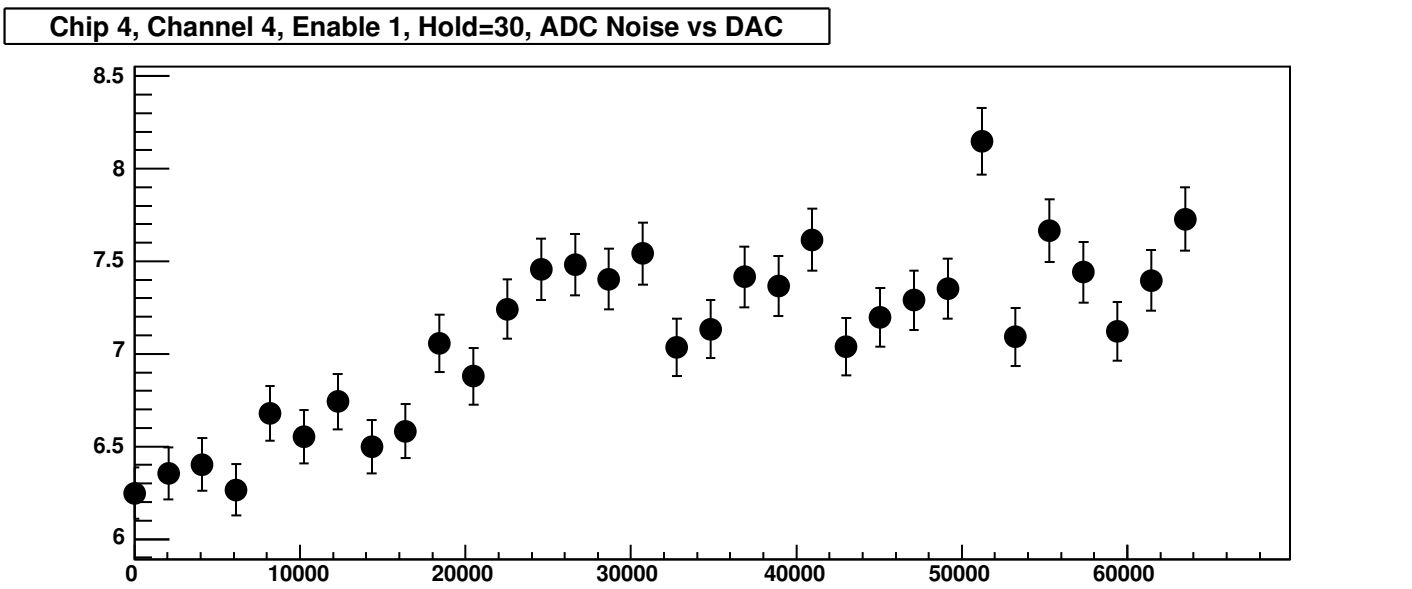
Chip 4, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC

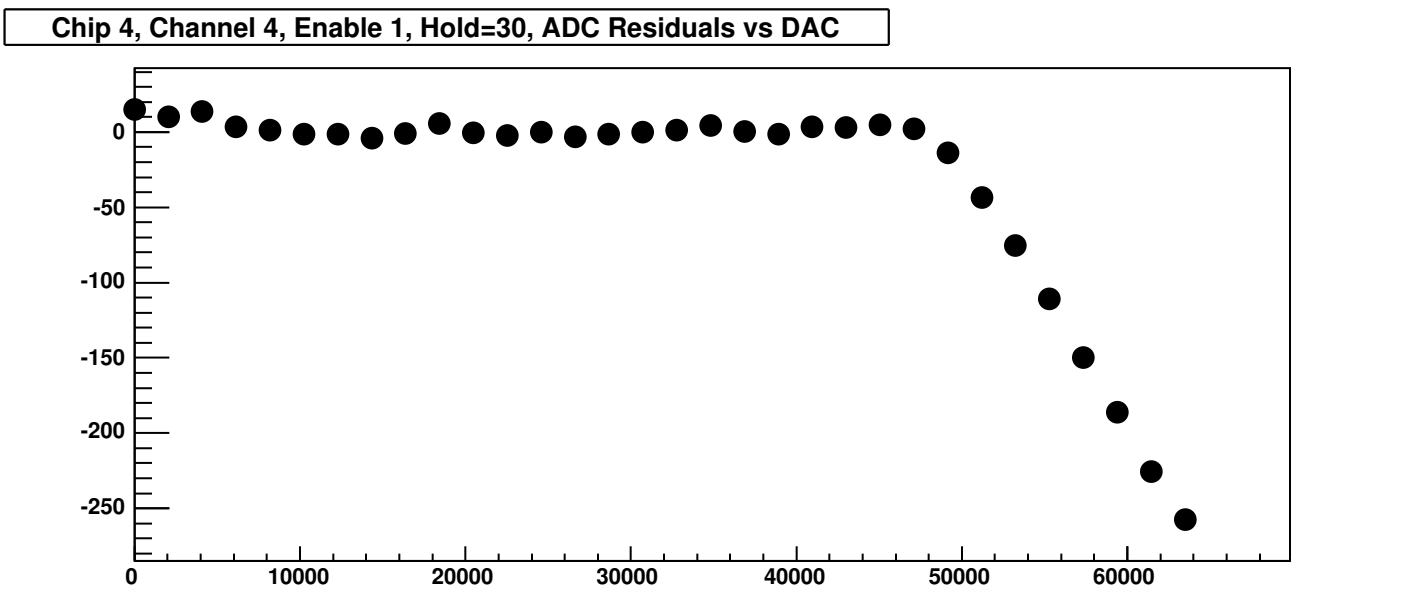


Chip 4, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

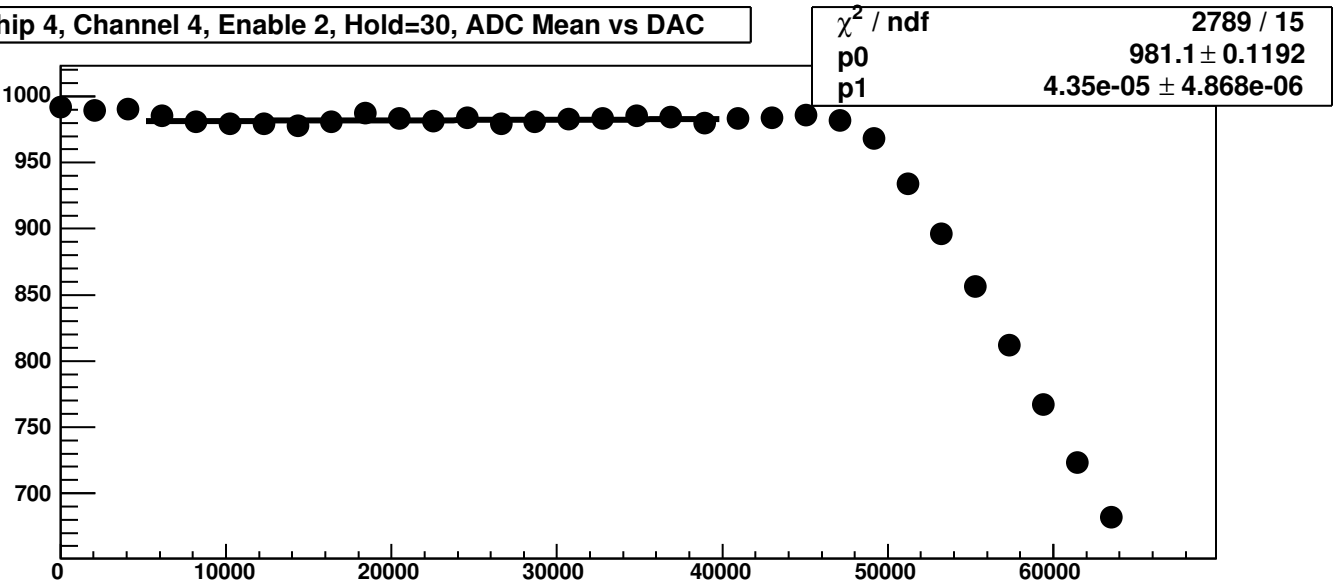



Chip 4, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2306 / 15
p0 978.4 ± 0.1244
p1 0.001654 ± 5.22e-06
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

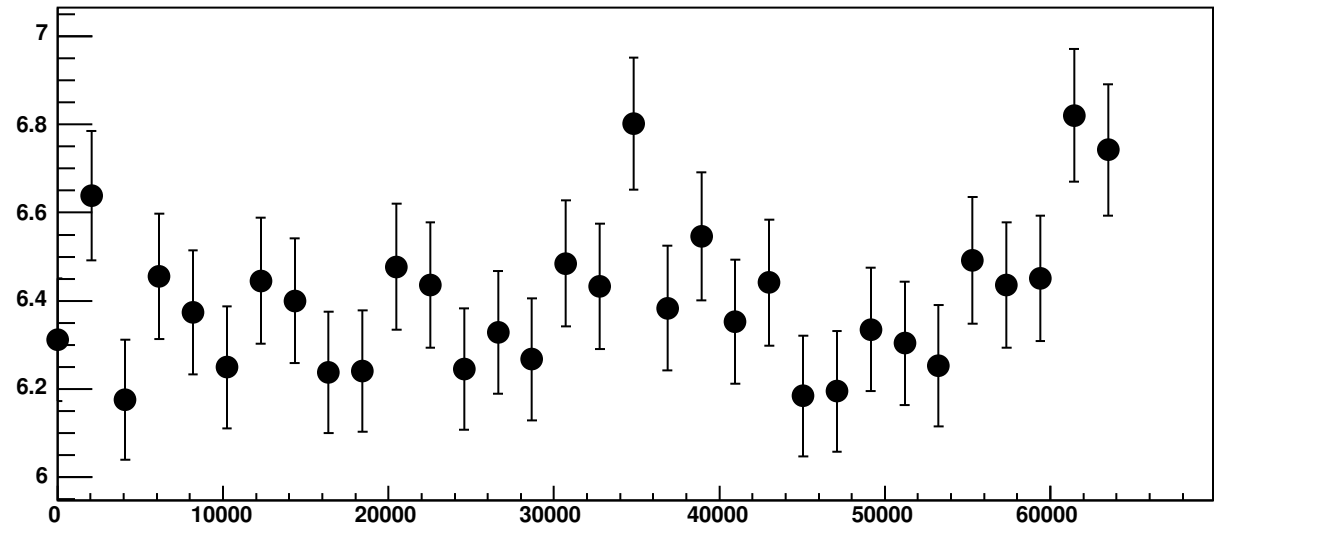

Chip 4, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

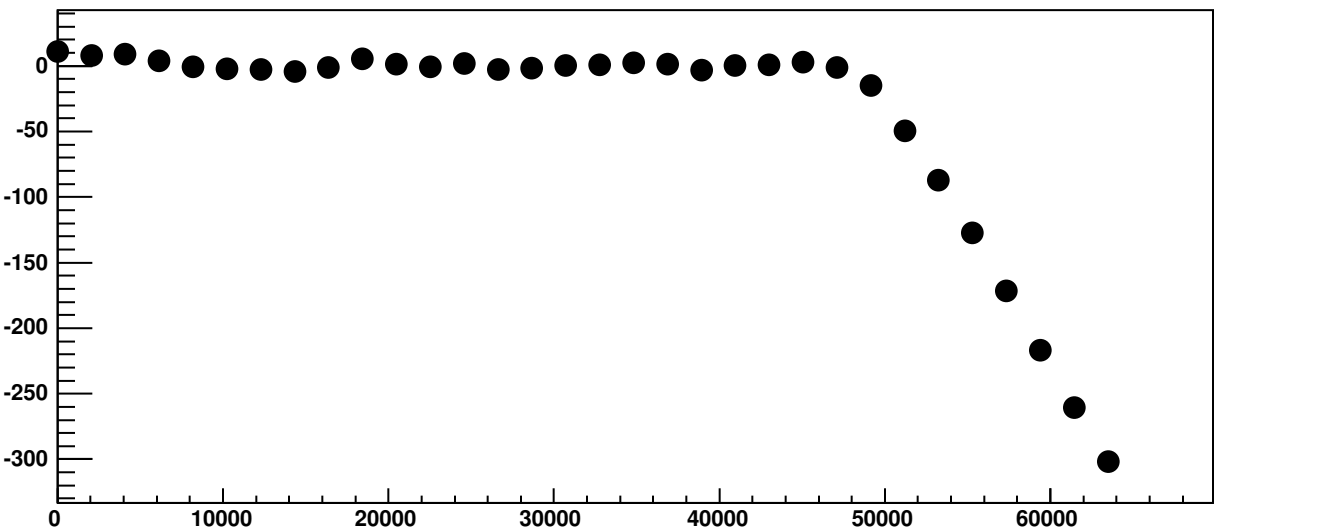
Chip 4, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

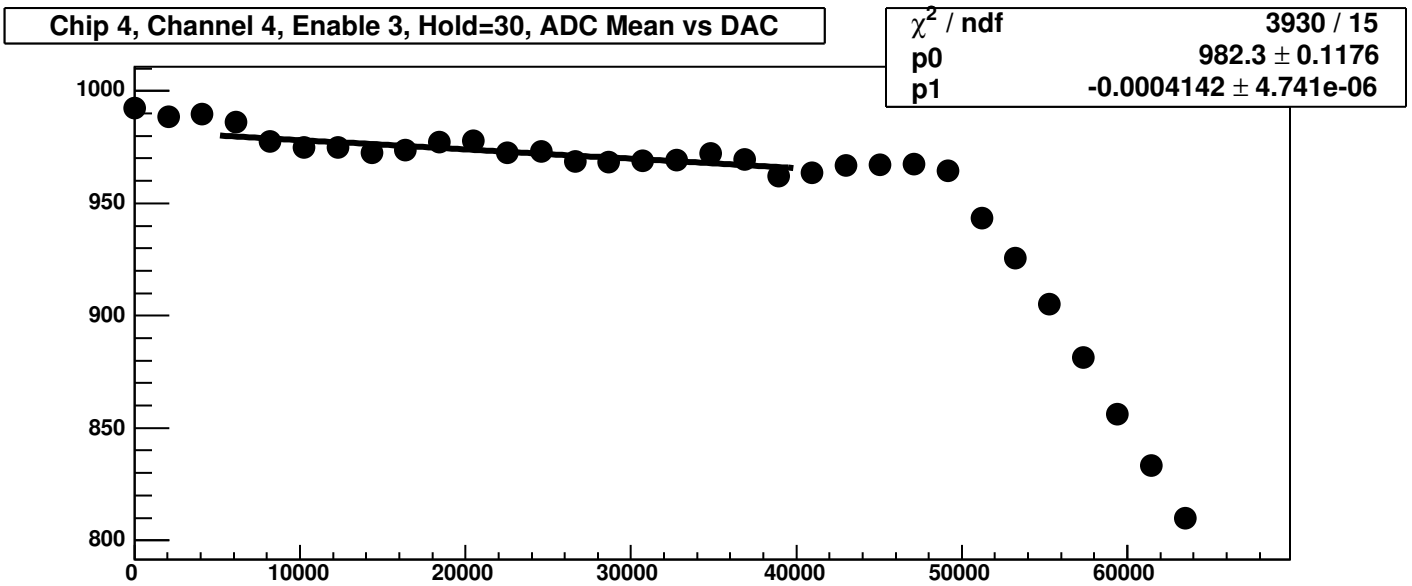


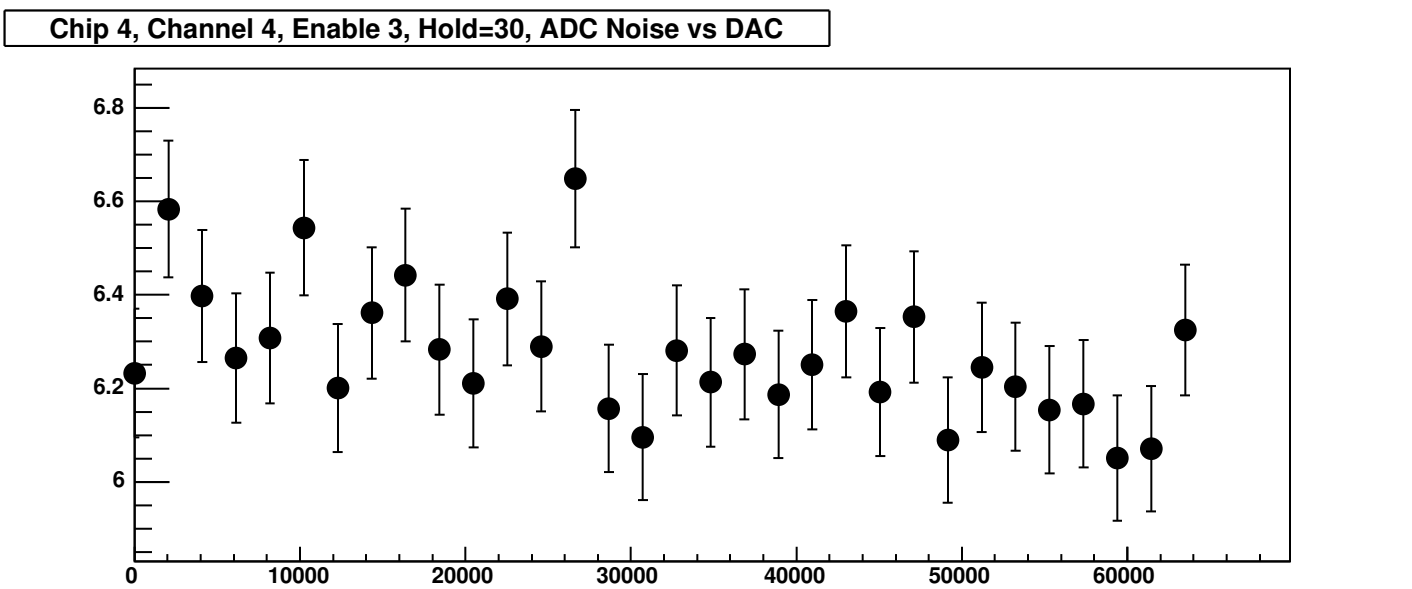
Chip 4, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC

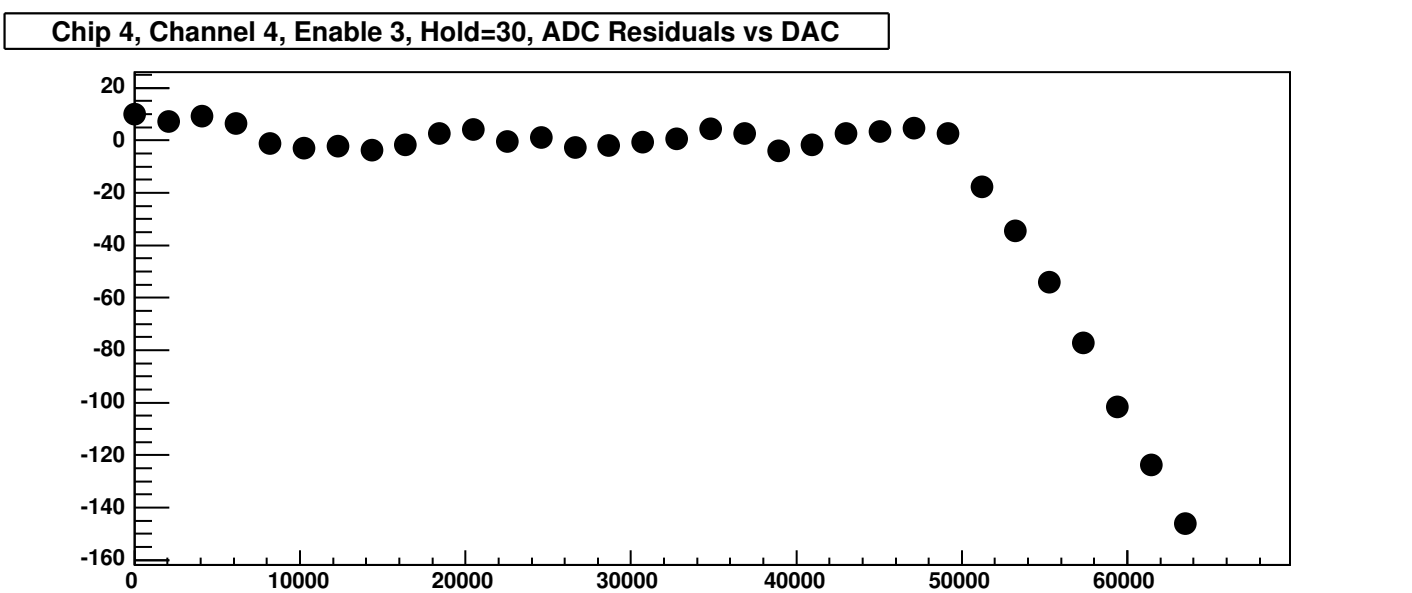


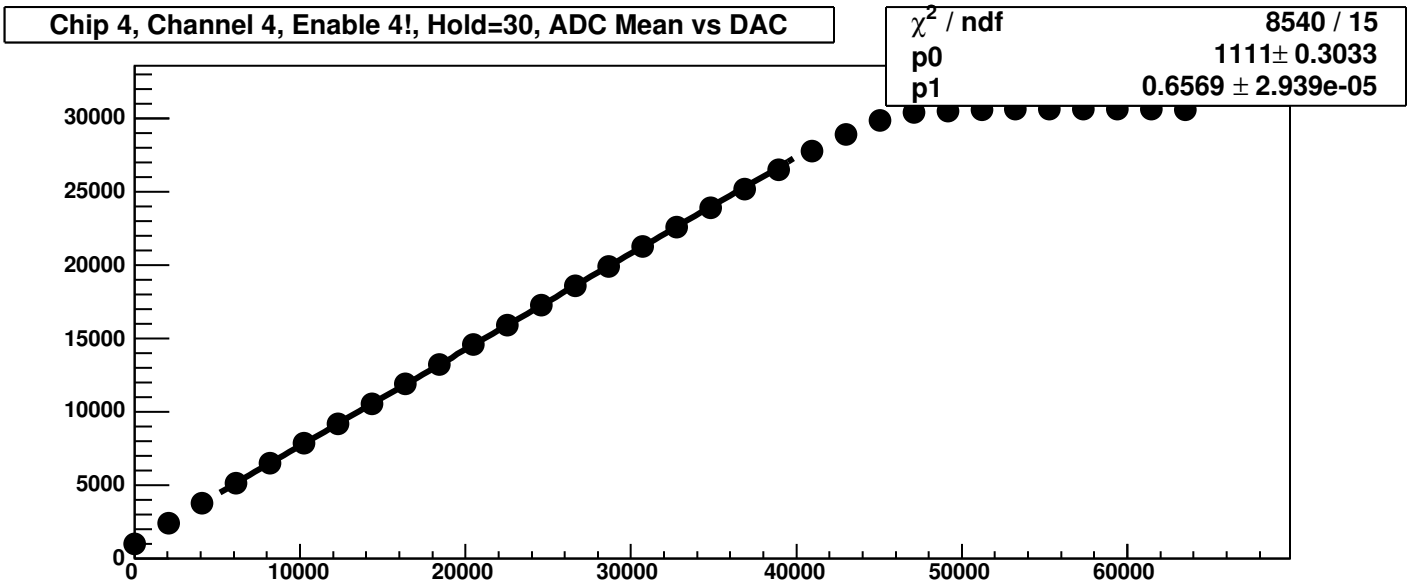
Chip 4, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

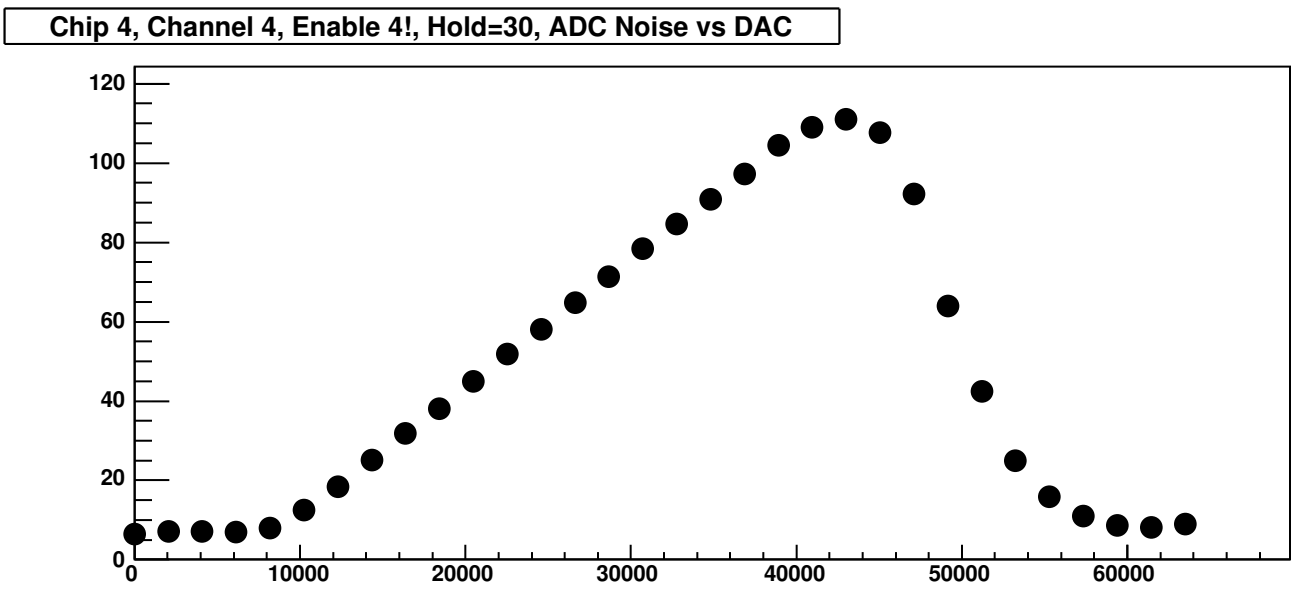


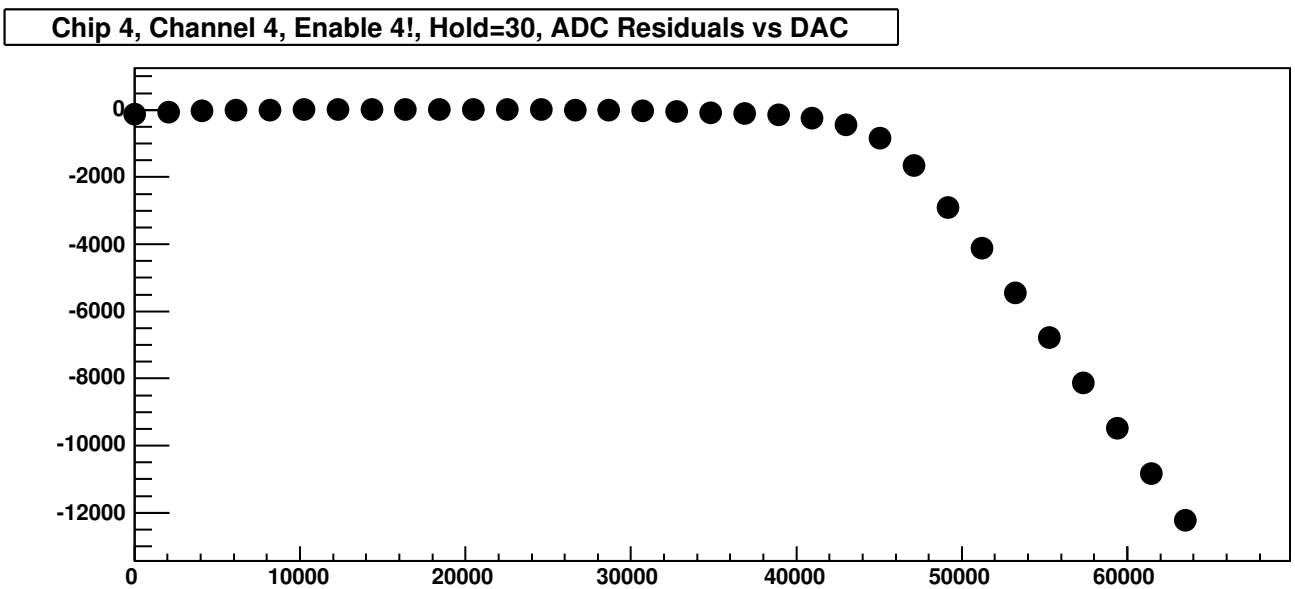

Chip 4, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3930 / 15
p0 982.3 ± 0.1176
p1 -0.0004142 ± 4.741e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

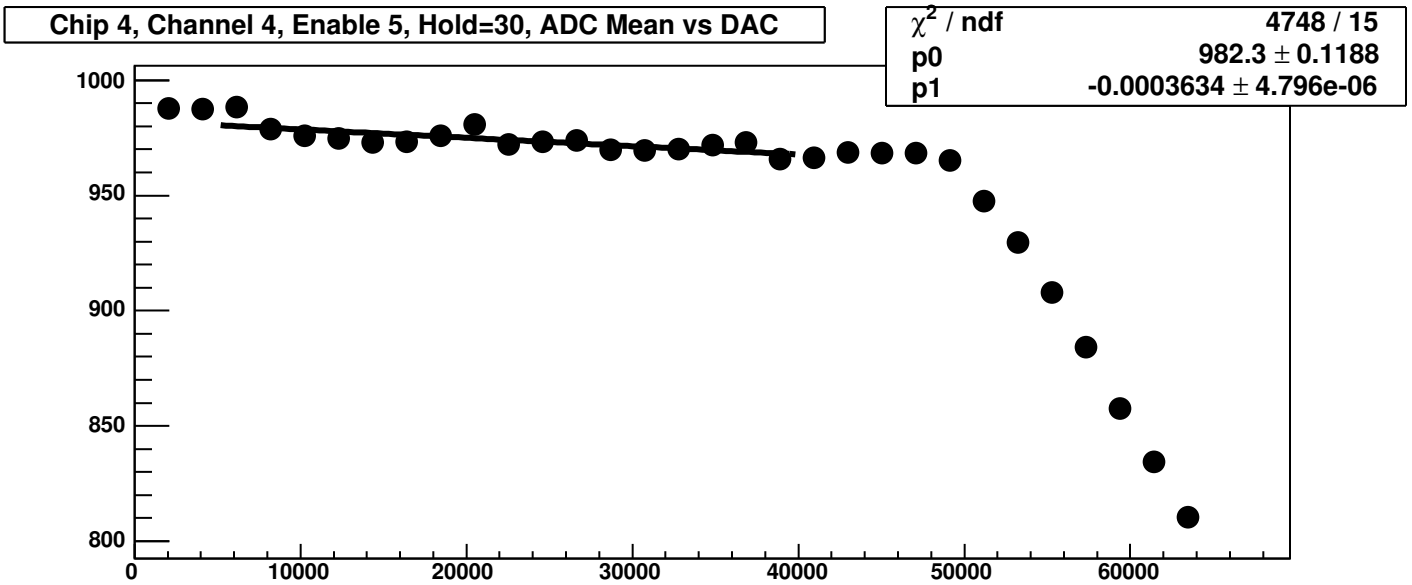

Chip 4, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

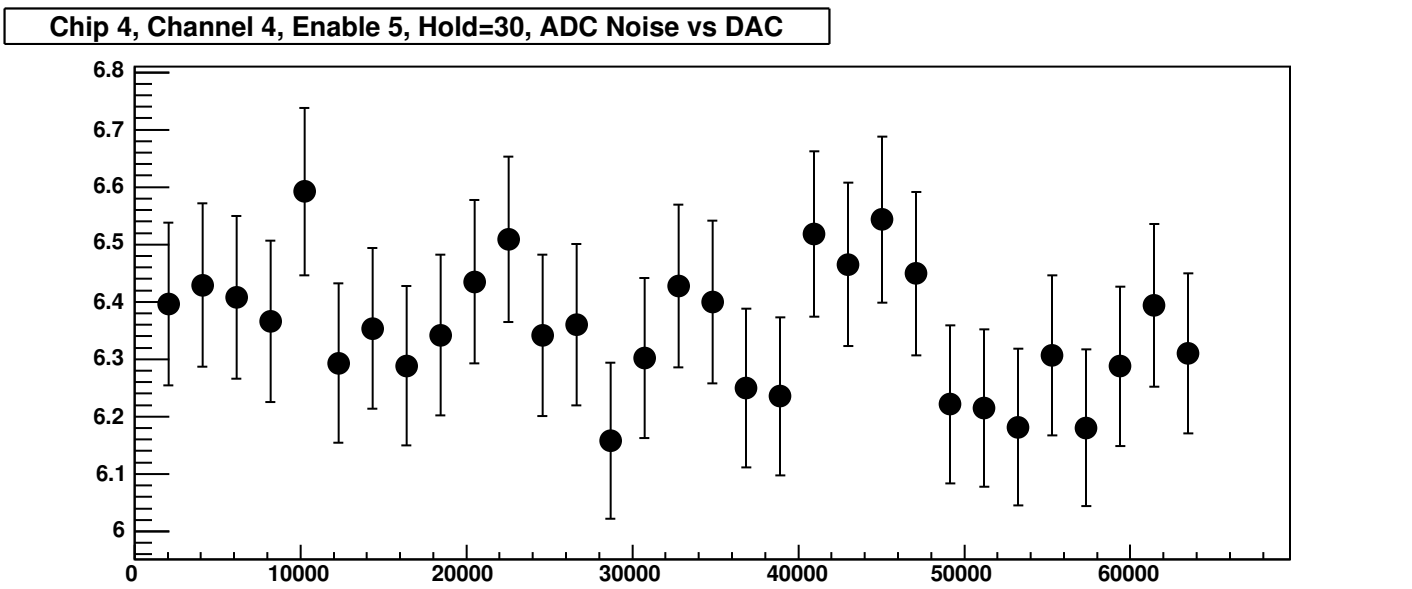

Chip 4, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

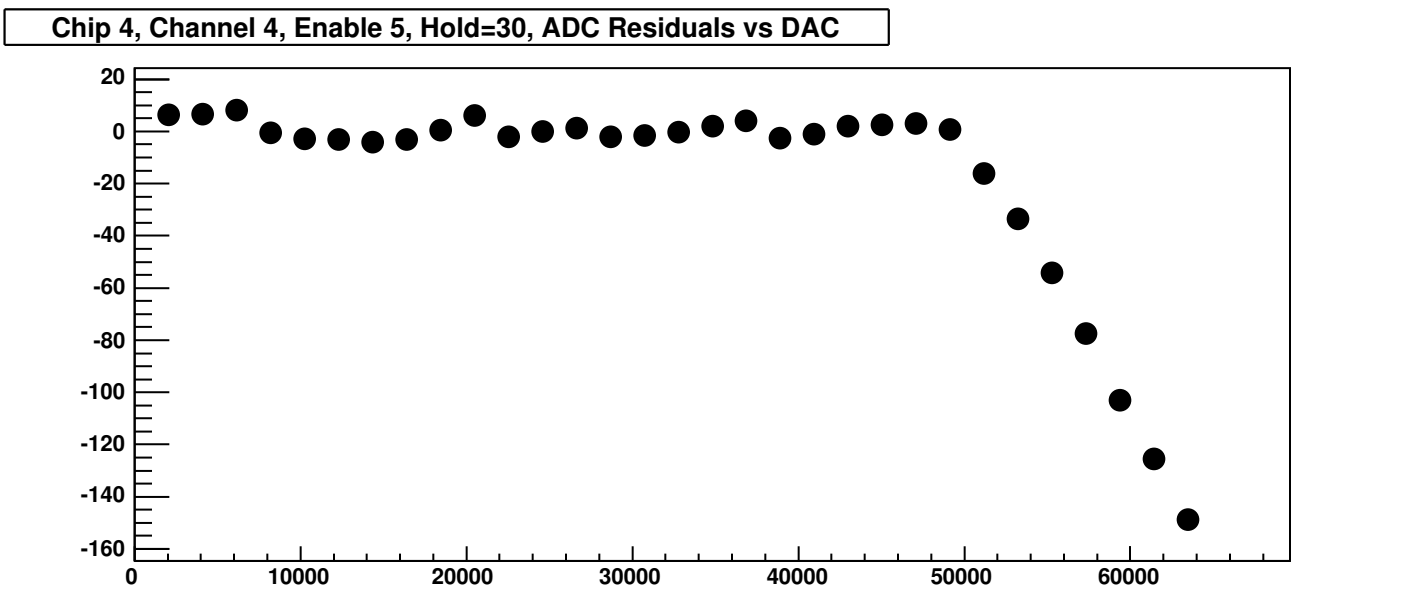

Chip 4, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 8540 / 15
p0 1111 ± 0.3033
p1 0.6569 ± 2.939e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

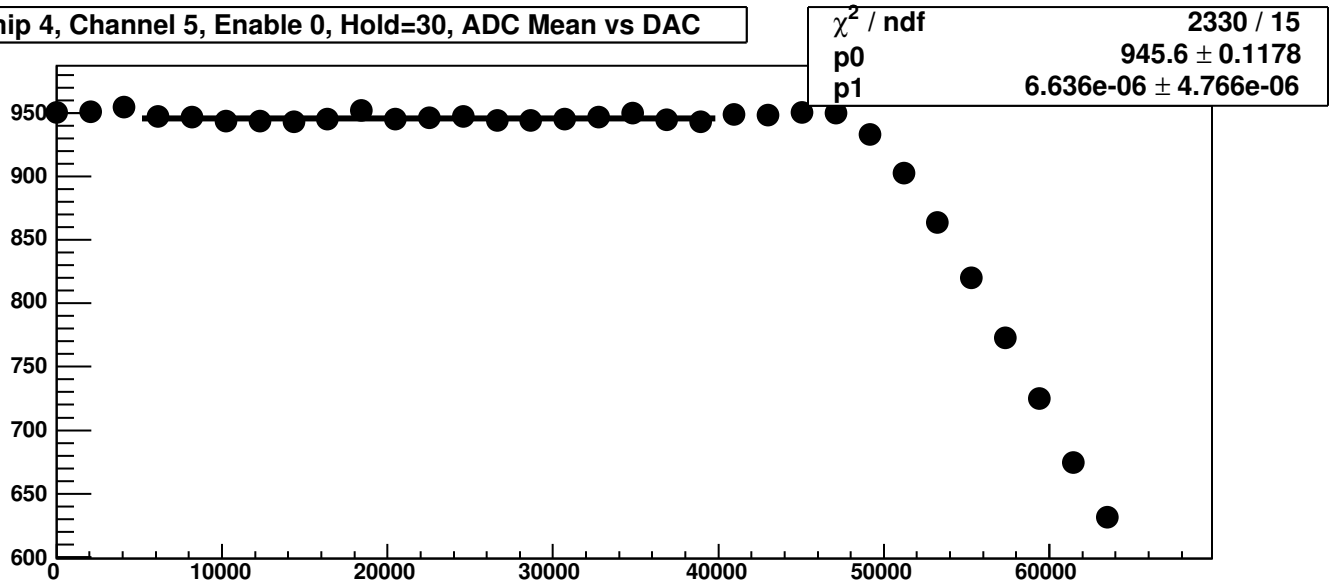

Chip 4, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4748 / 15
p0 982.3 ± 0.1188
p1 -0.0003634 ± 4.796e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

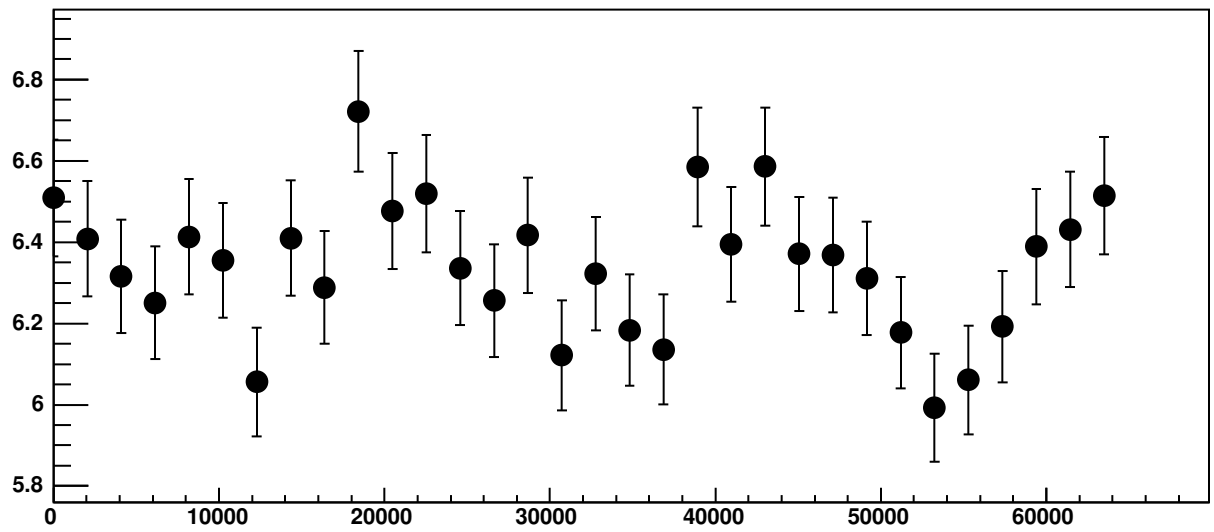

Chip 4, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.7
6.6
6.5
6.4
6.3
6.2
6.1
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

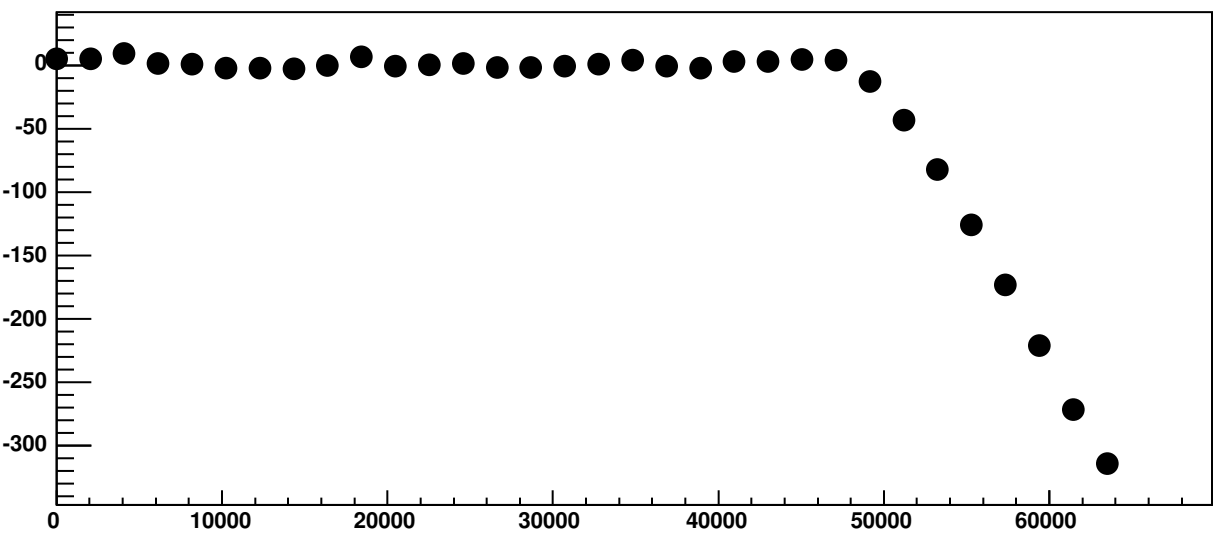
Chip 4, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC

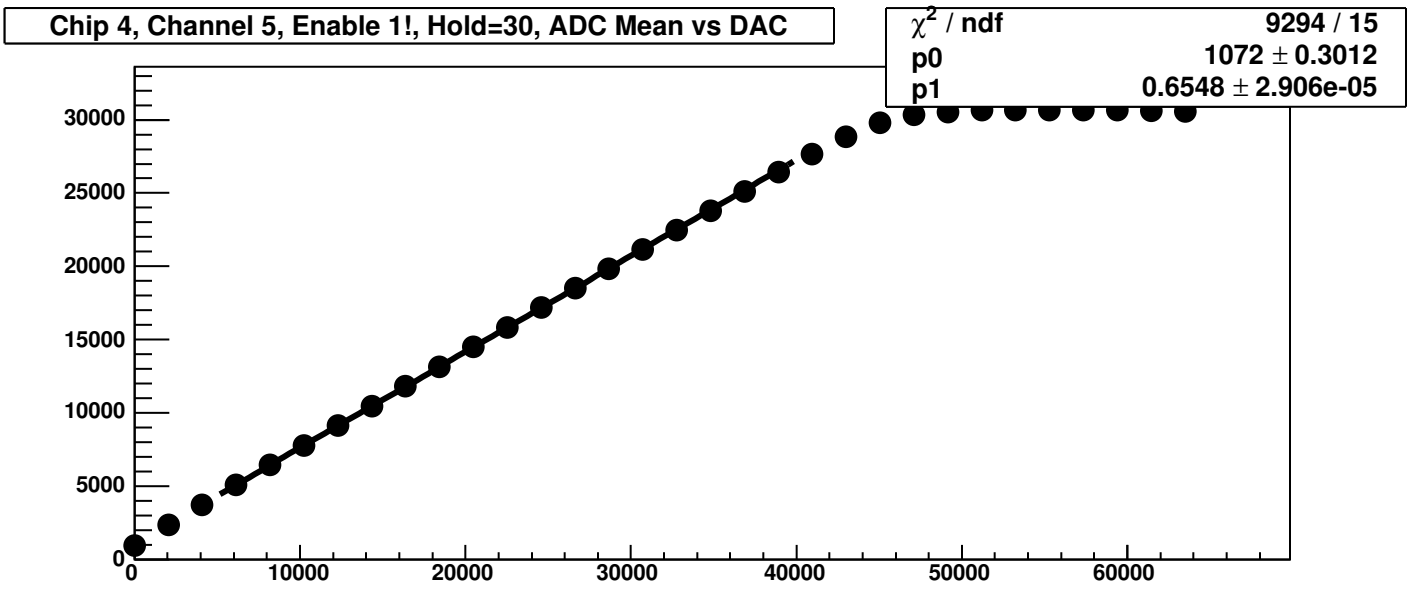


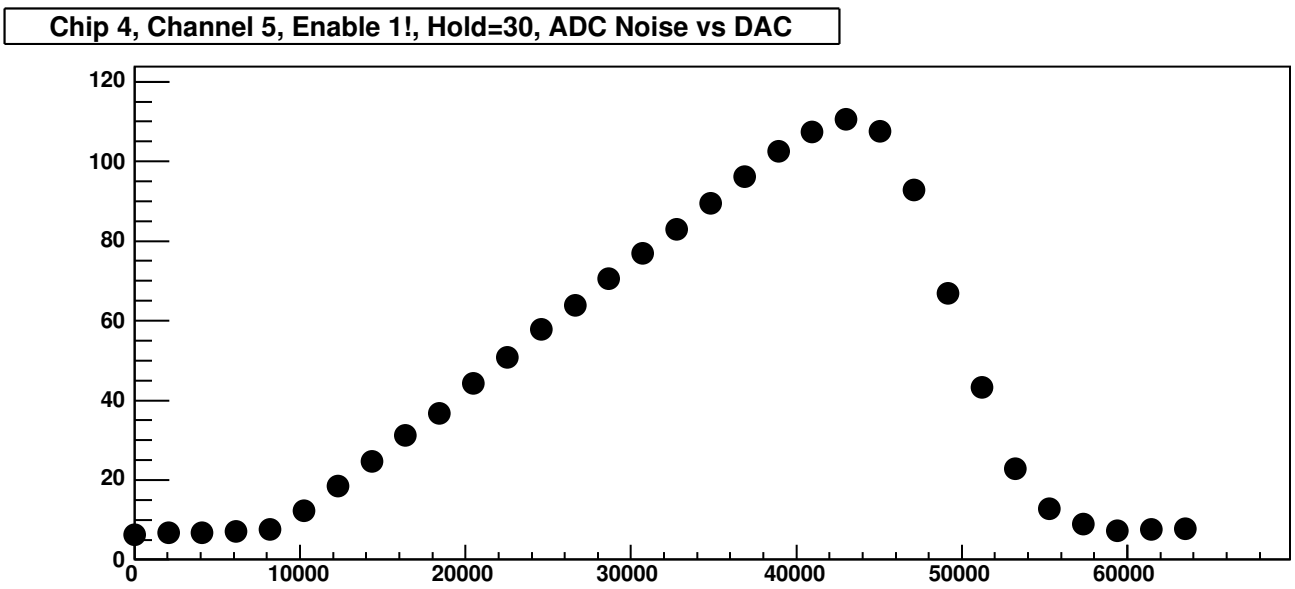
Chip 4, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC

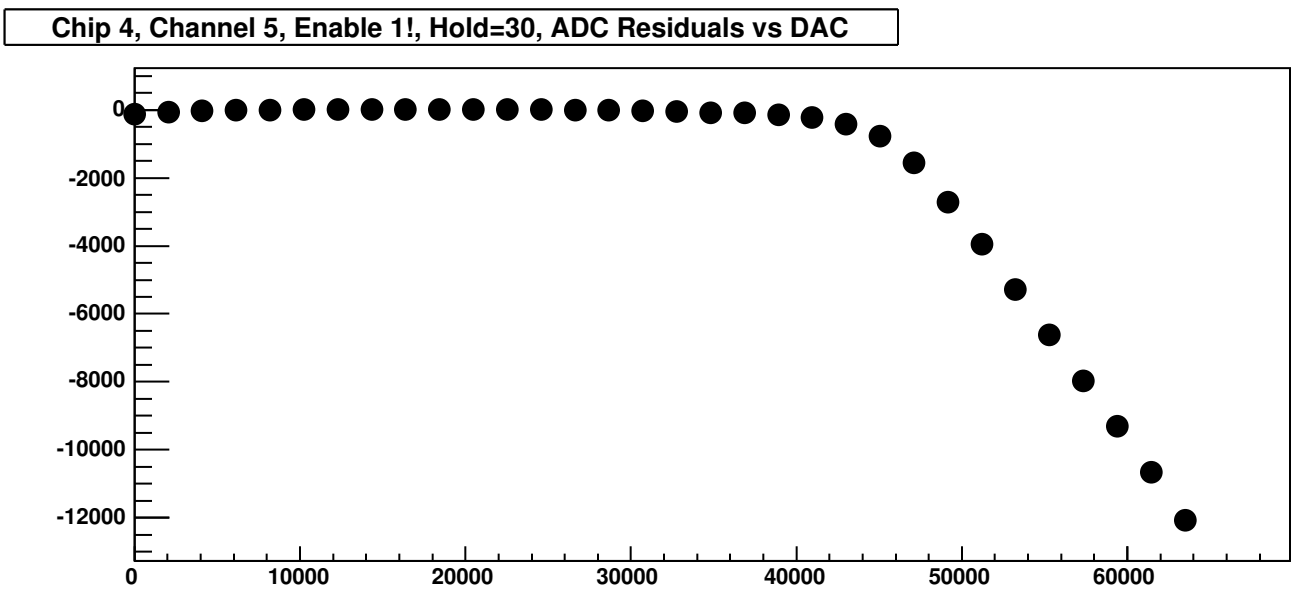


Chip 4, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

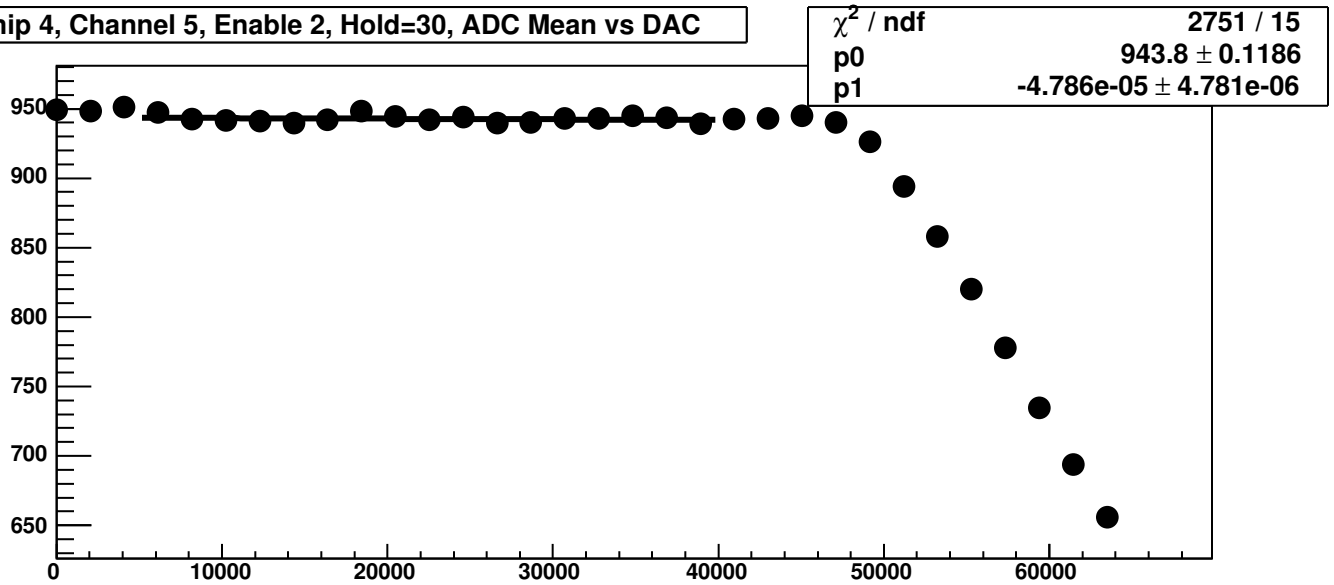



Chip 4, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 9294 / 15
p0 1072 ± 0.3012
p1 0.6548 ± 2.906e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

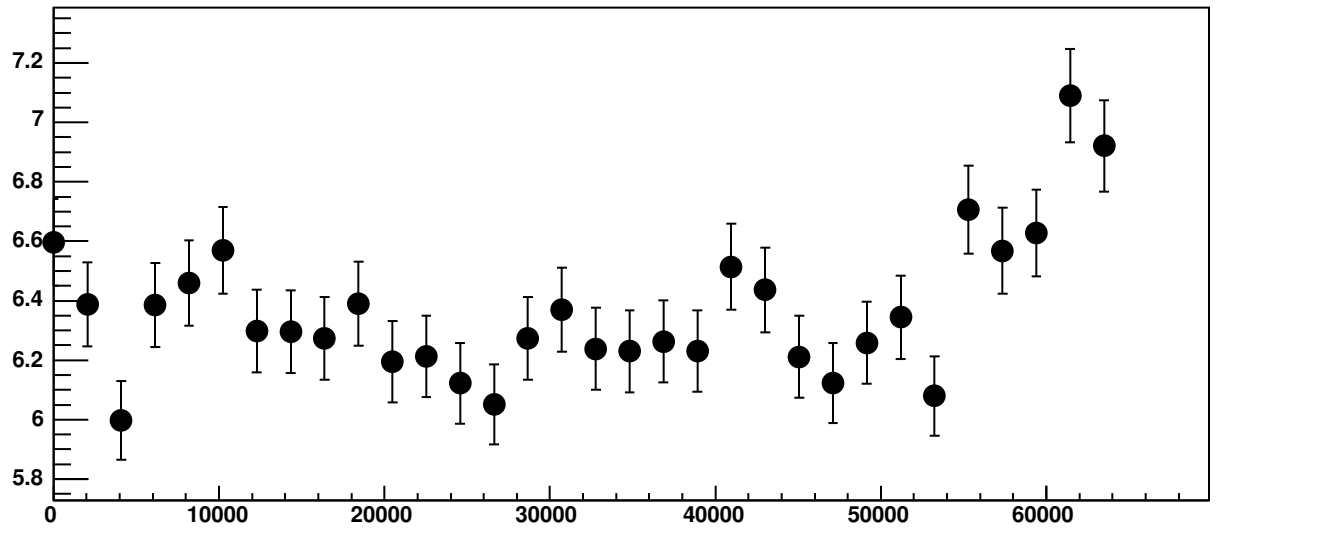

Chip 4, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

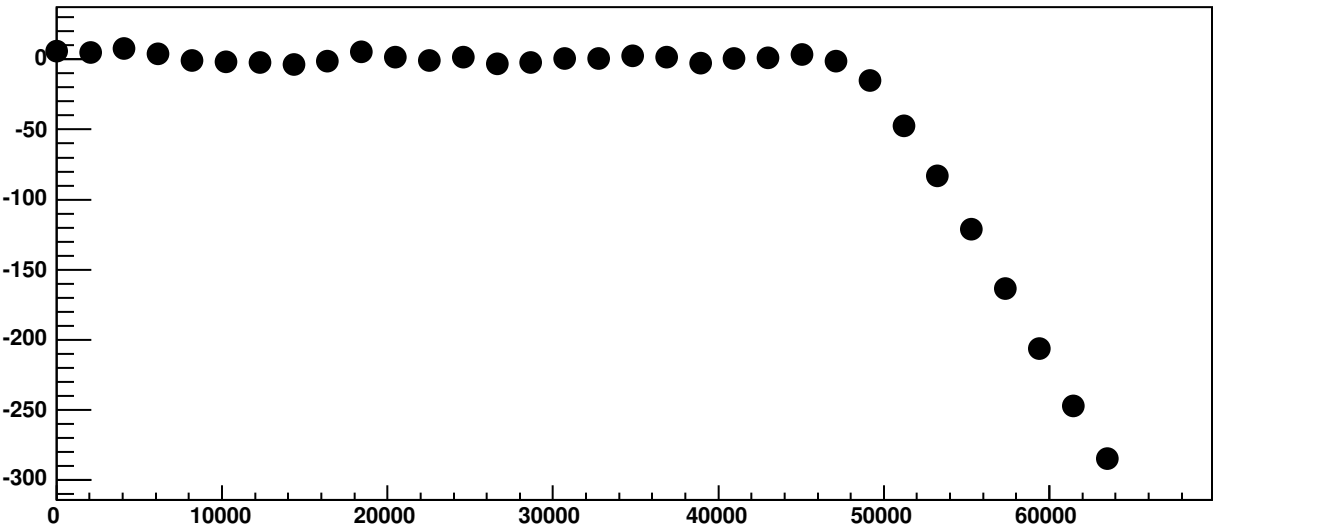
Chip 4, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC

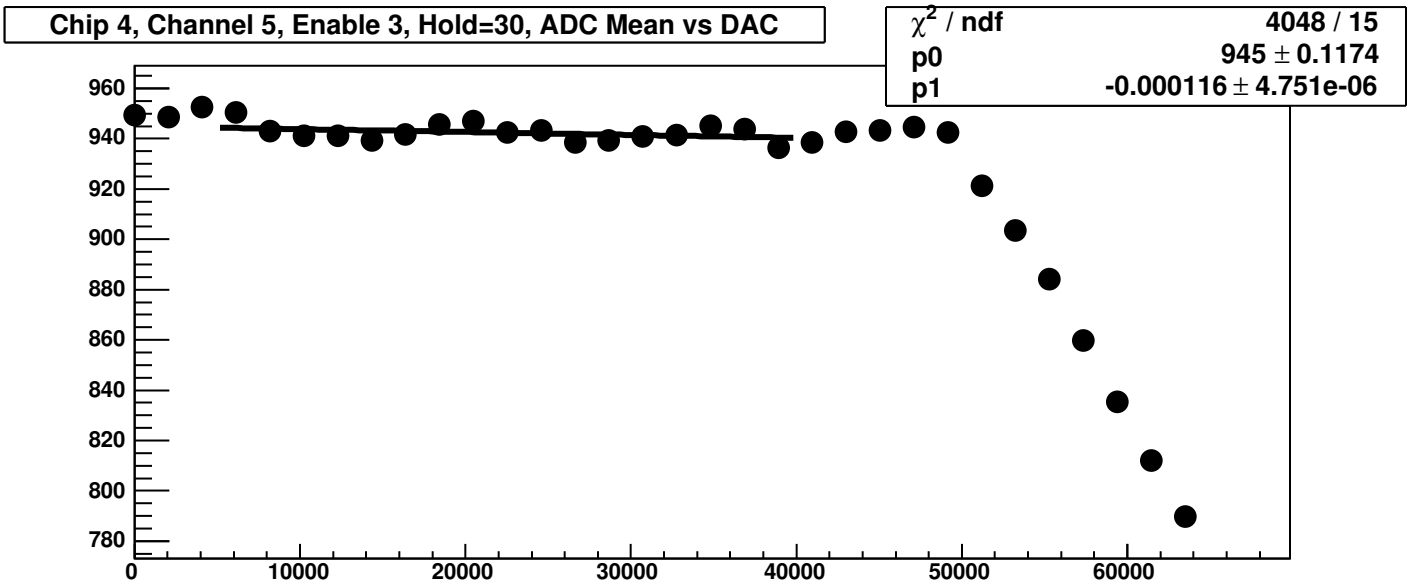


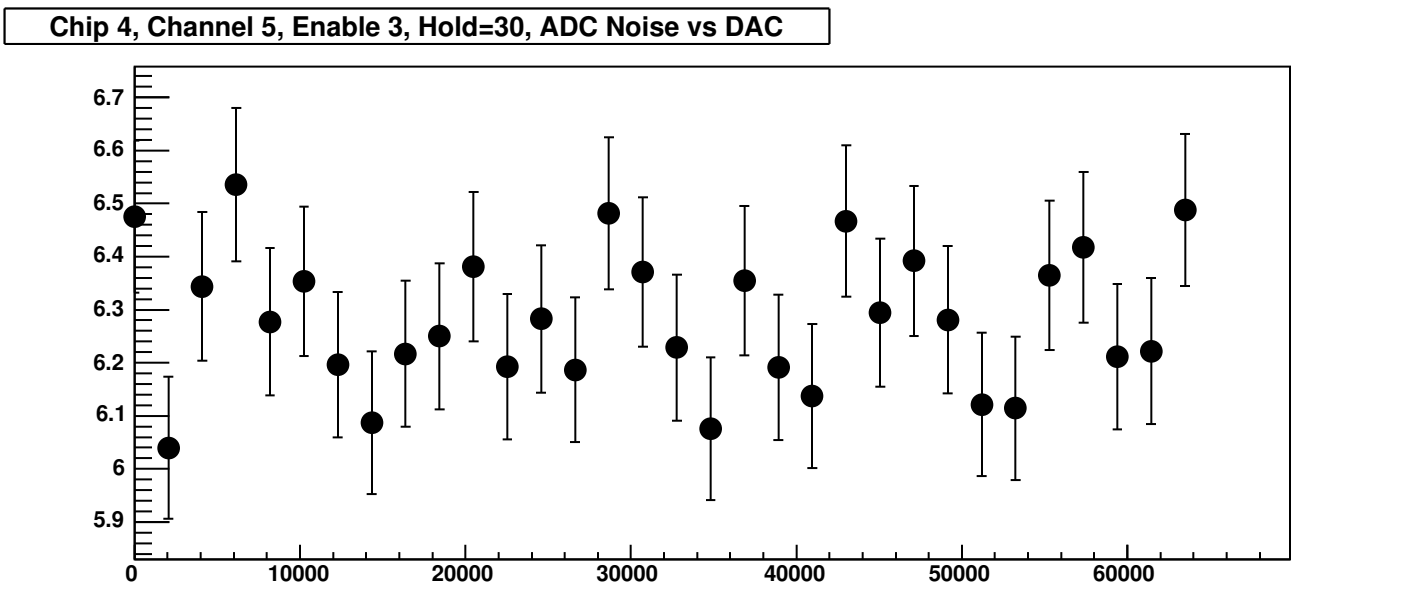
Chip 4, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC

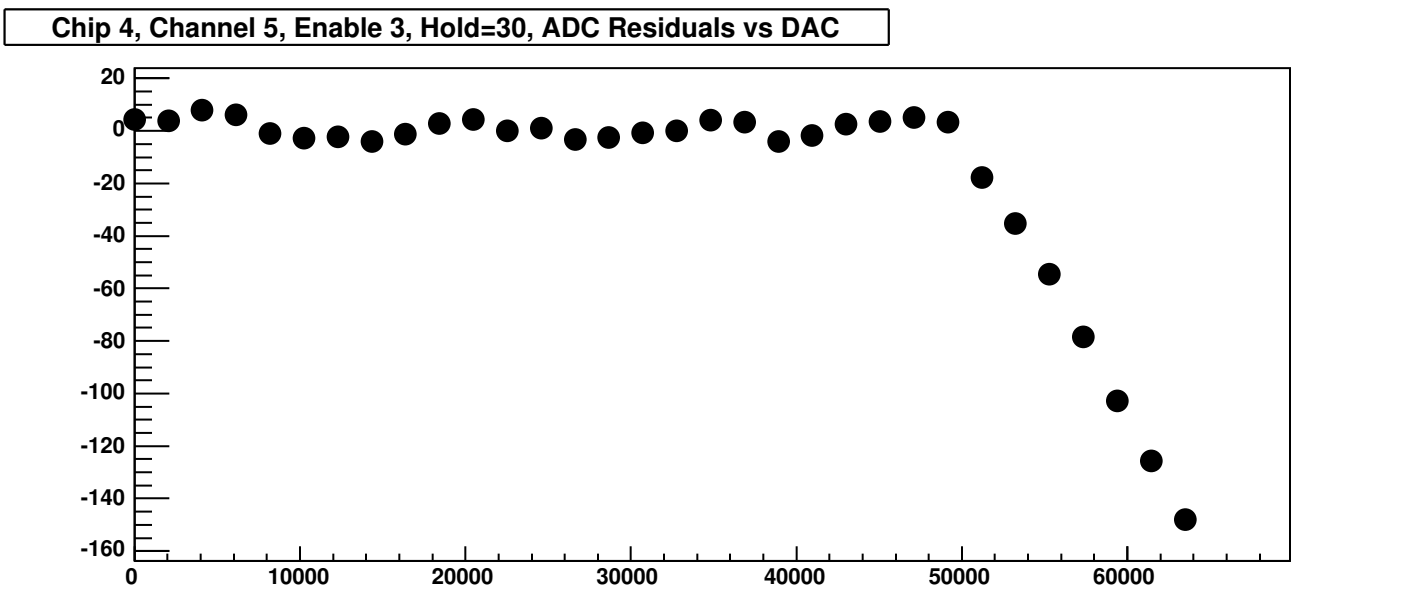


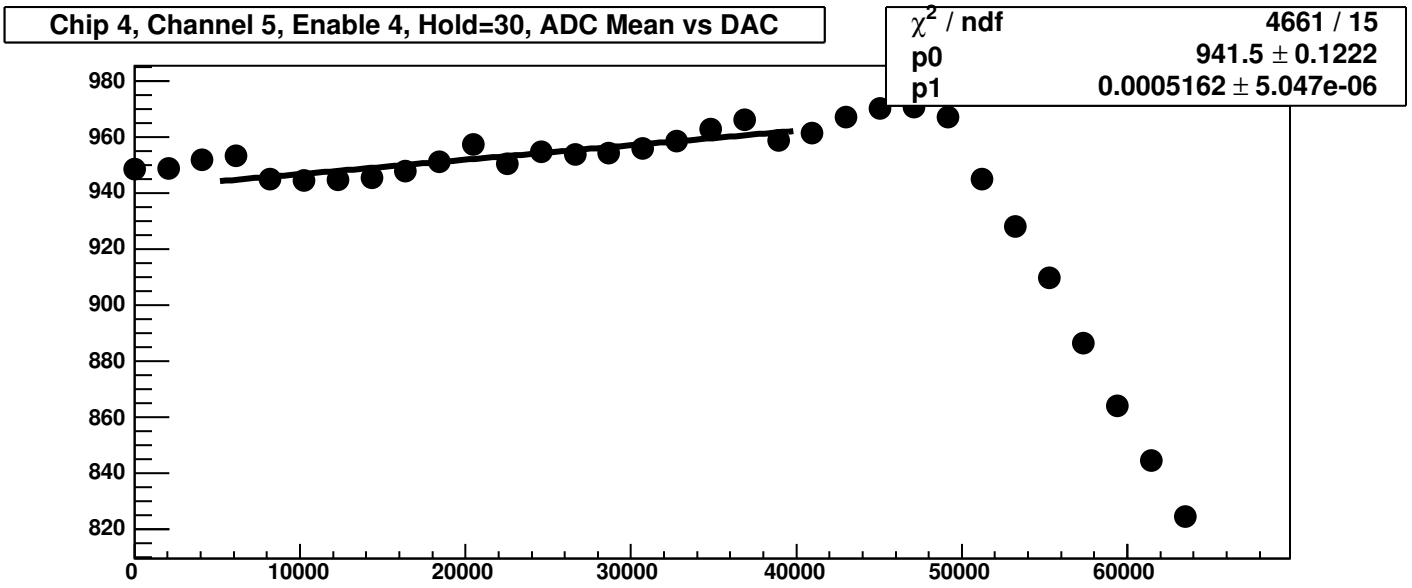
Chip 4, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC

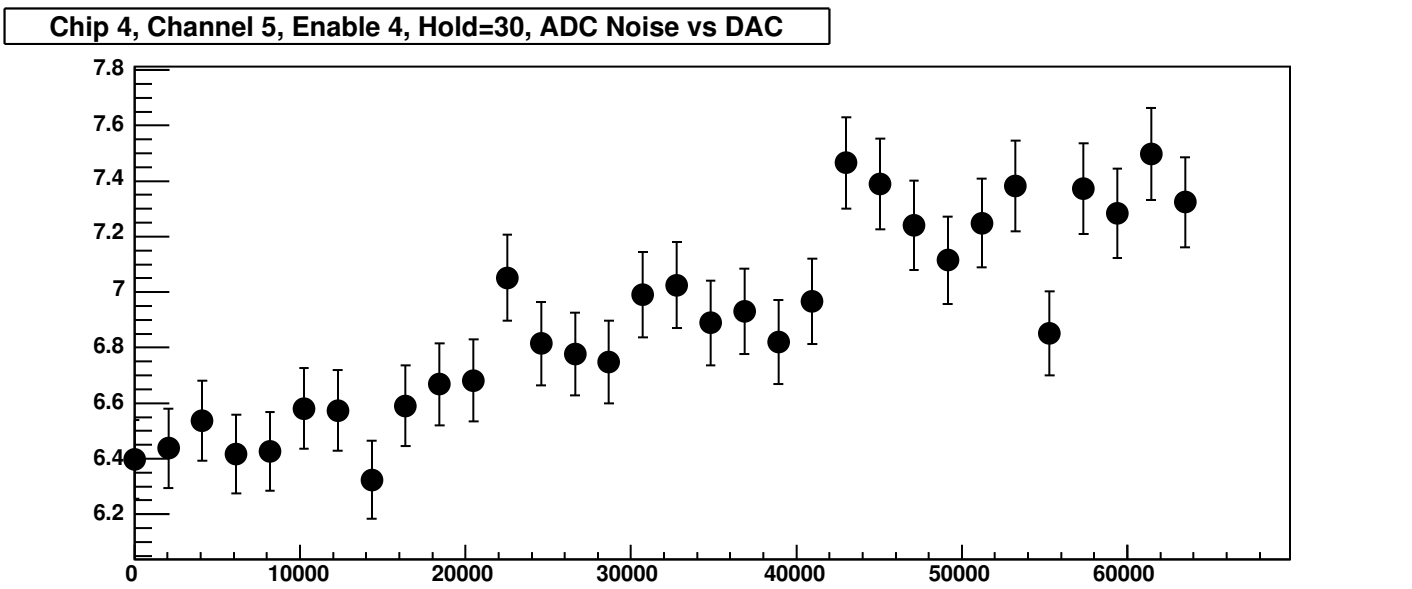


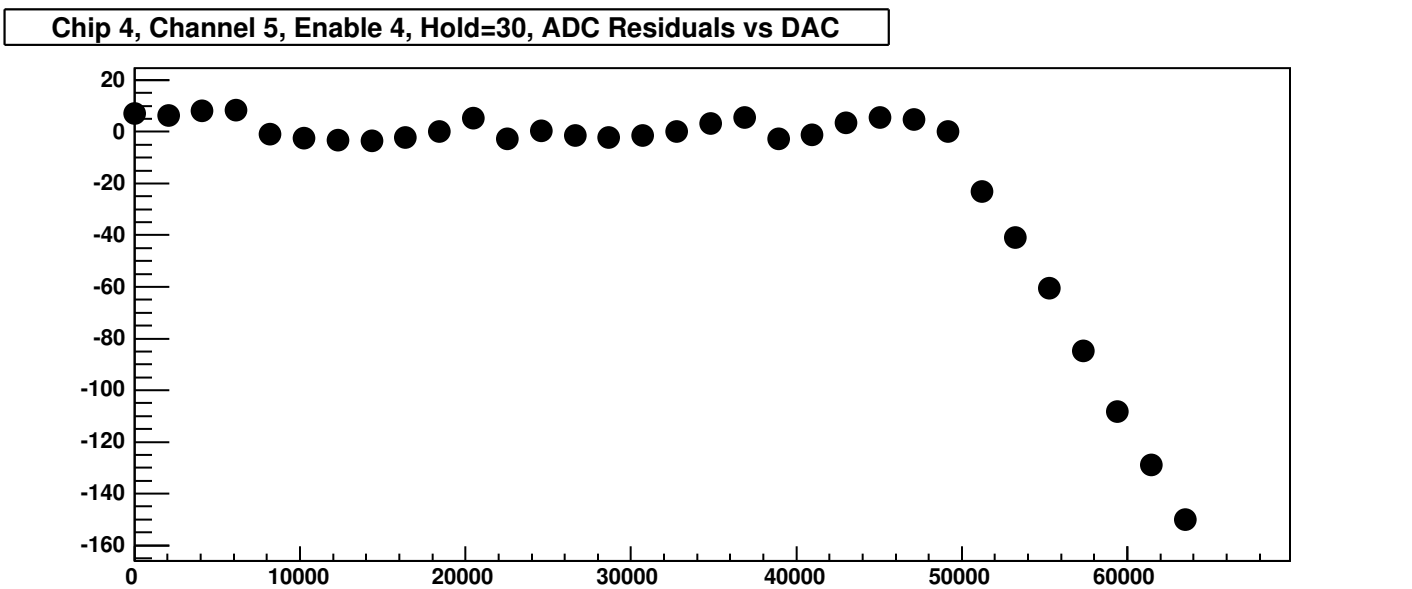

Chip 4, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4048 / 15
p0 945 ± 0.1174
p1 -0.000116 ± 4.751e-06
960
940
920
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

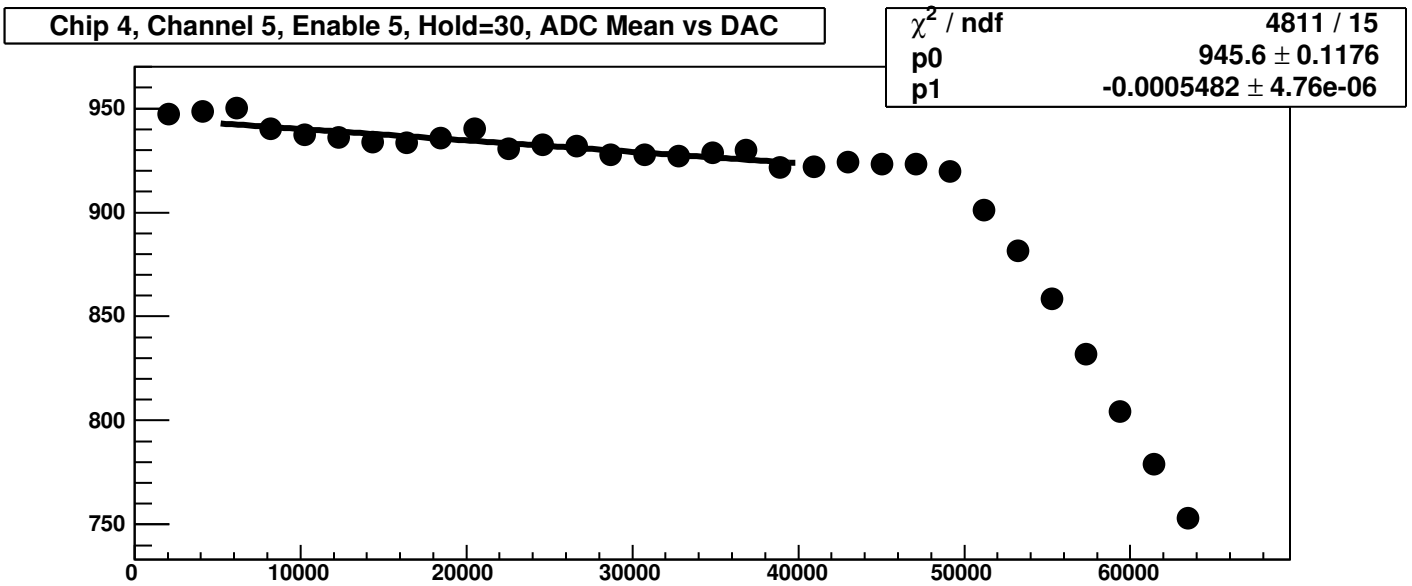

Chip 4, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
6.7
6.6
6.5
6.4
6.3
6.2
6.1
6
5.9
0
10000
20000
30000
40000
50000
60000

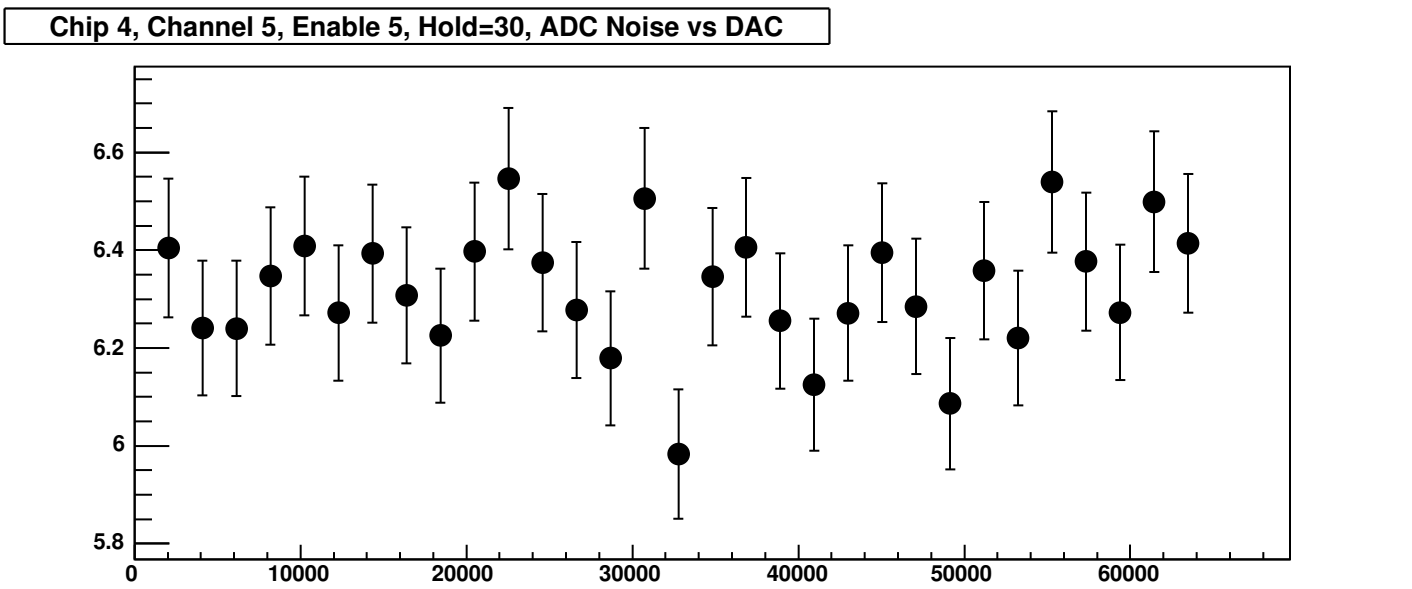

Chip 4, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

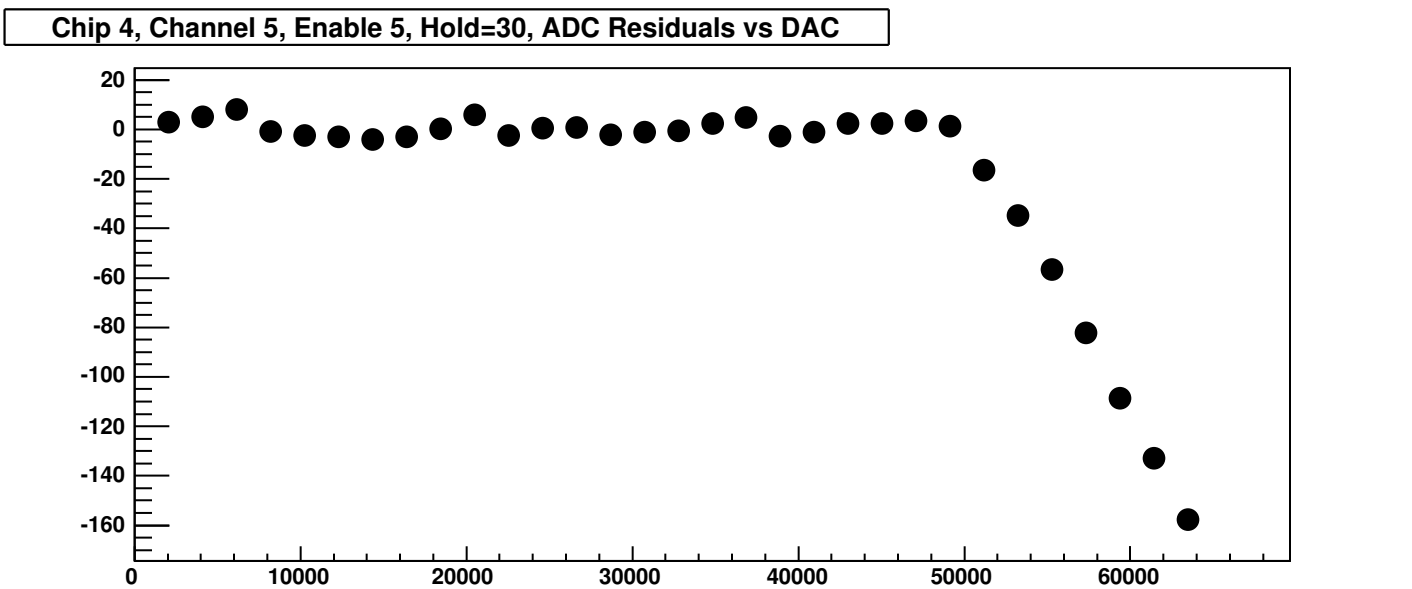

Chip 4, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4661 / 15
p0 941.5 ± 0.1222
p1 0.0005162 ± 5.047e-06
980
960
940
920
900
880
860
840
820
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

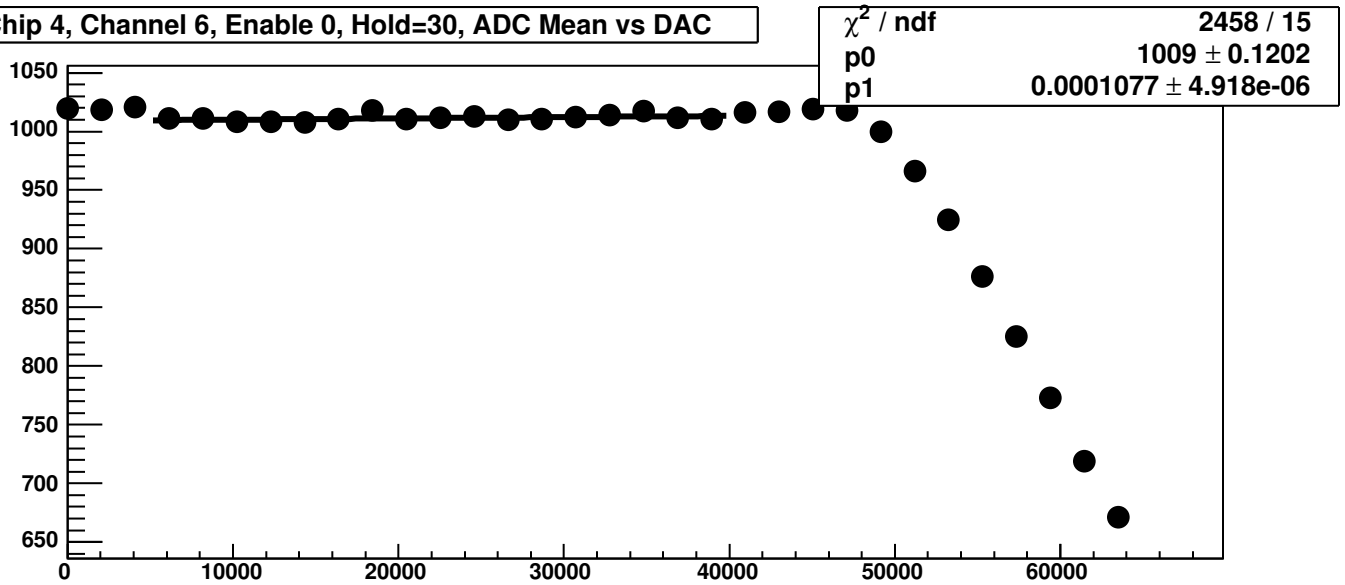

Chip 4, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4811 / 15
p0 945.6 ± 0.1176
p1 -0.0005482 ± 4.76e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

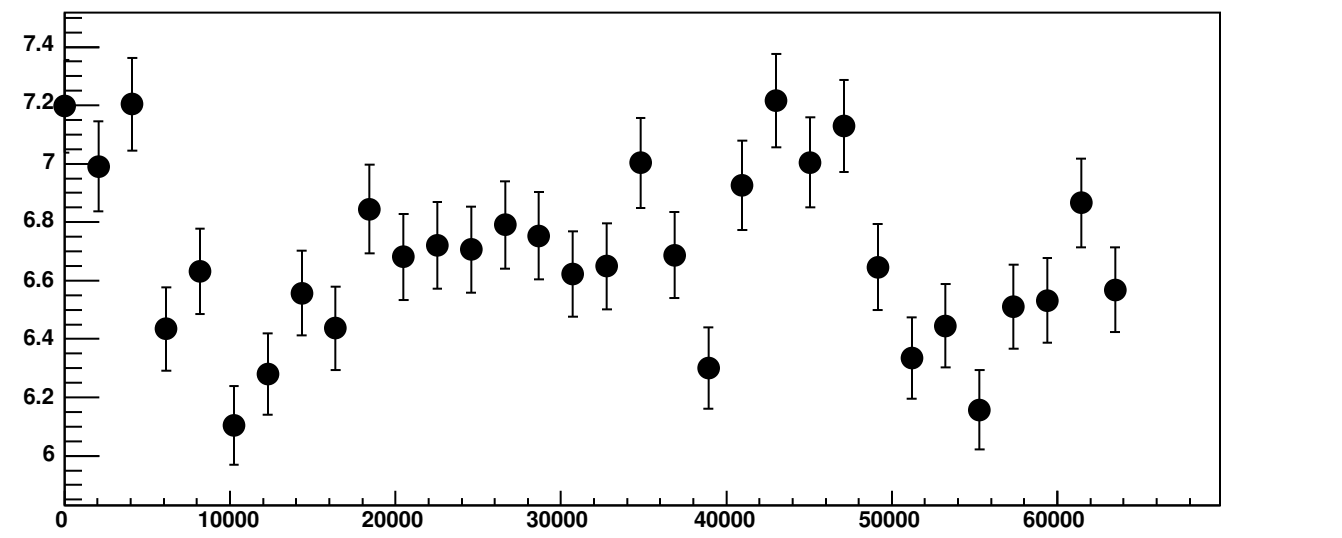

Chip 4, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

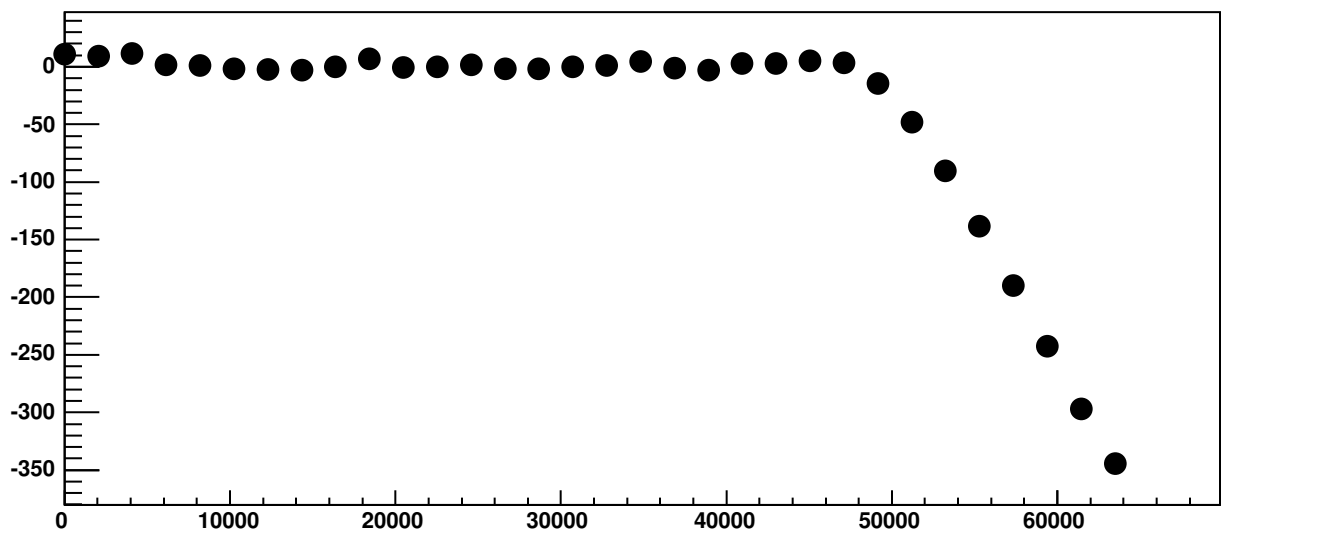
Chip 4, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC

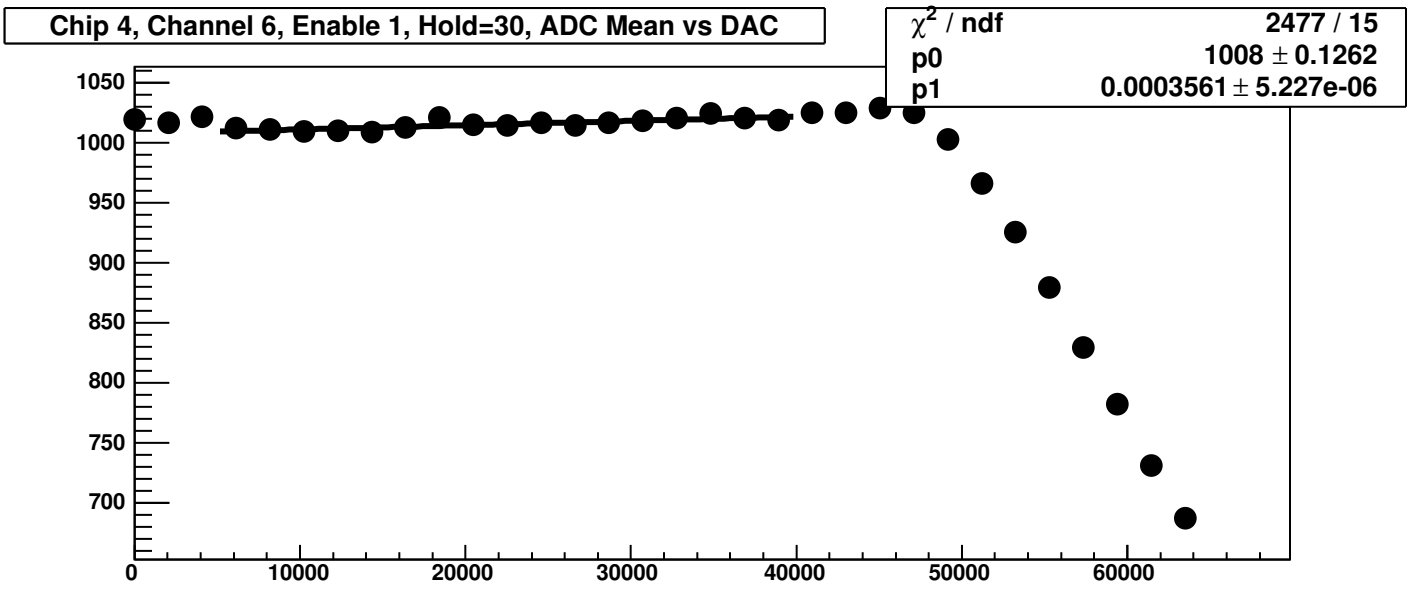


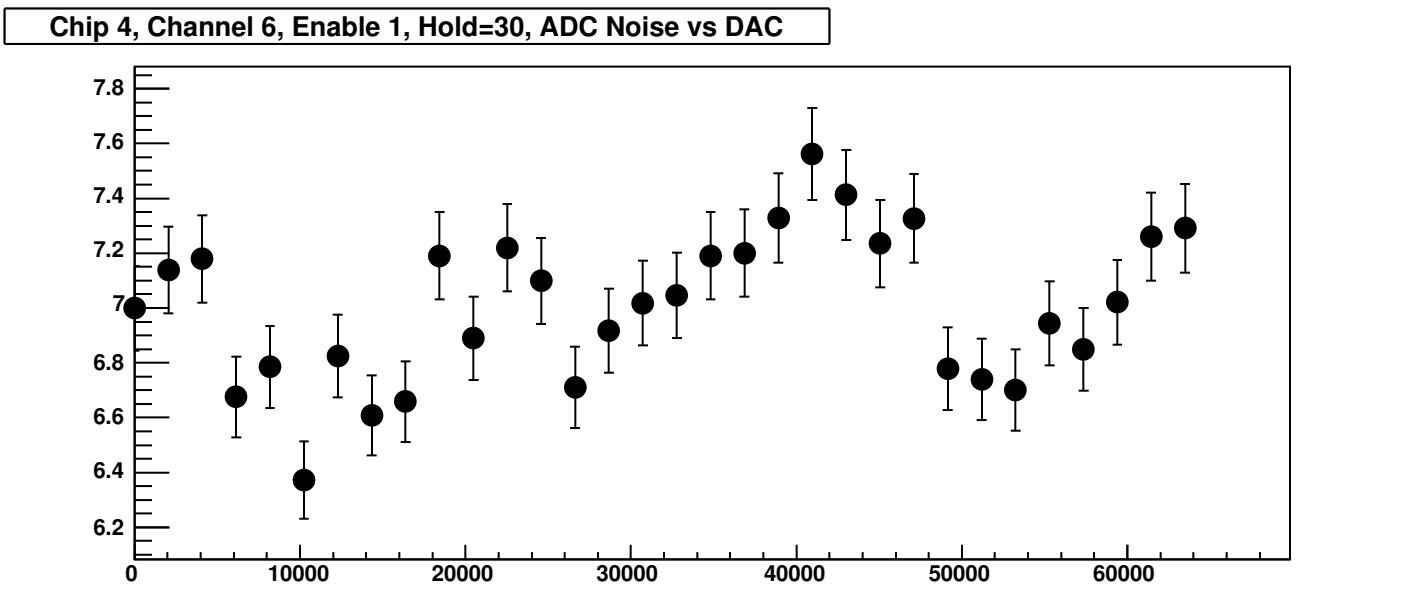
Chip 4, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC

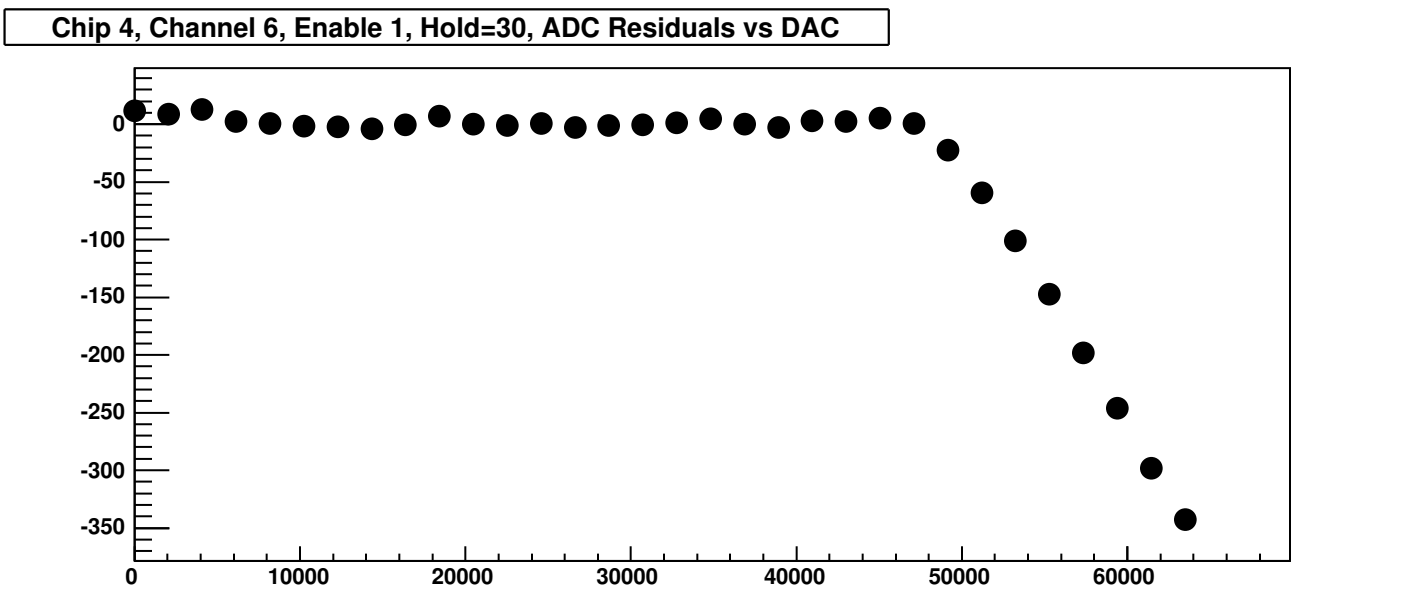


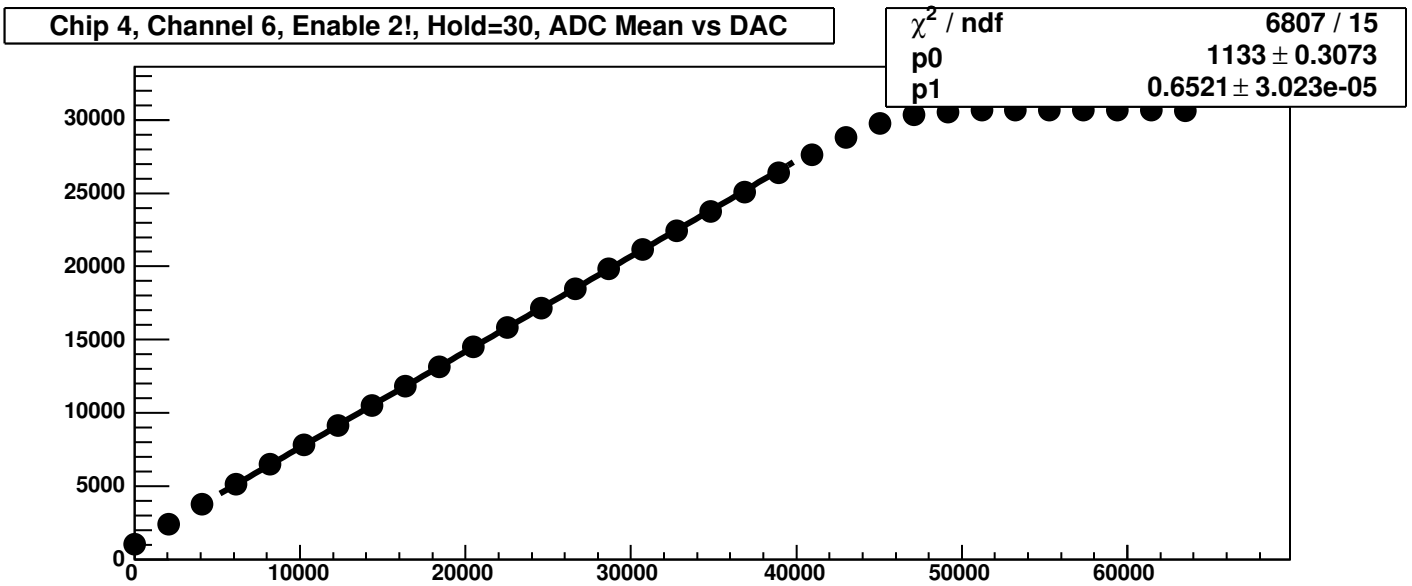
Chip 4, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

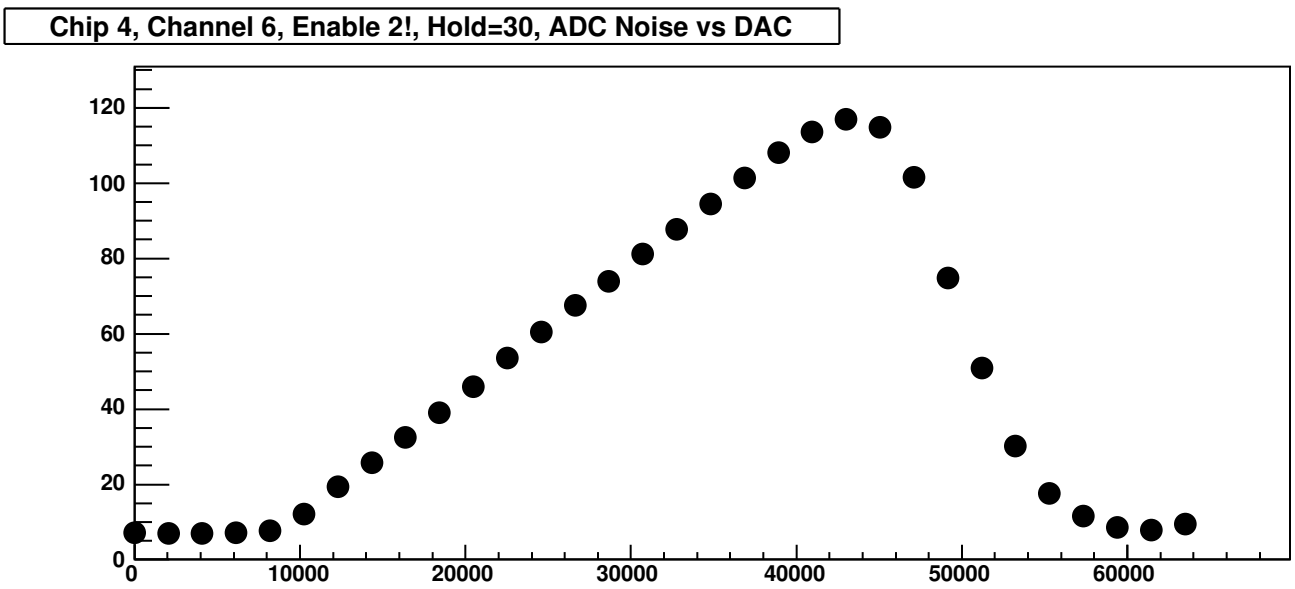


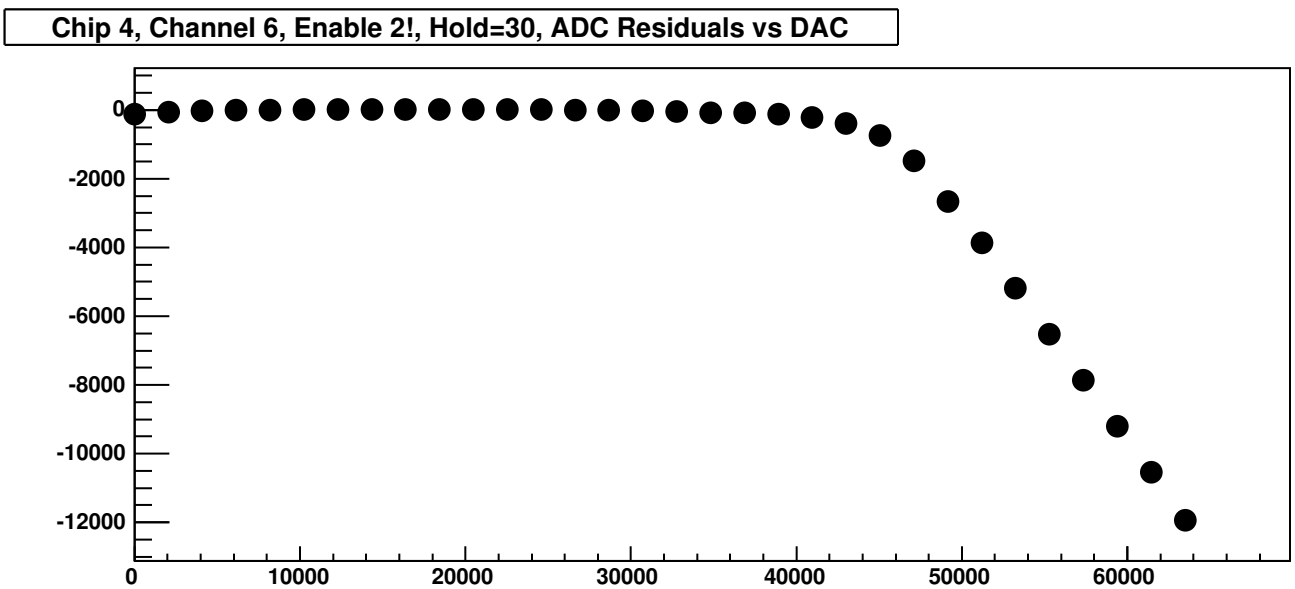

Chip 4, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2477 / 15
p0 1008 ± 0.1262
p1 0.0003561 ± 5.227e-06
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

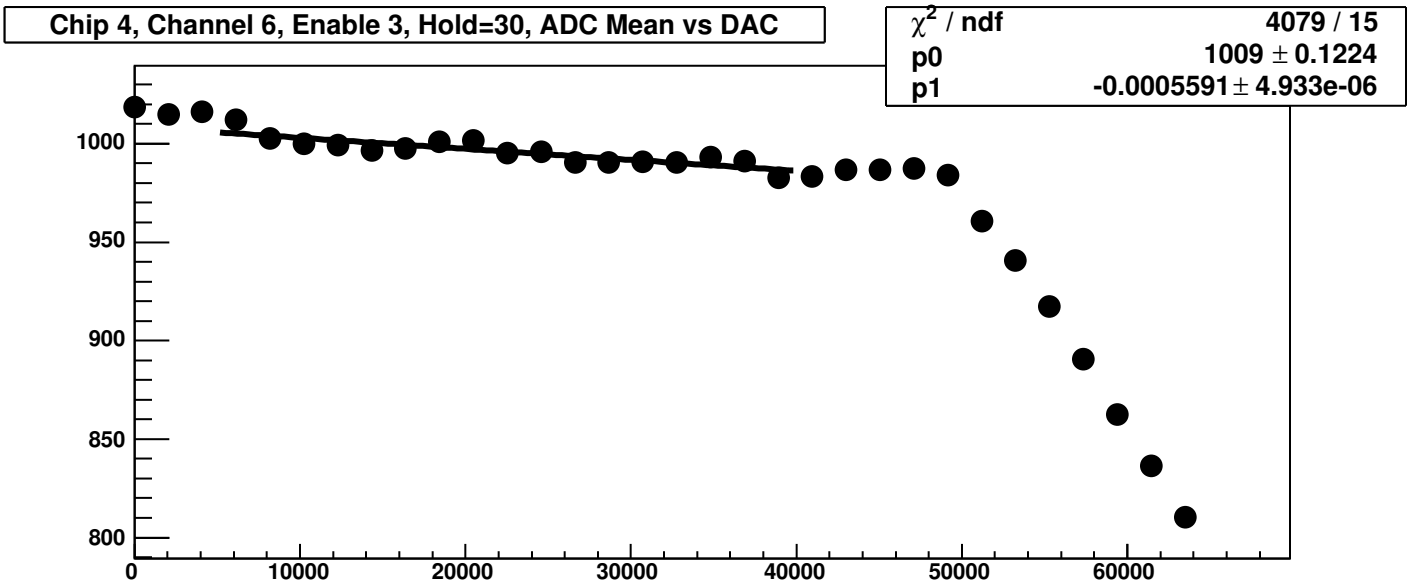

Chip 4, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

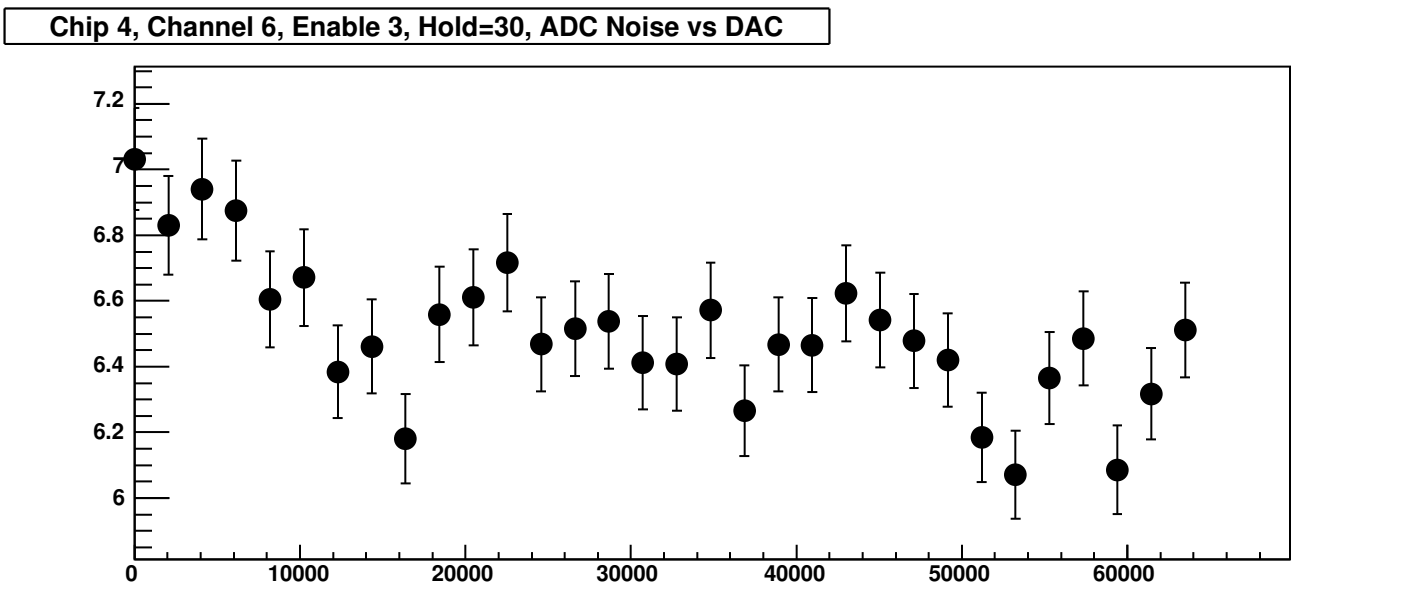

Chip 4, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

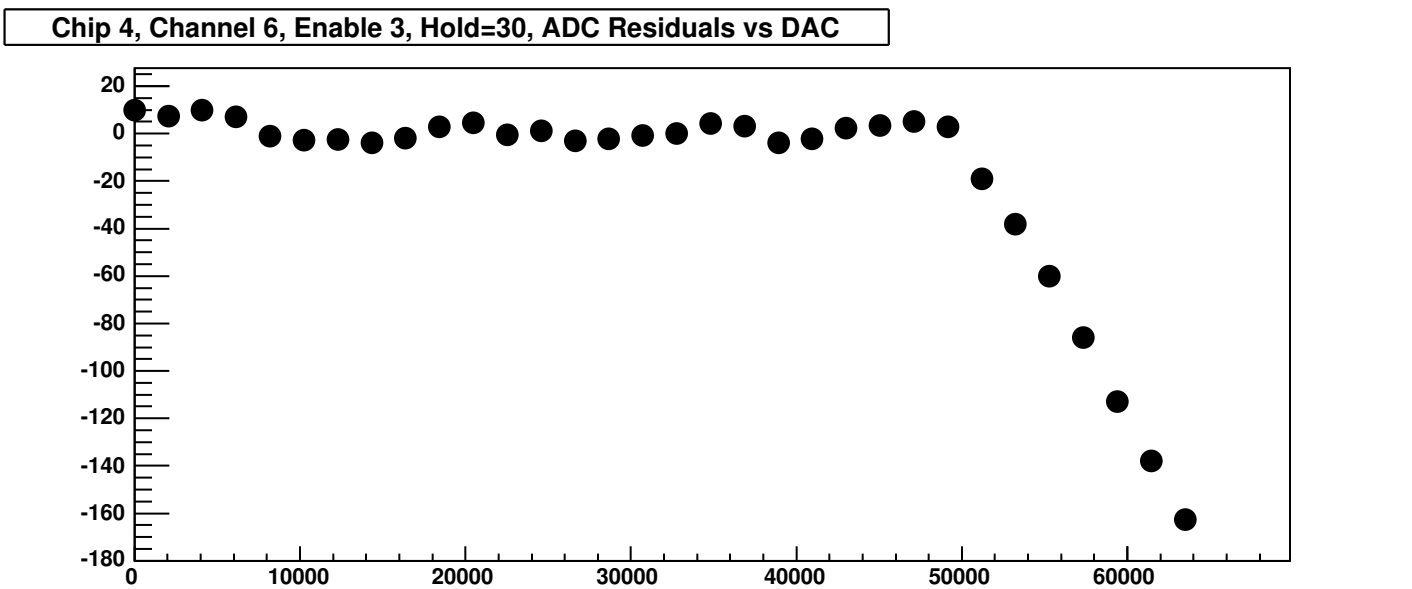

Chip 4, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 6807 / 15
p0 1133 ± 0.3073
p1 0.6521 ± 3.023e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

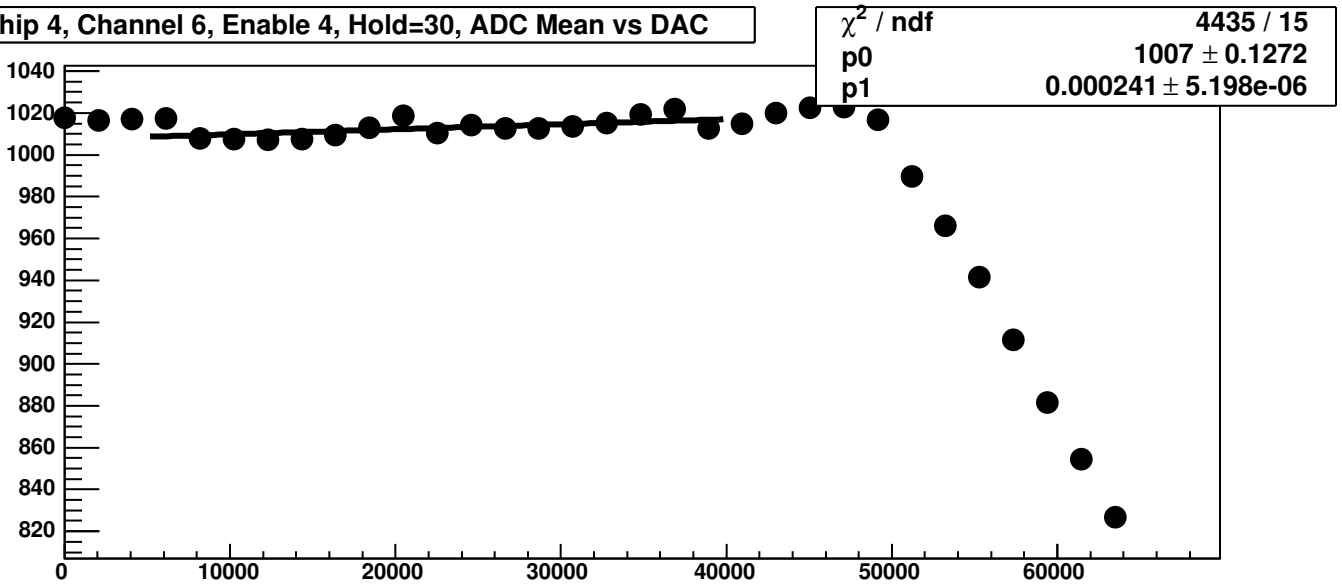

Chip 4, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4079 / 15
p0 1009 ± 0.1224
p1 -0.0005591 ± 4.933e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

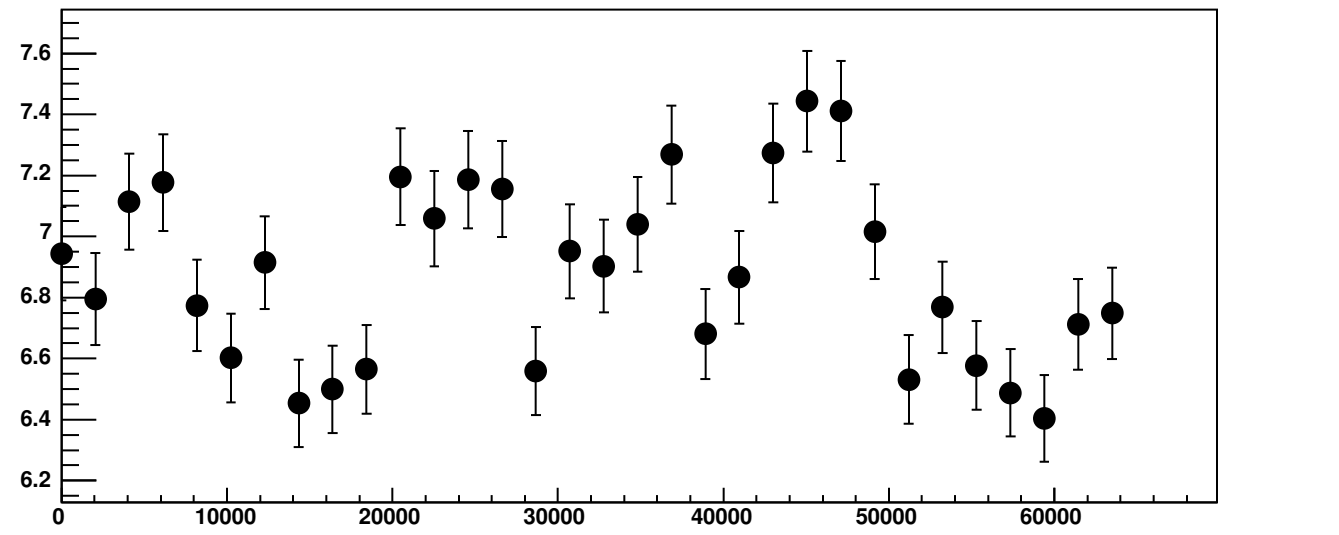

Chip 4, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

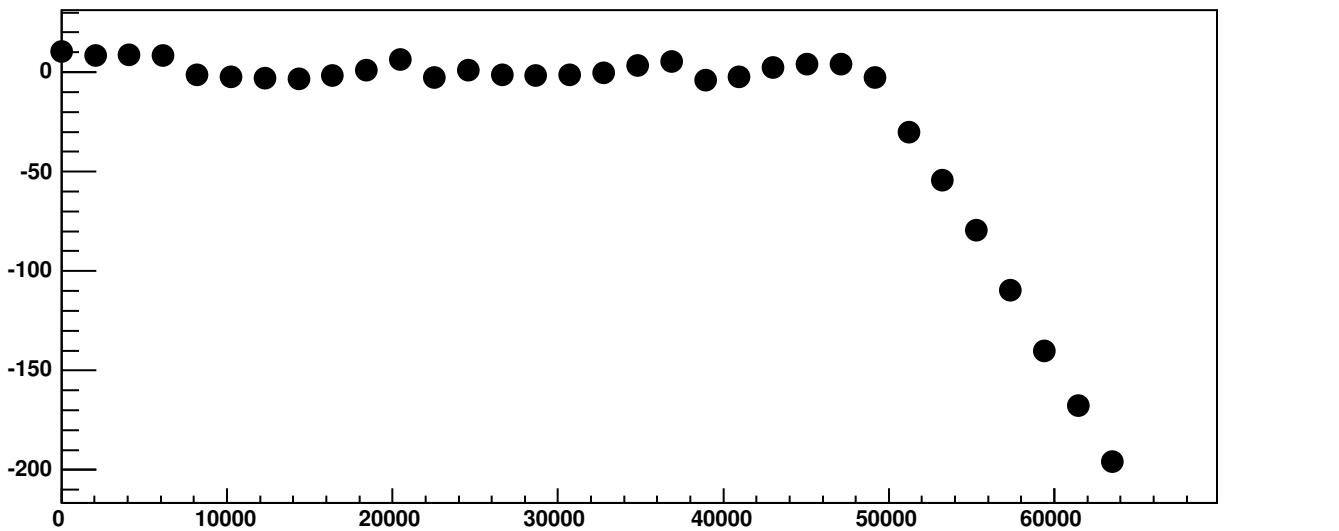
Chip 4, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC



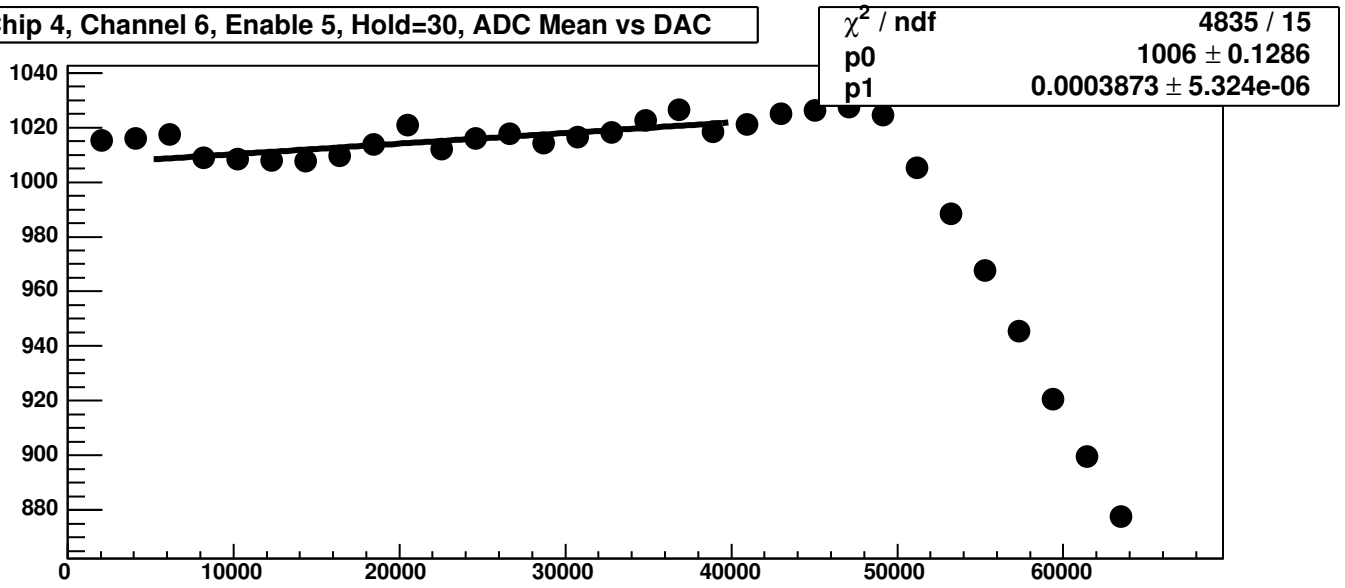
Chip 4, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



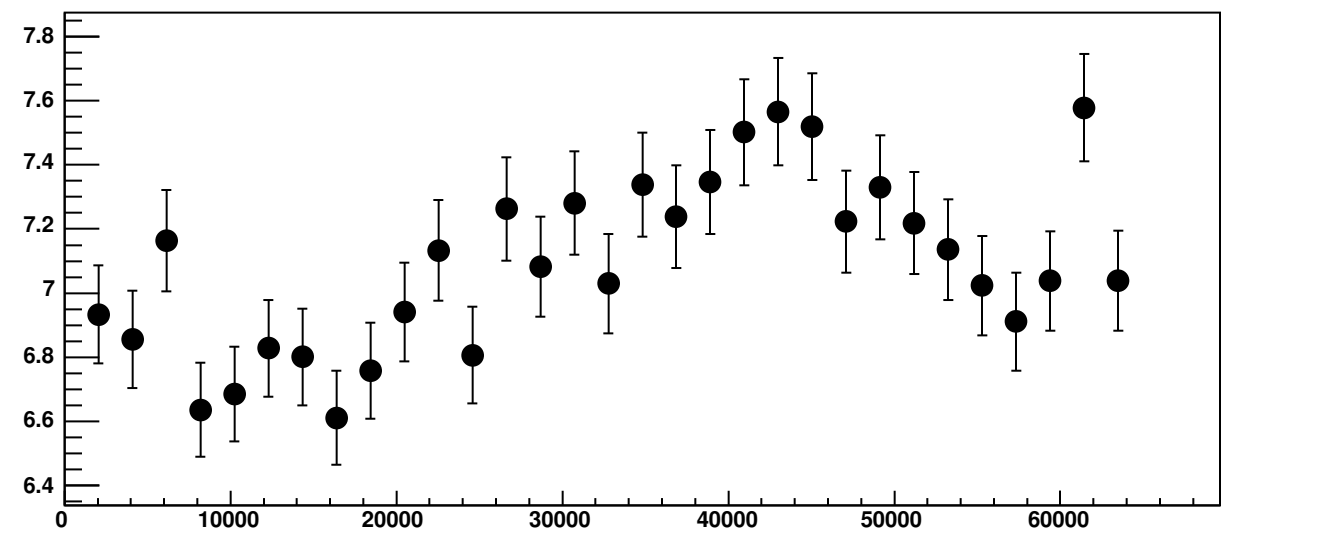
Chip 4, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC



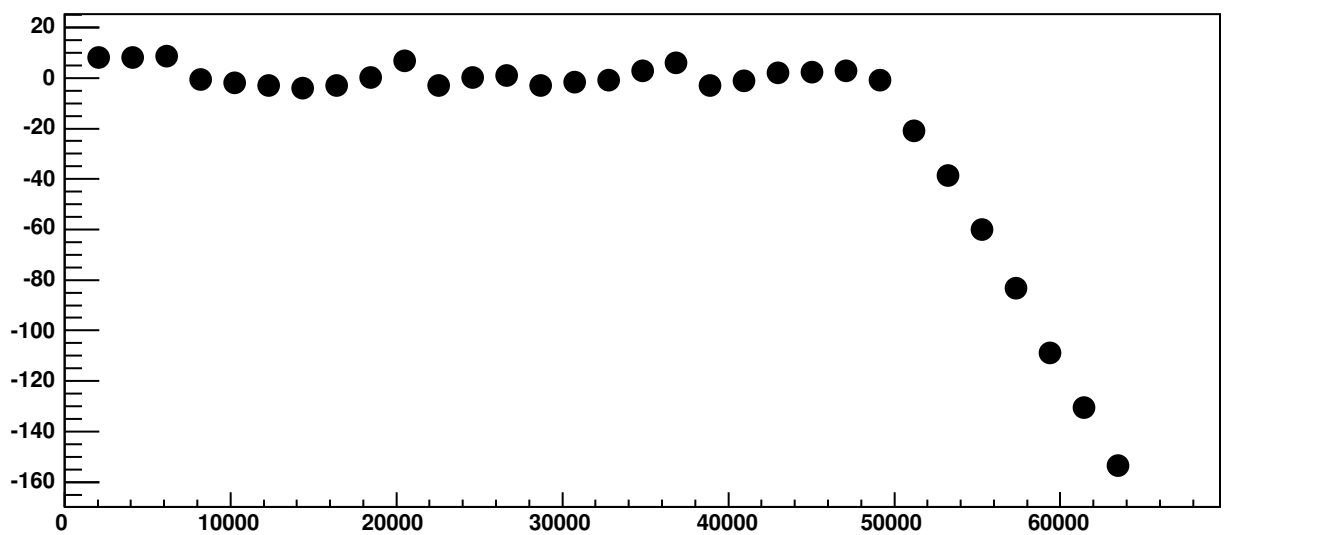
Chip 4, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC



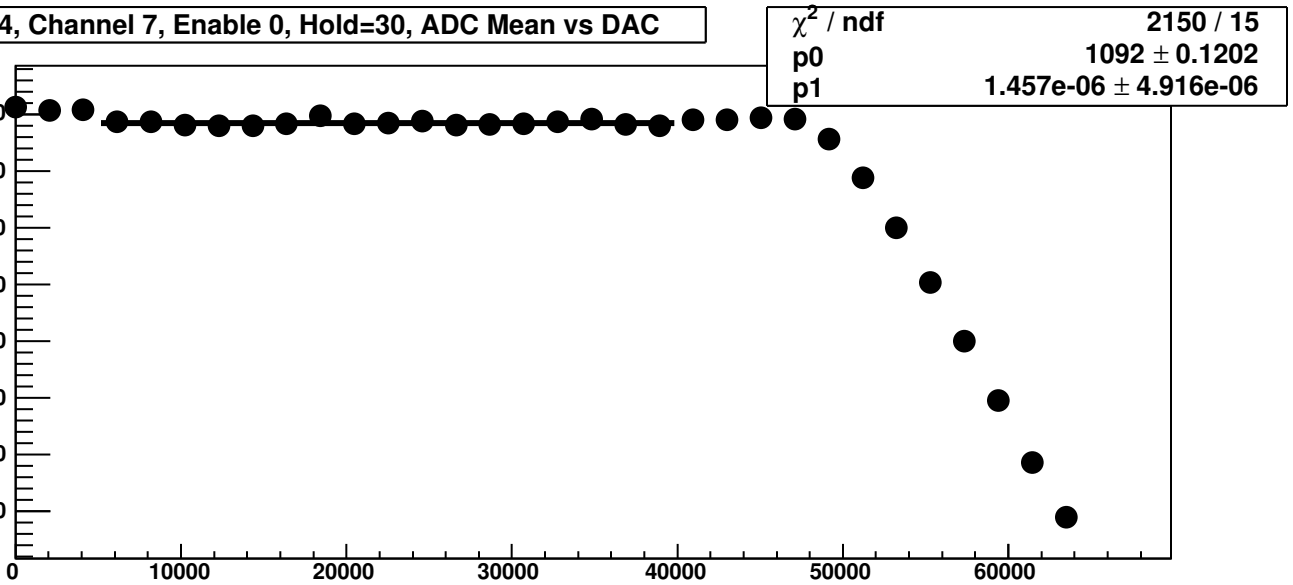
Chip 4, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC



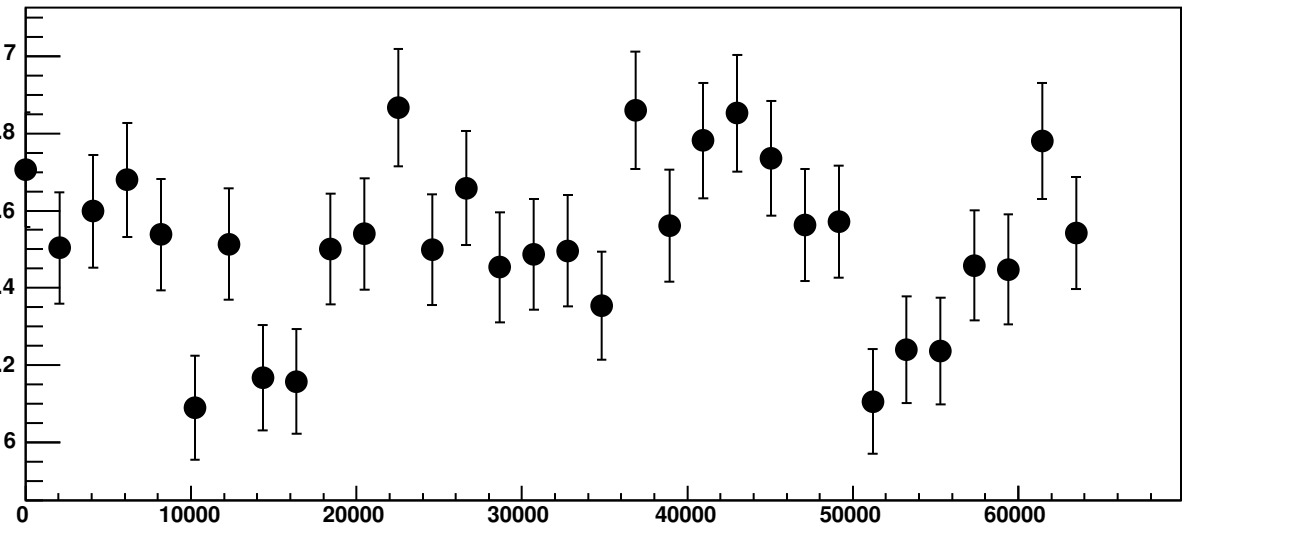
Chip 4, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC



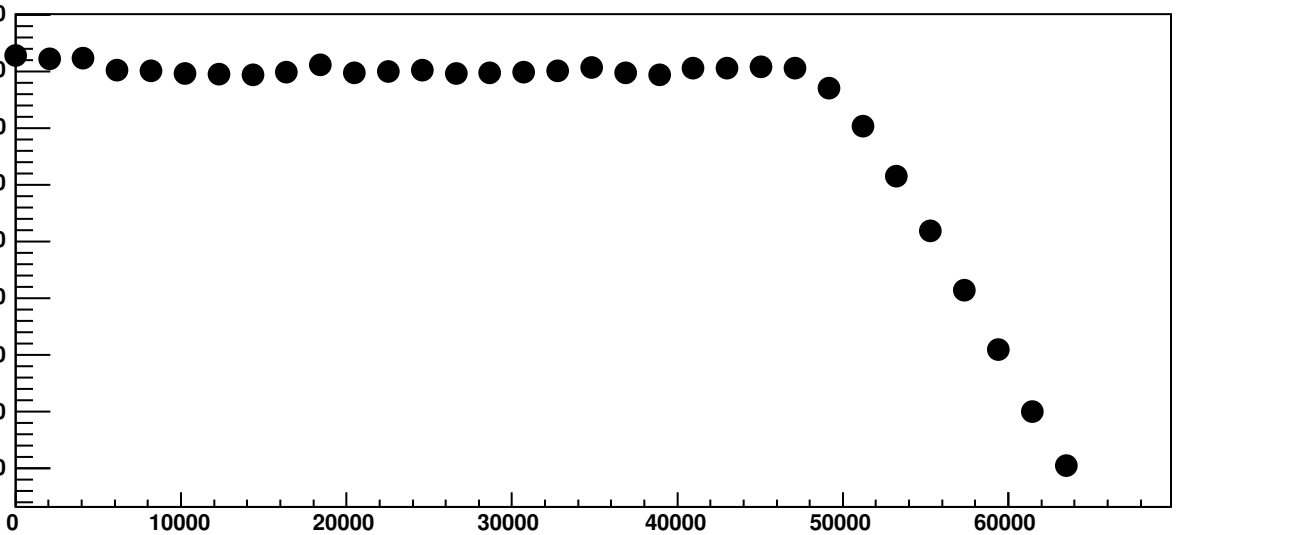
Chip 4, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC

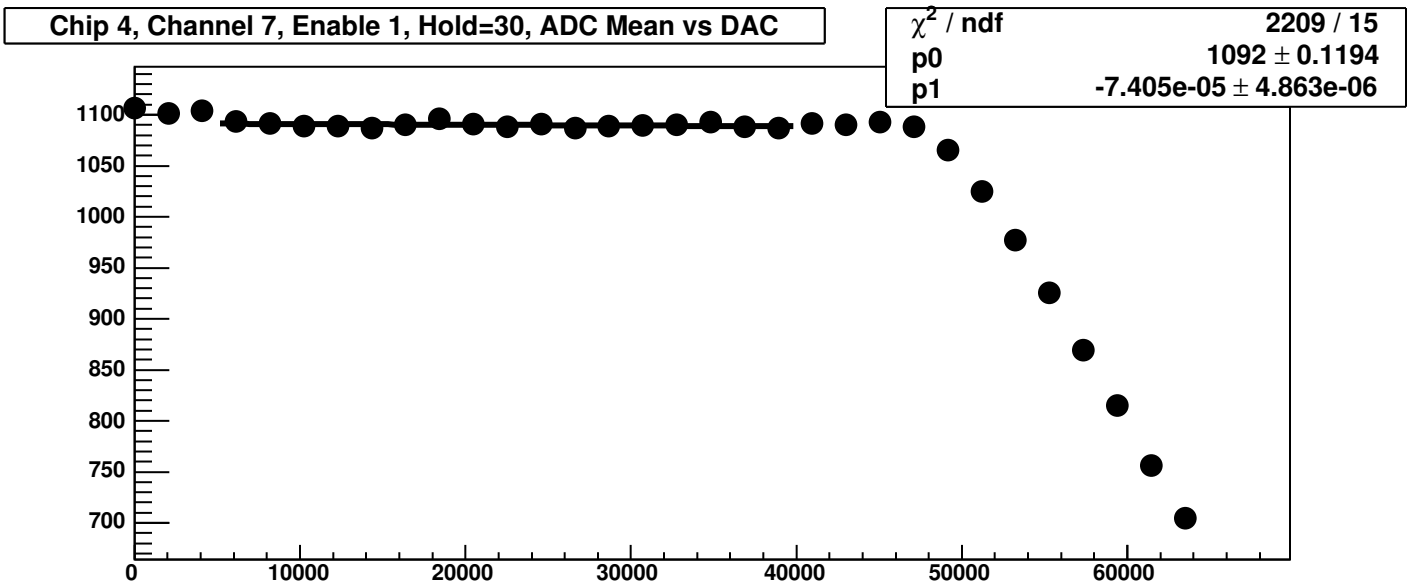


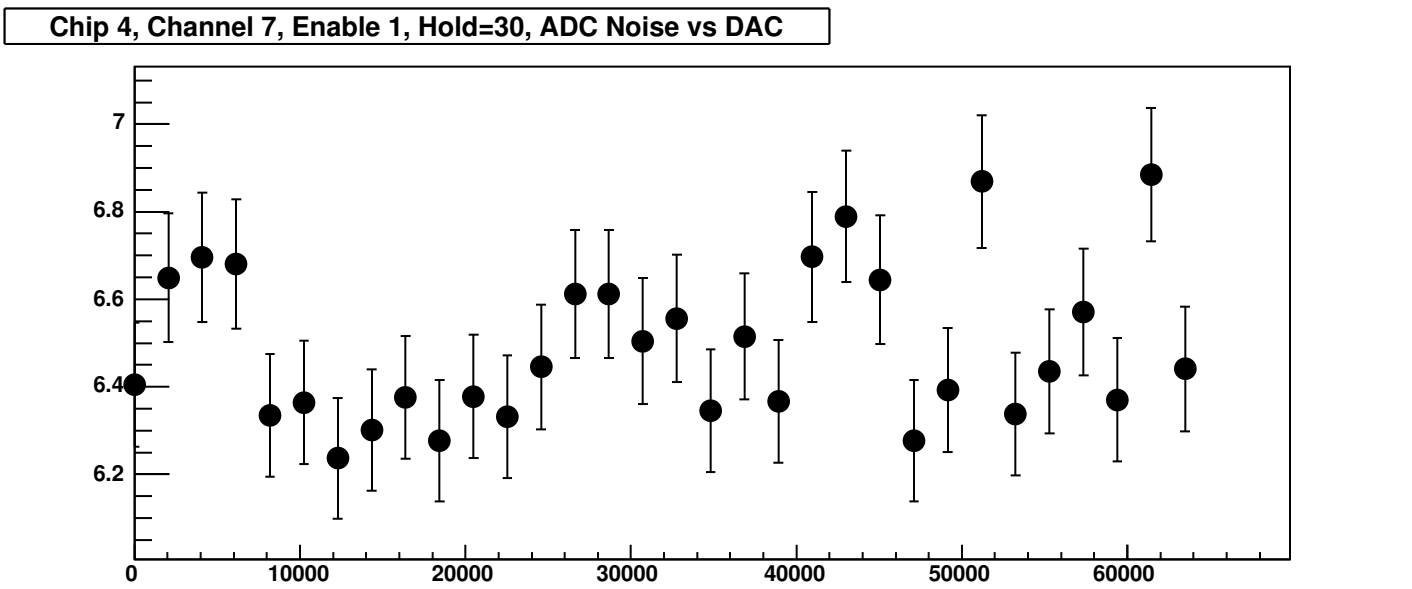
Chip 4, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC

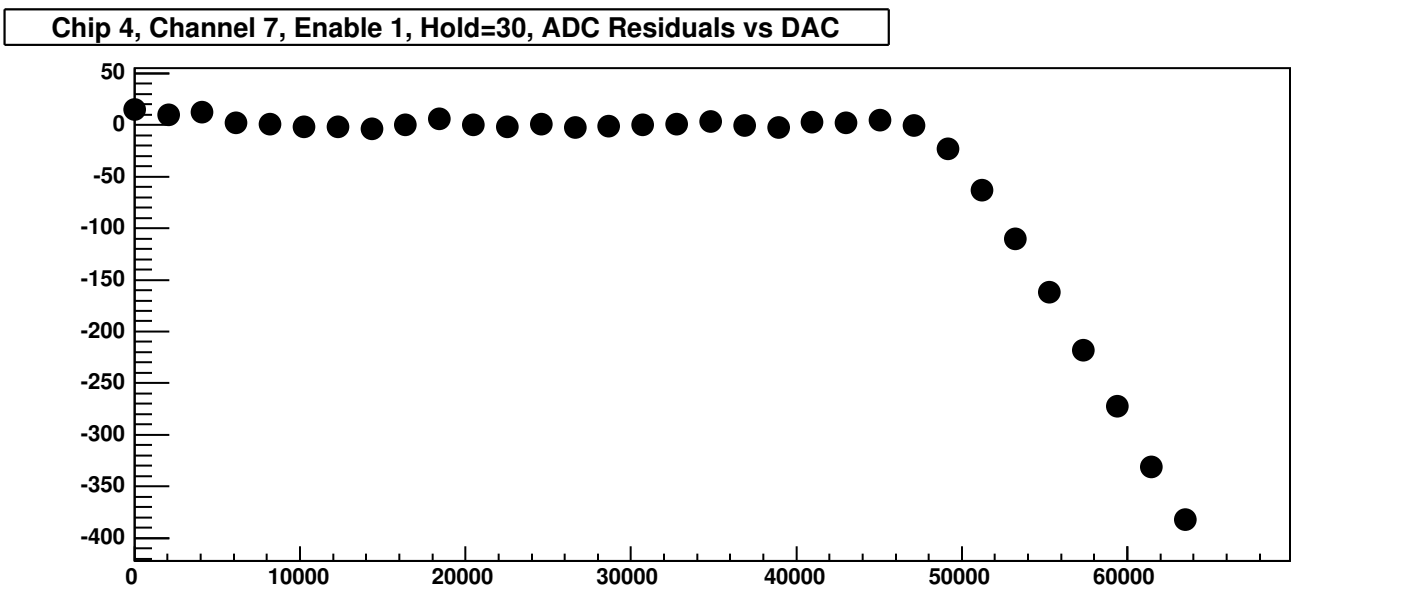


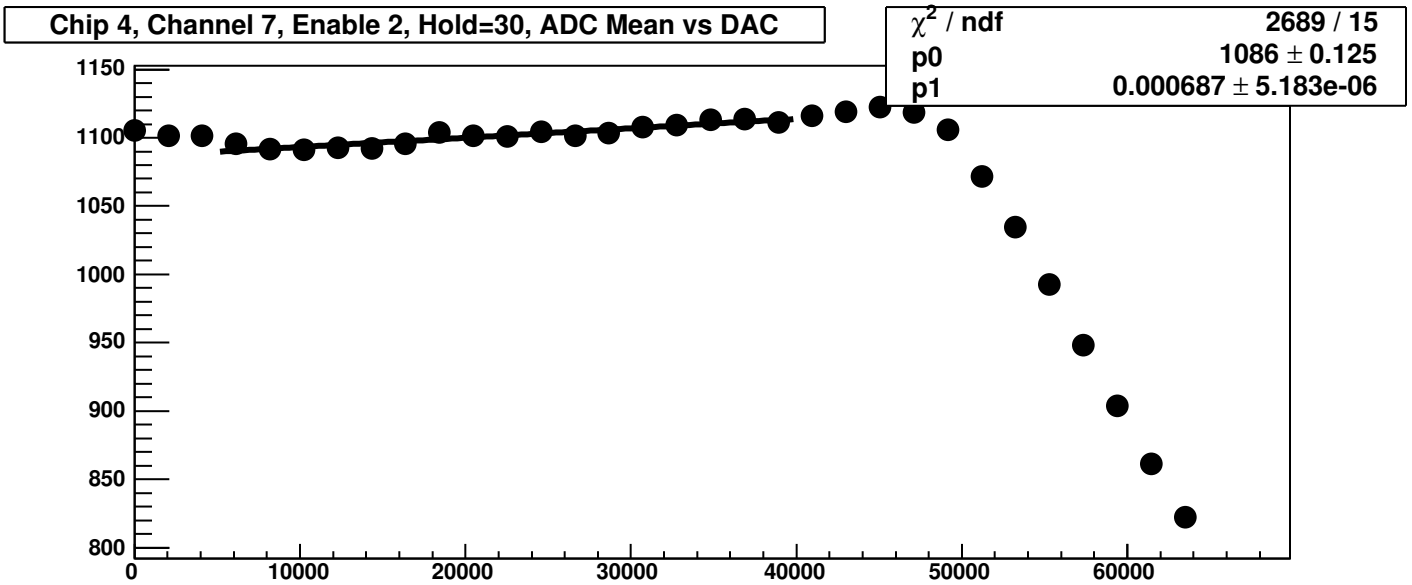
Chip 4, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

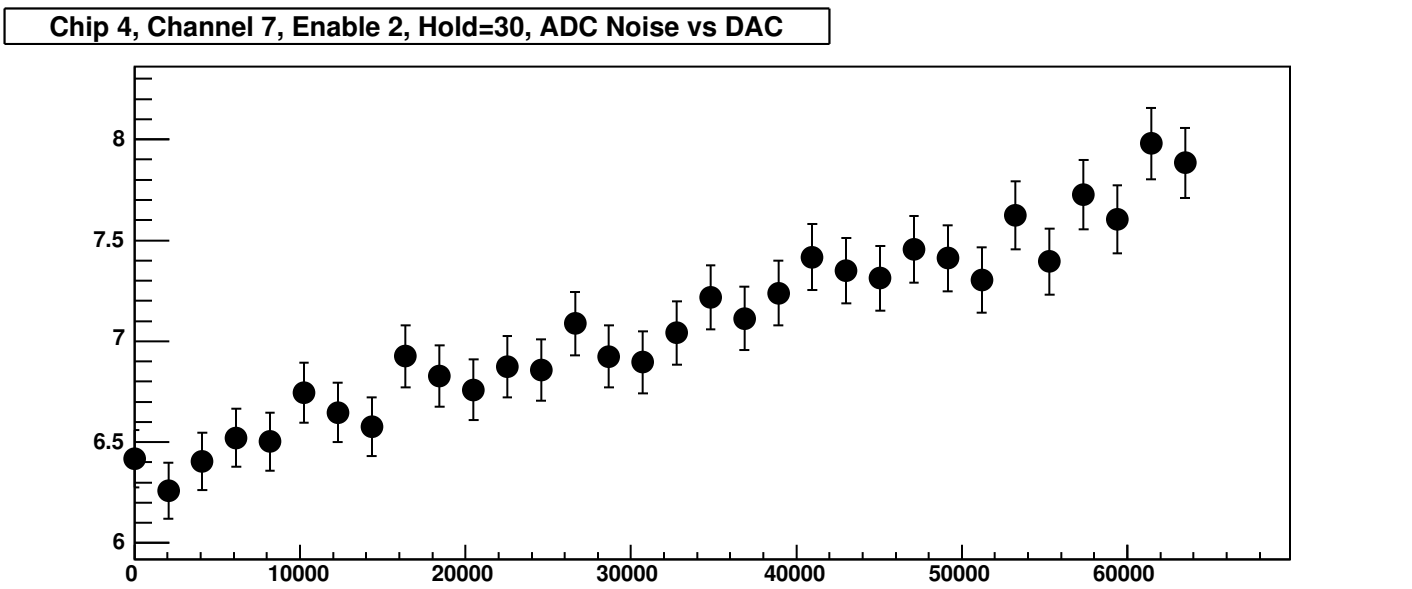


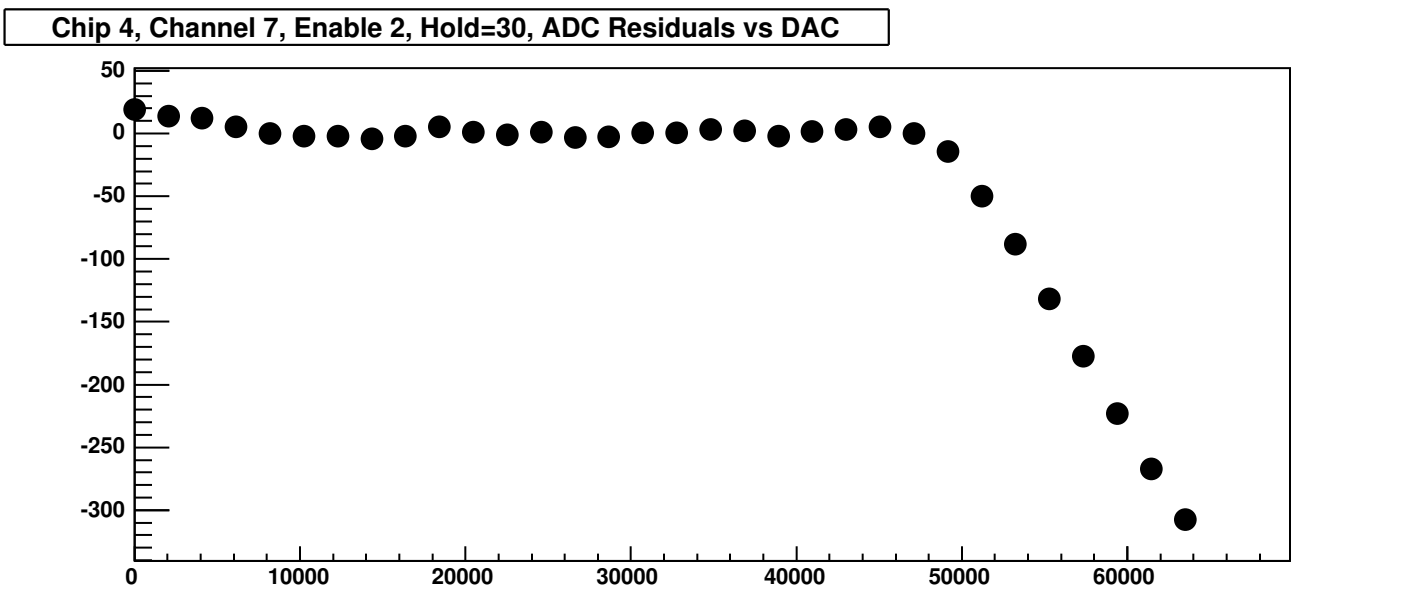

Chip 4, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2209 / 15
p0 1092 ± 0.1194
p1 -7.405e-05 ± 4.863e-06
1100
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

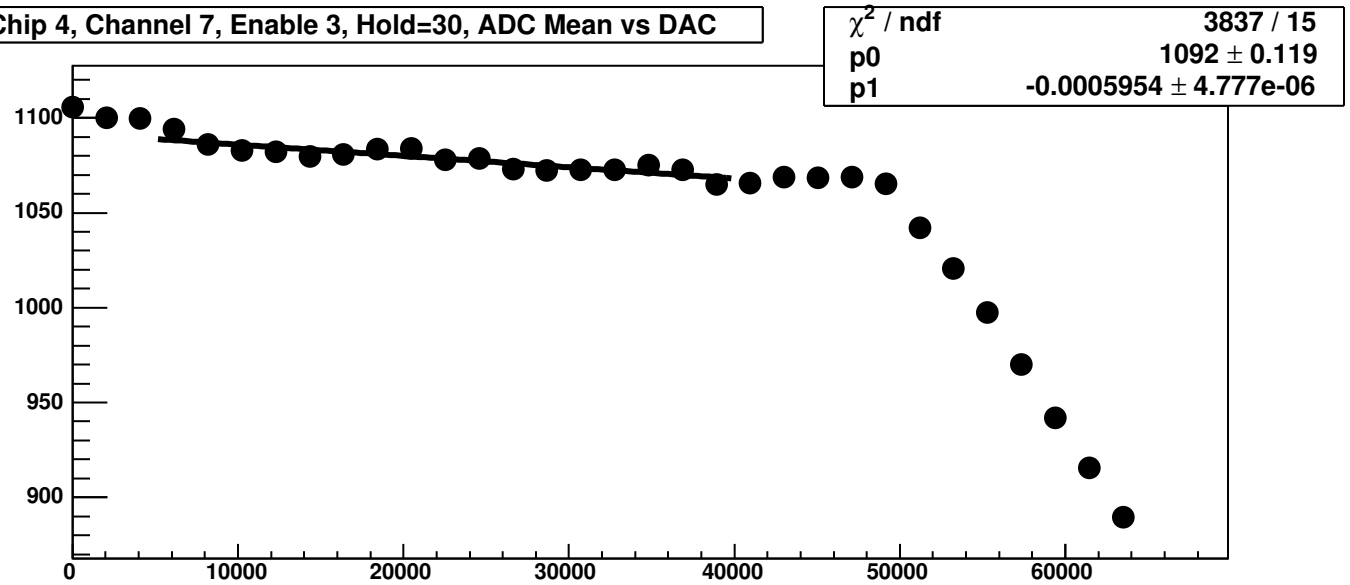

Chip 4, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2689 / 15
p0 1086 ± 0.125
p1 0.000687 ± 5.183e-06
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

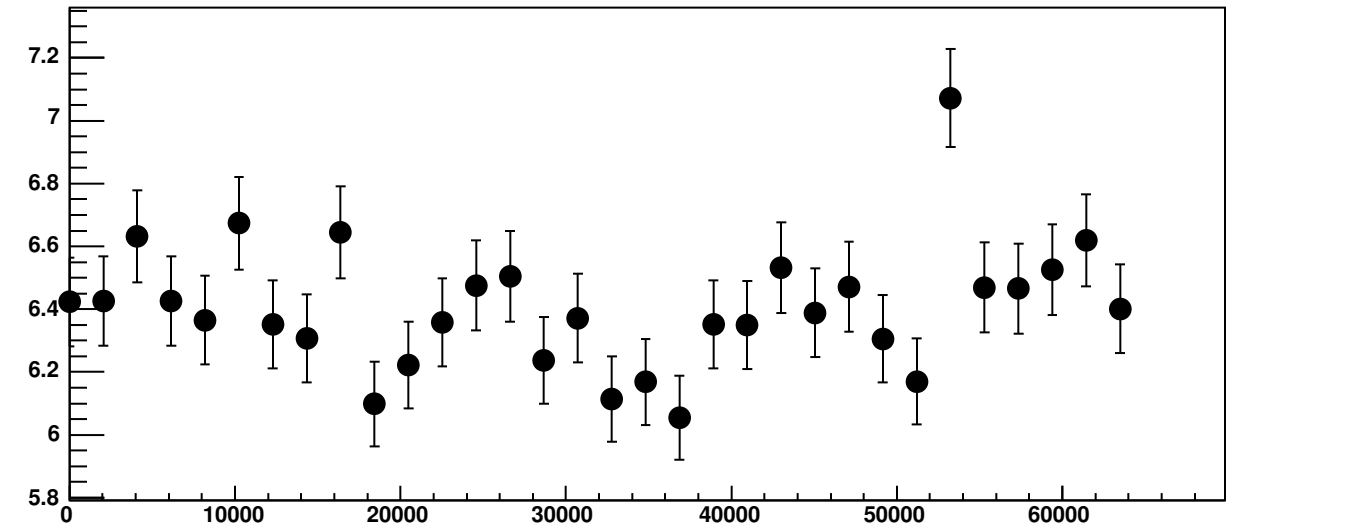

Chip 4, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC
8
7.5
7
6.5
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

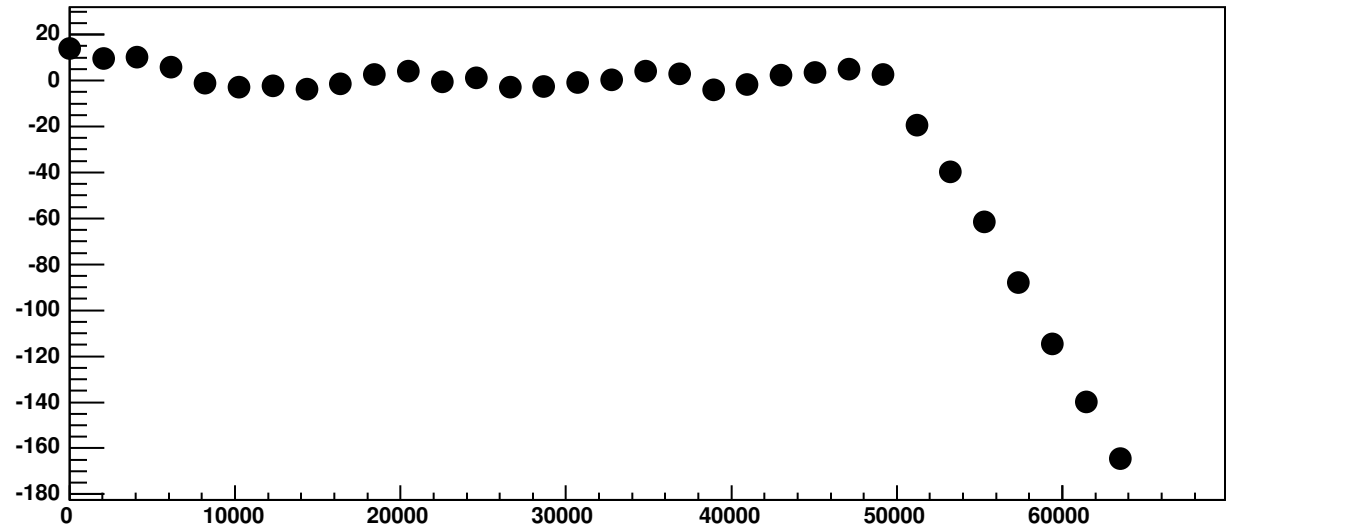
Chip 4, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC



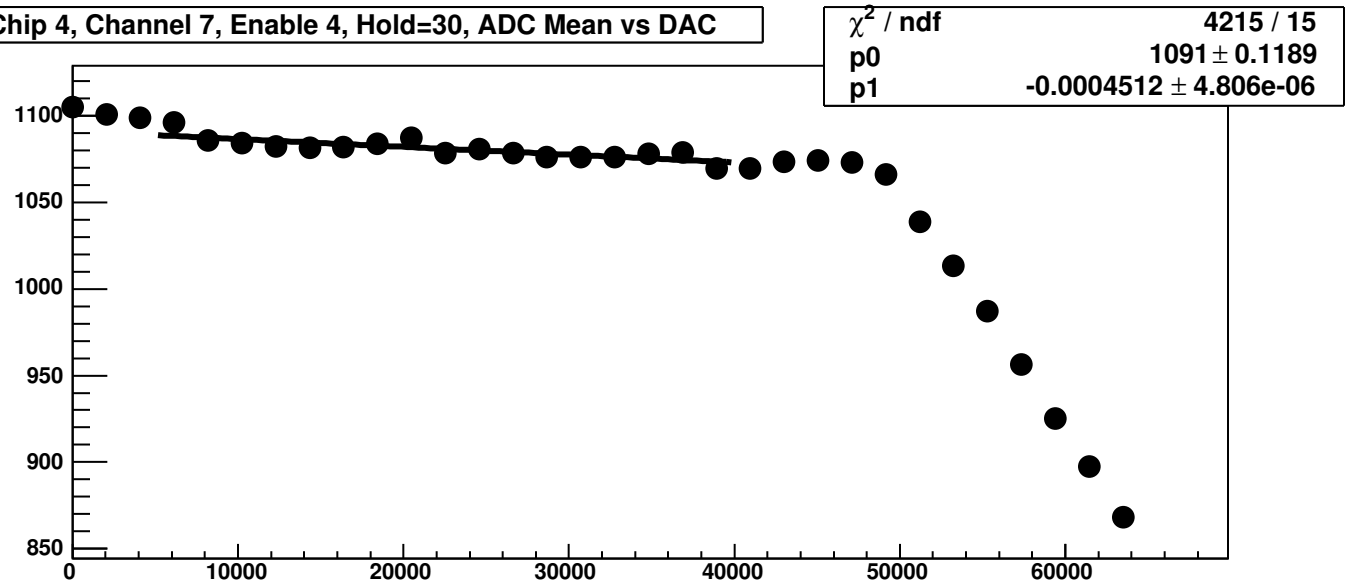
Chip 4, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC



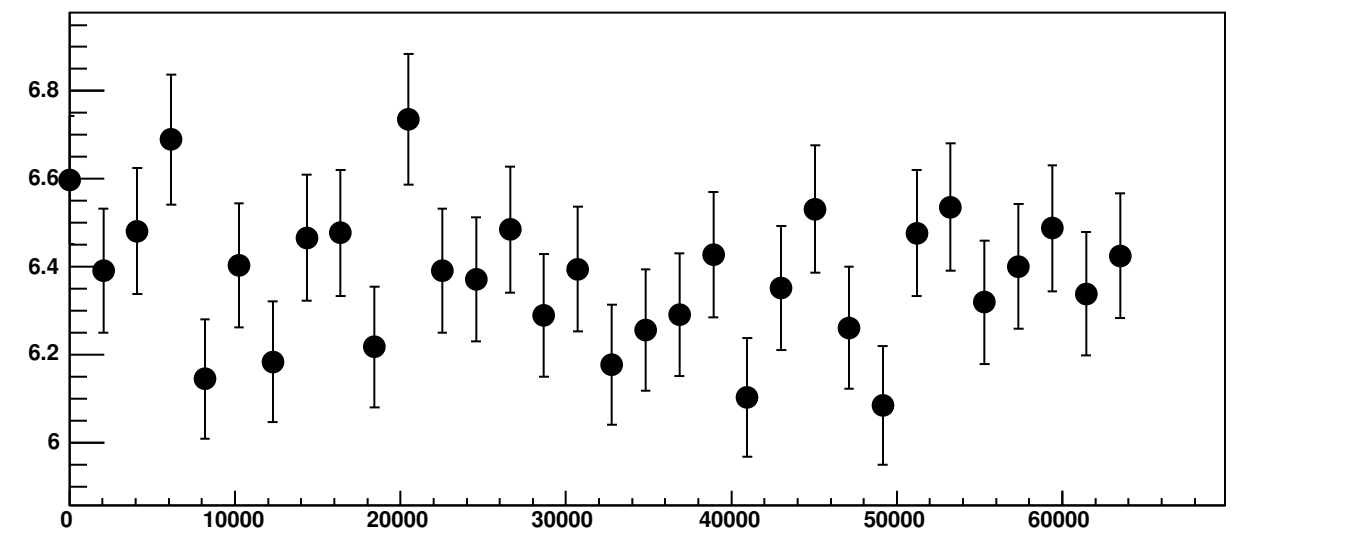
Chip 4, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC



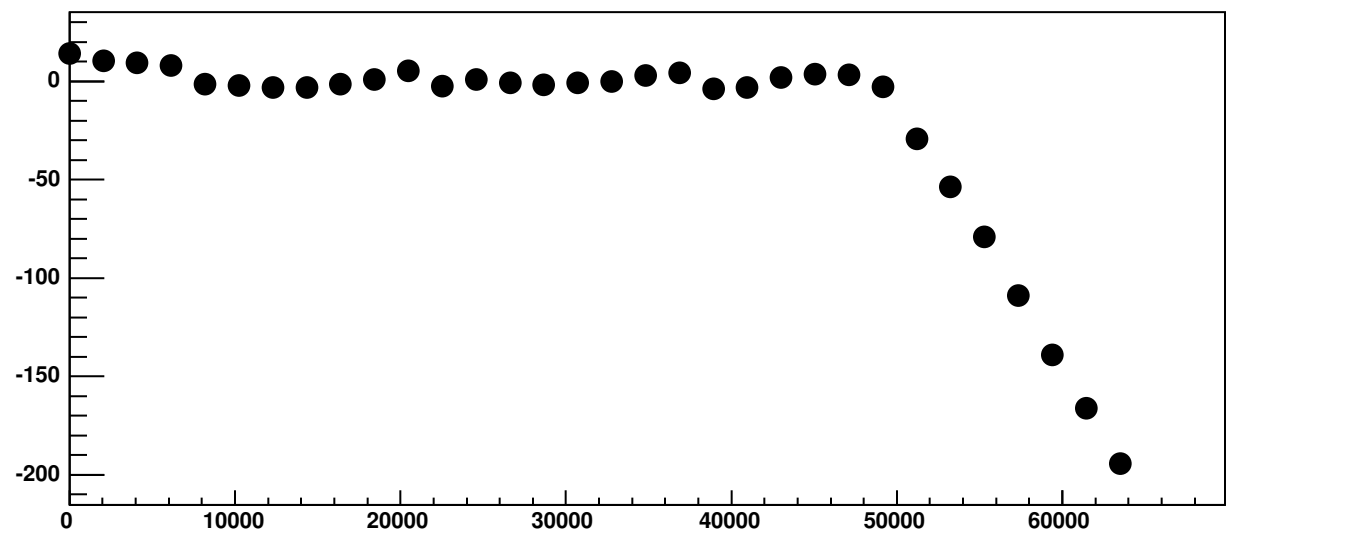
Chip 4, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC

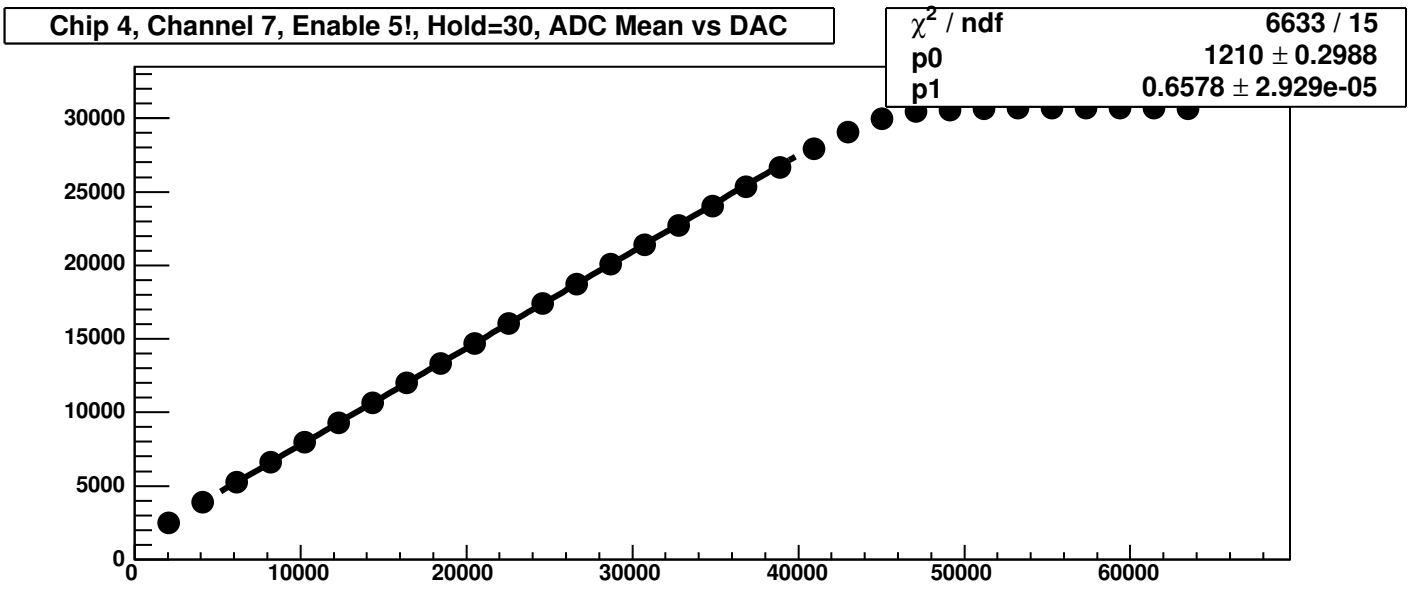


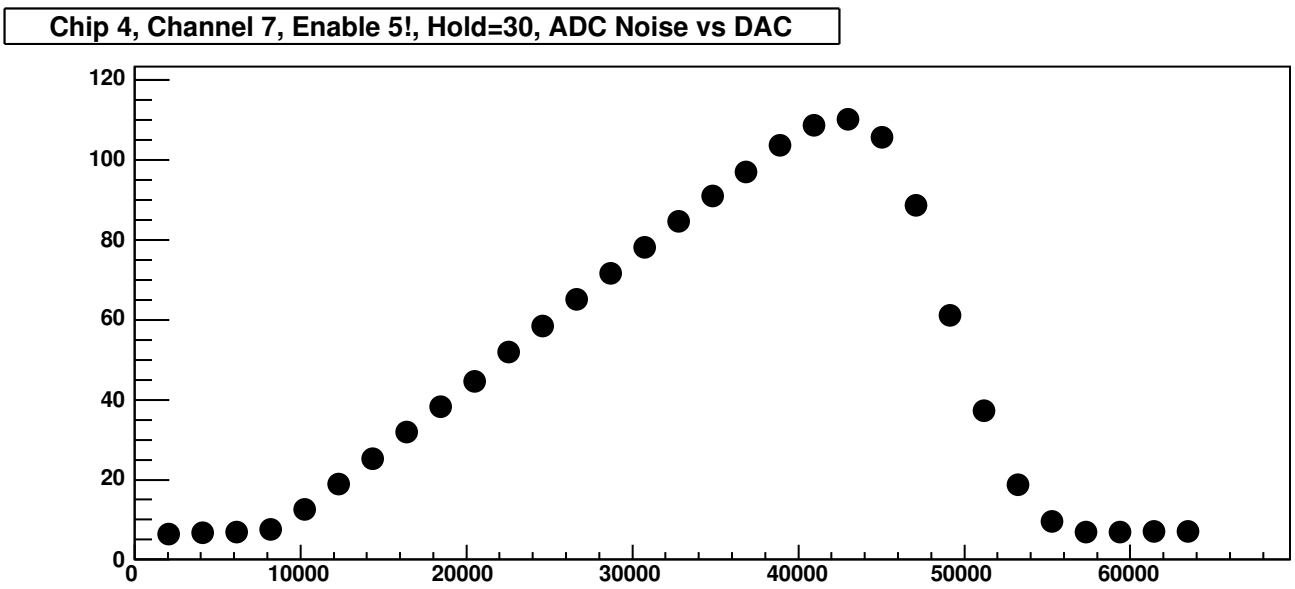
Chip 4, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC

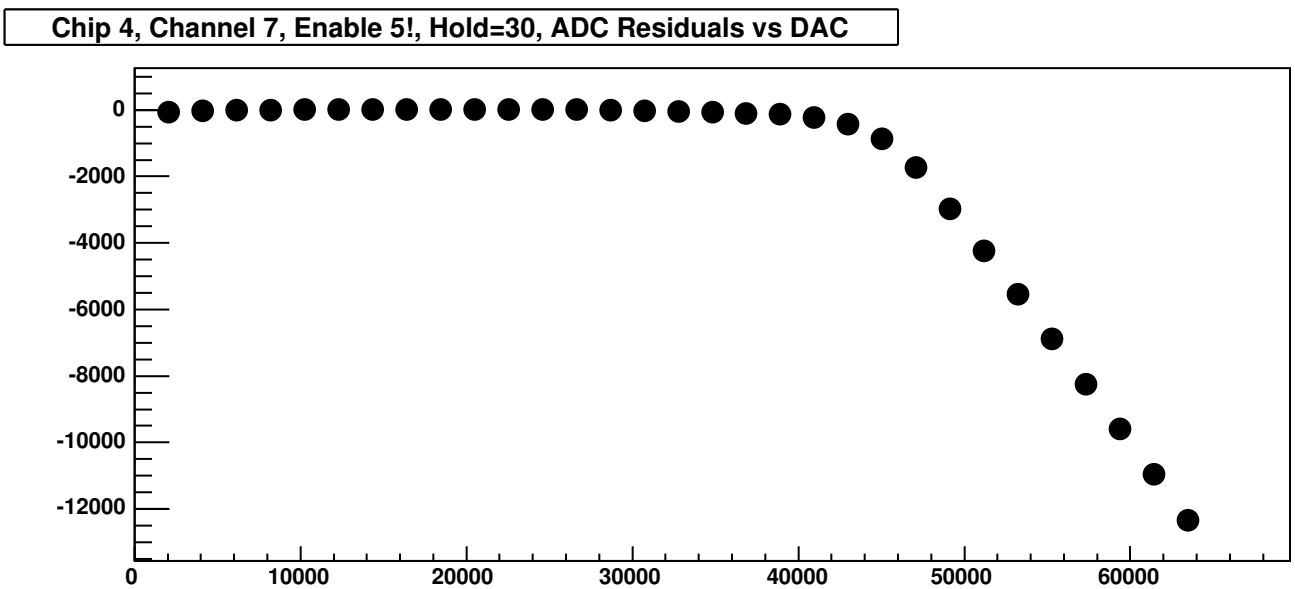


Chip 4, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC

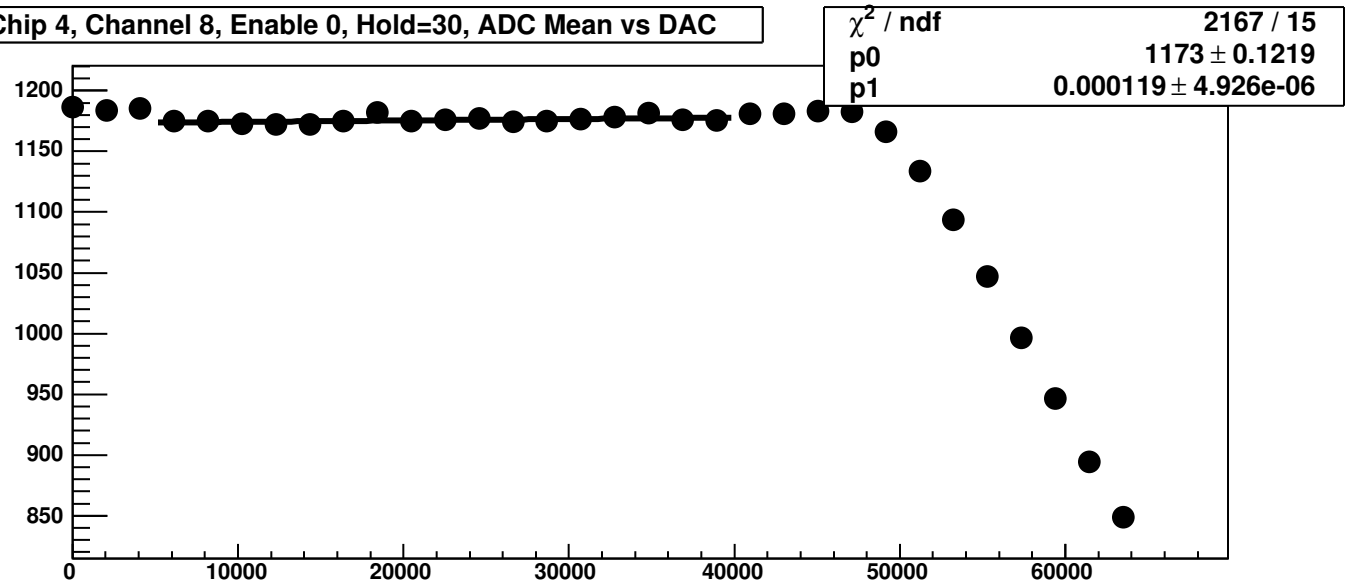



Chip 4, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 6633 / 15
p0 1210 ± 0.2988
p1 0.6578 ± 2.929e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

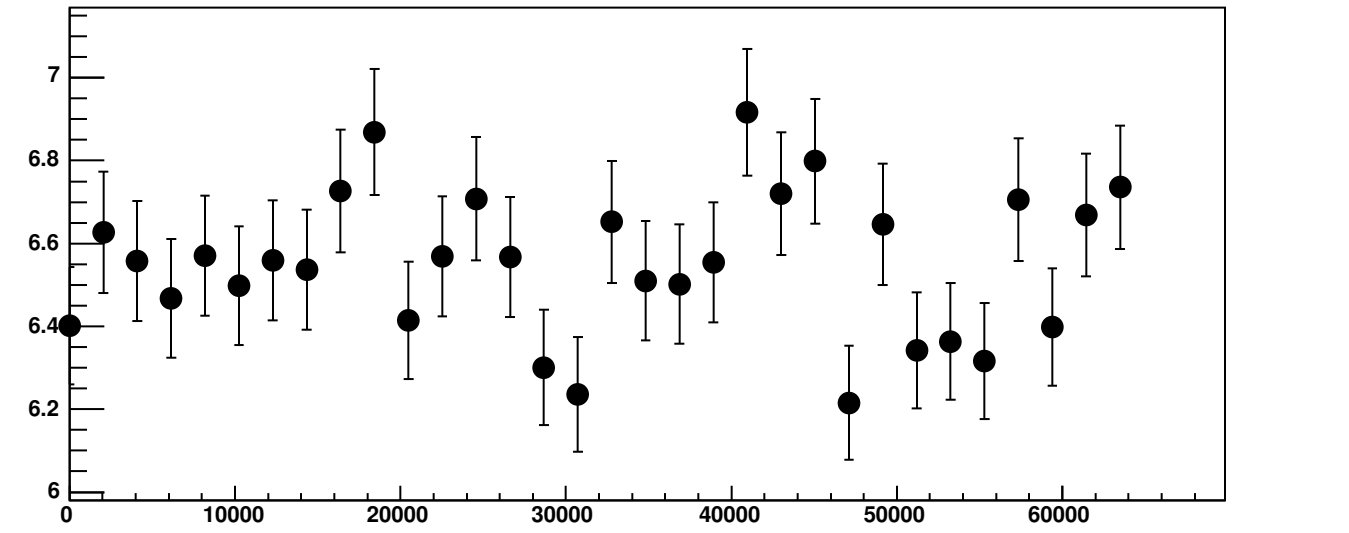

Chip 4, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

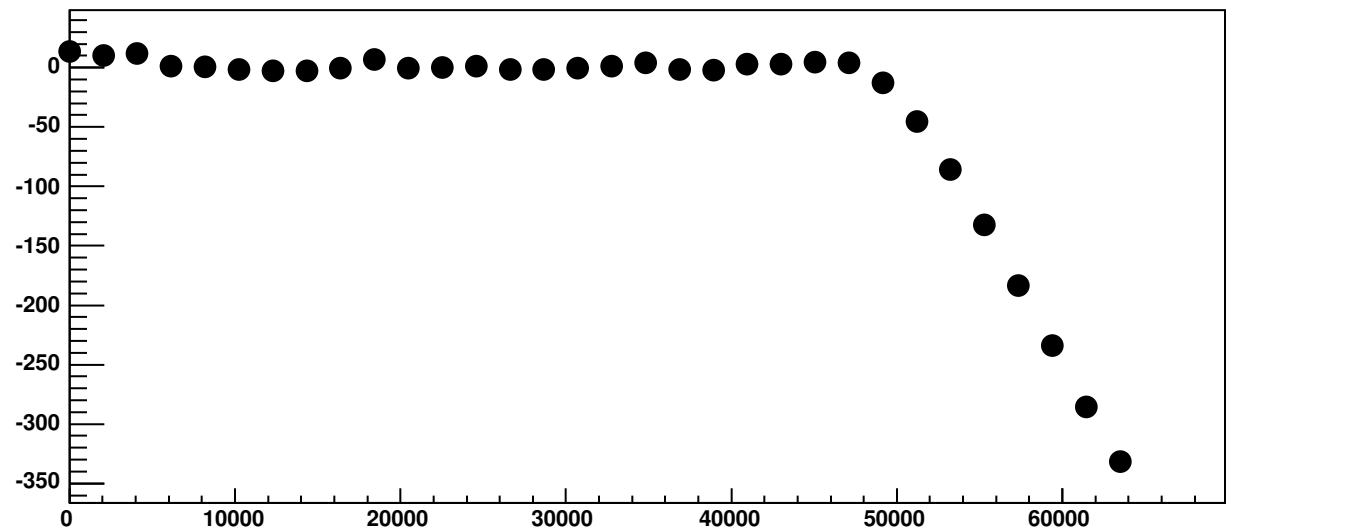
Chip 4, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC

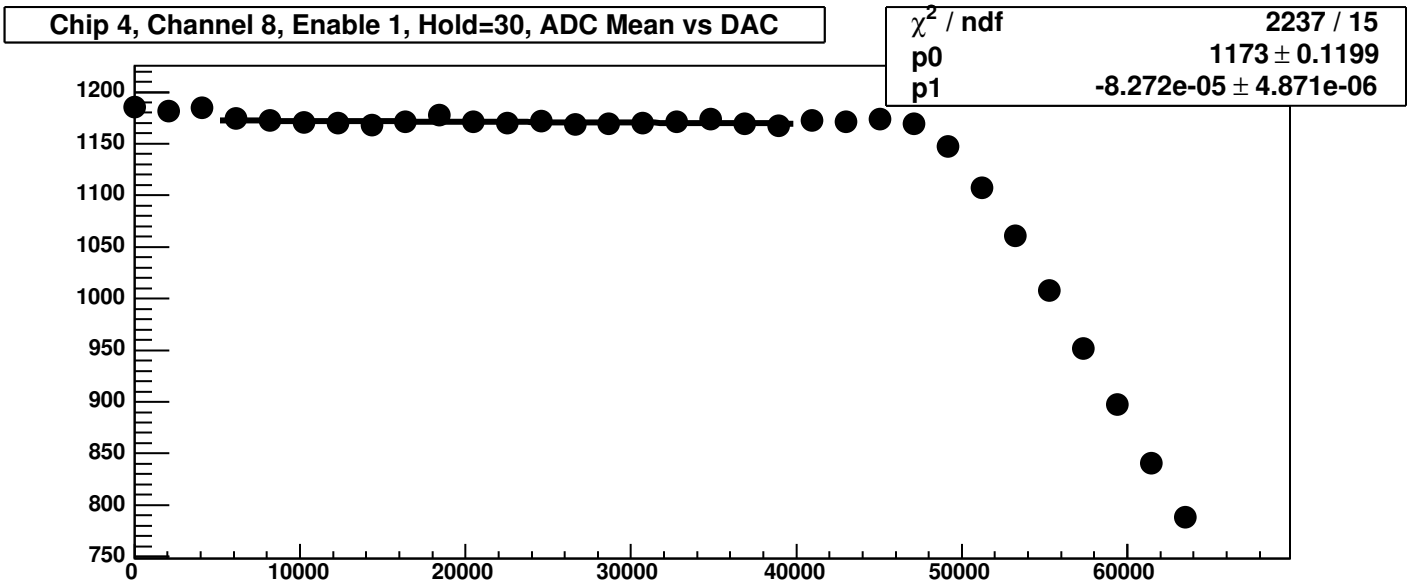


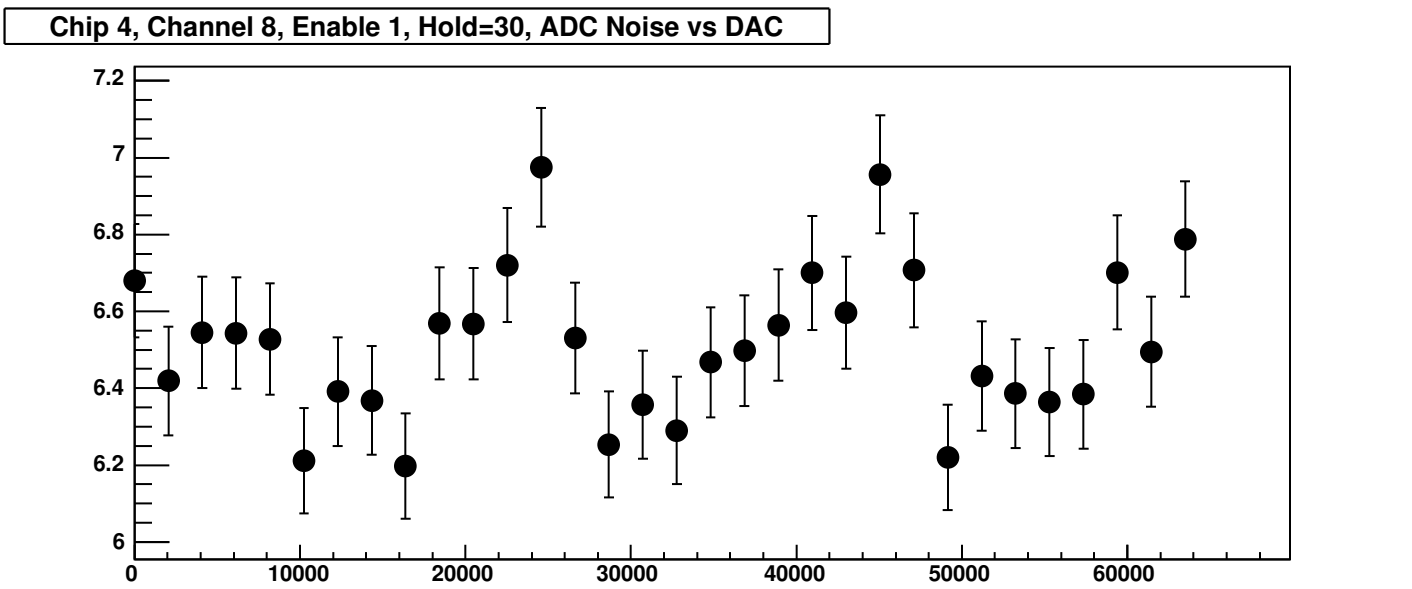
Chip 4, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC

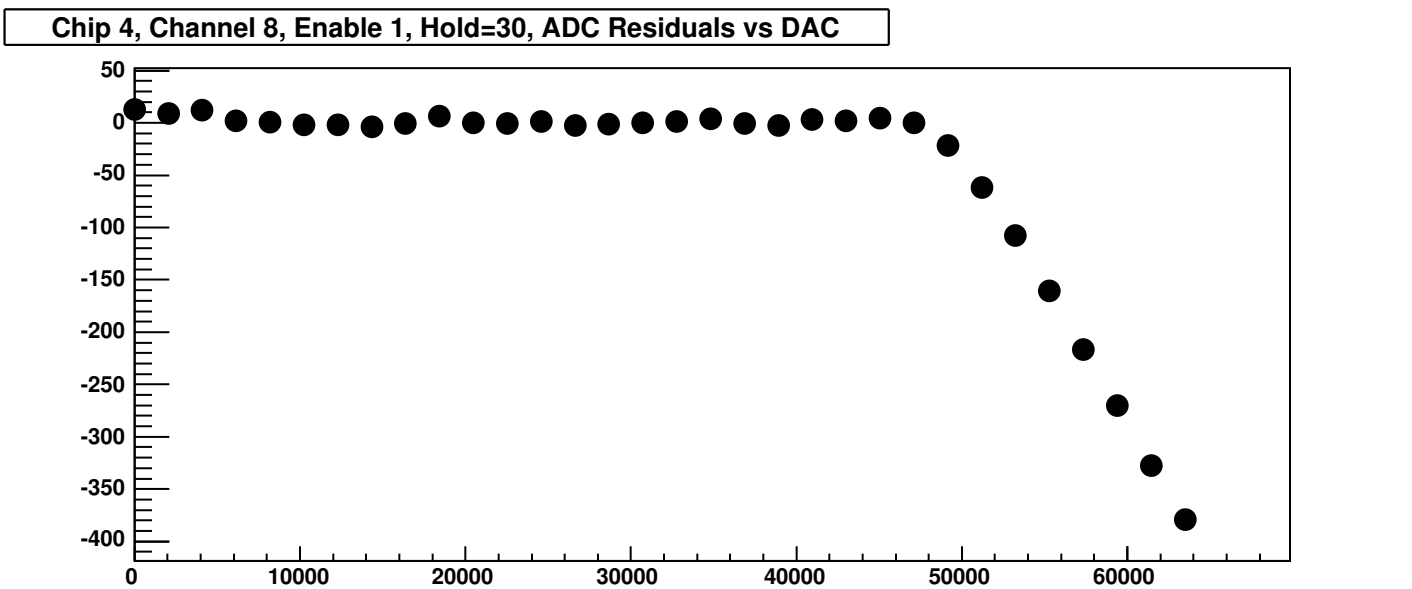


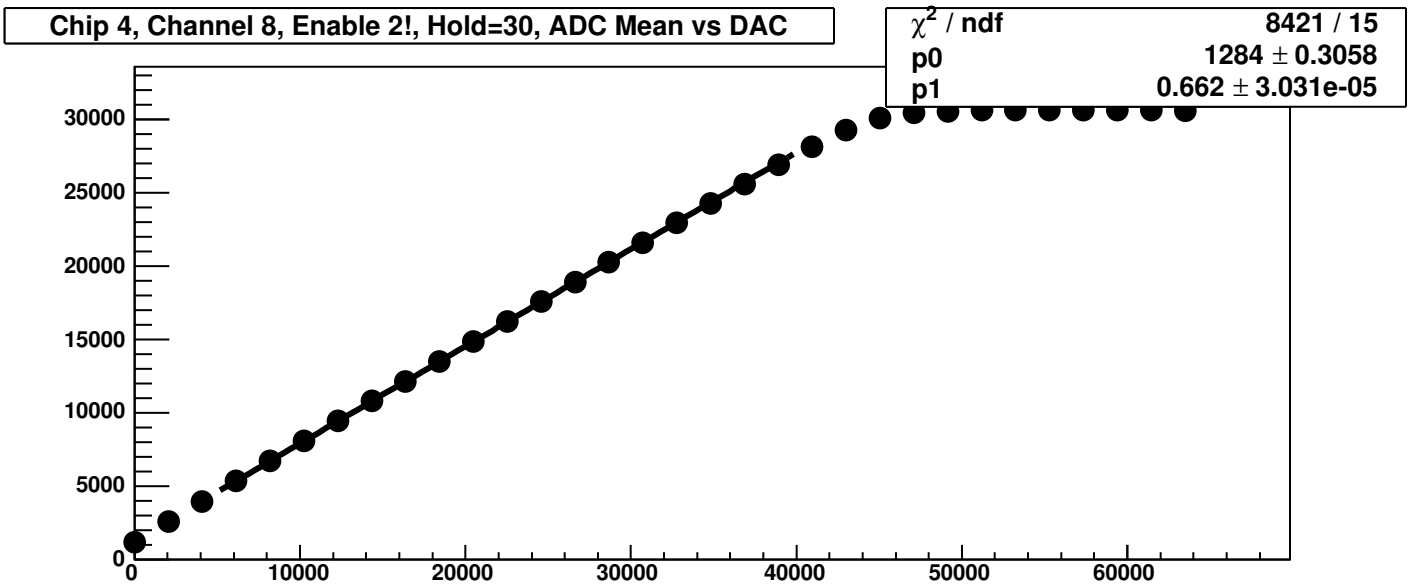
Chip 4, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC

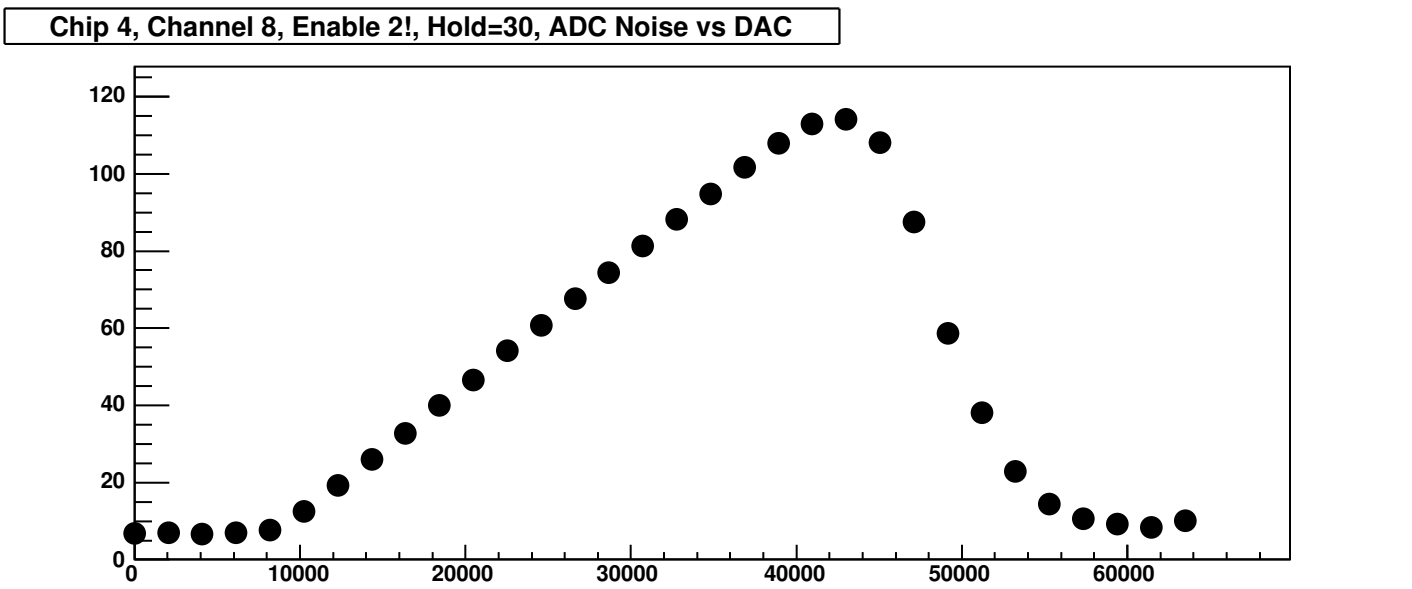


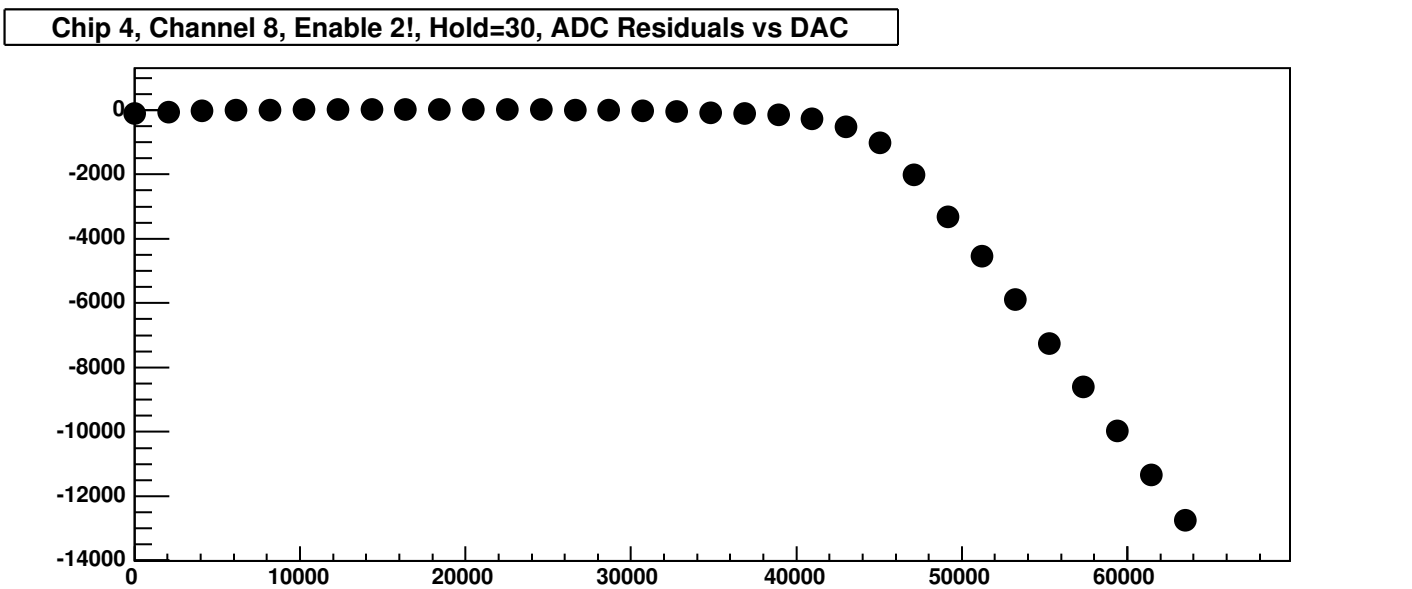

Chip 4, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2237 / 15
p0 1173 ± 0.1199
p1 -8.272e-05 ± 4.871e-06
1200
1150
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

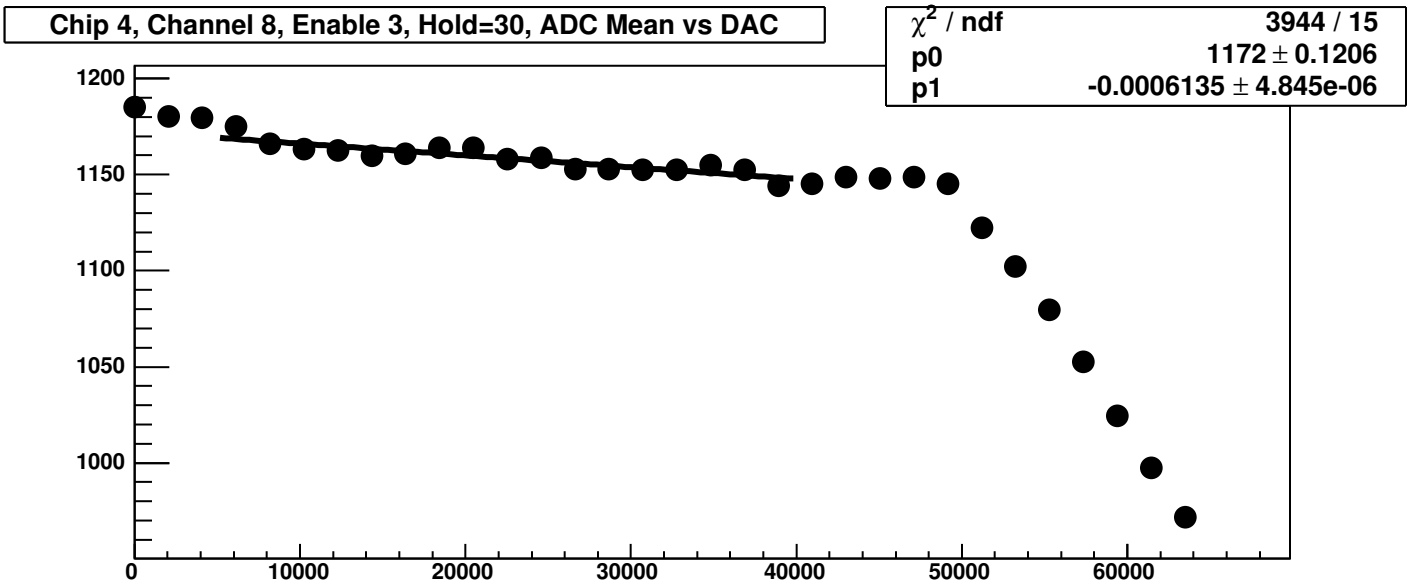

Chip 4, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

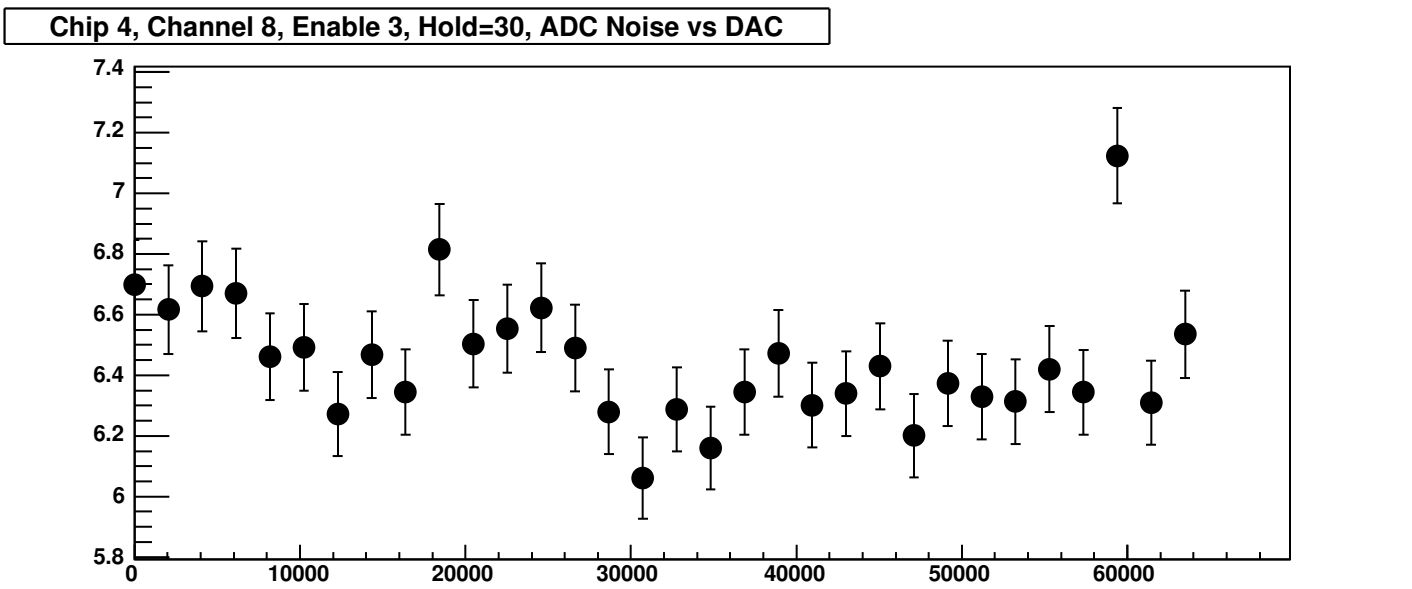

Chip 4, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

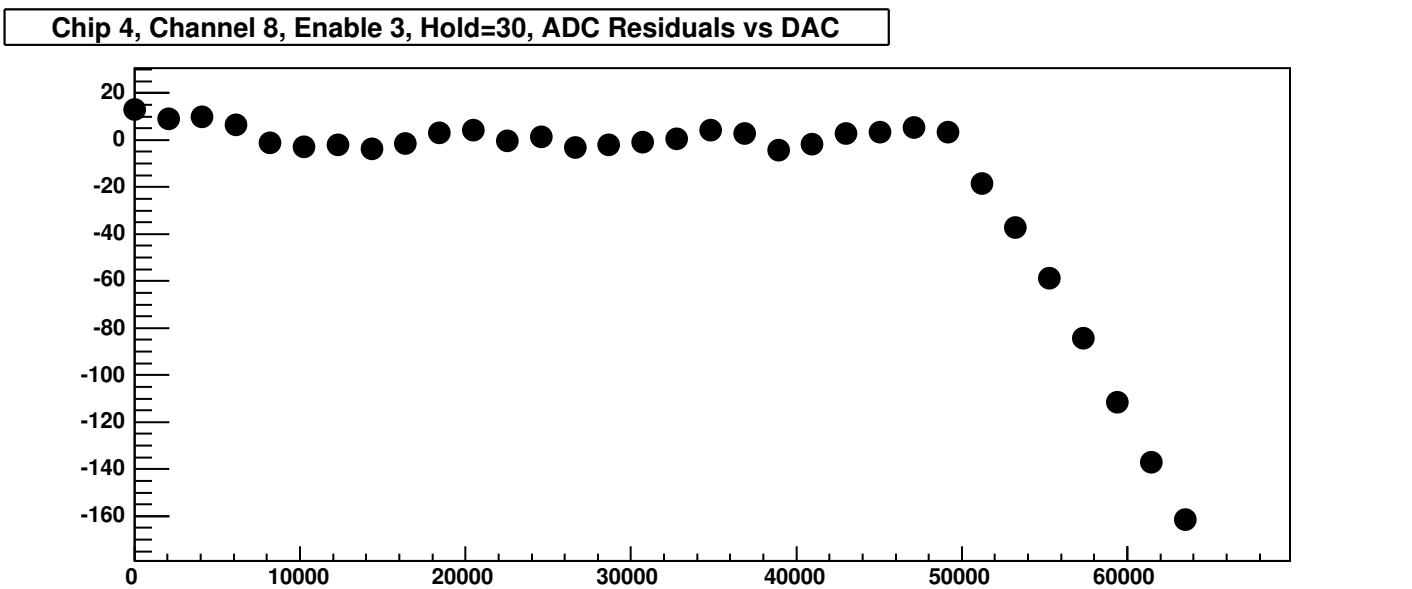

Chip 4, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 8421 / 15
p0 1284 ± 0.3058
p1 0.662 ± 3.031e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

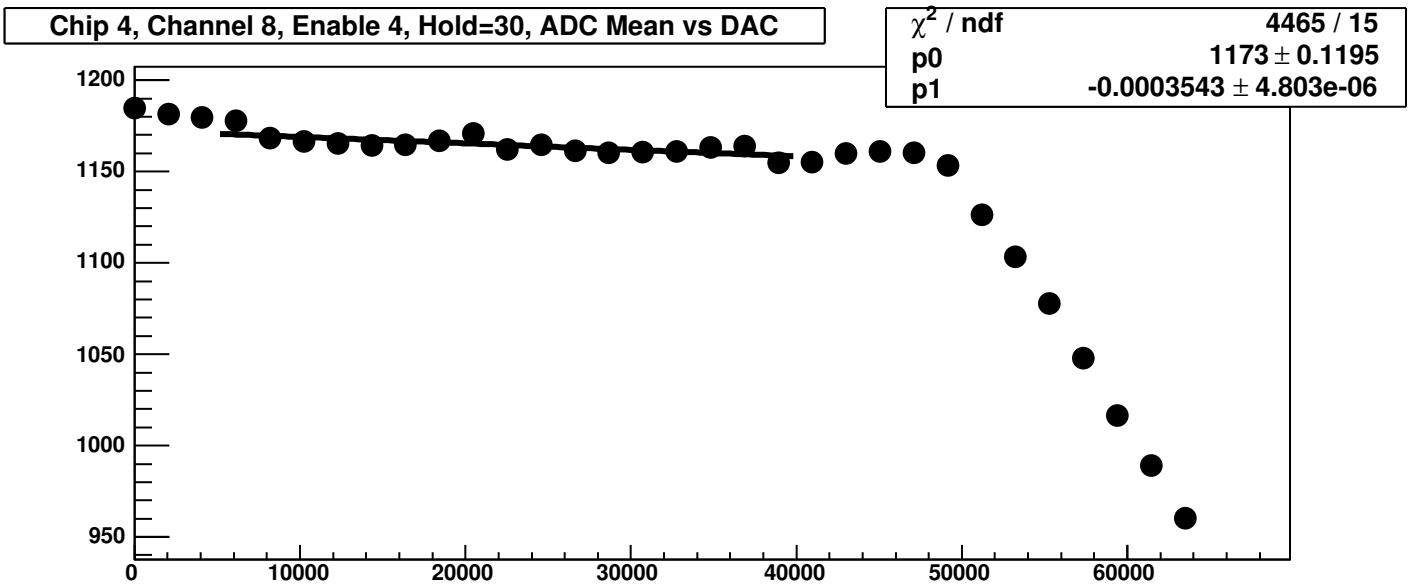

Chip 4, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

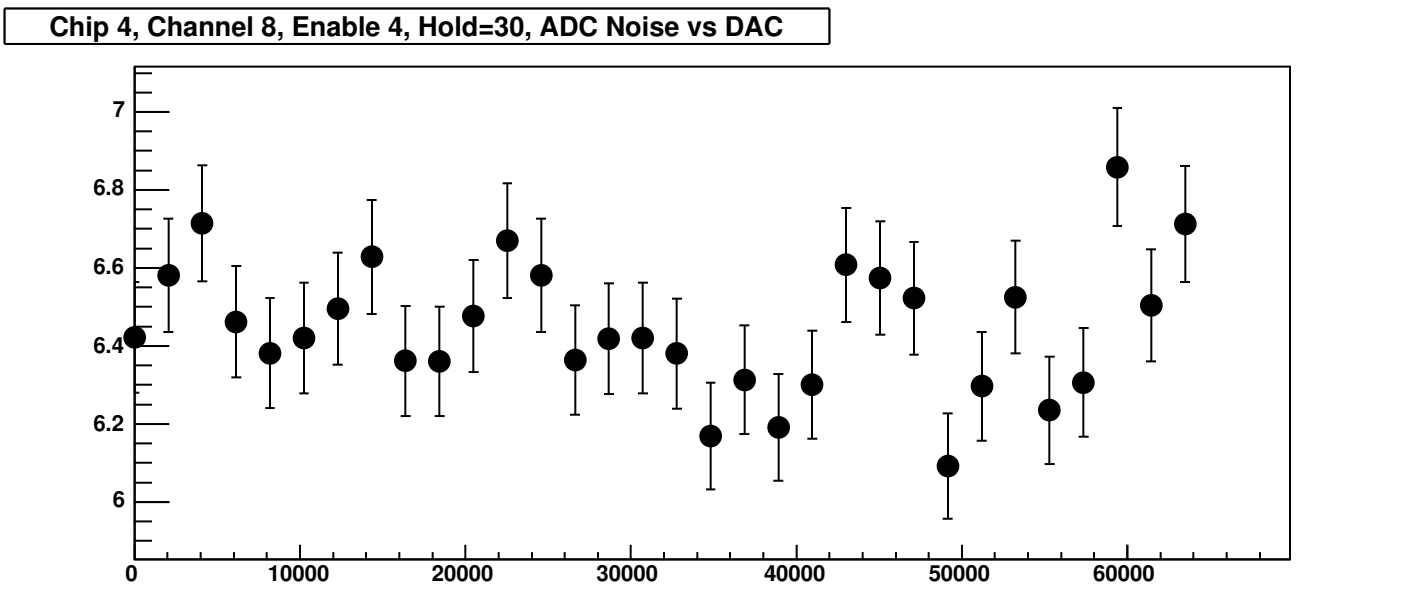

Chip 4, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

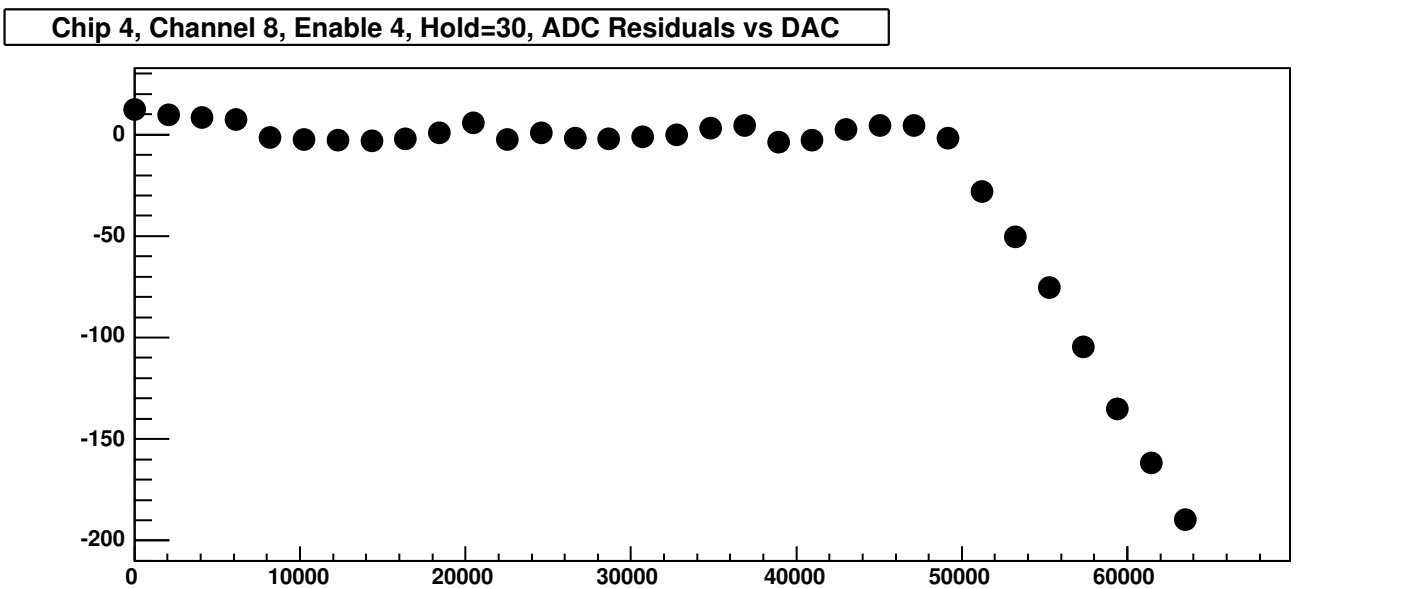

Chip 4, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3944 / 15
p0 1172 ± 0.1206
p1 -0.0006135 ± 4.845e-06
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

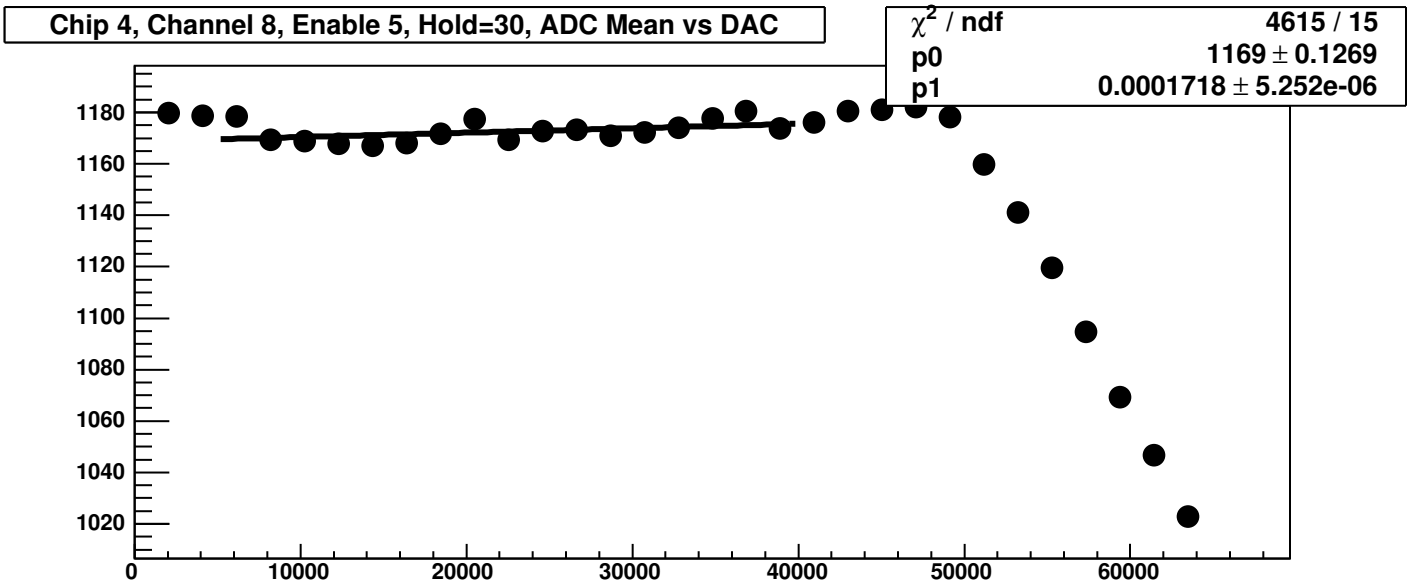

Chip 4, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

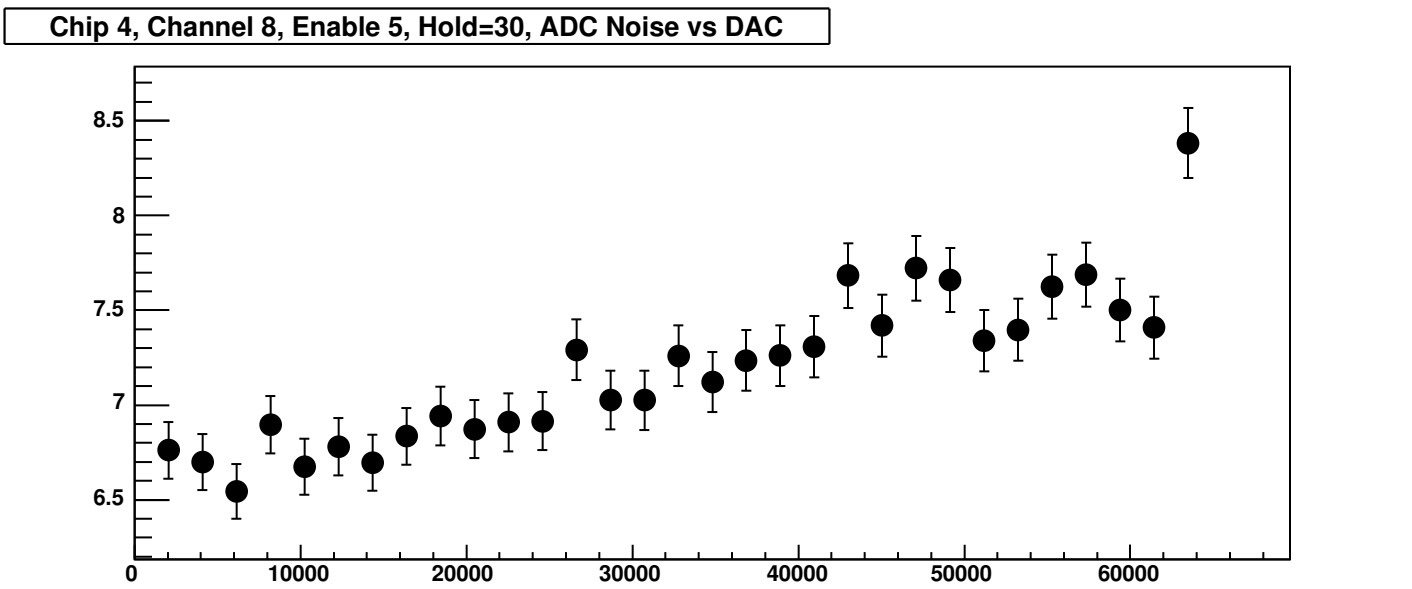

Chip 4, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

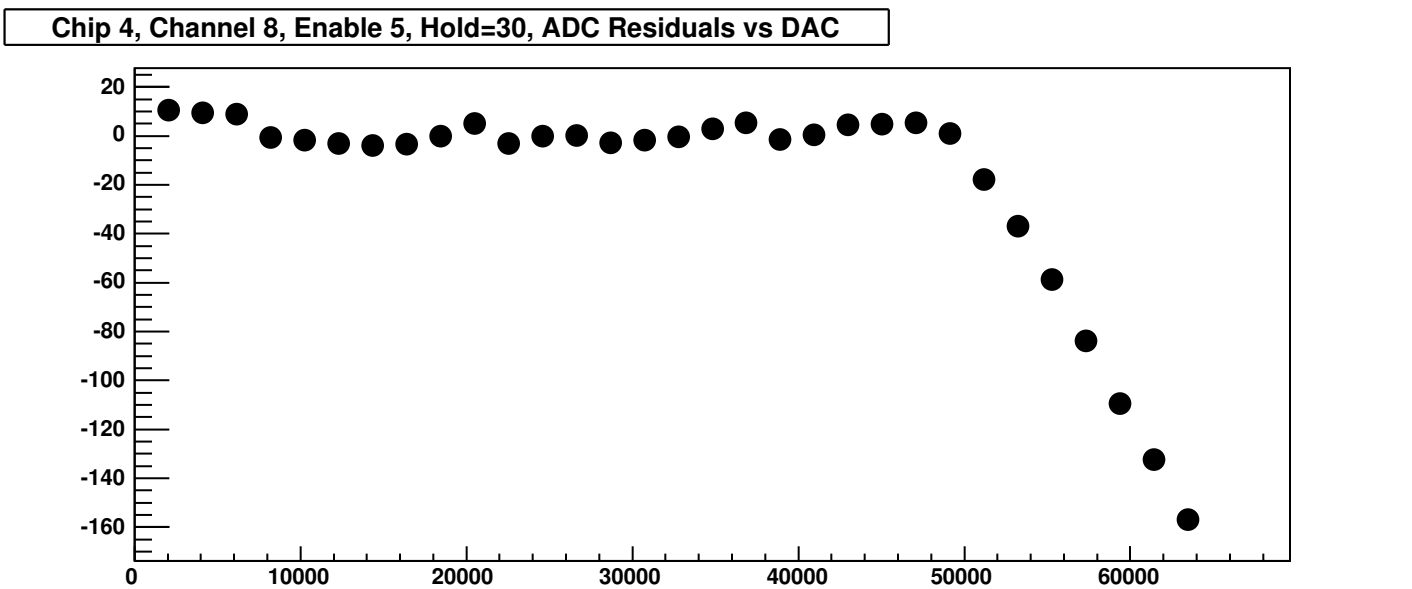

Chip 4, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4465 / 15
p0 1173 ± 0.1195
p1 -0.0003543 ± 4.803e-06
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

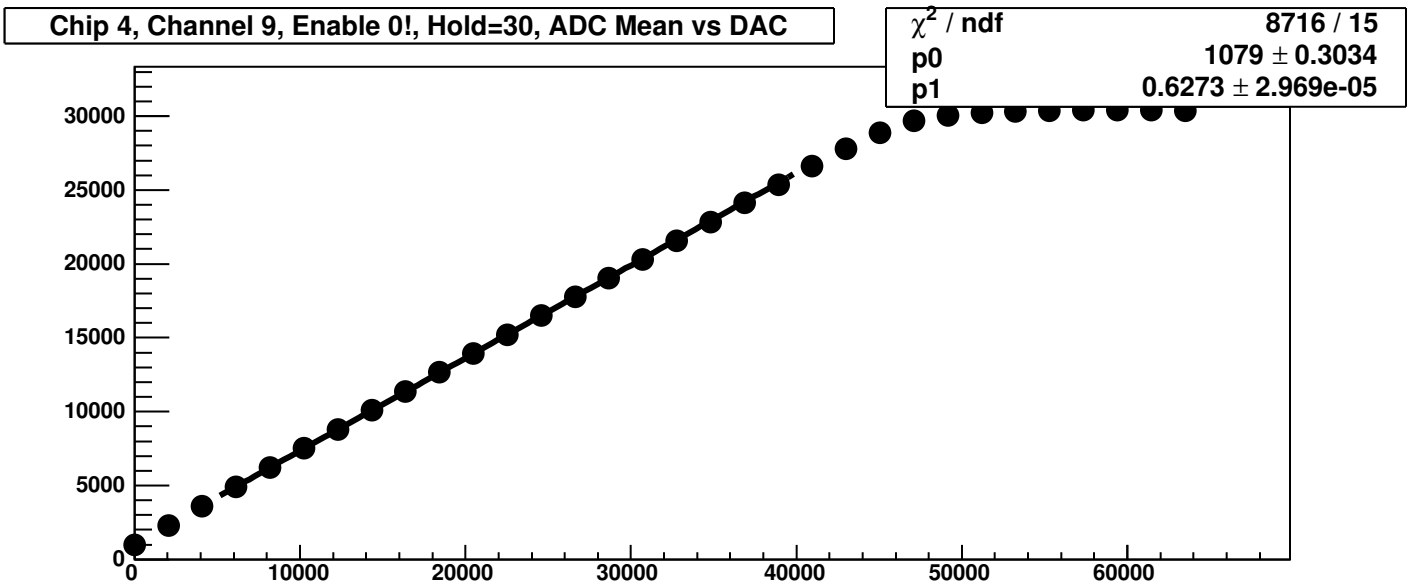

Chip 4, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

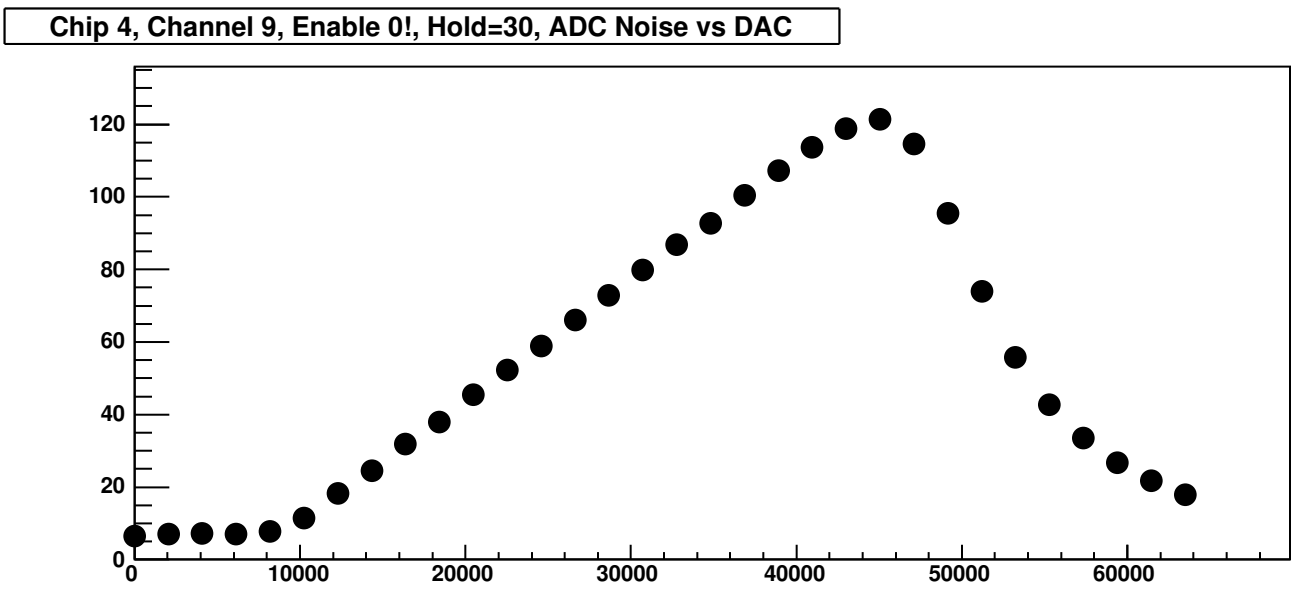

Chip 4, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

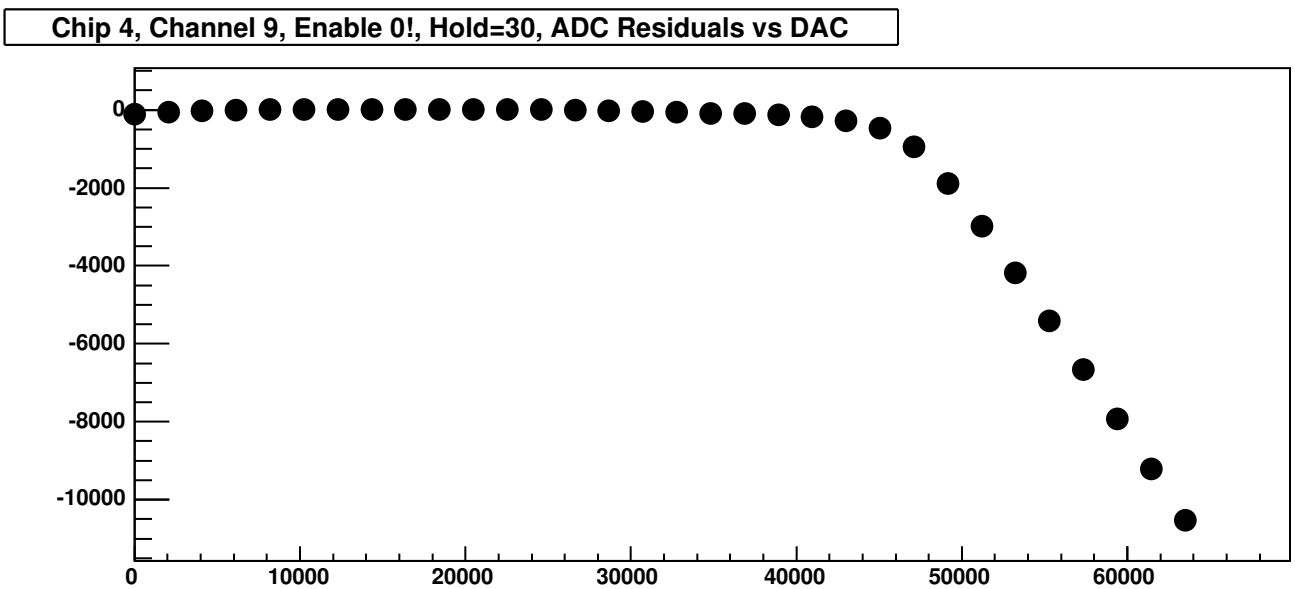

Chip 4, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4615 / 15
p0 1169 ± 0.1269
p1 0.0001718 ± 5.252e-06
1180
1160
1140
1120
1100
1080
1060
1040
1020
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000

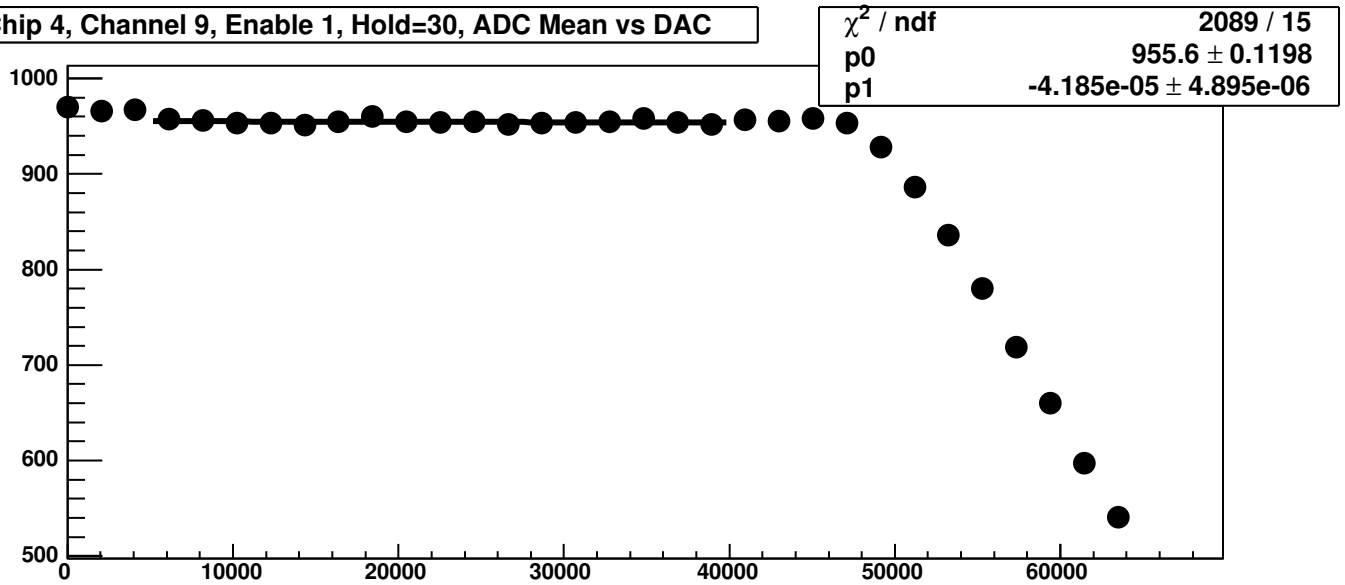

Chip 4, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 8716 / 15
p0 1079 ± 0.3034
p1 0.6273 ± 2.969e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

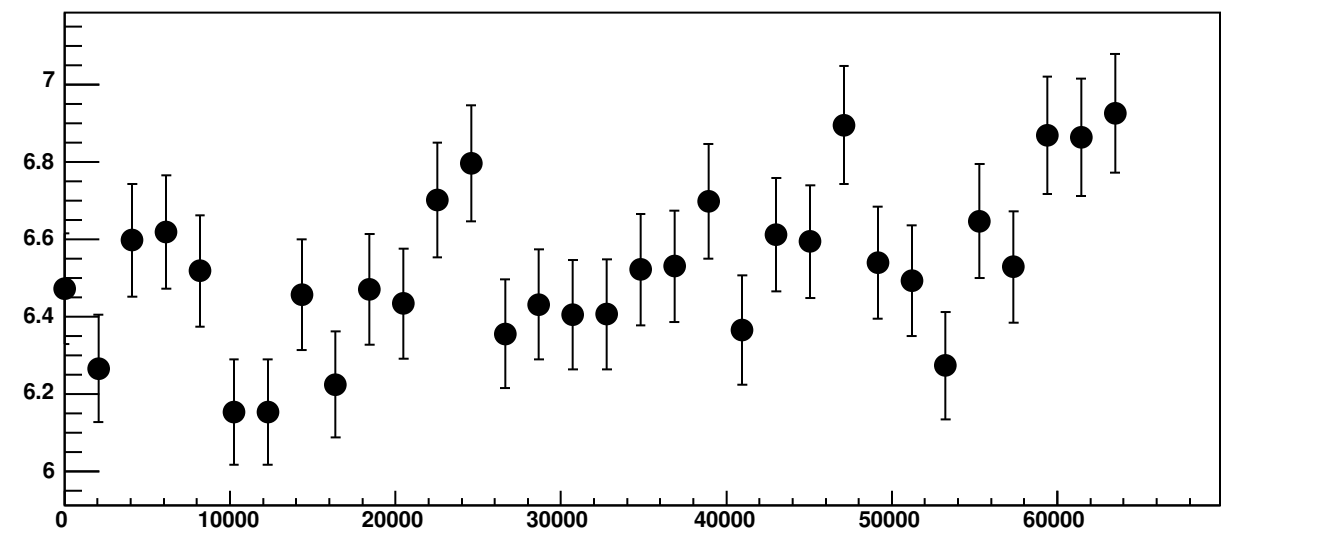

Chip 4, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

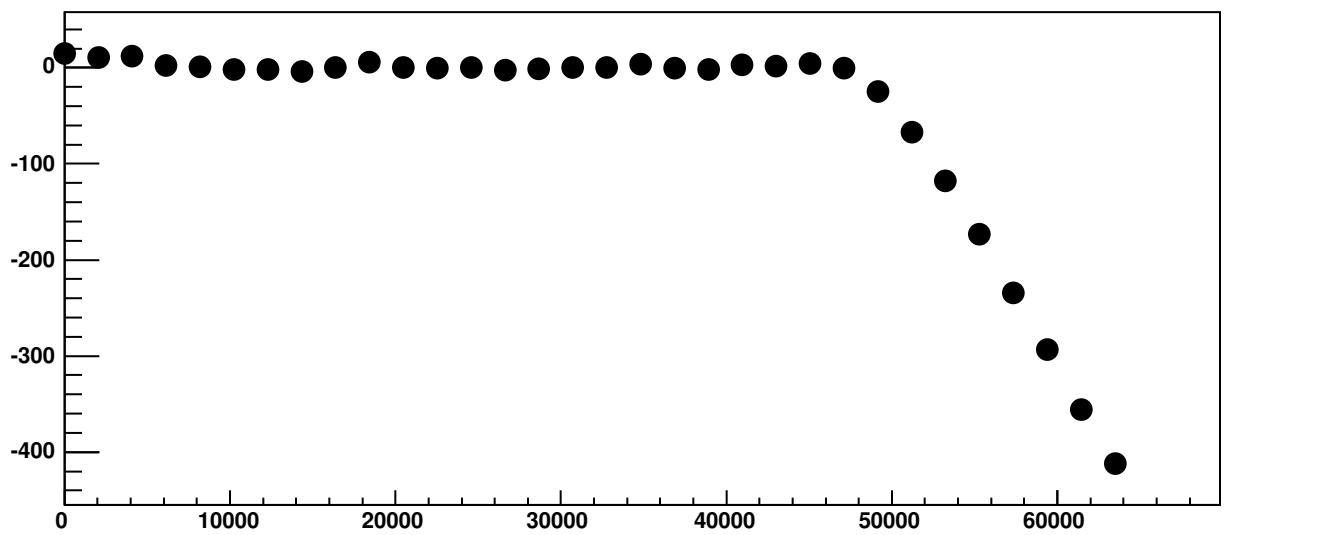
Chip 4, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

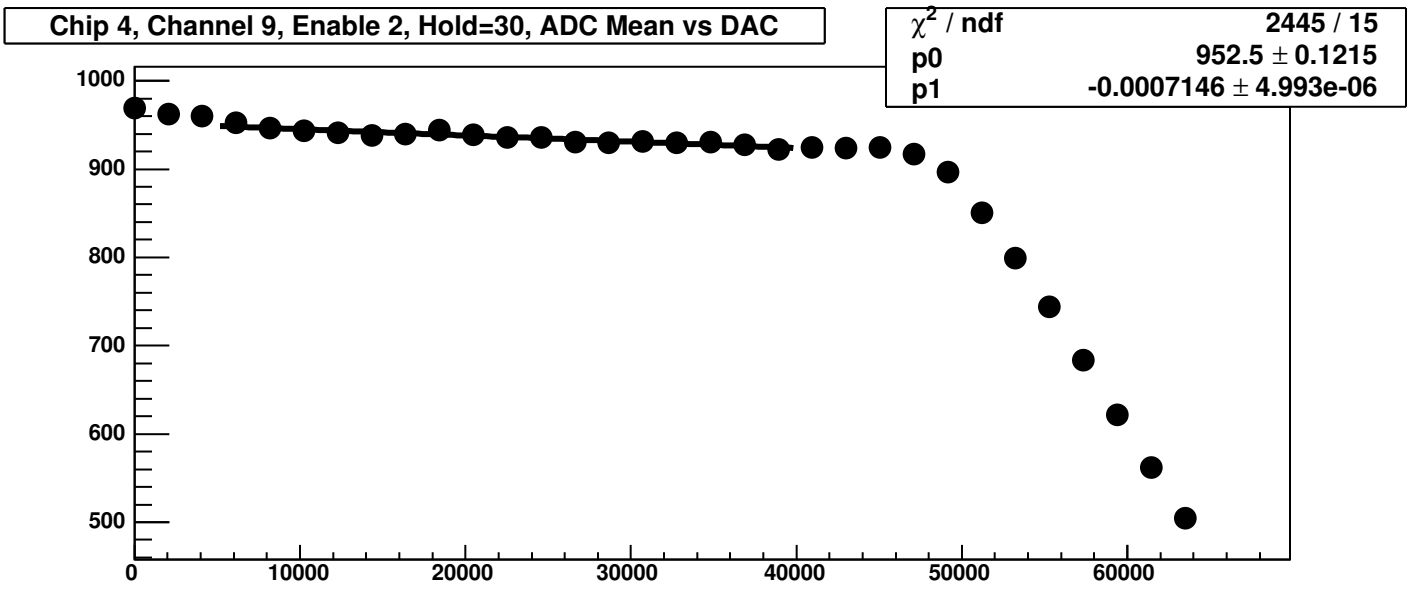


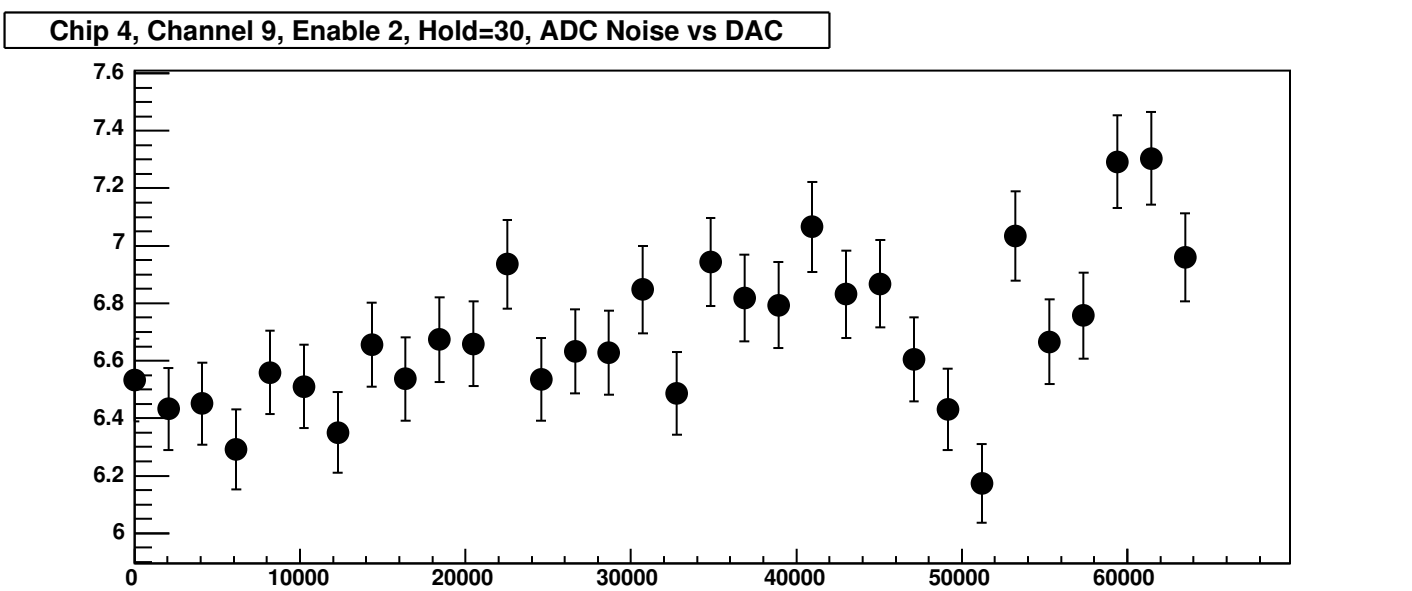
Chip 4, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC

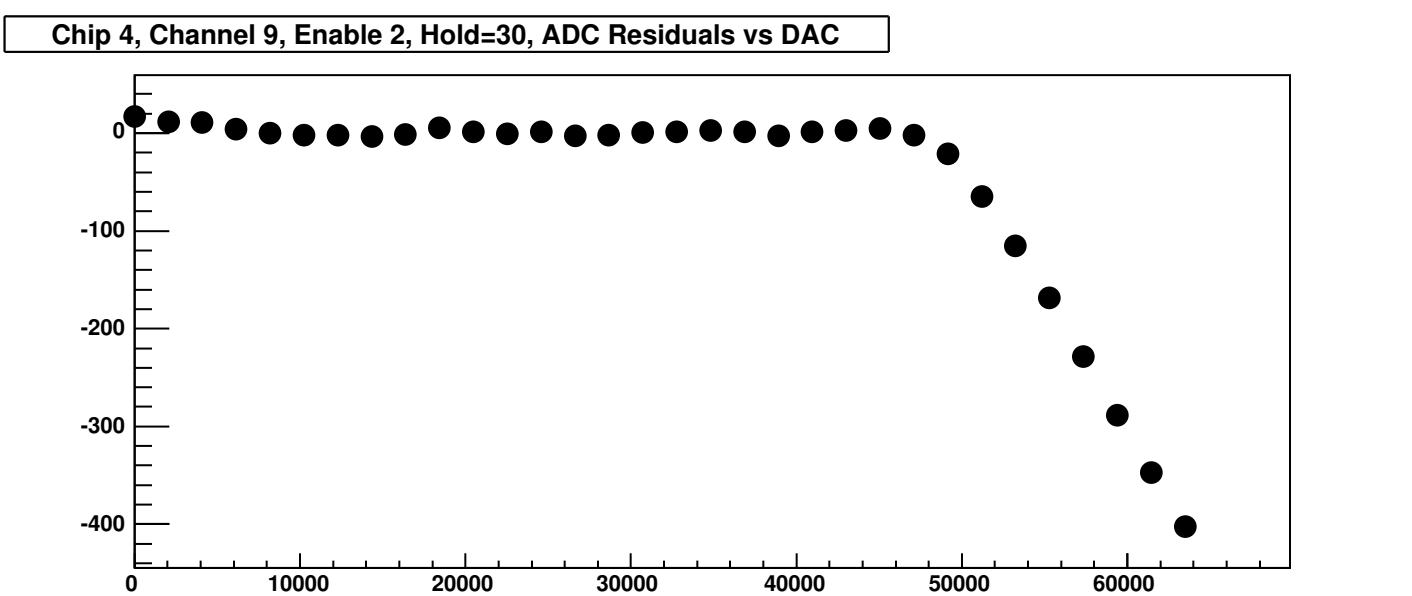


Chip 4, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

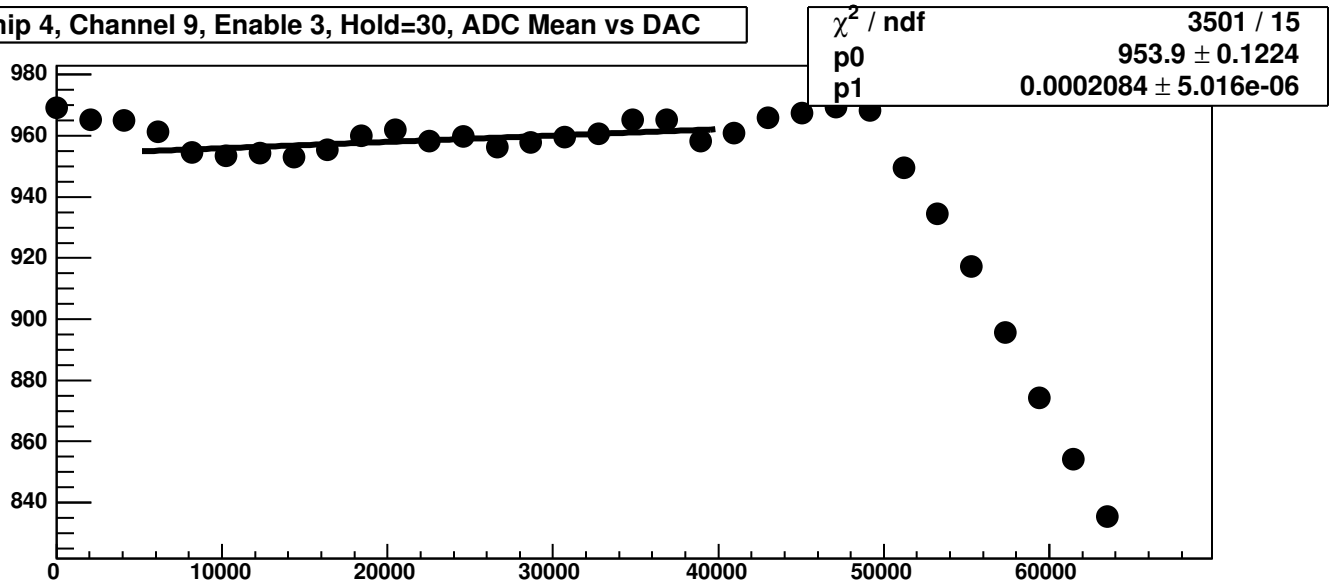



Chip 4, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2445 / 15
p0 952.5 ± 0.1215
p1 -0.0007146 ± 4.993e-06
1000
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000

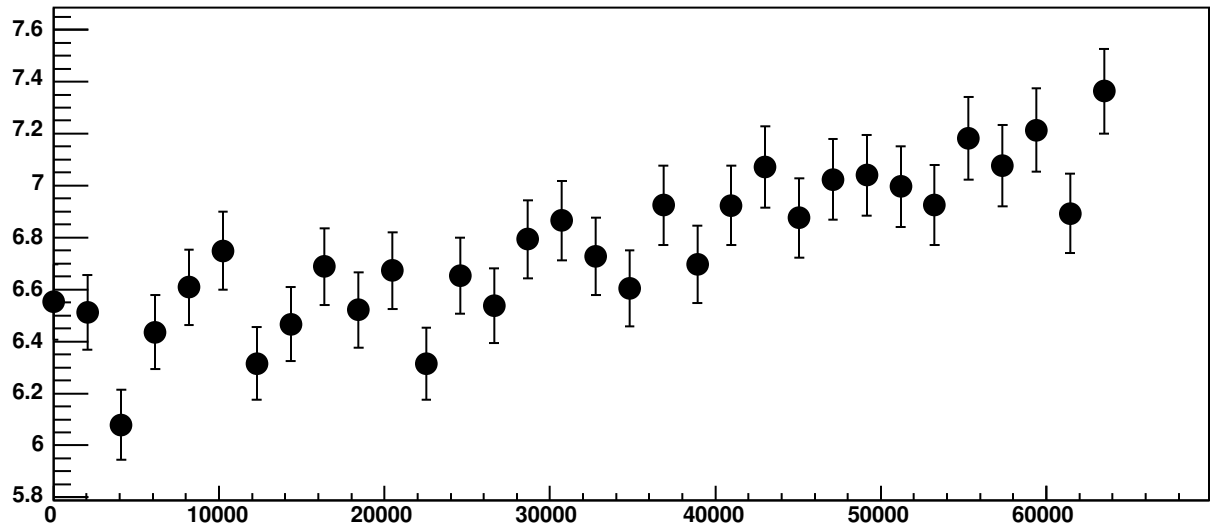

Chip 4, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

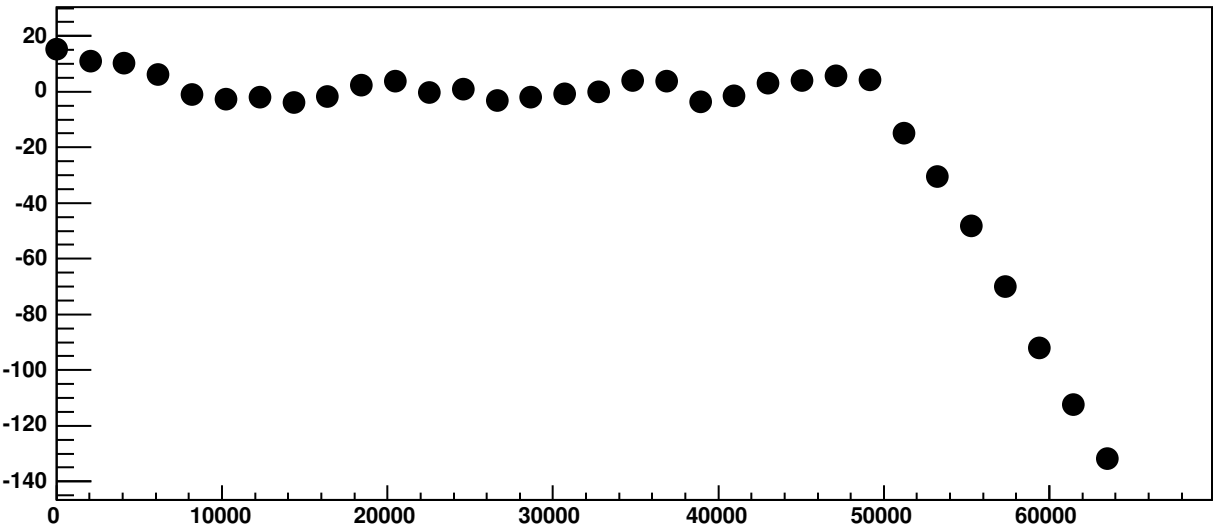
Chip 4, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC



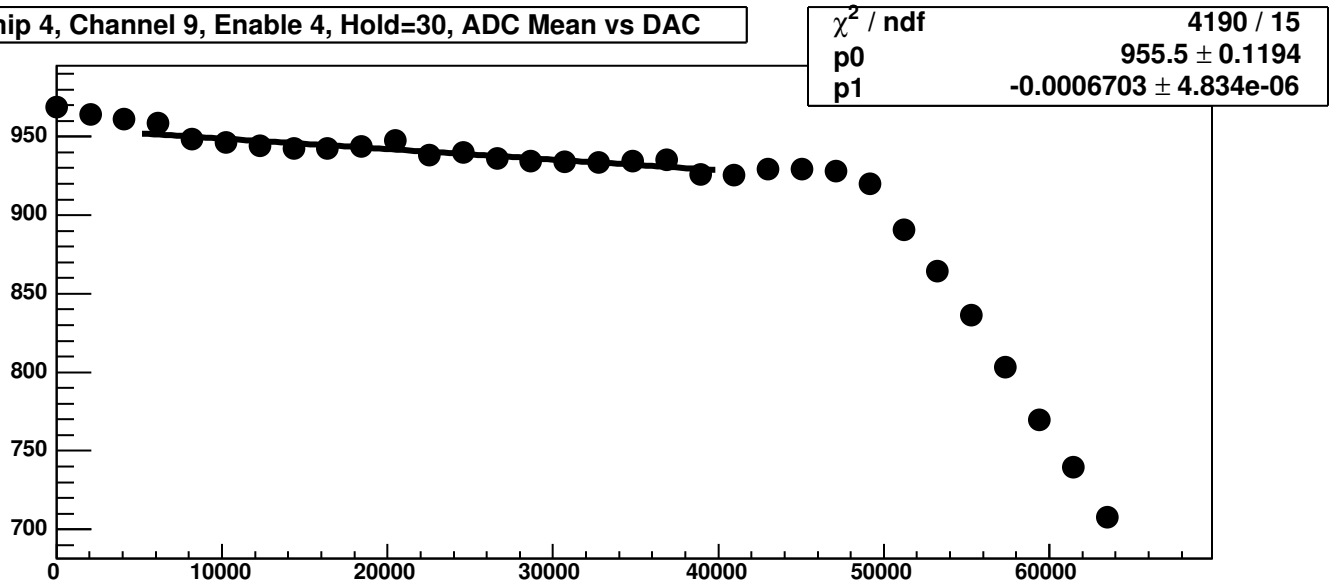
Chip 4, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC



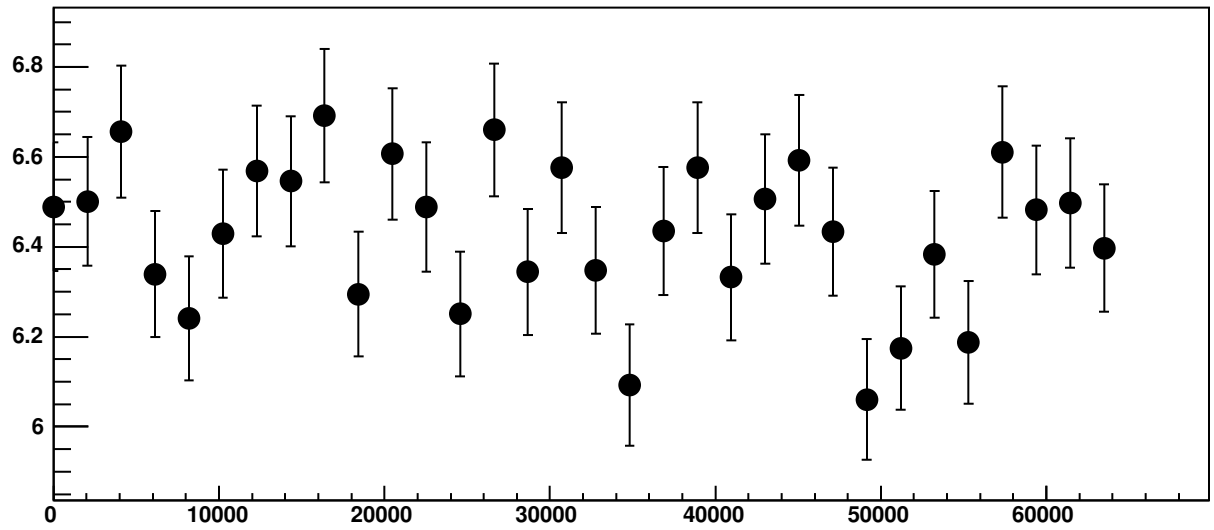
Chip 4, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC



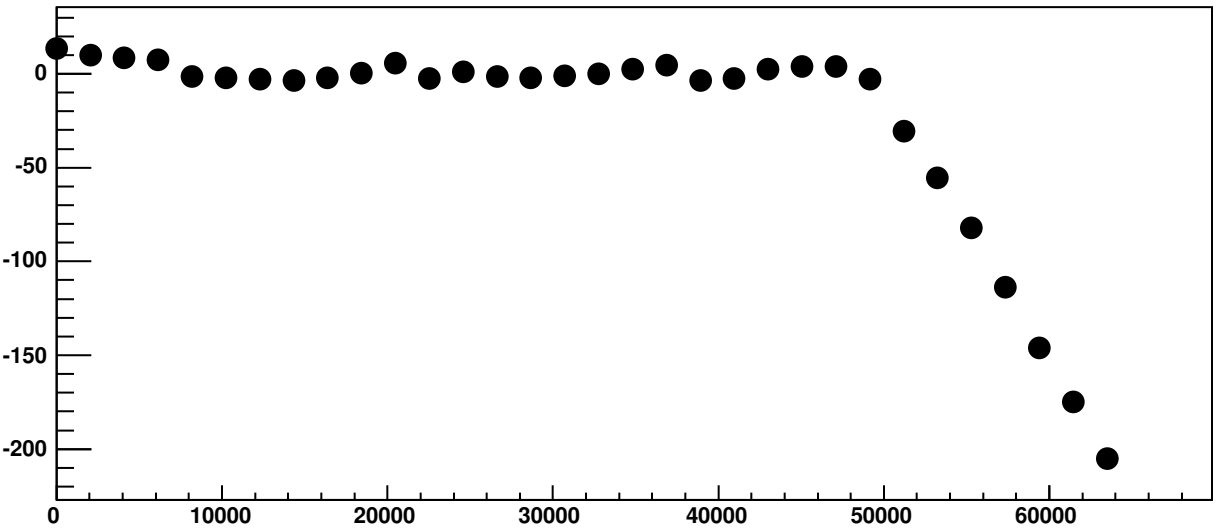
Chip 4, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC



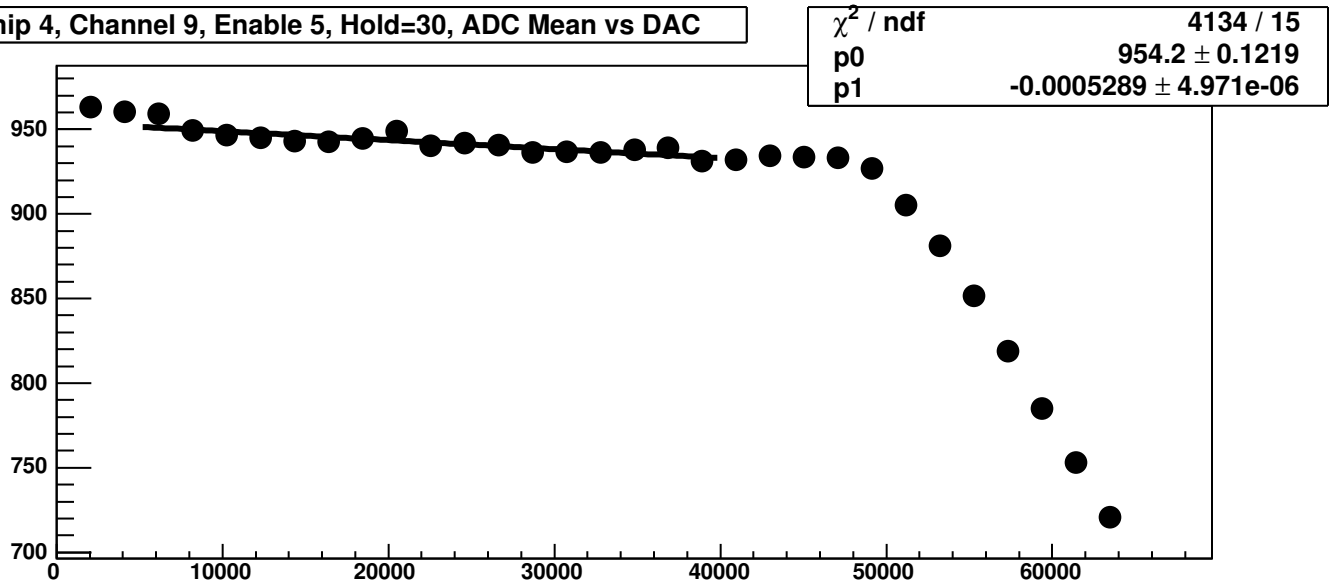
Chip 4, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



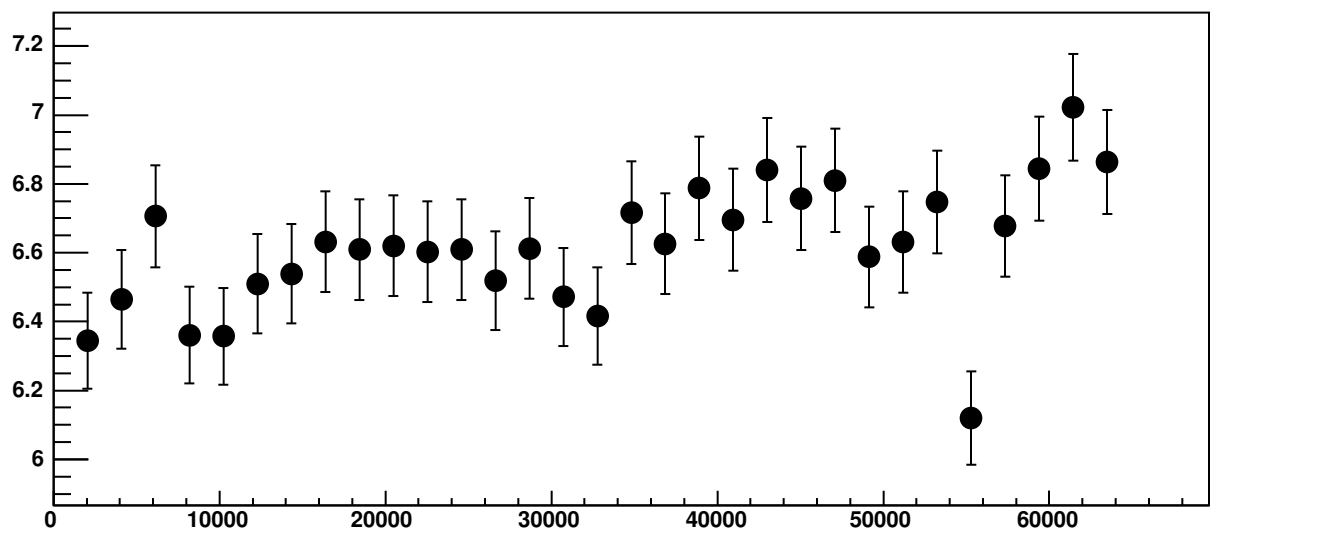
Chip 4, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



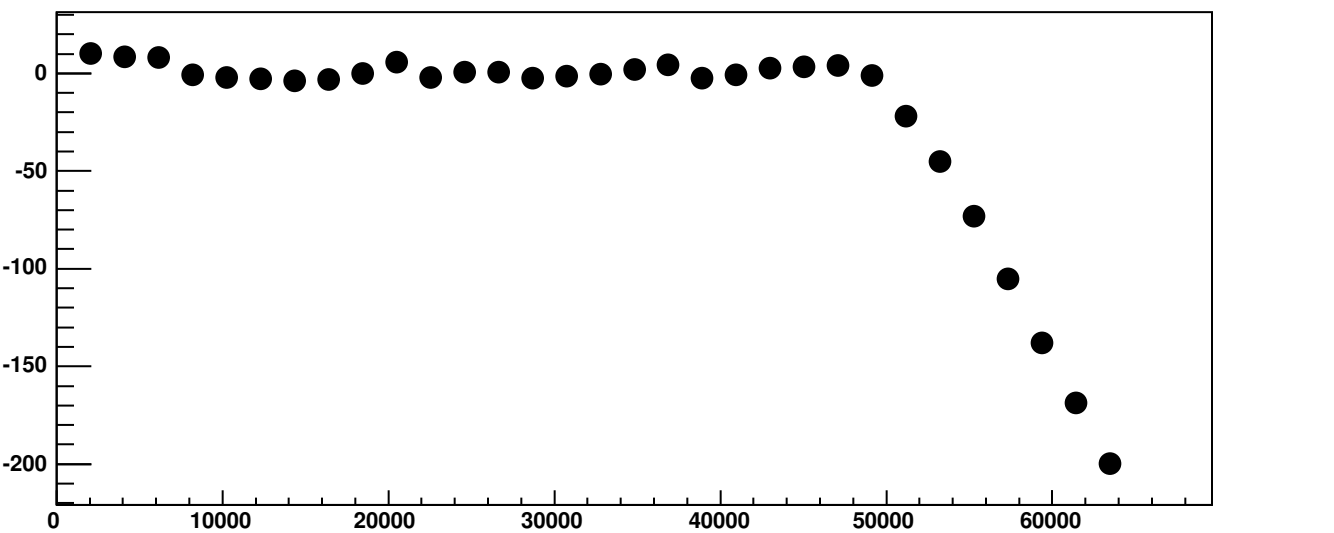
Chip 4, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC

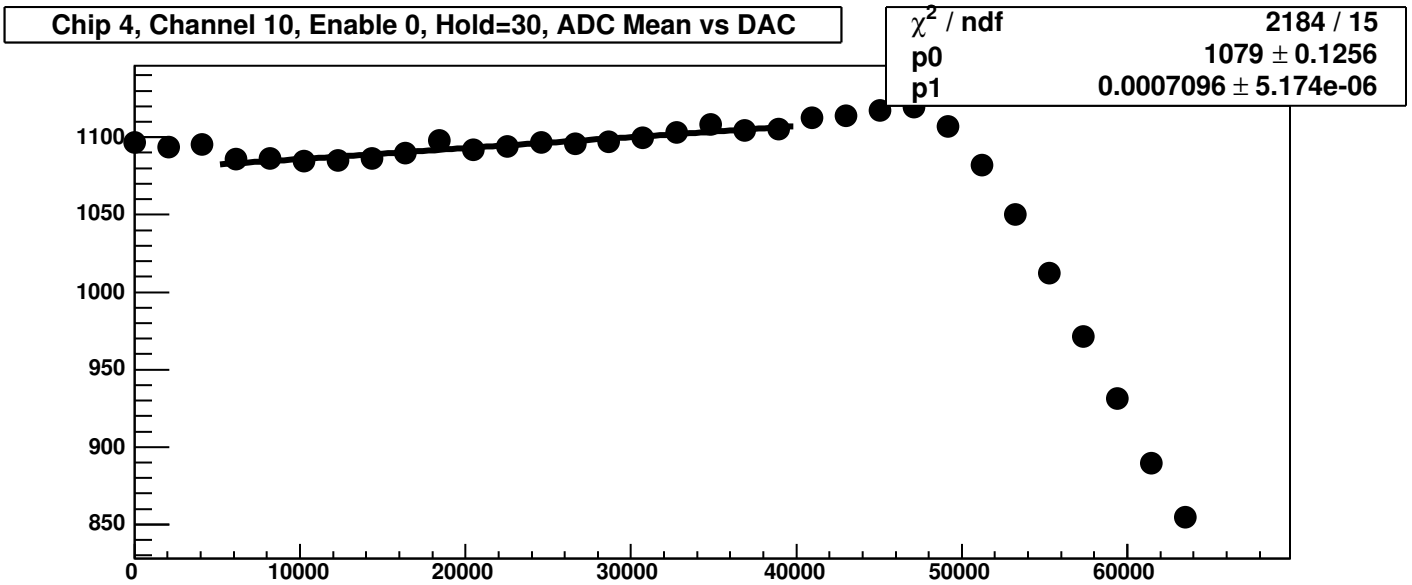


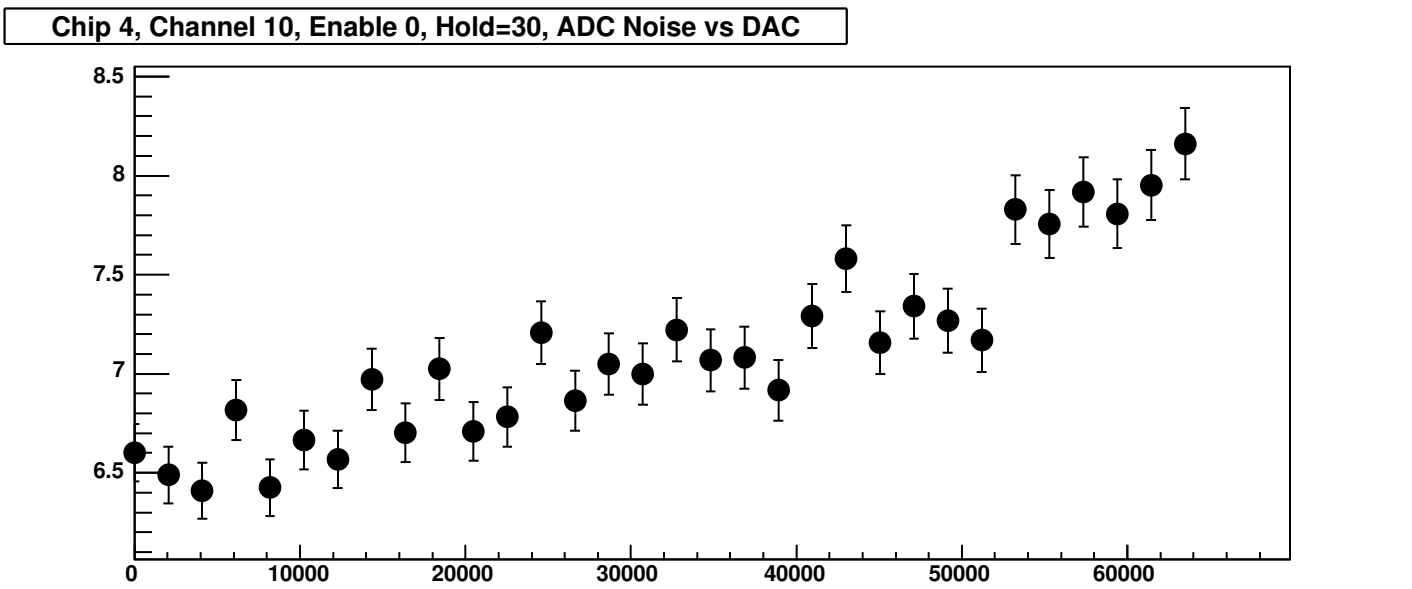
Chip 4, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC

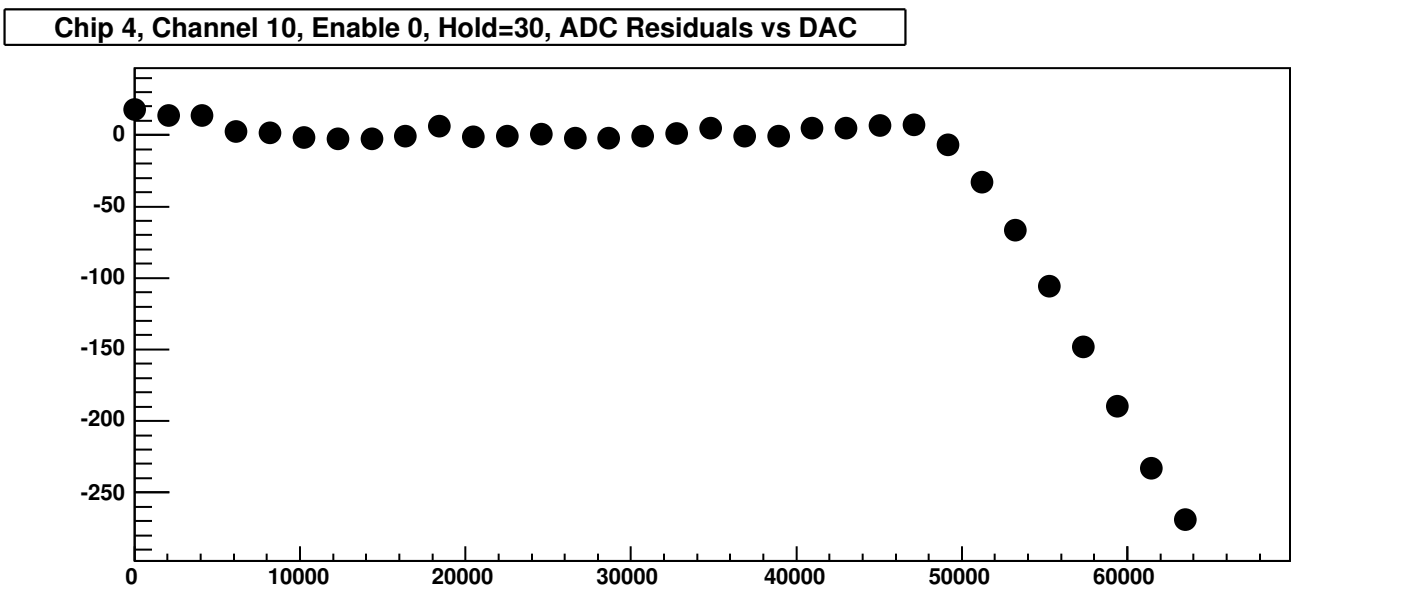


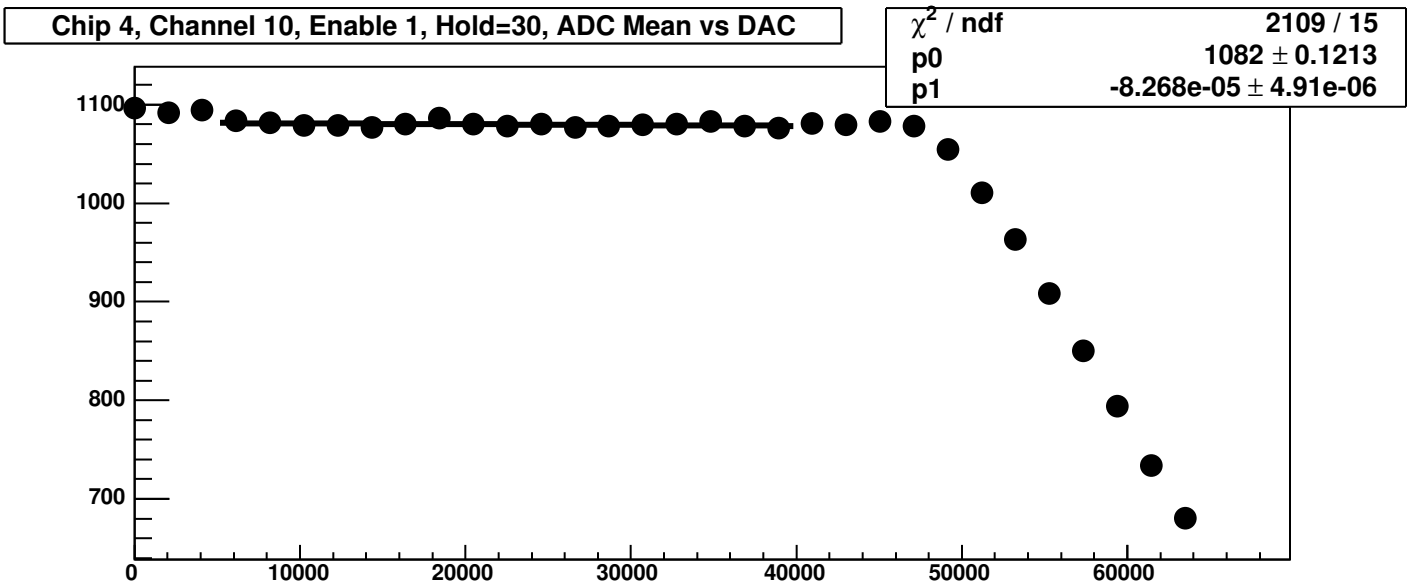
Chip 4, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

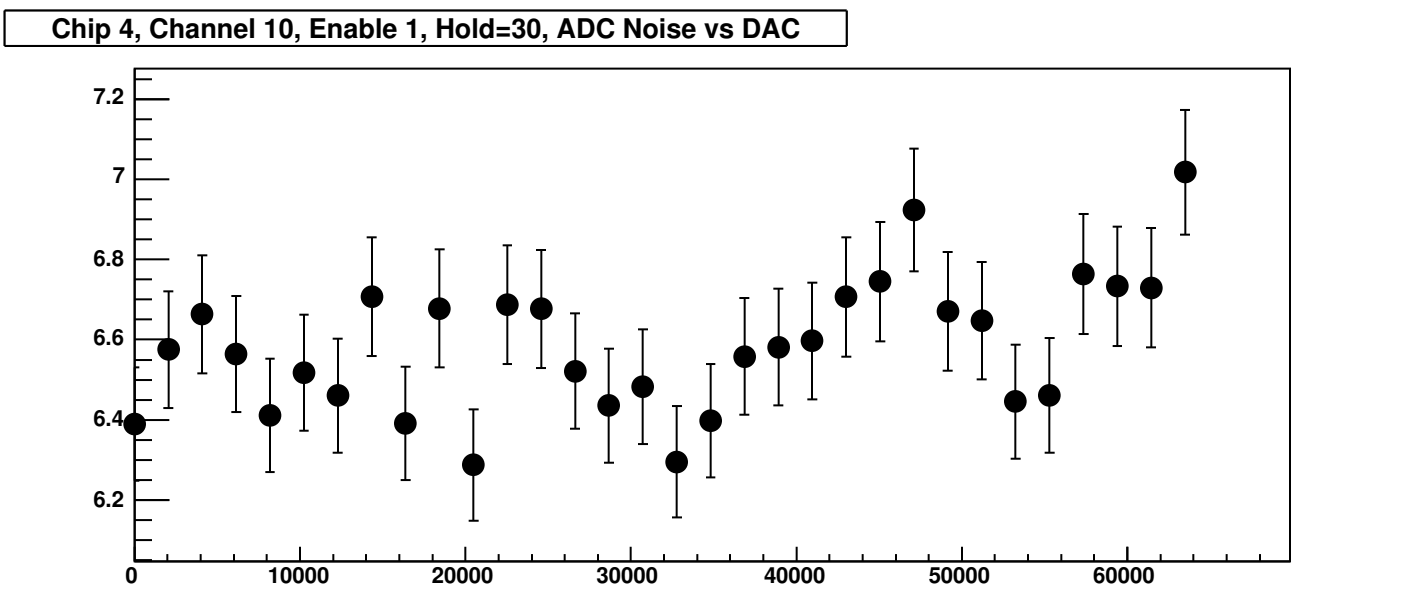


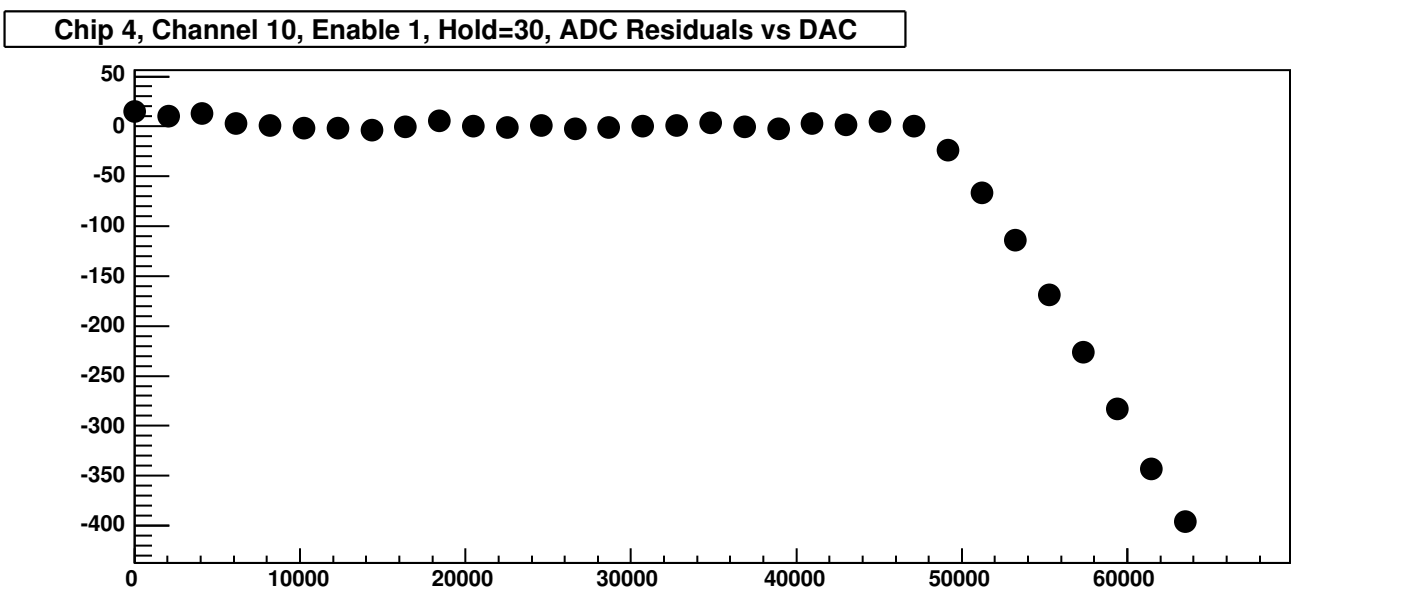

Chip 4, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2184 / 15
p0 1079 ± 0.1256
p1 0.0007096 ± 5.174e-06
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

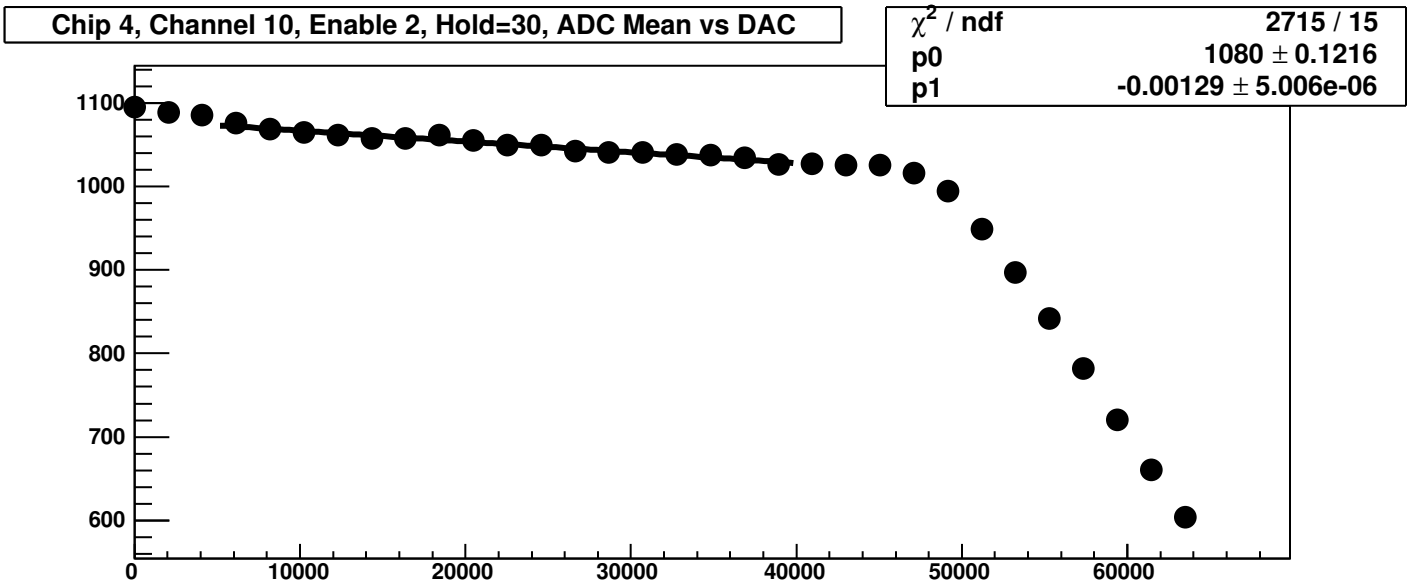

Chip 4, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000

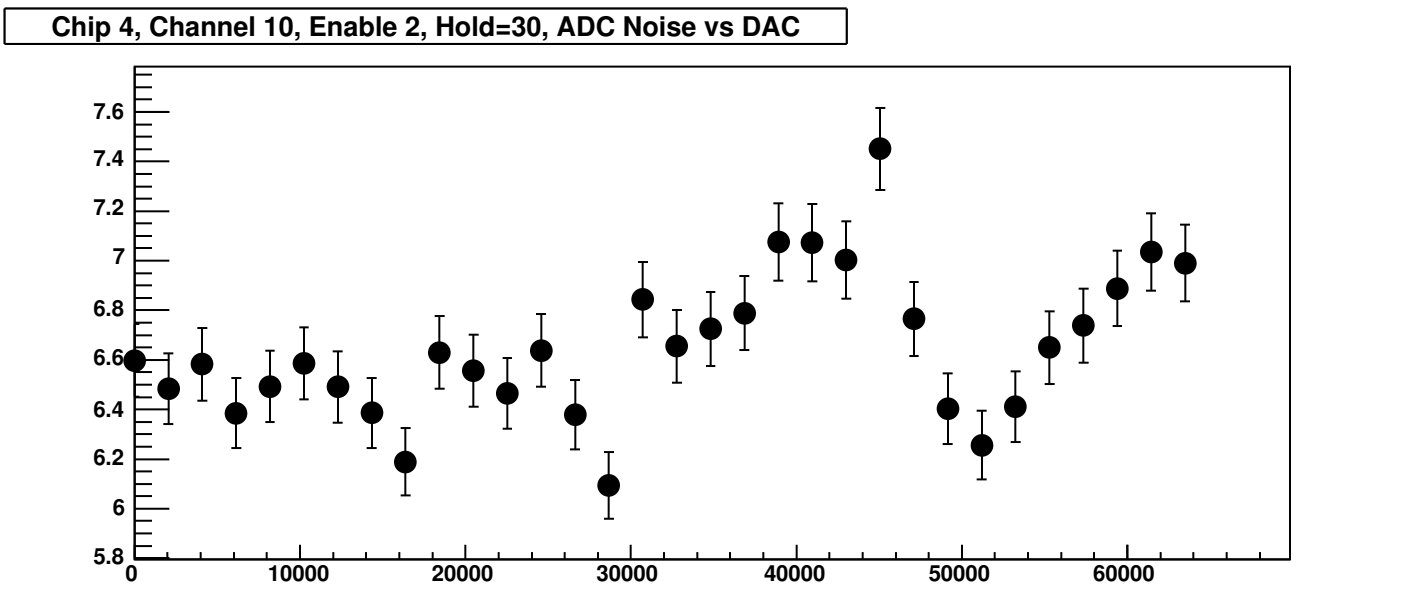

Chip 4, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

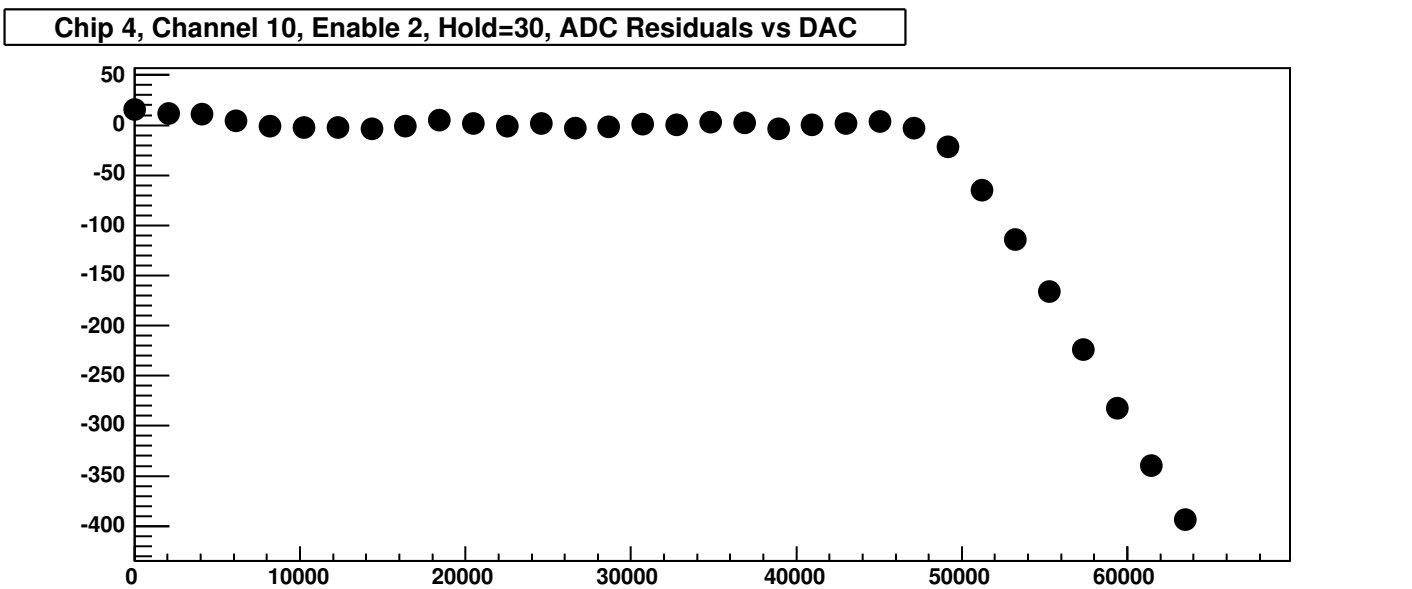

Chip 4, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2109 / 15
p0 1082 ± 0.1213
p1 -8.268e-05 ± 4.91e-06
1100
1000
900
800
700
0
10000
20000
30000
40000
50000
60000

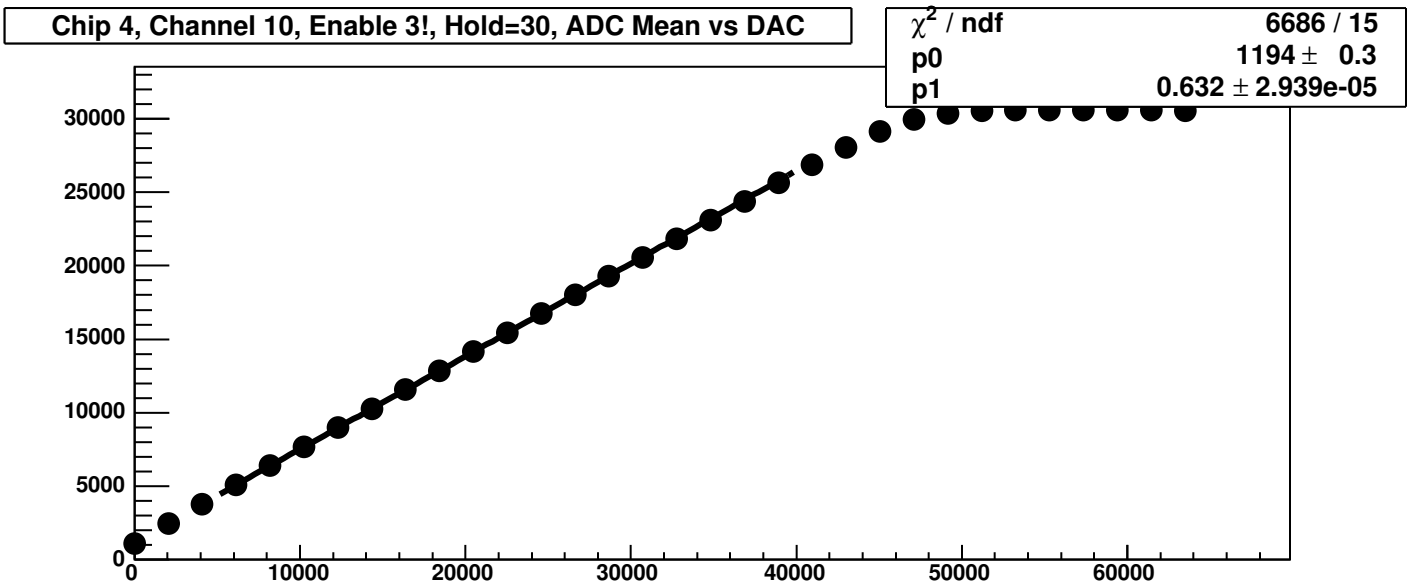

Chip 4, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

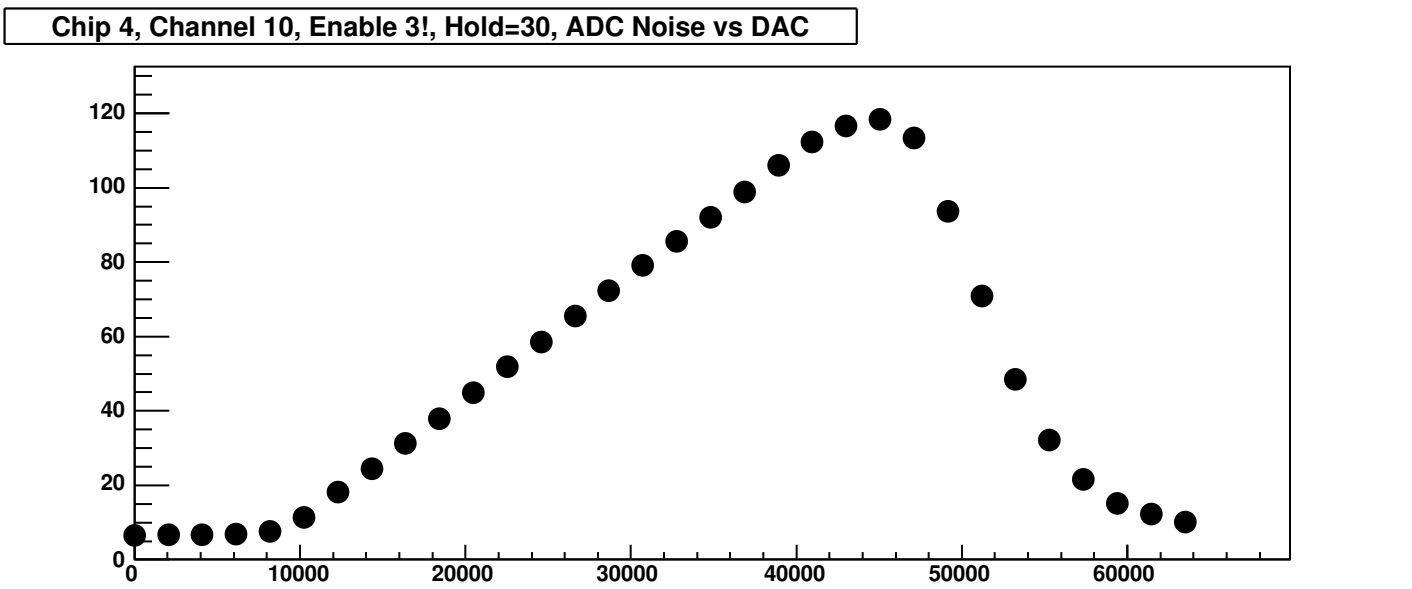

Chip 4, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

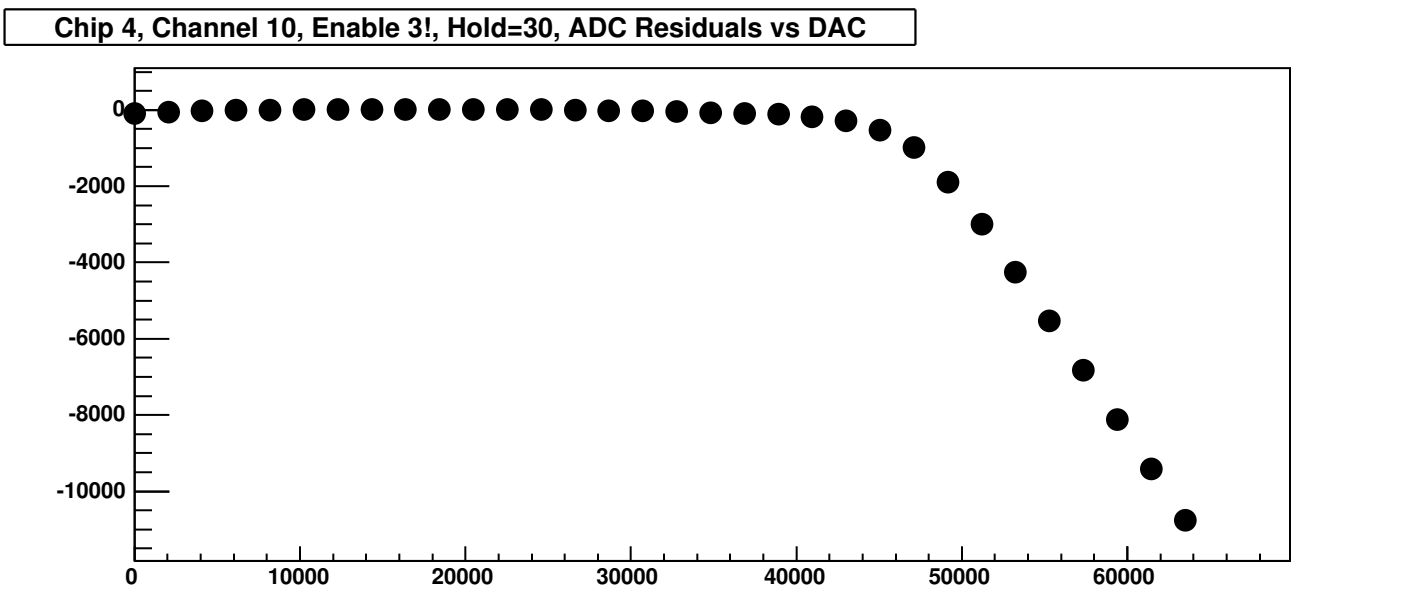

Chip 4, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2715 / 15
p0 1080 ± 0.1216
p1 -0.00129 ± 5.006e-06
1100
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

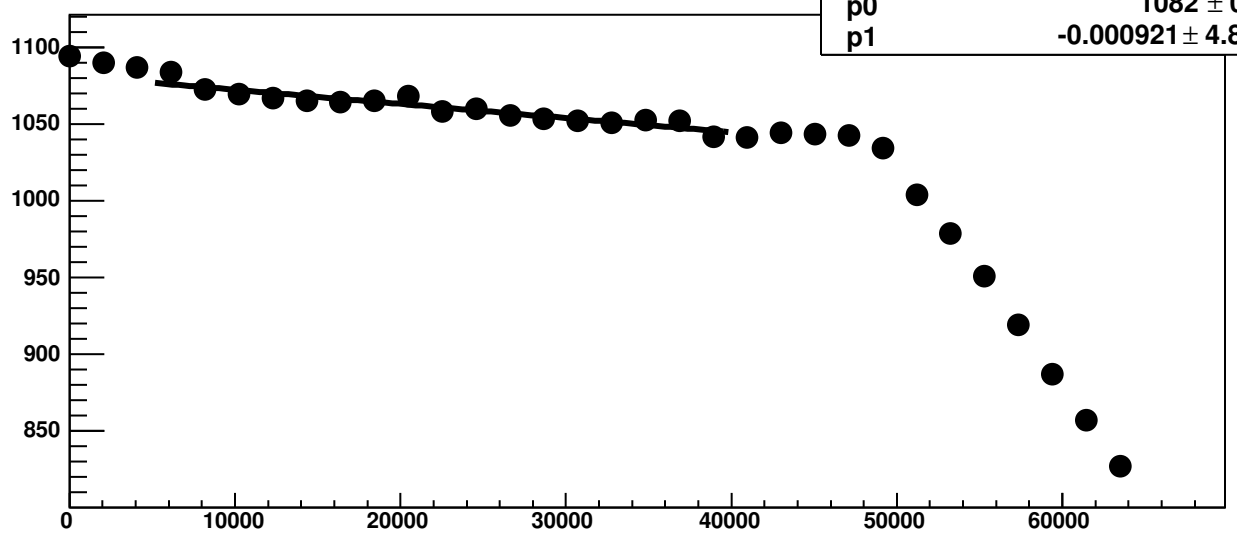

Chip 4, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 6686 / 15
p0 1194 ± 0.3
p1 0.632 ± 2.939e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

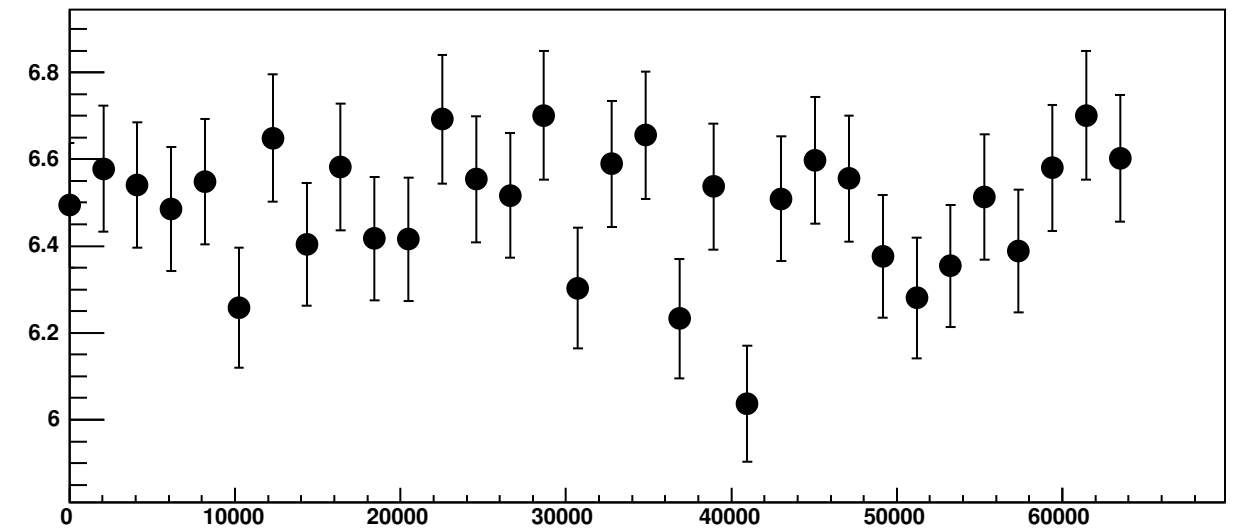

Chip 4, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

Chip 4, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

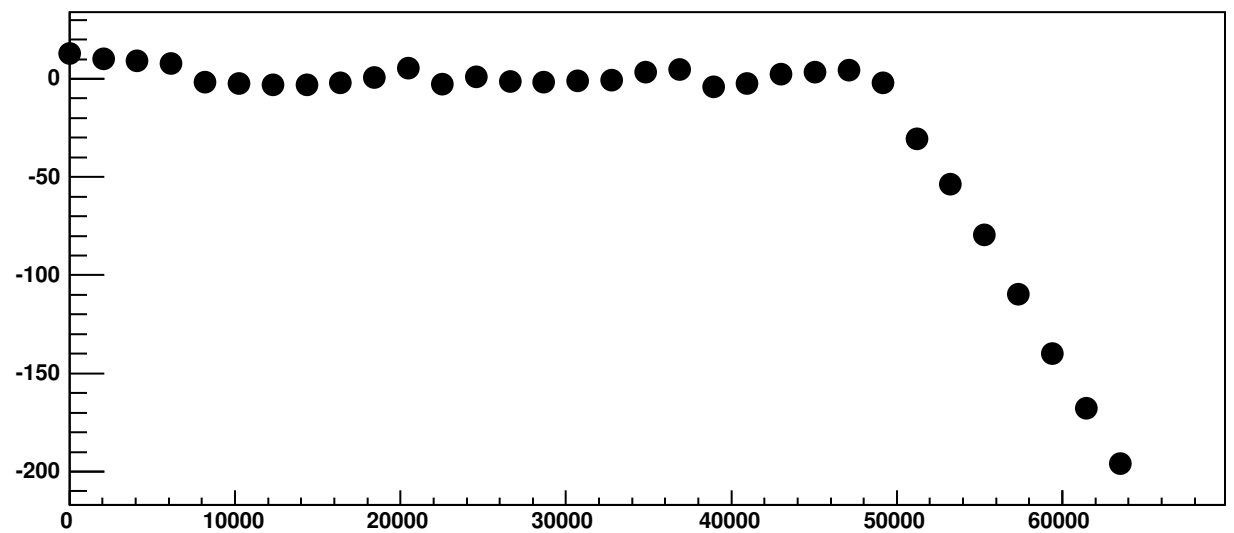
χ^2 / ndf	4616 / 15
p0	1082 ± 0.1206
p1	-0.000921 ± 4.89e-06



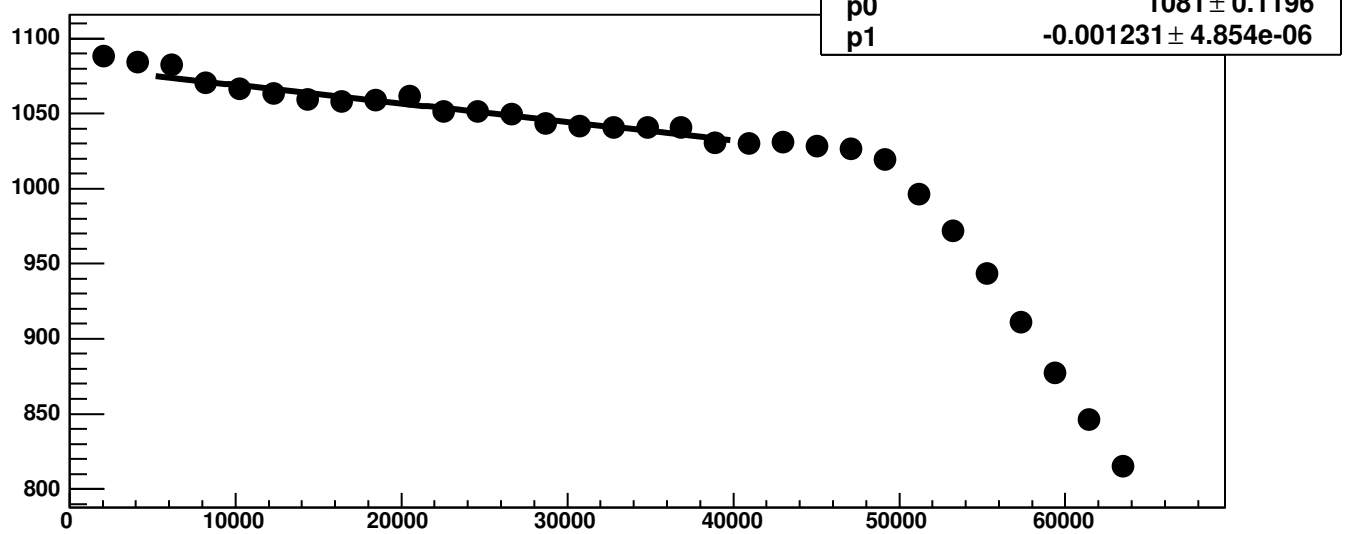
Chip 4, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



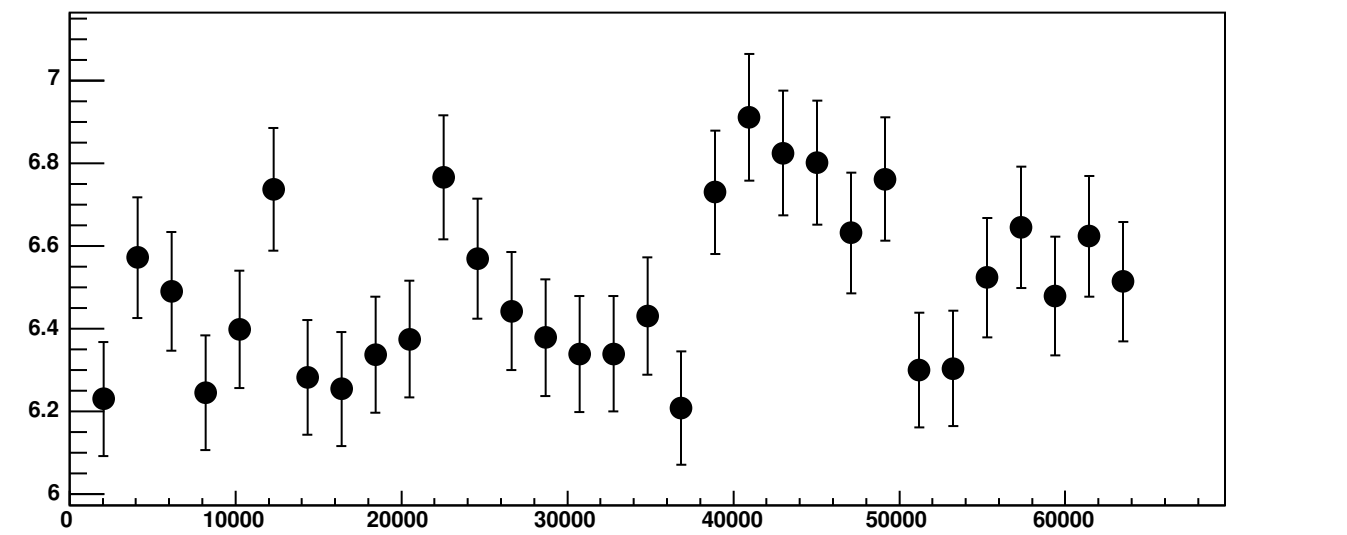
Chip 4, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



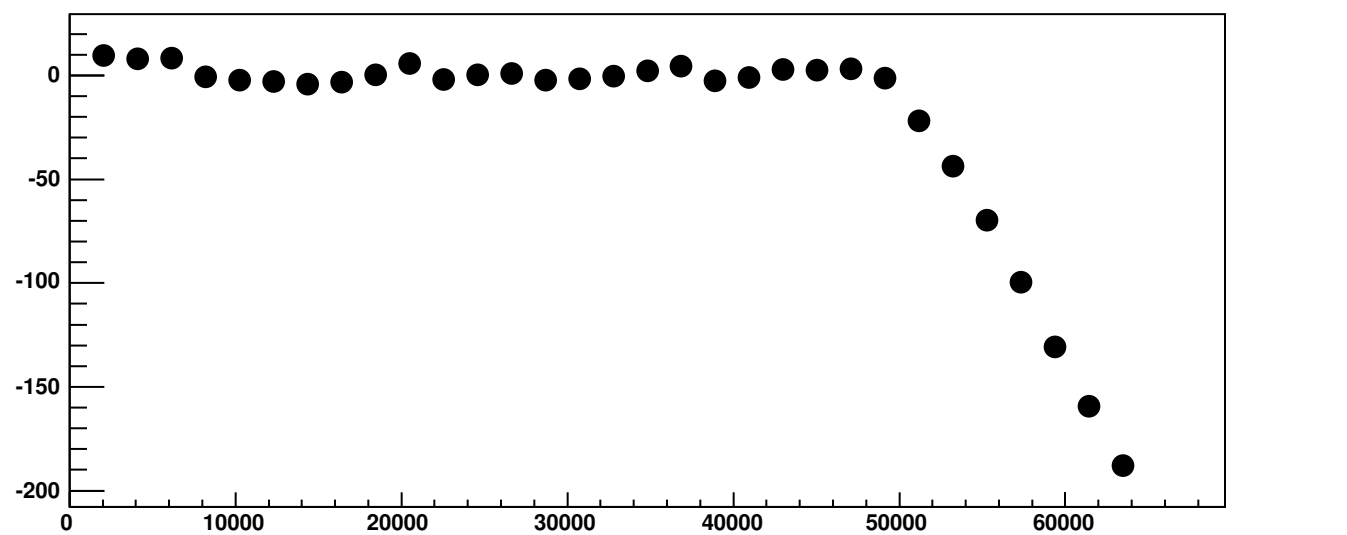
Chip 4, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC

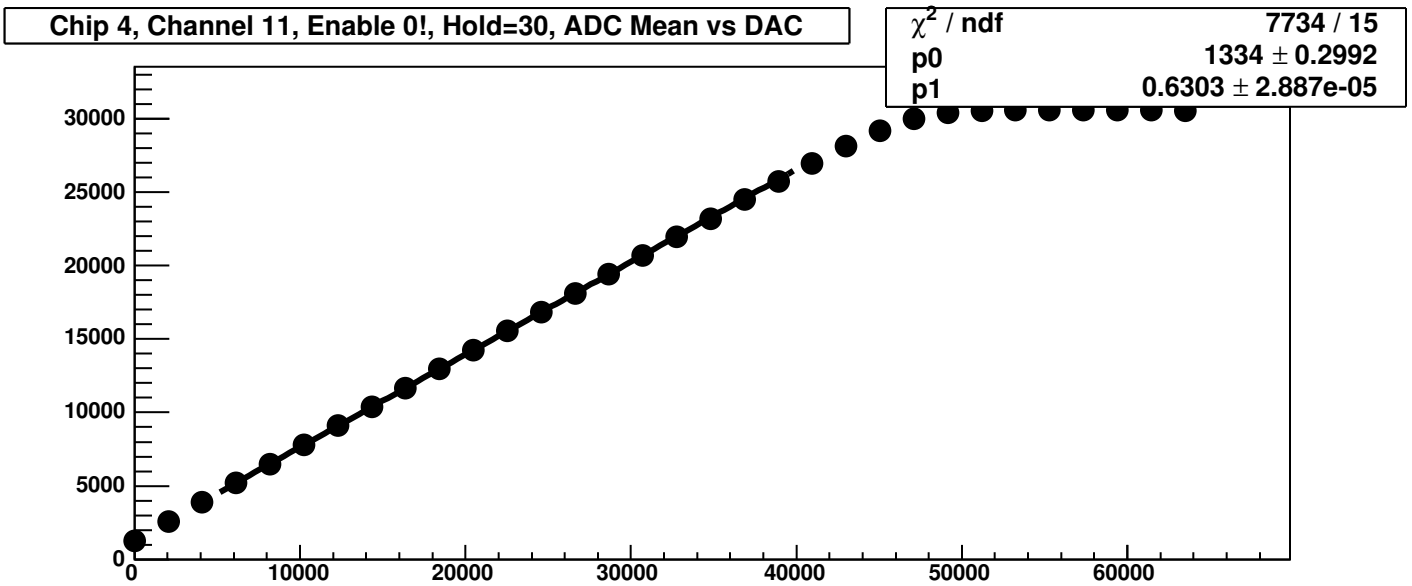


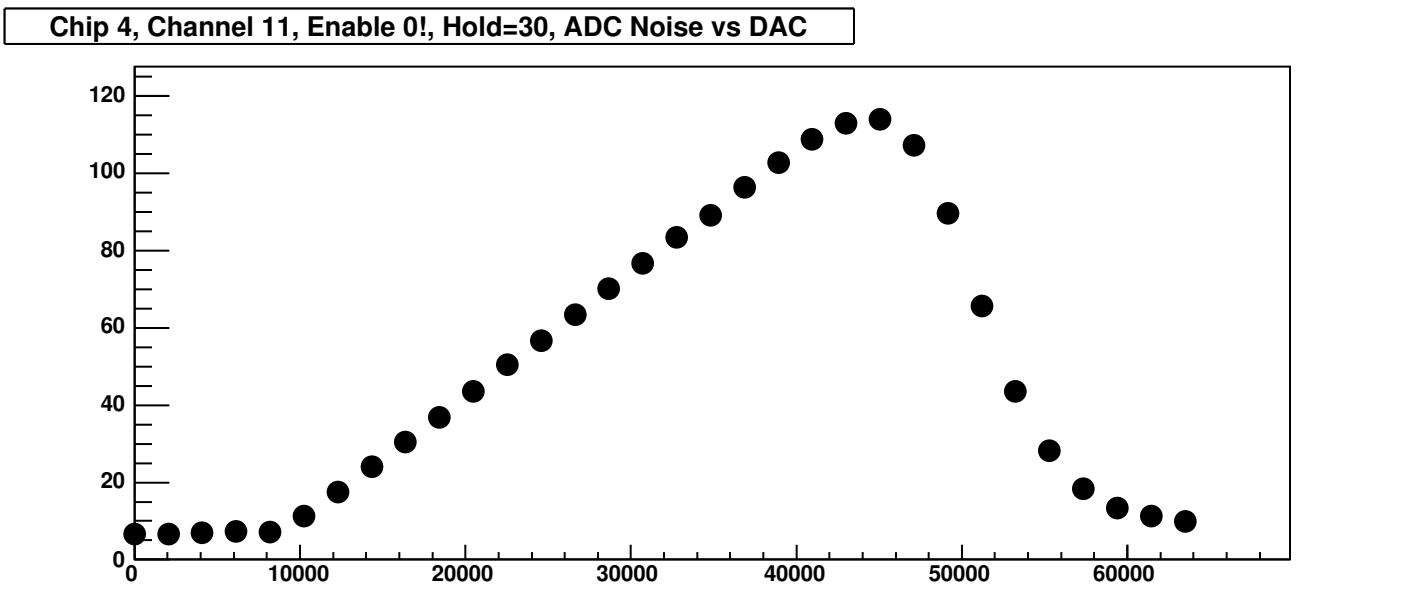
Chip 4, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC

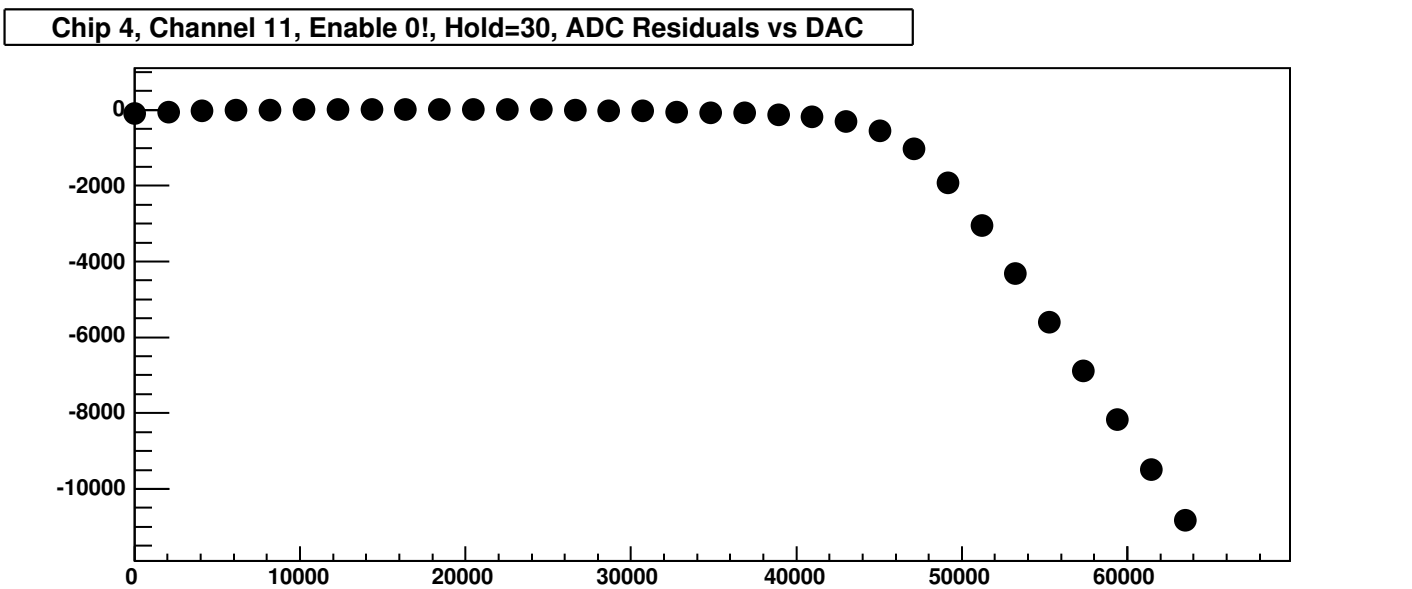


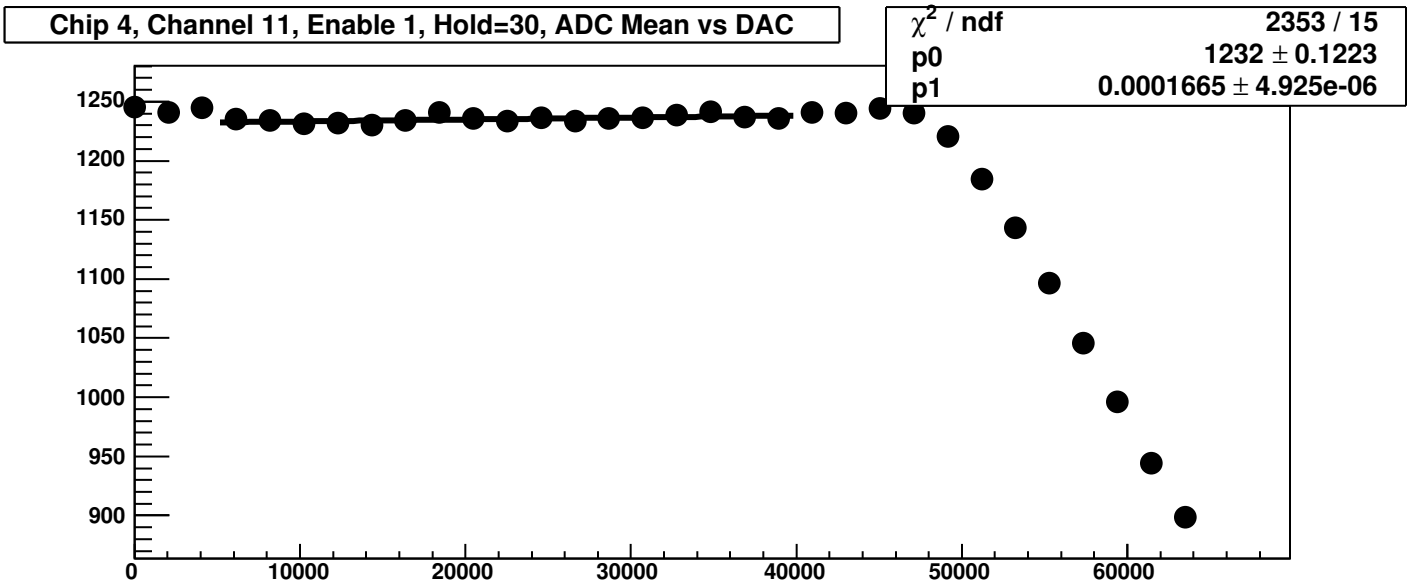
Chip 4, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC

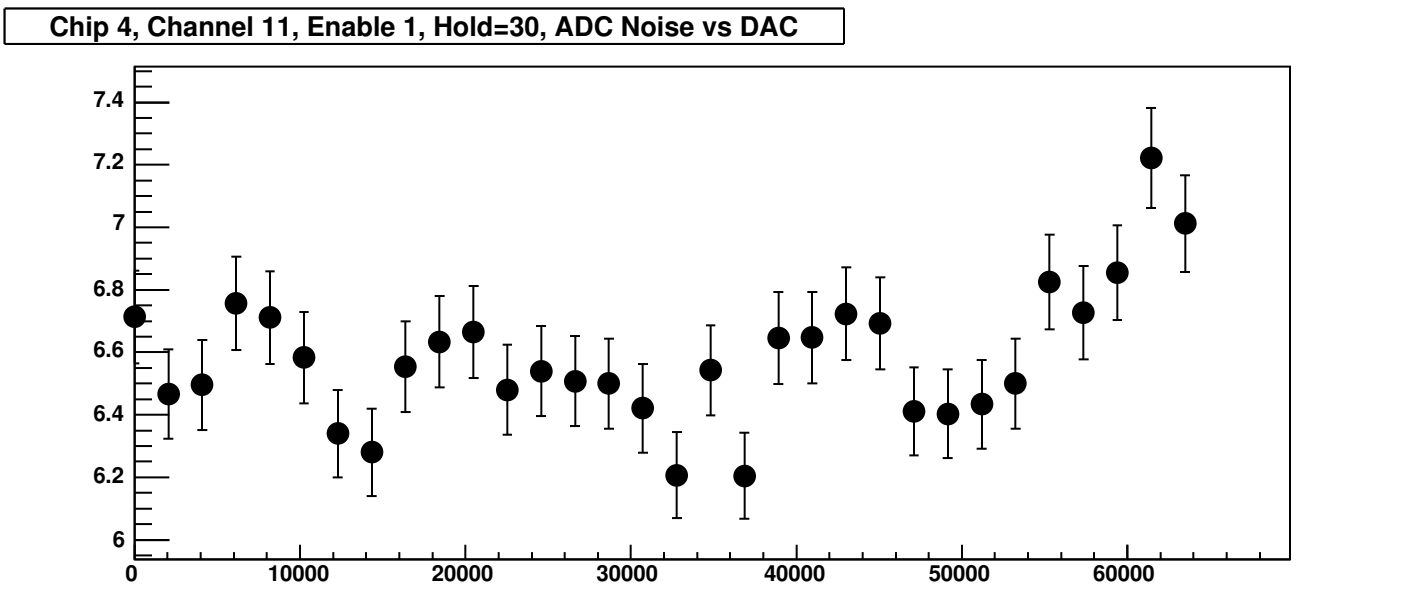


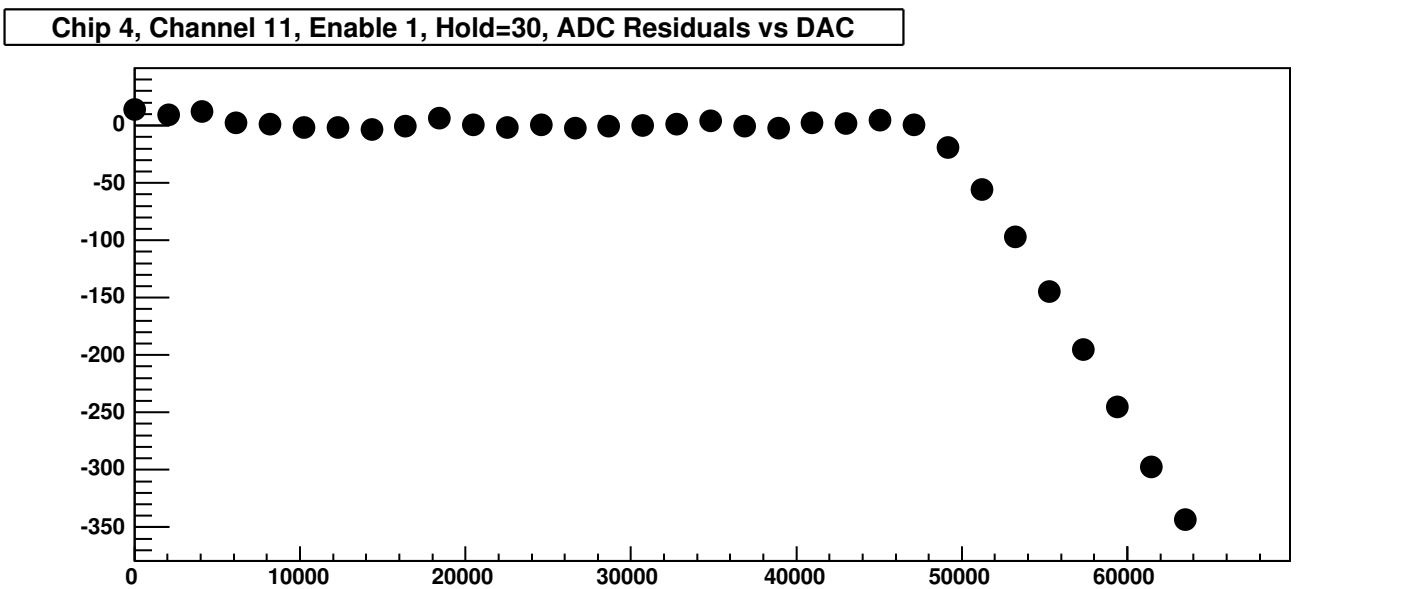

Chip 4, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 7734 / 15
p0 1334 ± 0.2992
p1 0.6303 ± 2.887e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

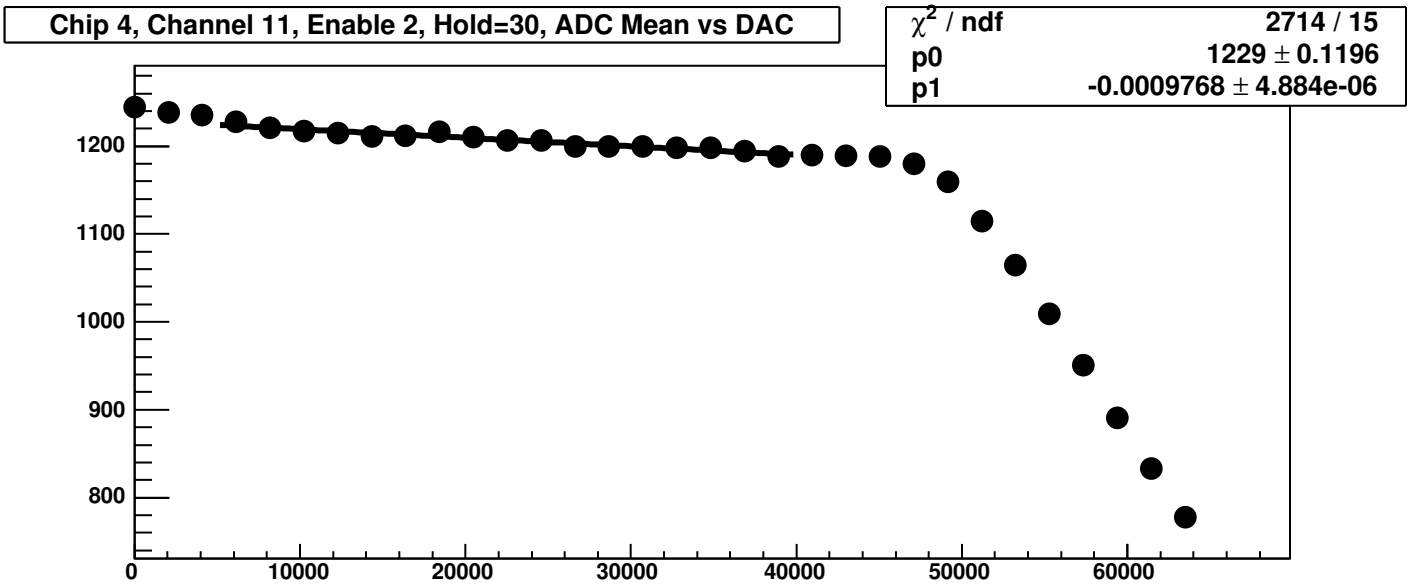

Chip 4, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

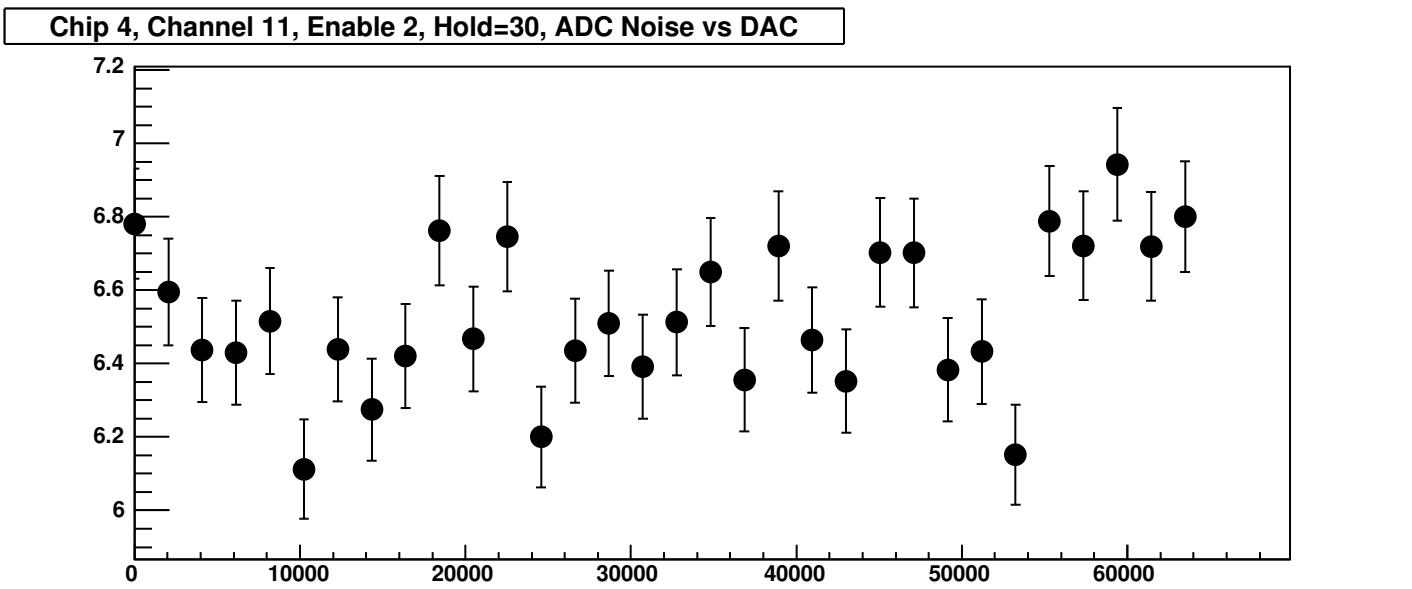

Chip 4, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

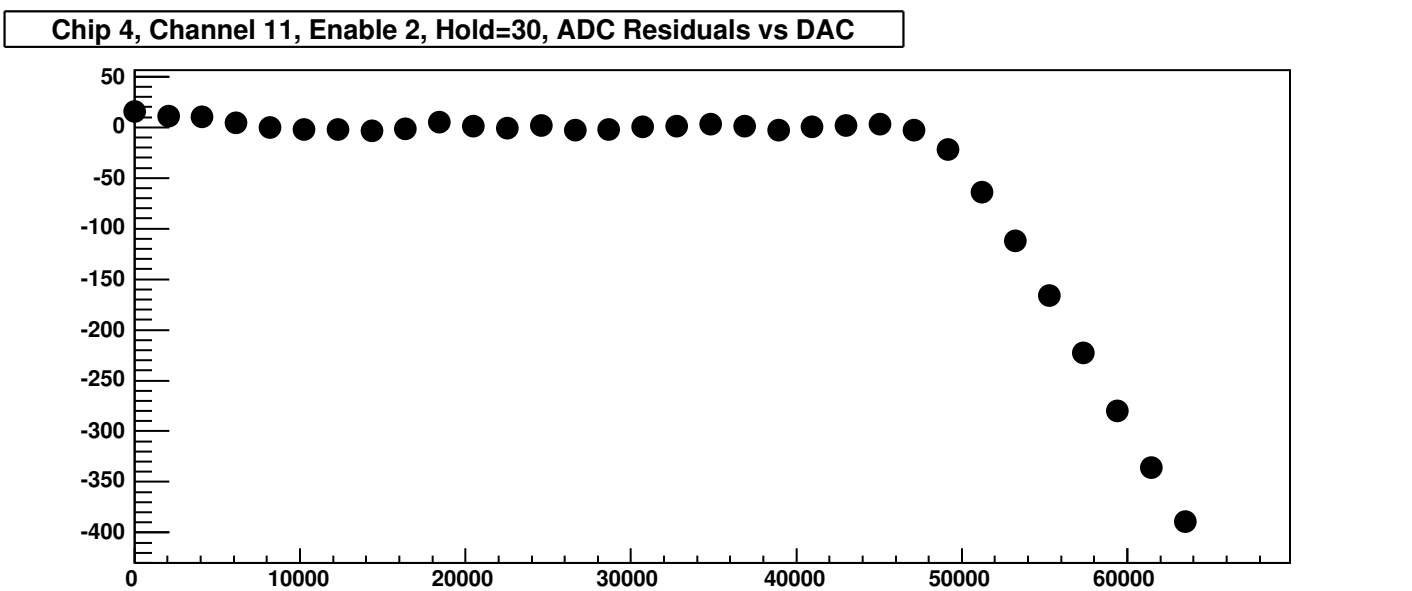

Chip 4, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2353 / 15
p0 1232 ± 0.1223
p1 0.0001665 ± 4.925e-06
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

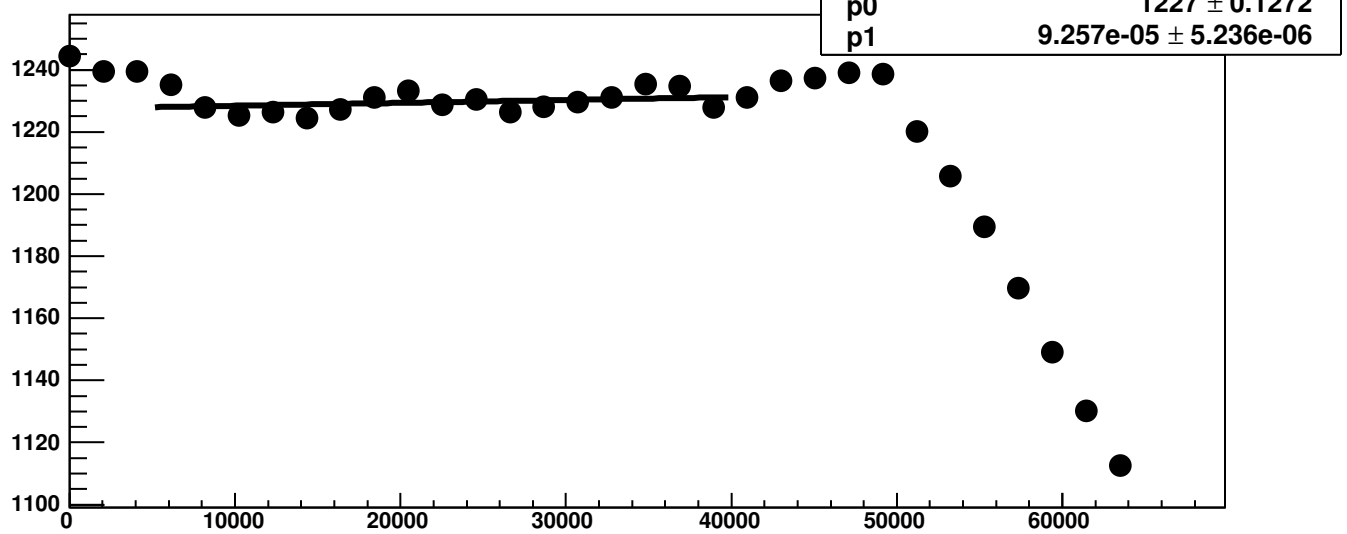

Chip 4, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2714 / 15
p0 1229 ± 0.1196
p1 -0.0009768 ± 4.884e-06
1200
1100
1000
900
800
0
10000
20000
30000
40000
50000
60000

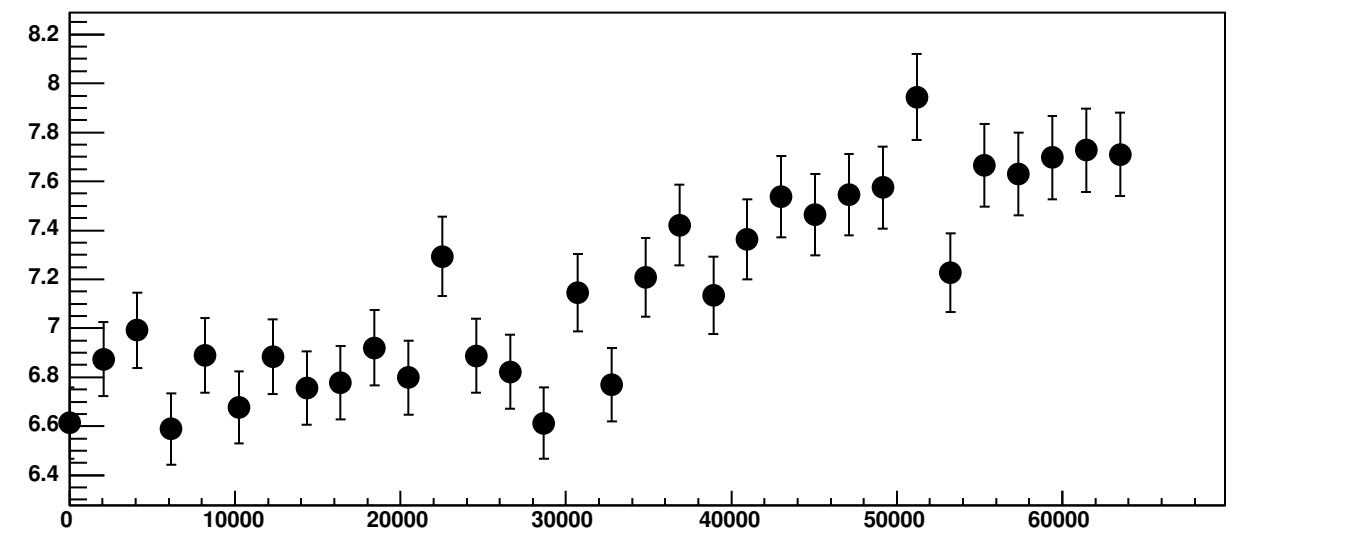

Chip 4, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

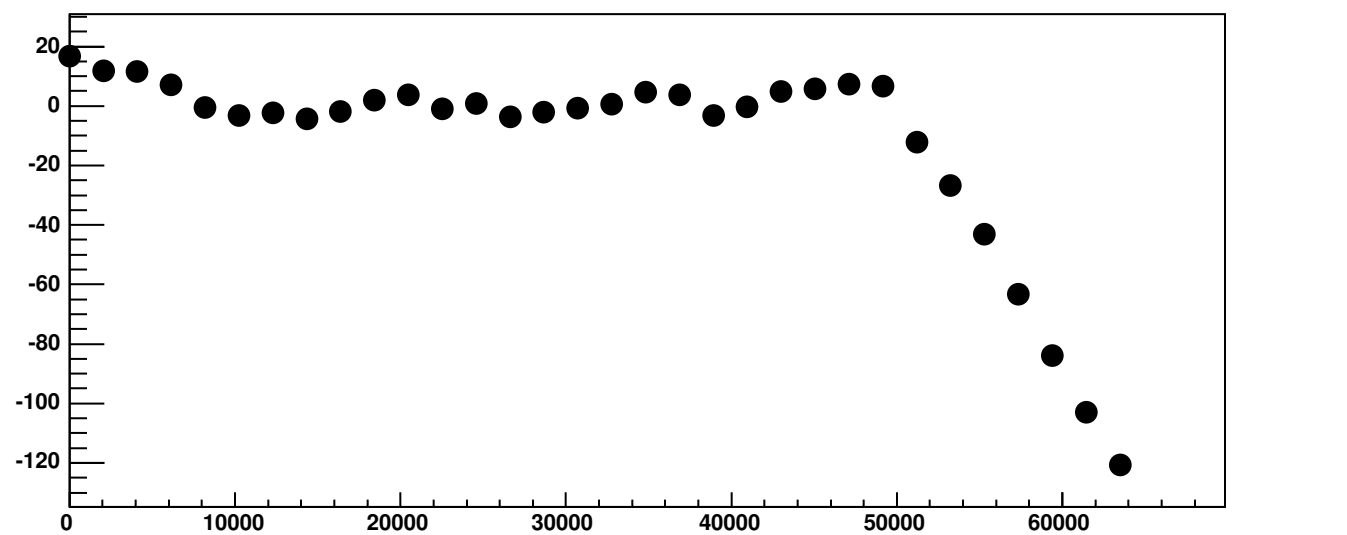
Chip 4, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC

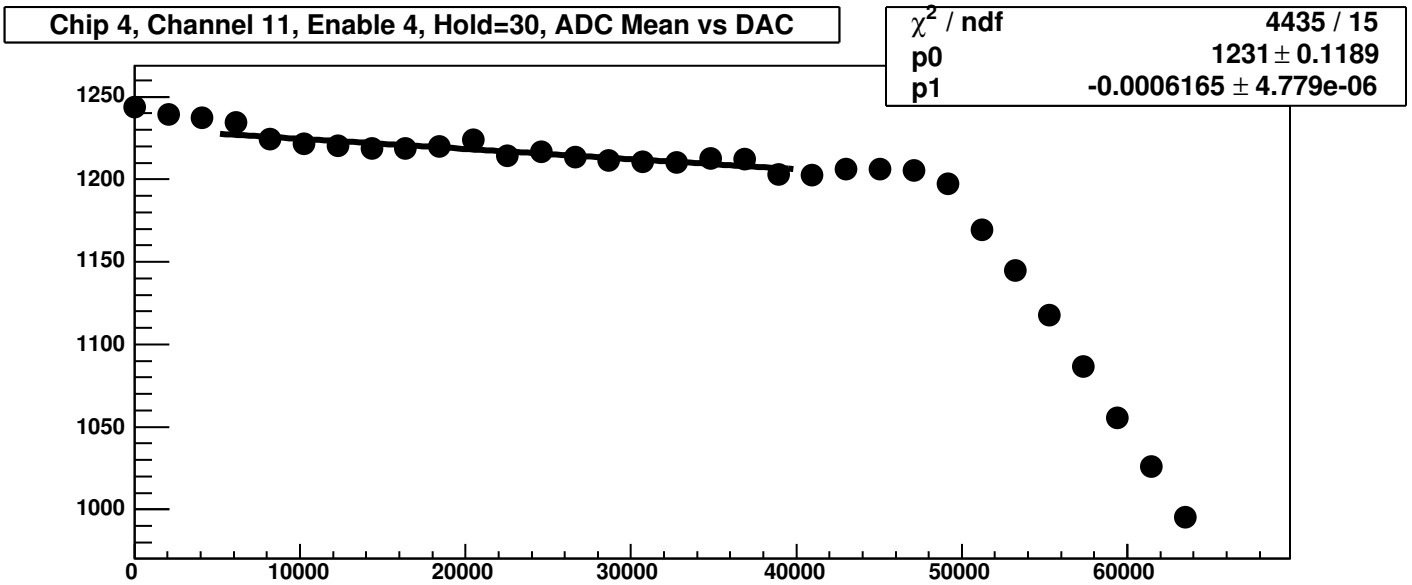


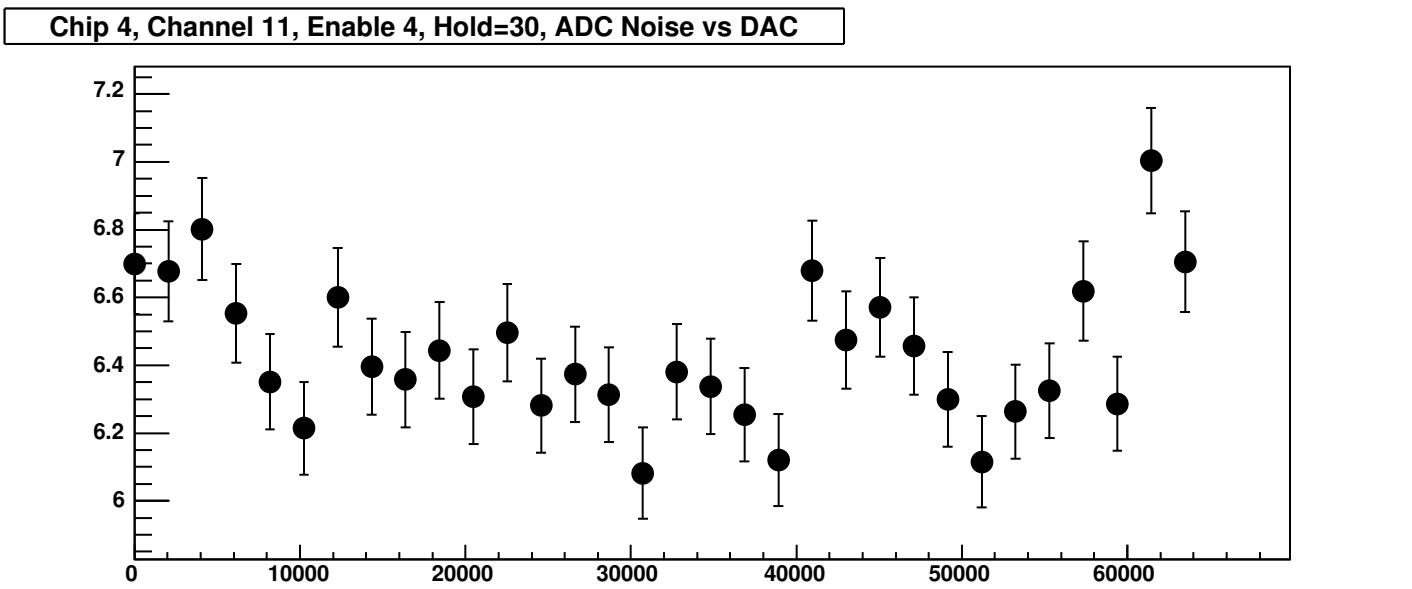
Chip 4, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC

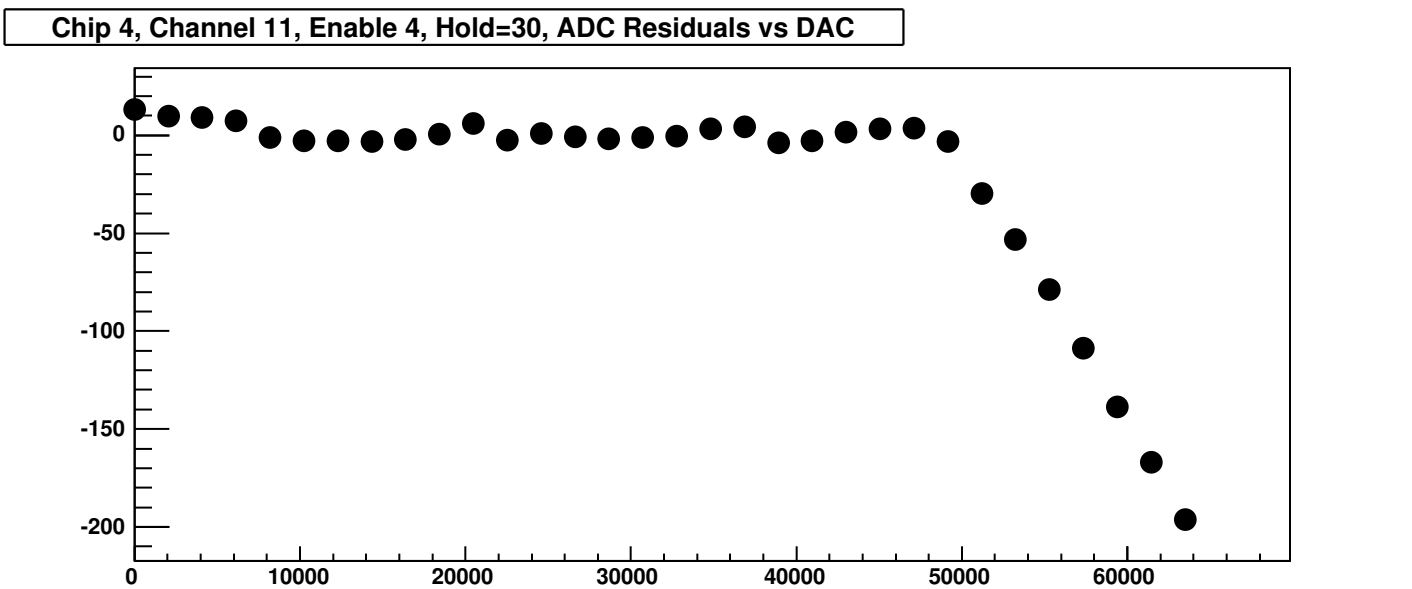


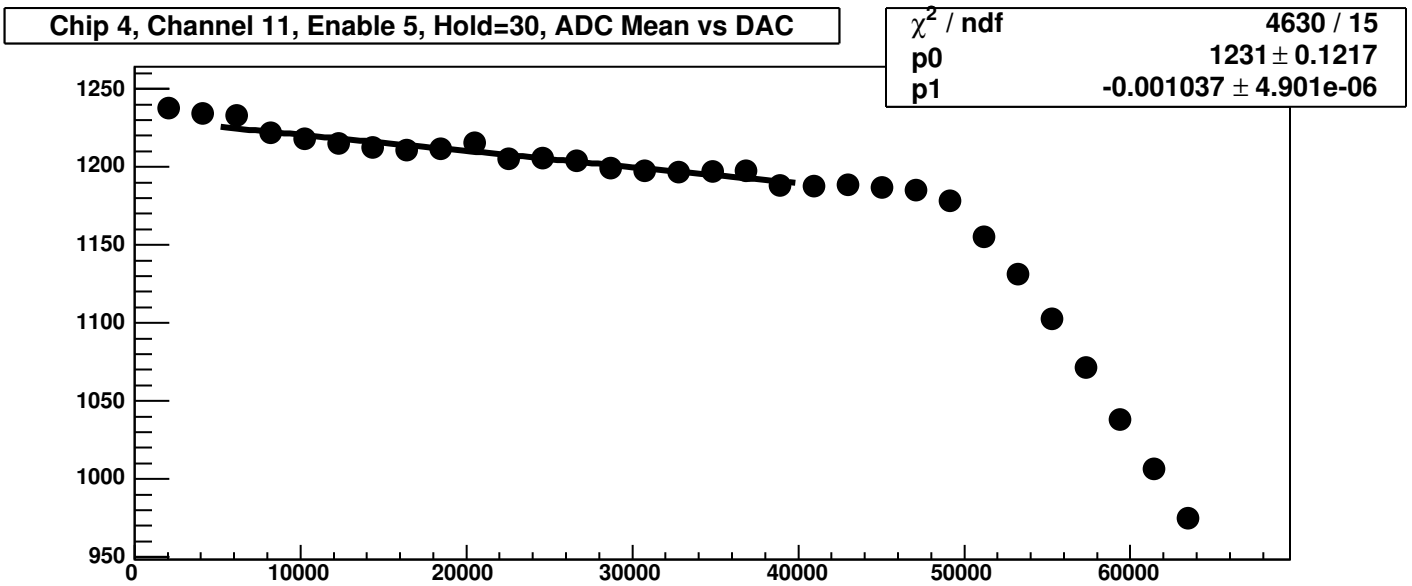
Chip 4, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC

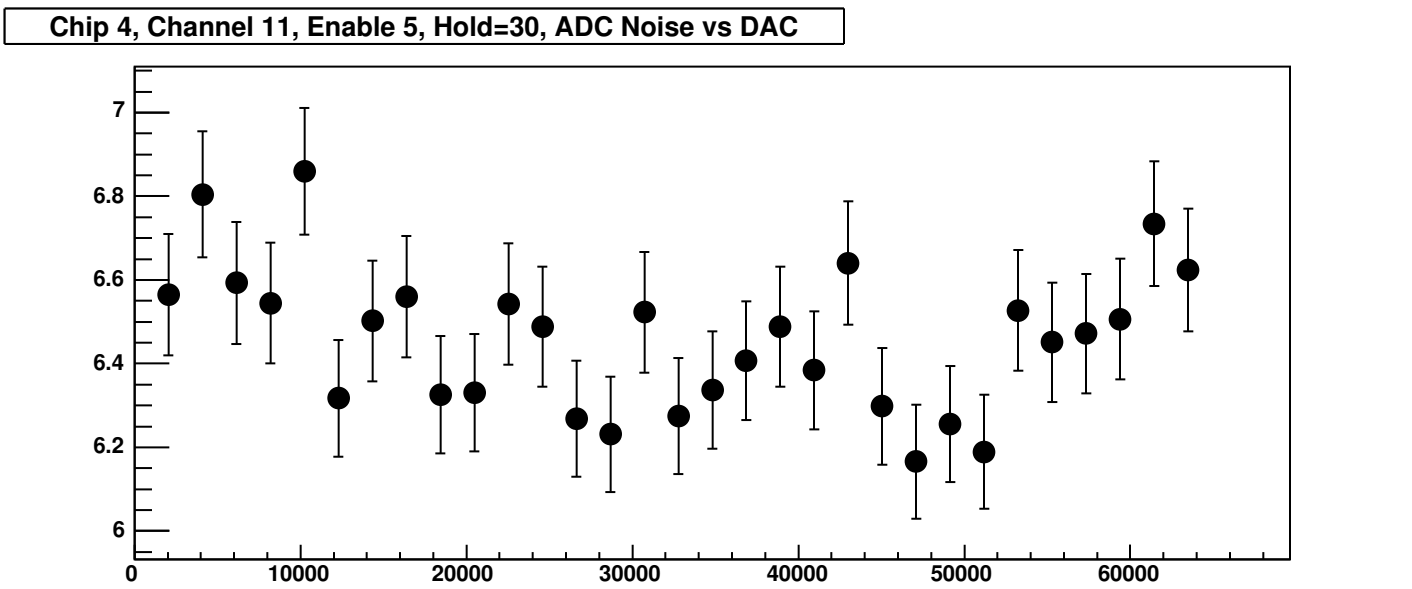


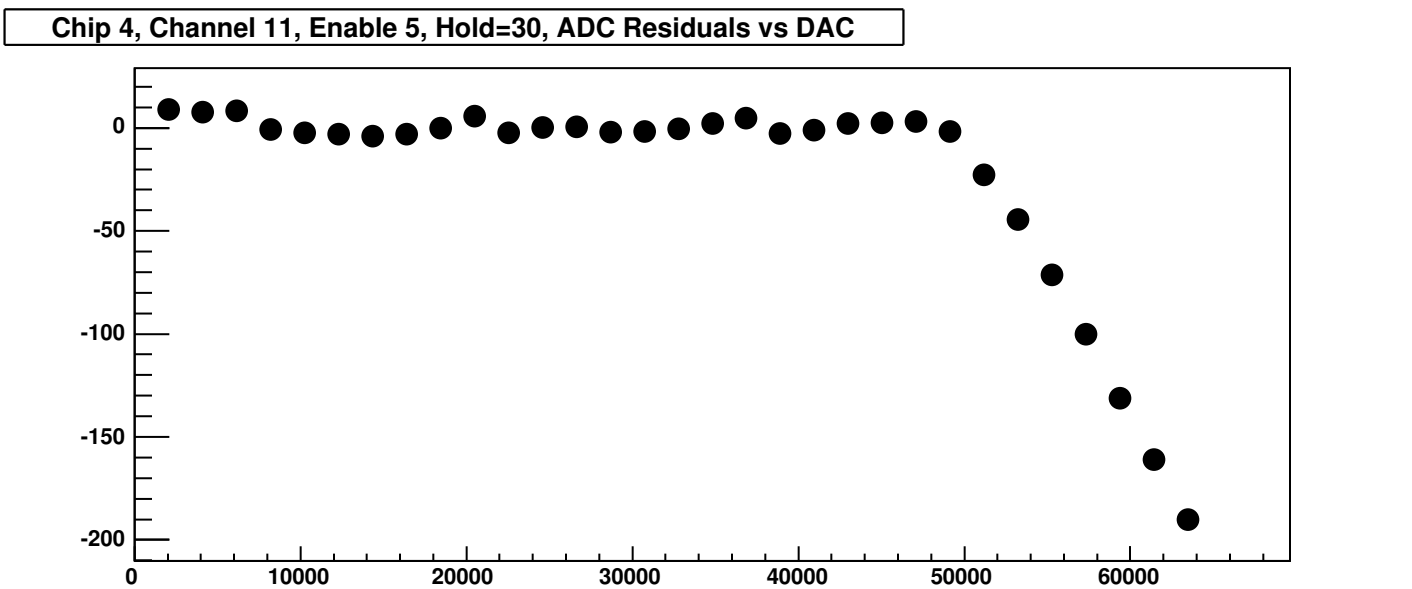

Chip 4, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4435 / 15
p0 1231 ± 0.1189
p1 -0.0006165 ± 4.779e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

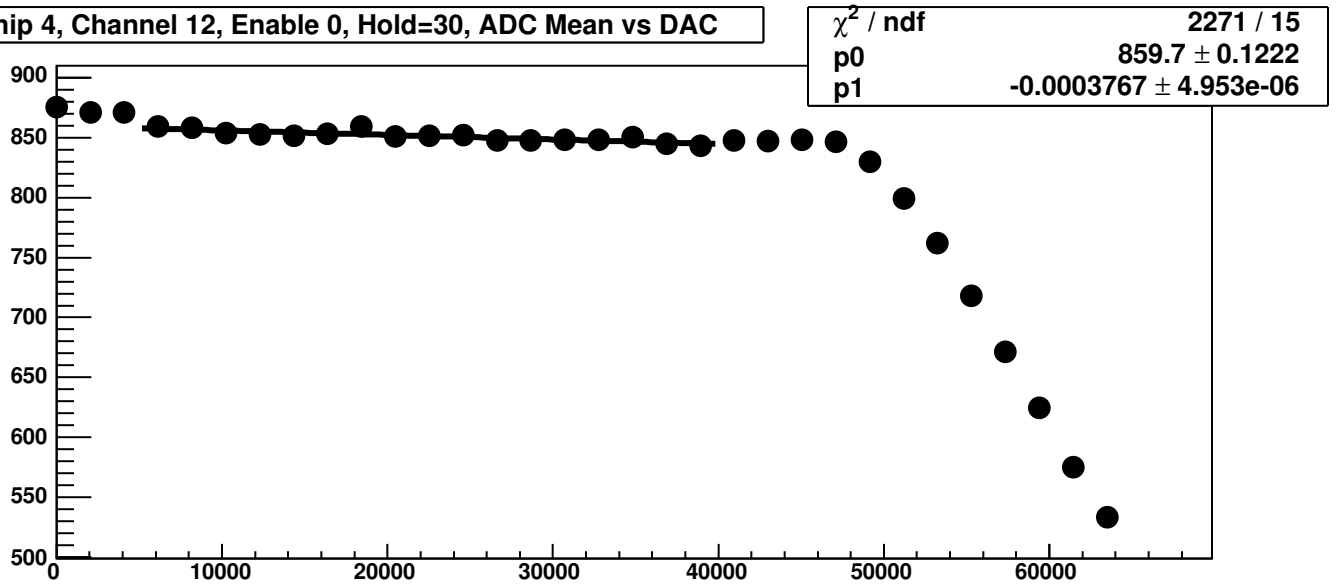

Chip 4, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4630 / 15
p0 1231 ± 0.1217
p1 -0.001037 ± 4.901e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

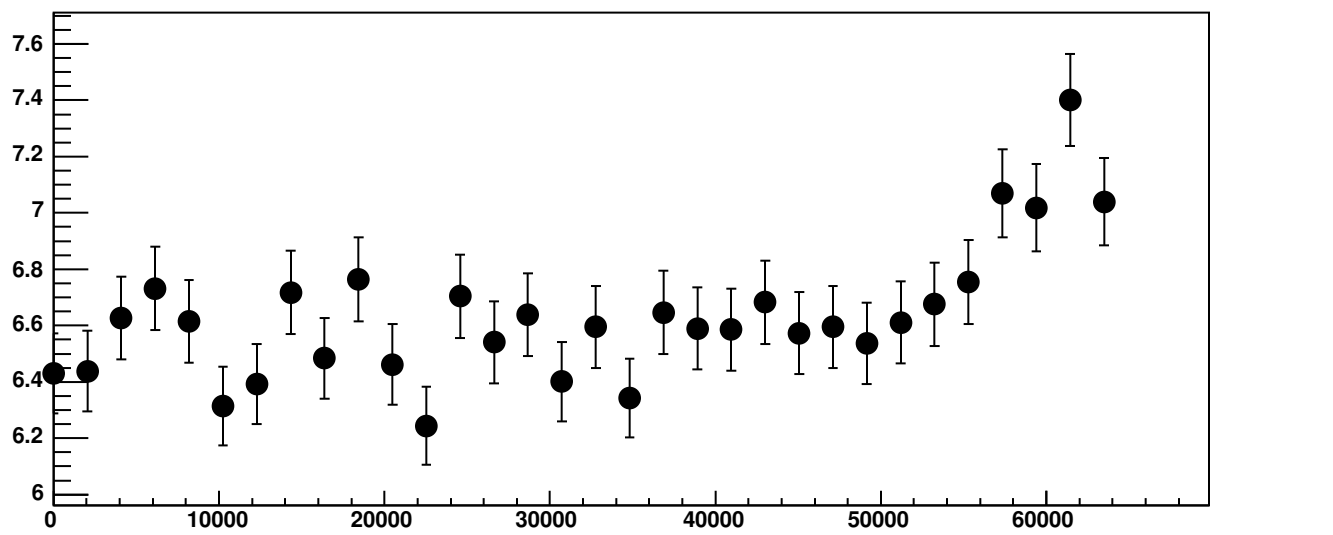

Chip 4, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

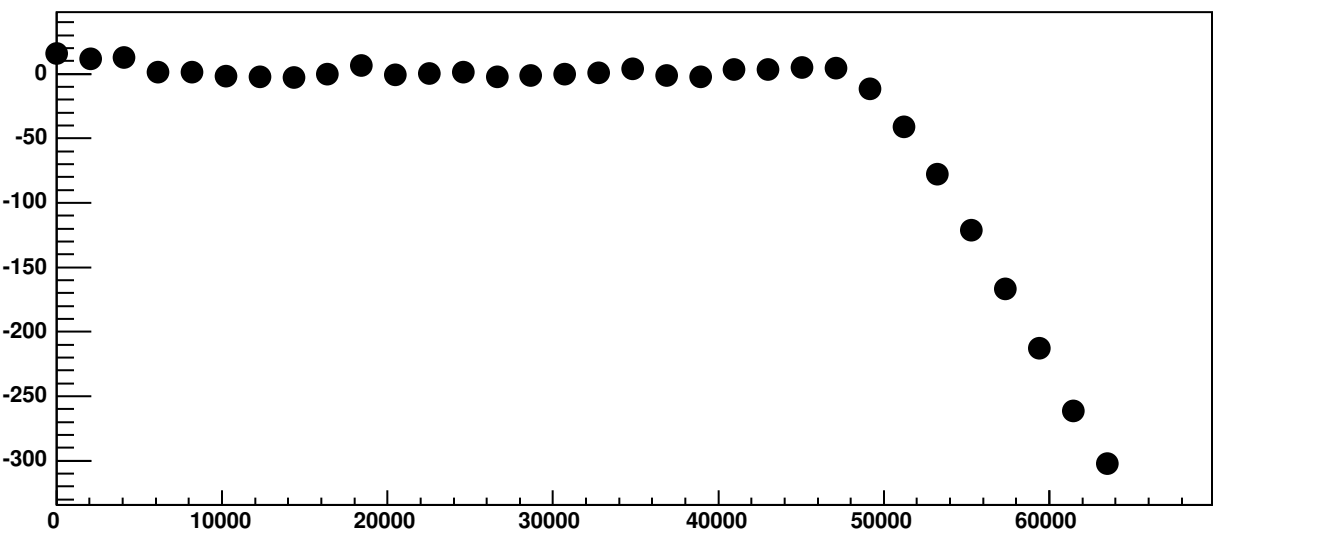
Chip 4, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

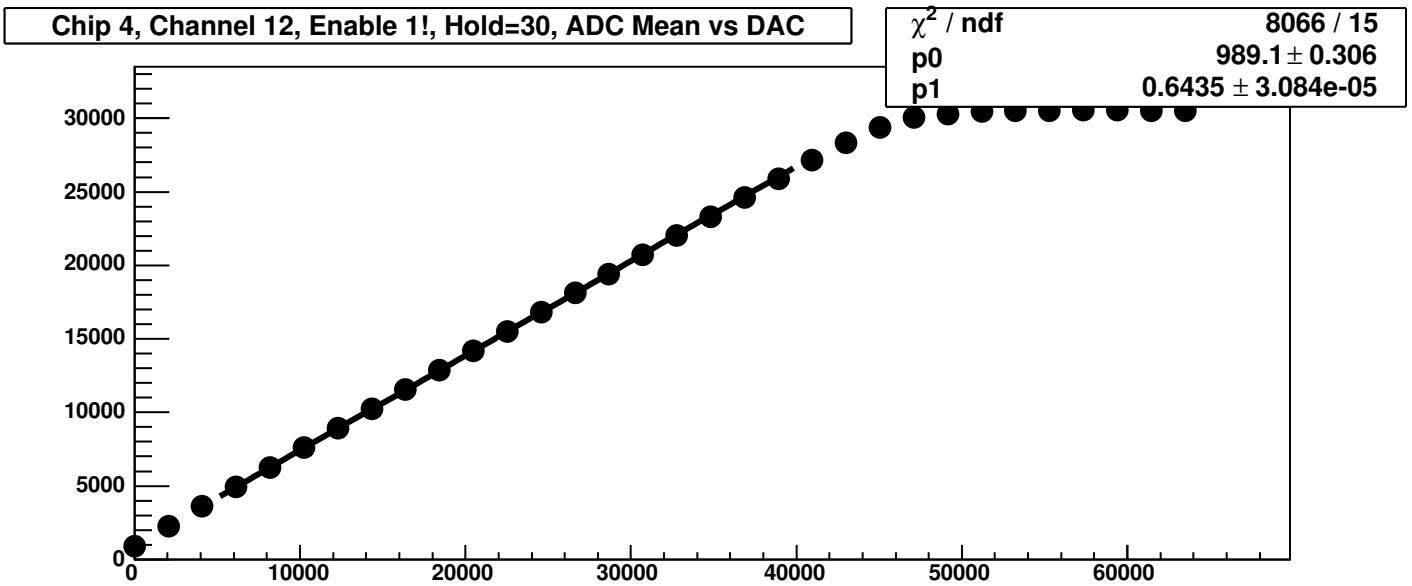


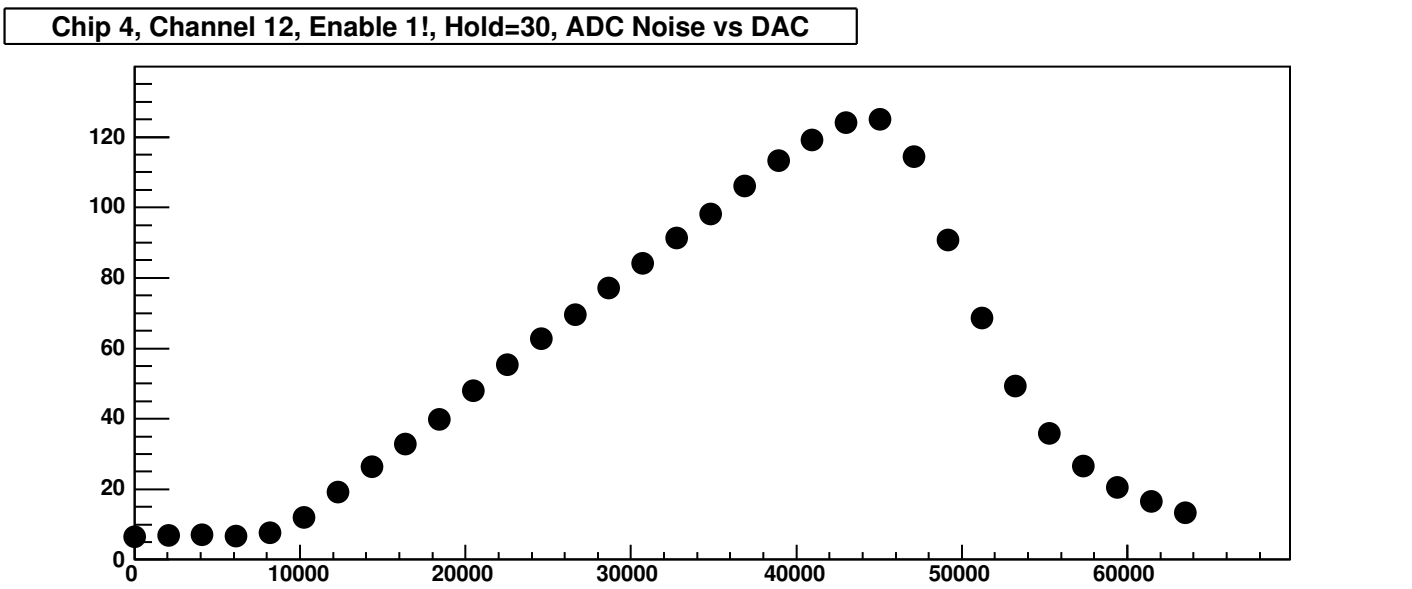
Chip 4, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC

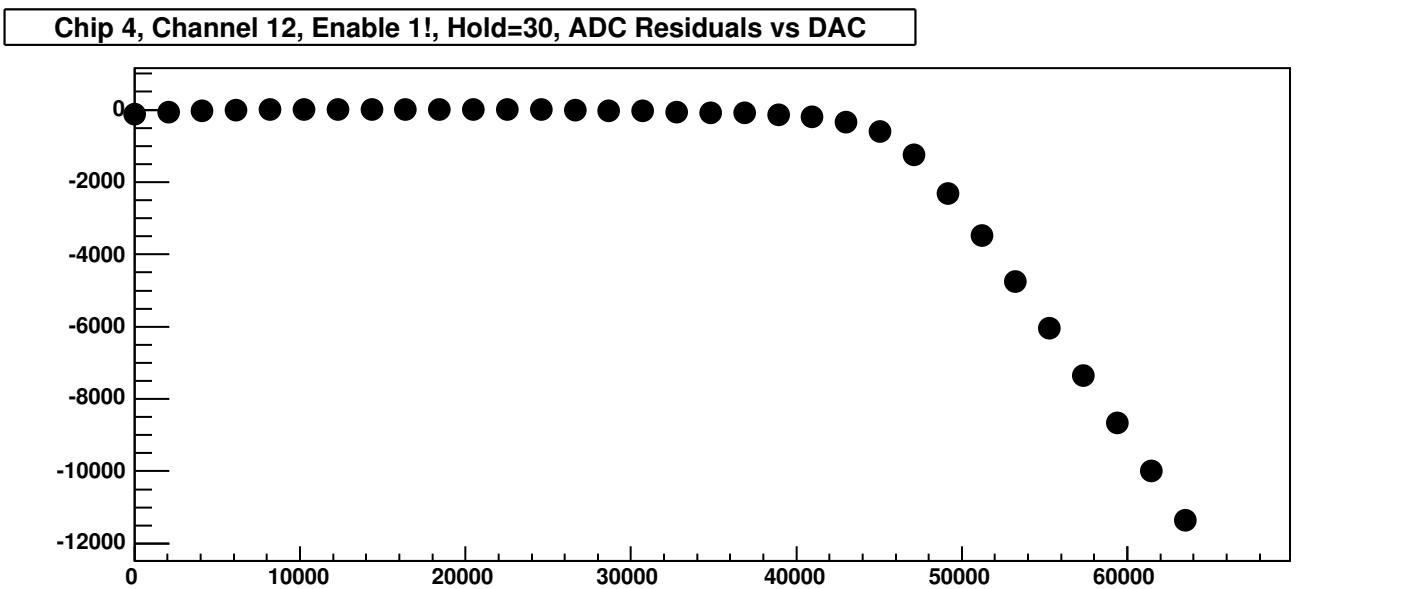


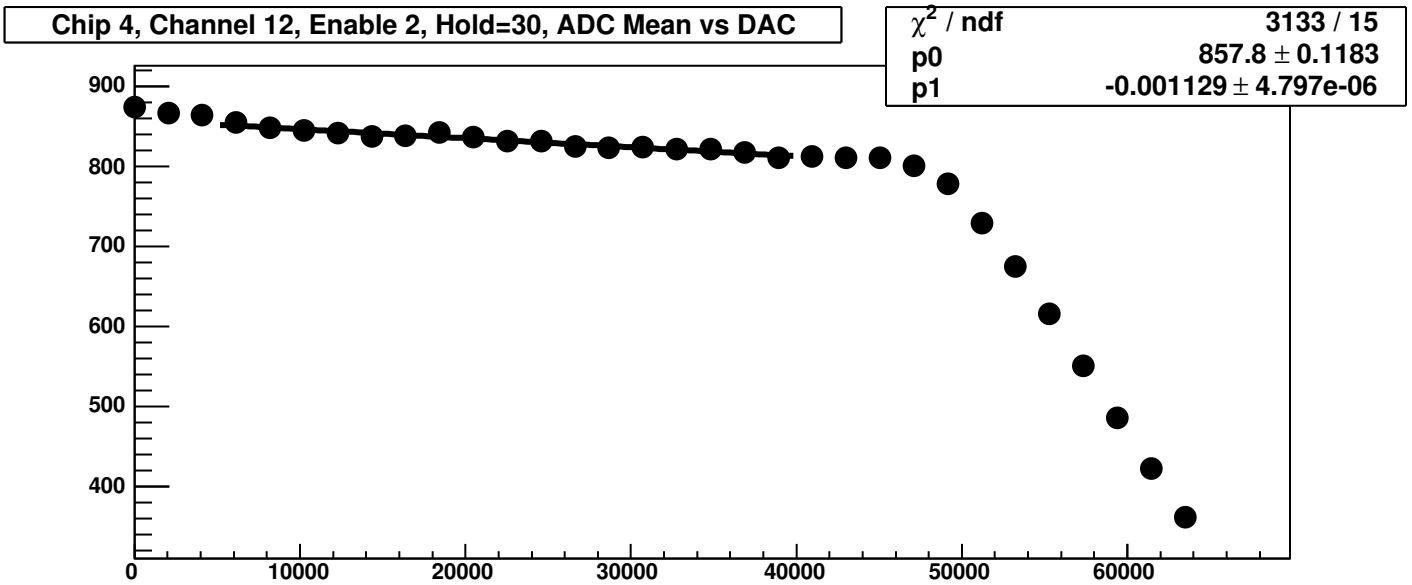
Chip 4, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

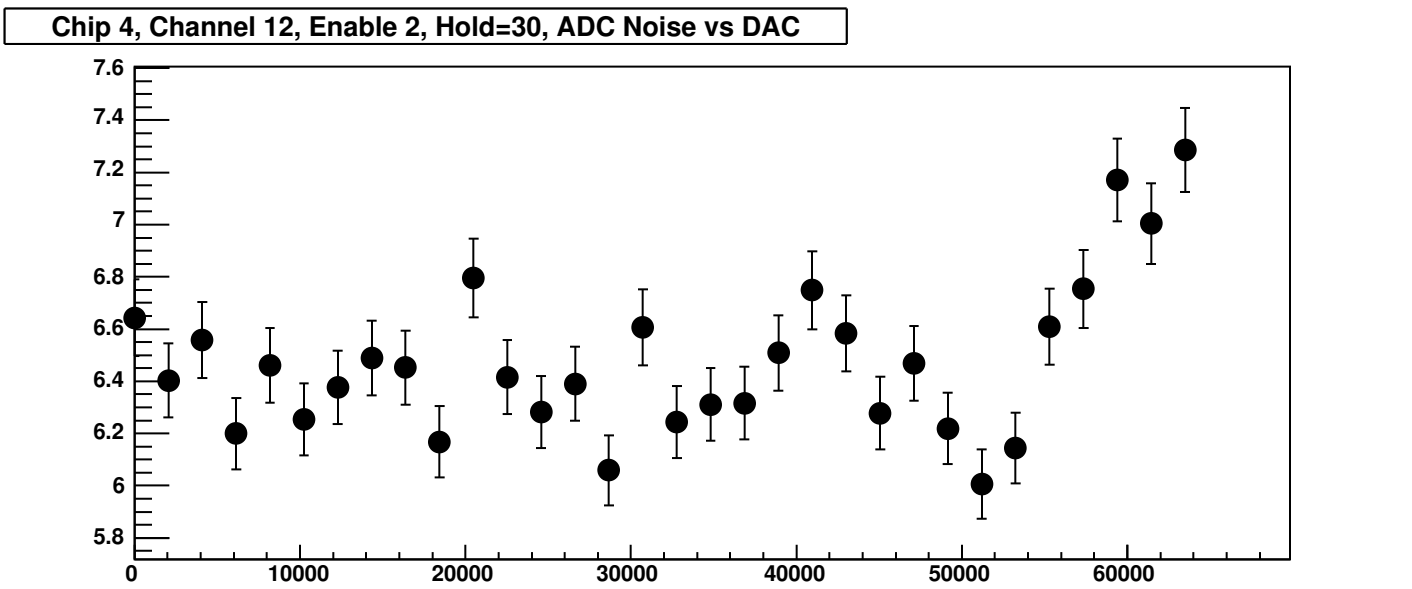


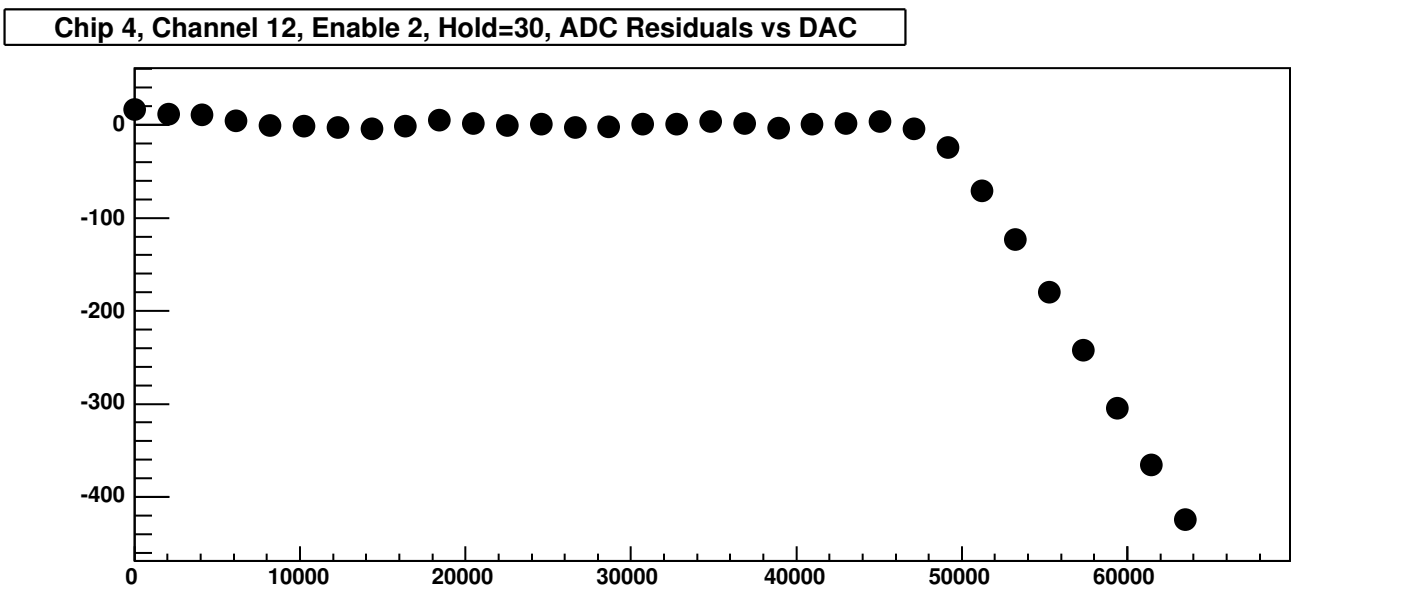

Chip 4, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 8066 / 15
p0 989.1 ± 0.306
p1 0.6435 ± 3.084e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

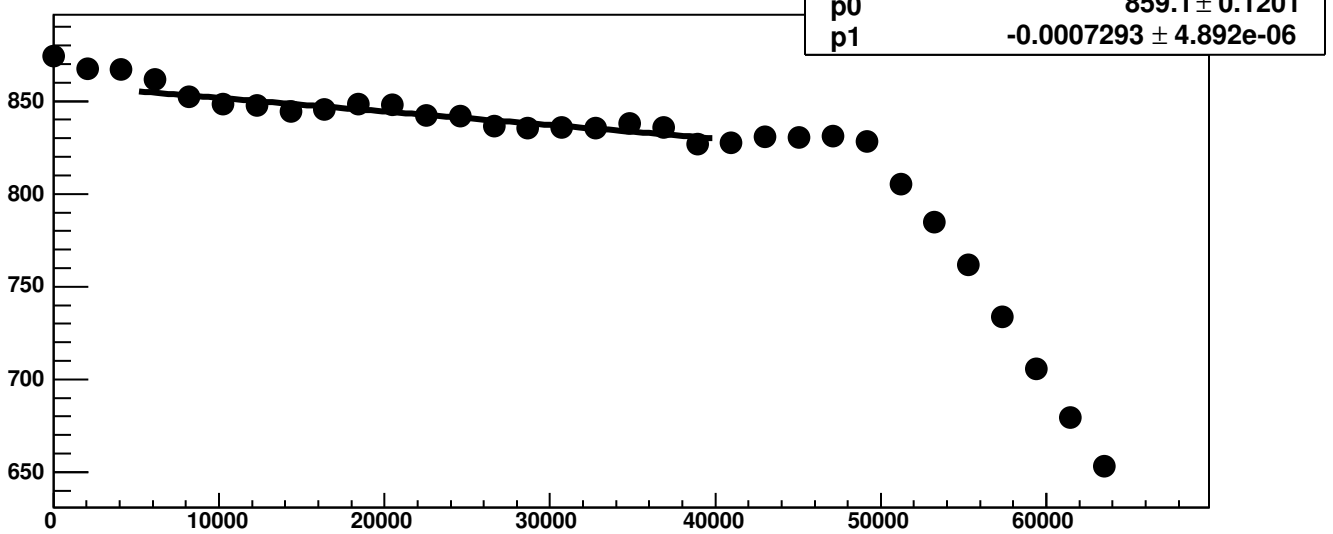

Chip 4, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3133 / 15
p0 857.8 ± 0.1183
p1 -0.001129 ± 4.797e-06
900
800
700
600
500
400
0
10000
20000
30000
40000
50000
60000

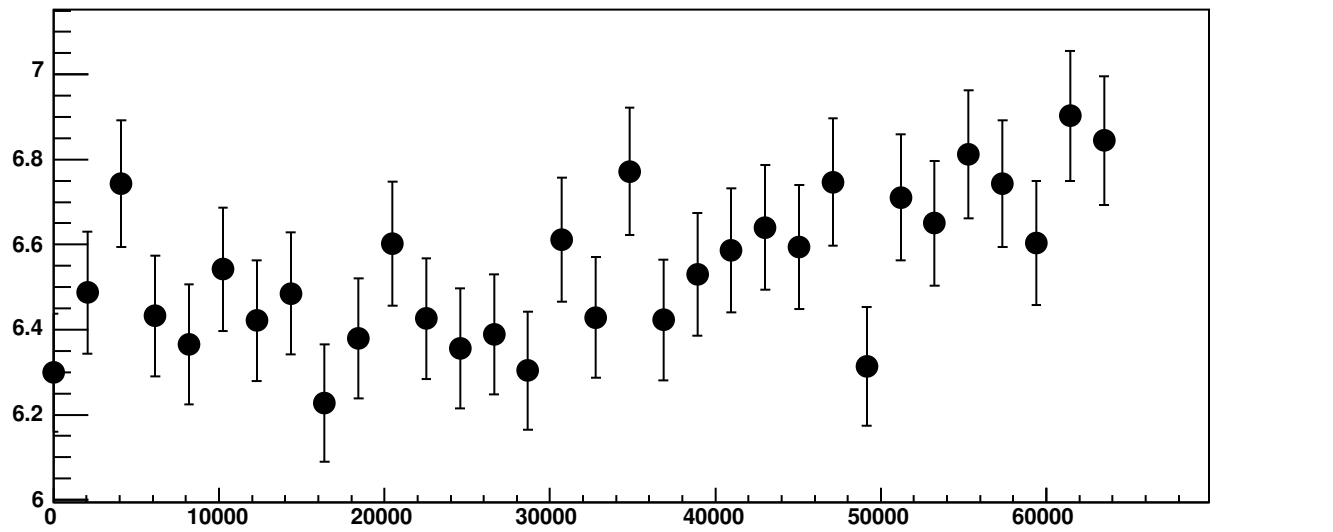

Chip 4, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

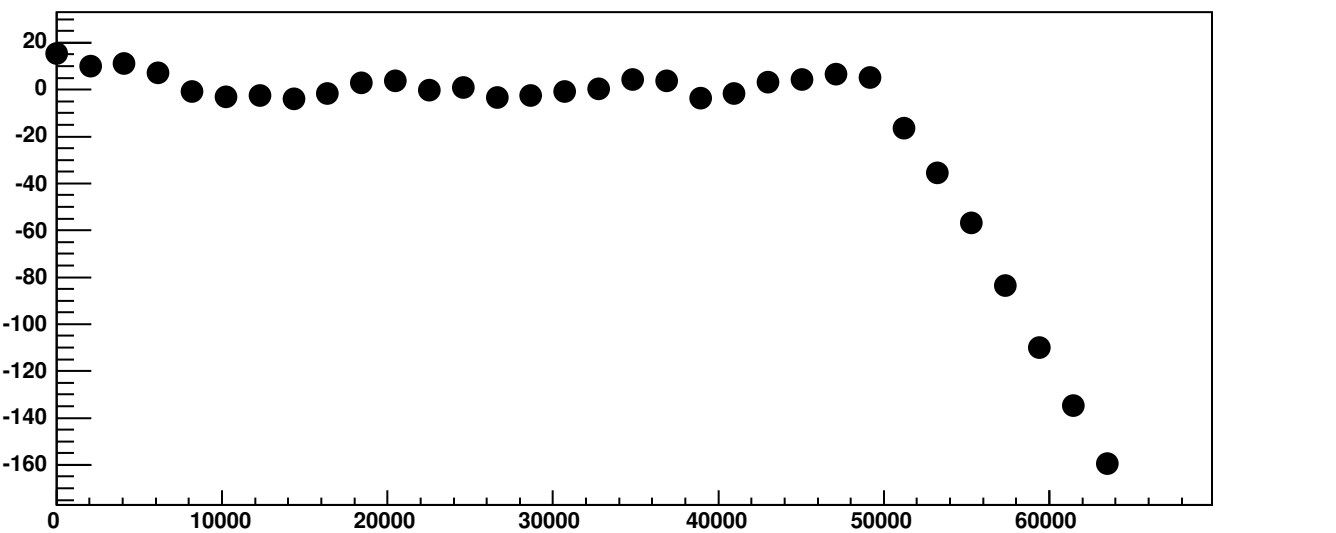
Chip 4, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC



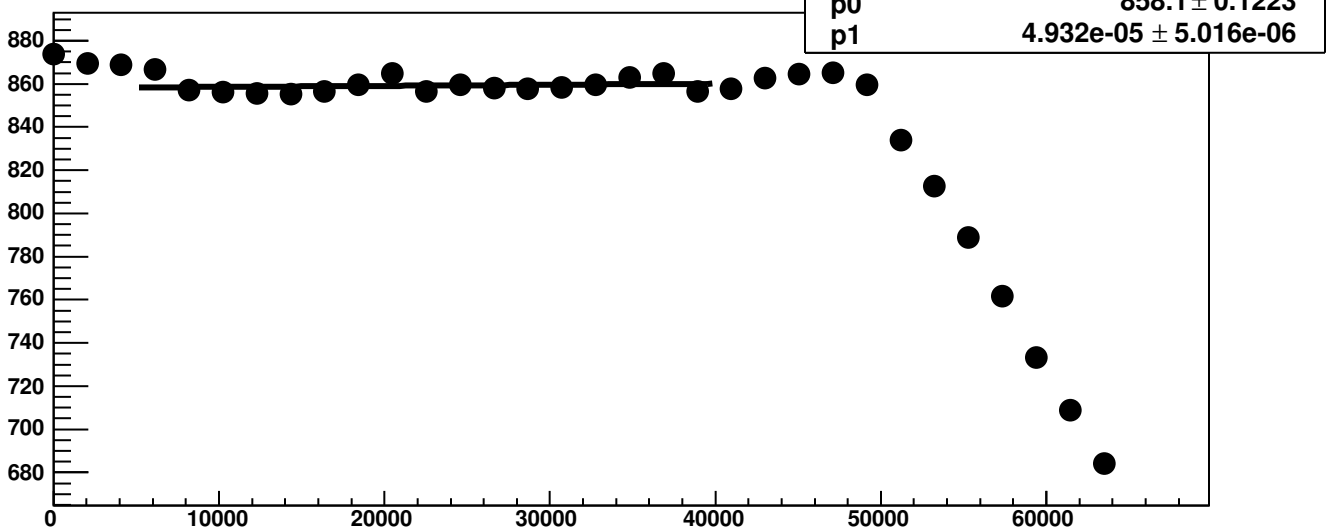
Chip 4, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



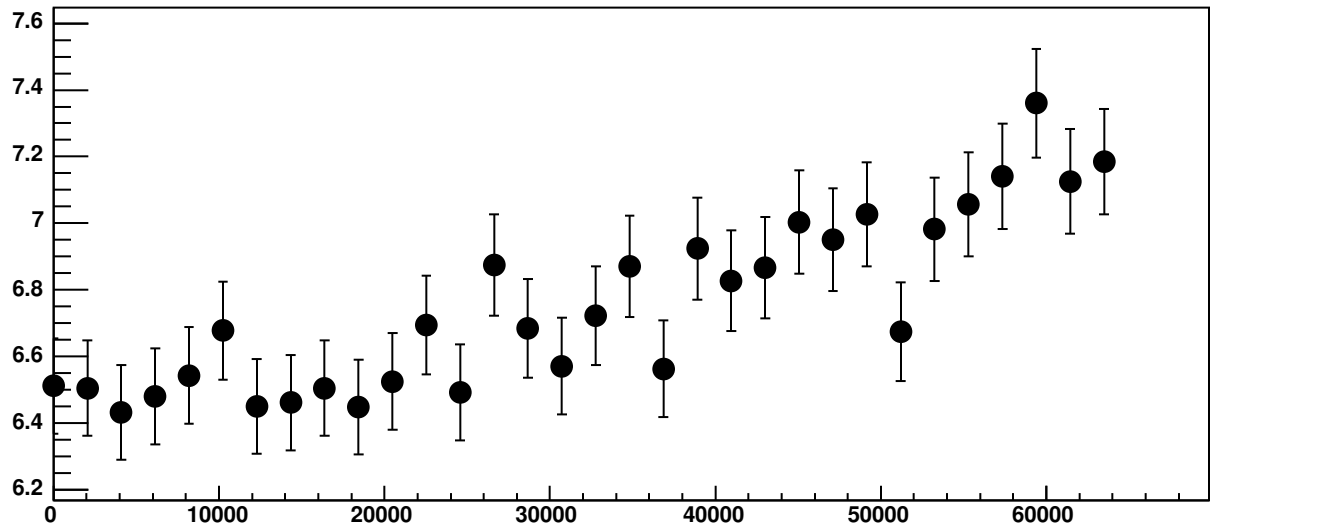
Chip 4, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC



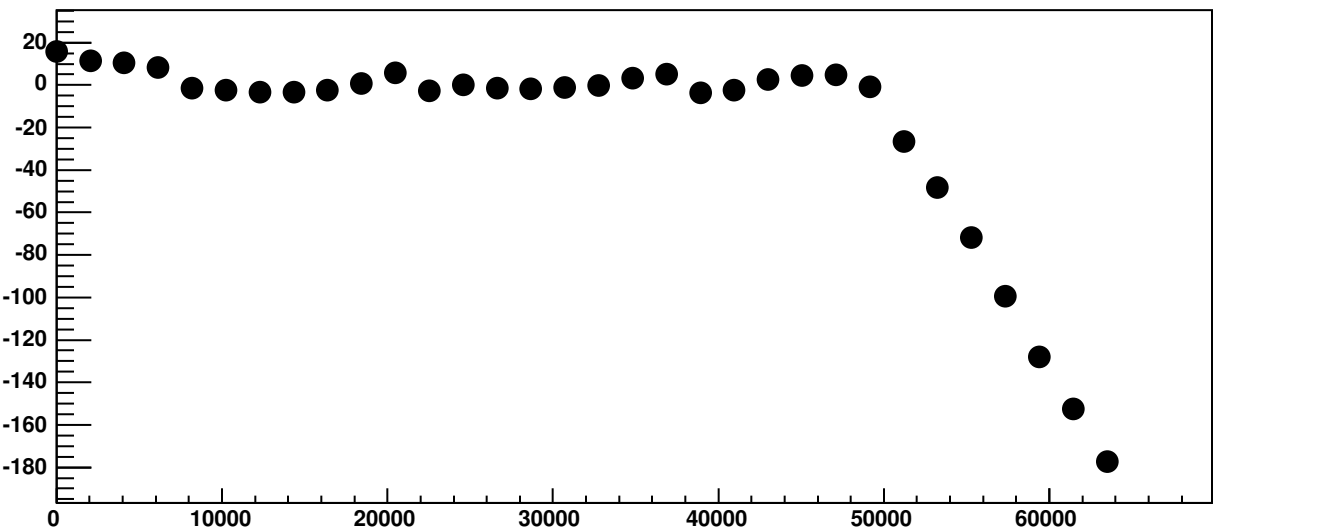
Chip 4, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC

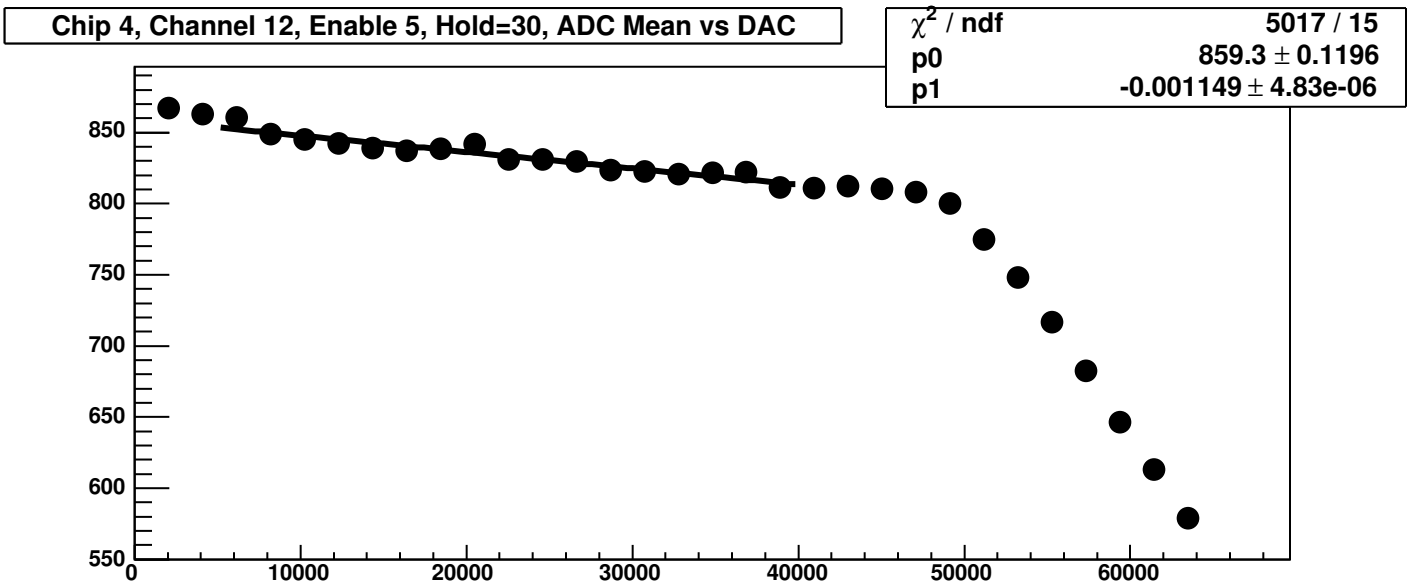


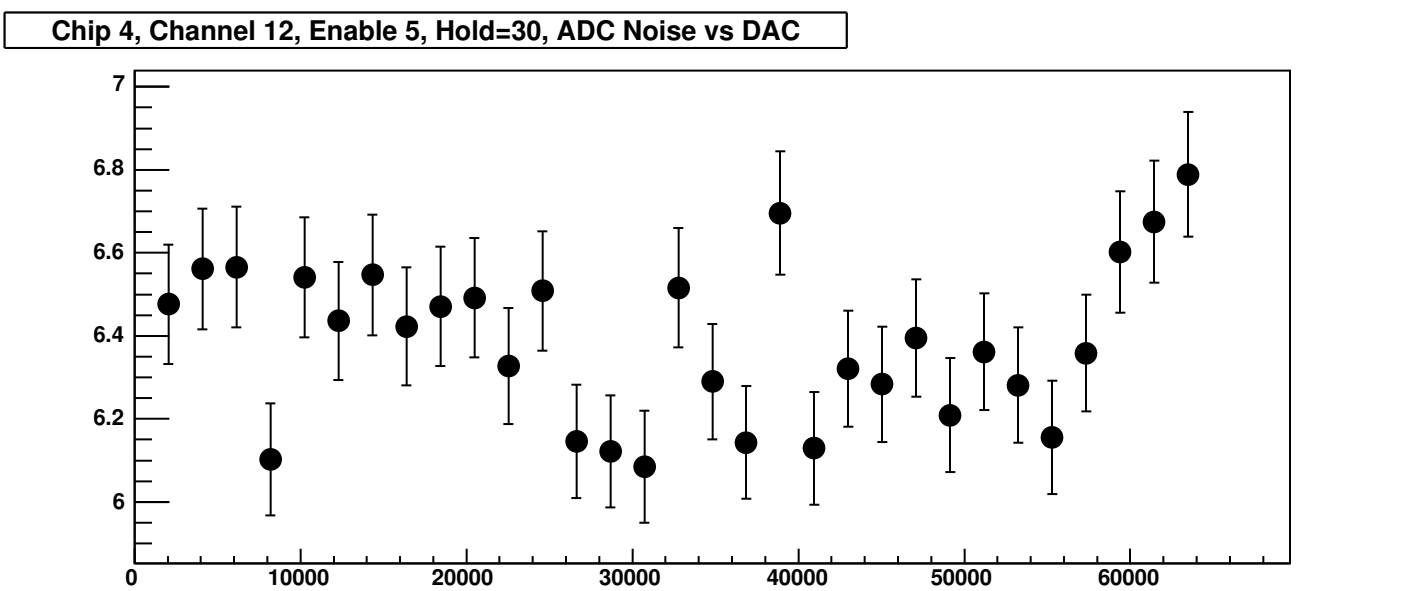
Chip 4, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC

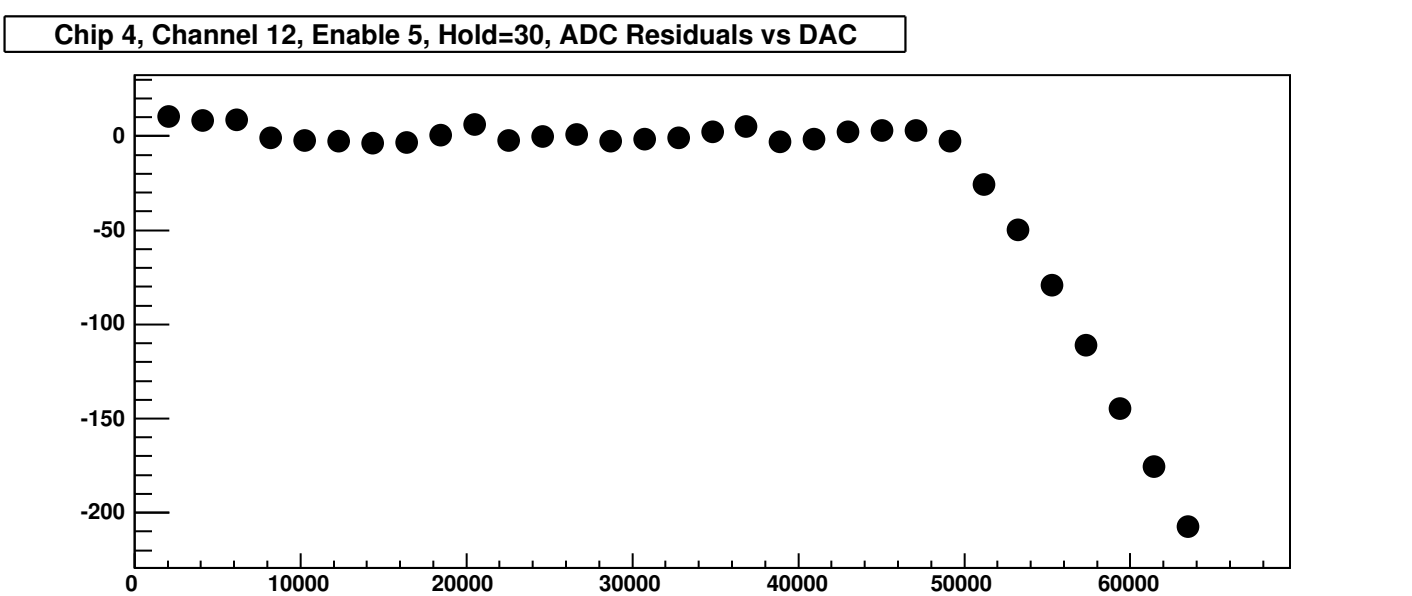


Chip 4, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC

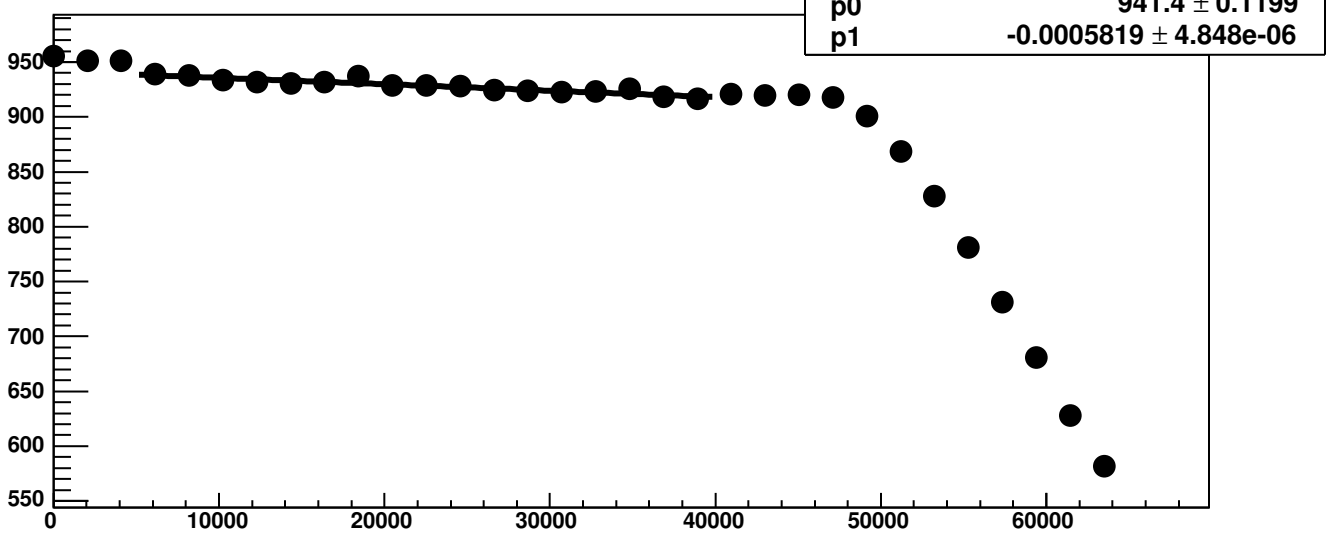



Chip 4, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5017 / 15
p0 859.3 ± 0.1196
p1 -0.001149 ± 4.83e-06
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

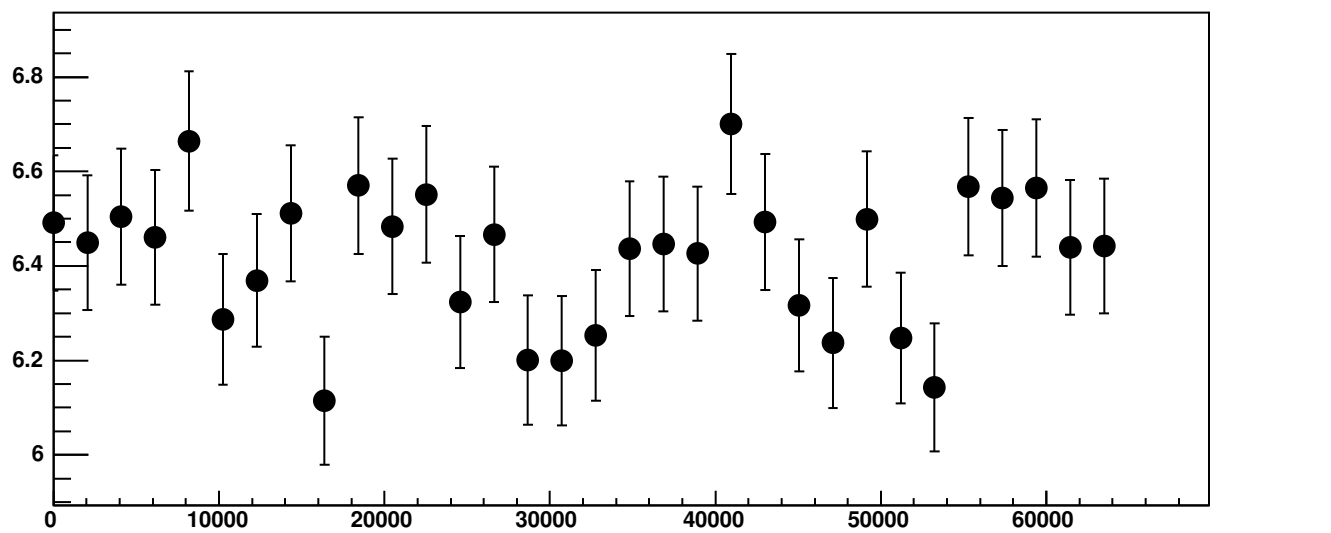

Chip 4, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

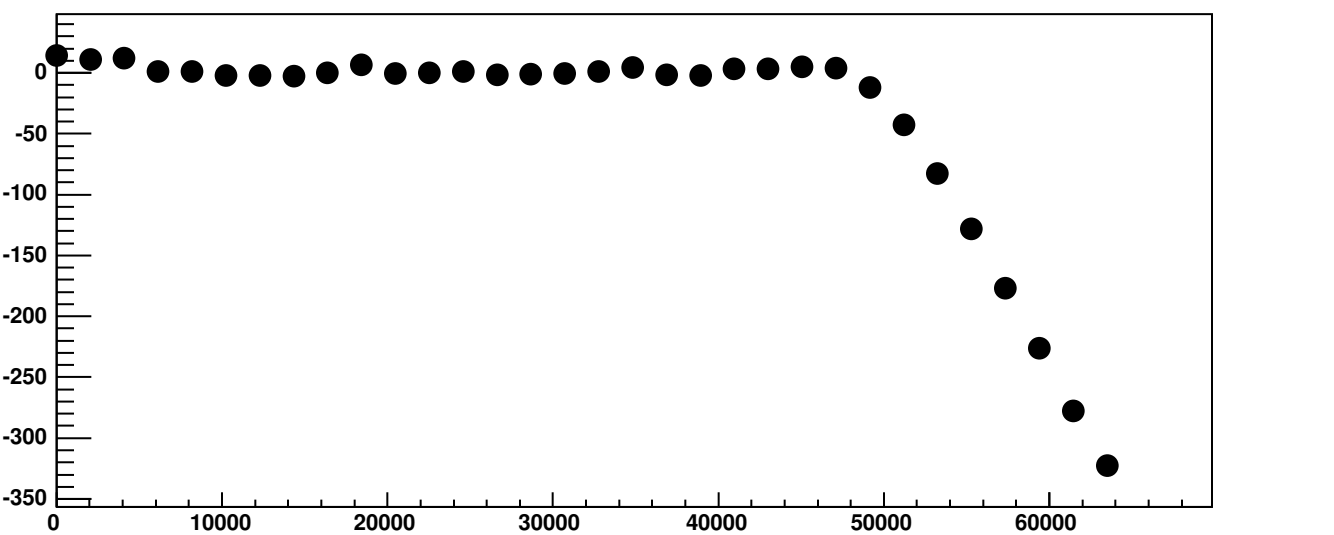
Chip 4, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC



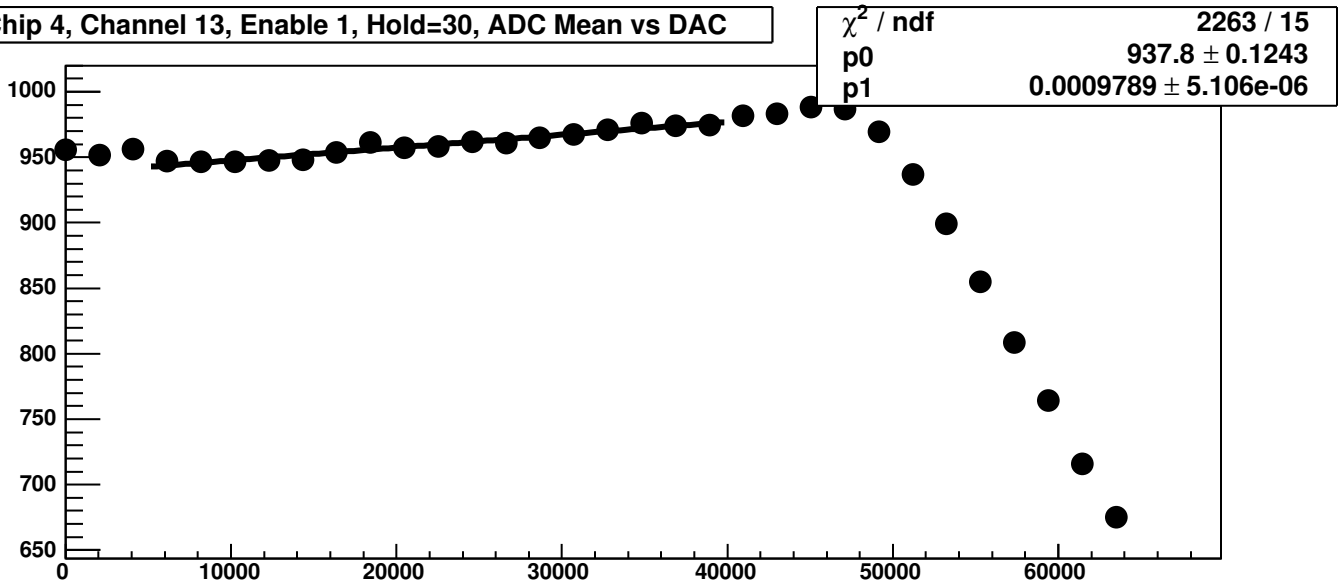
Chip 4, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



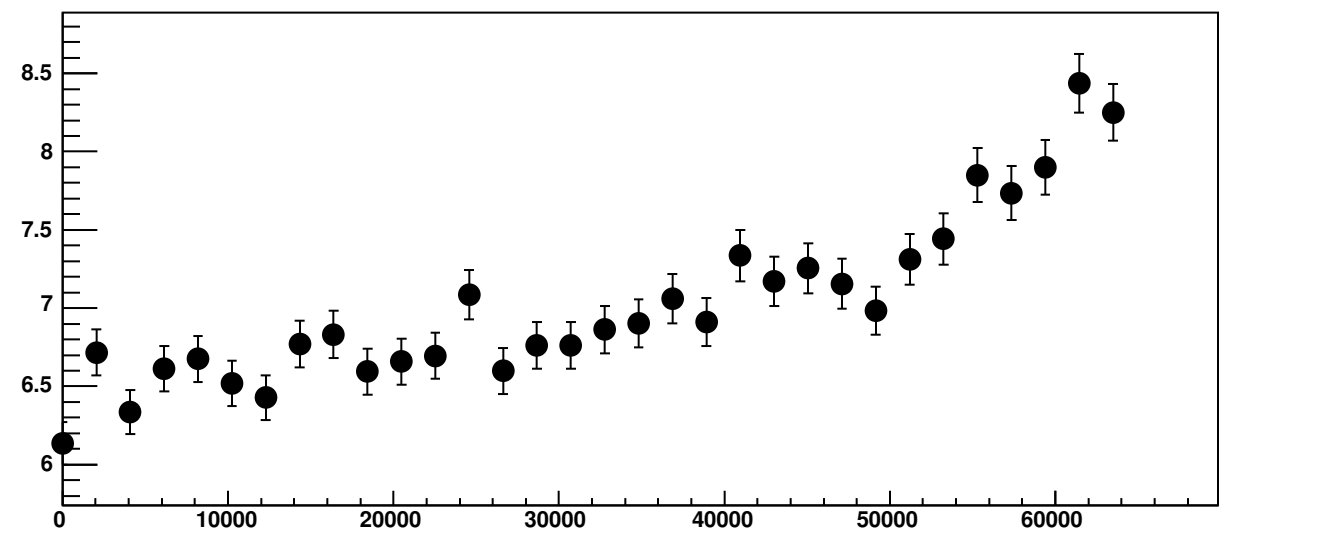
Chip 4, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC



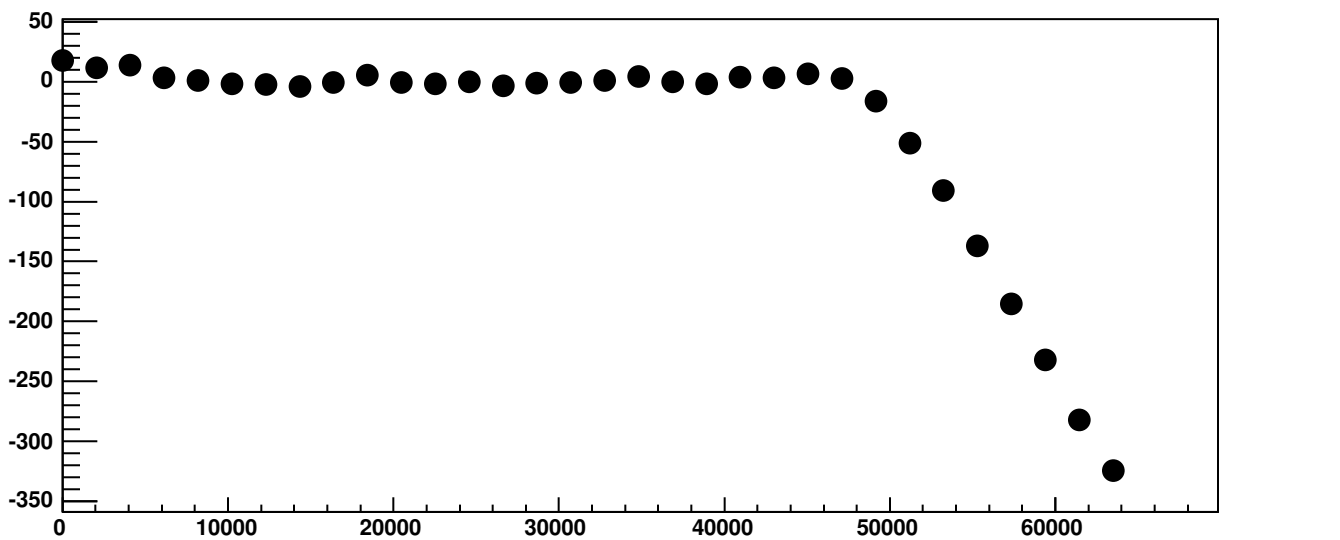
Chip 4, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC

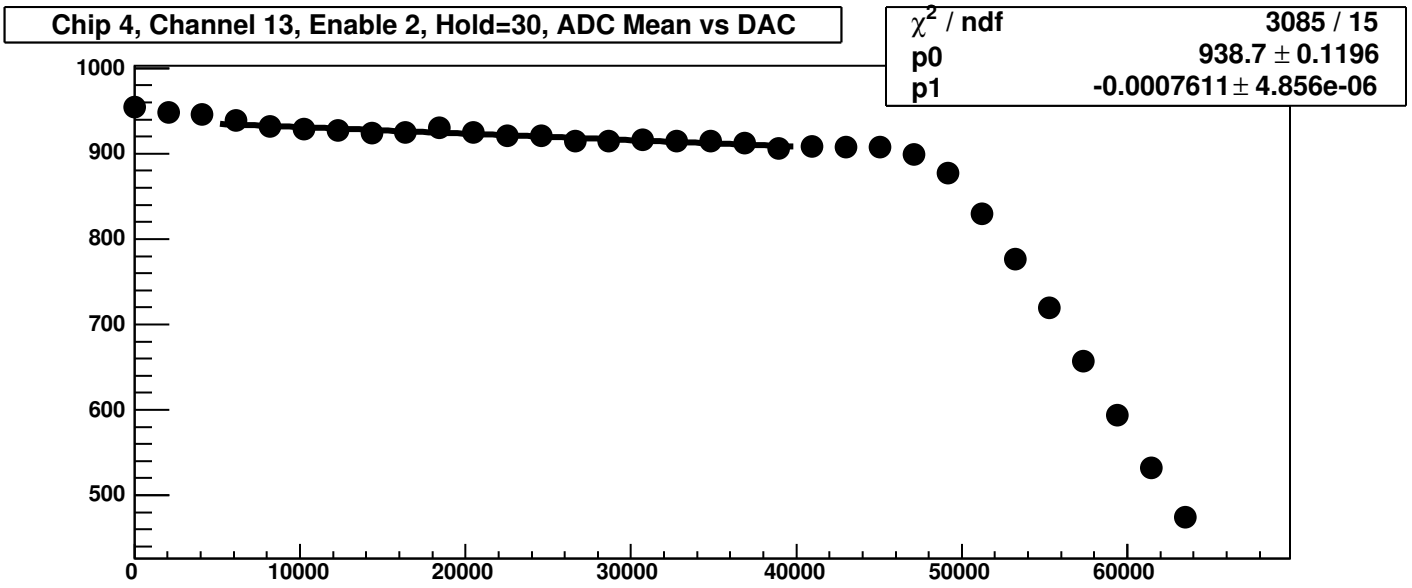


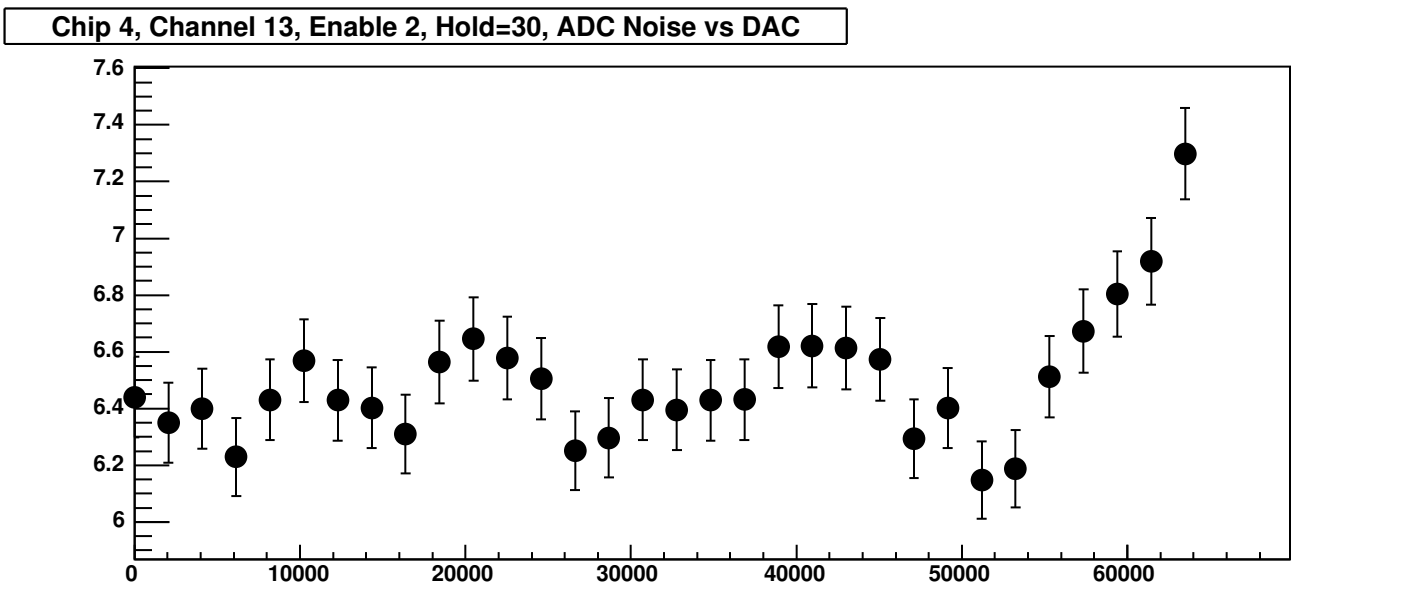
Chip 4, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC

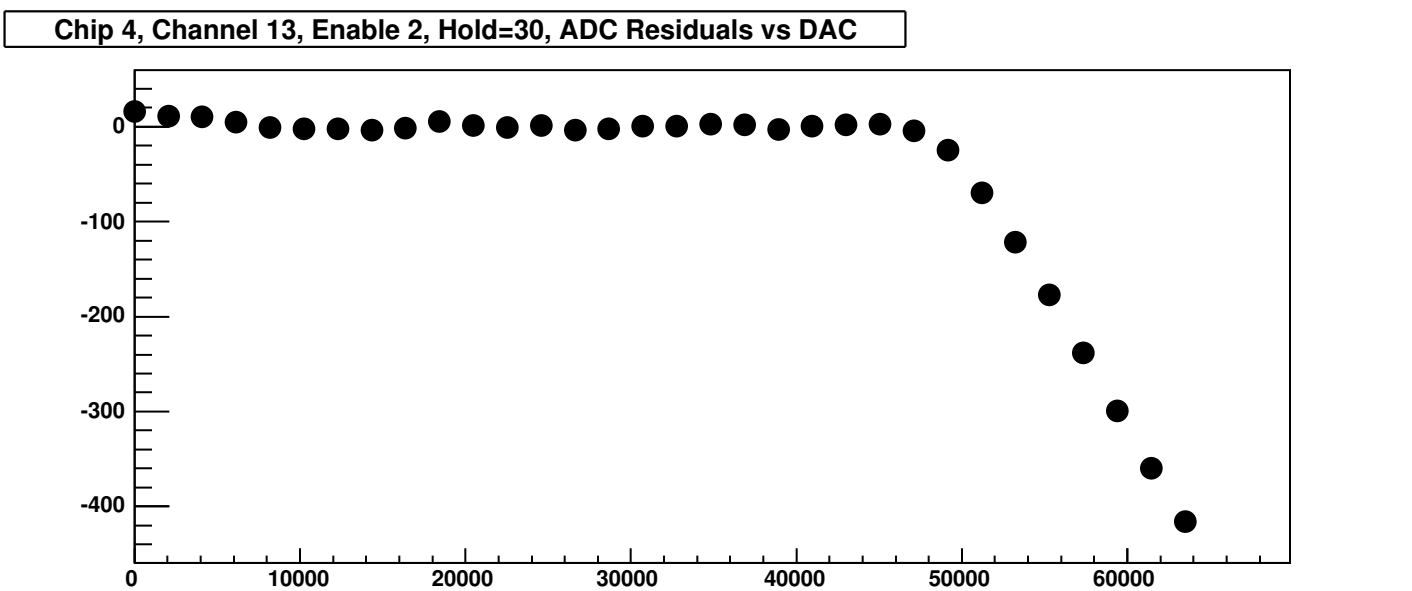


Chip 4, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC



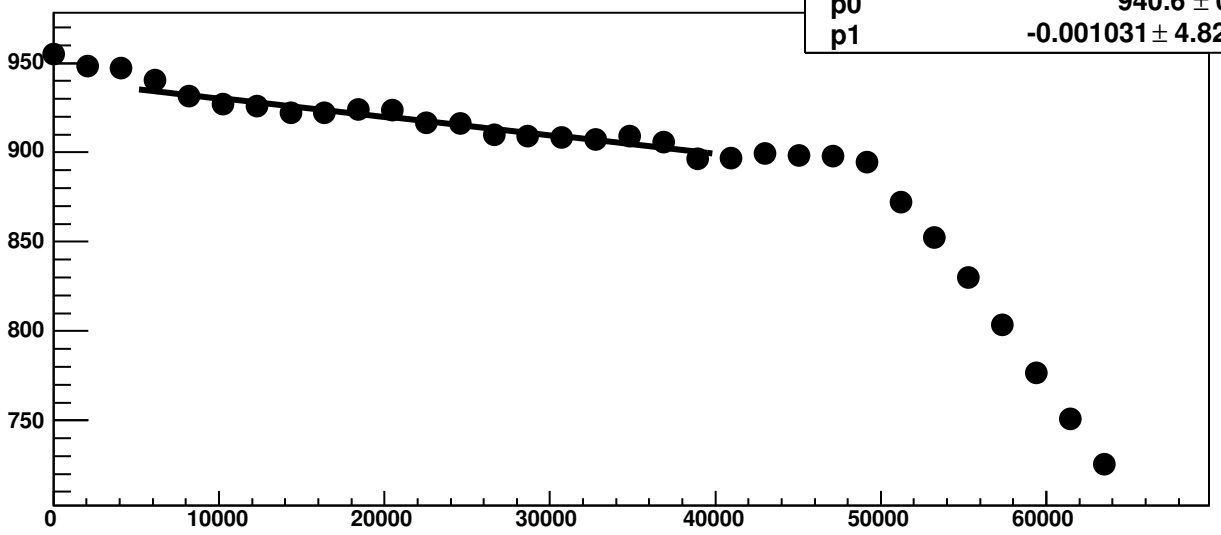

Chip 4, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3085 / 15
p0 938.7 ± 0.1196
p1 -0.0007611 ± 4.856e-06
1000
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

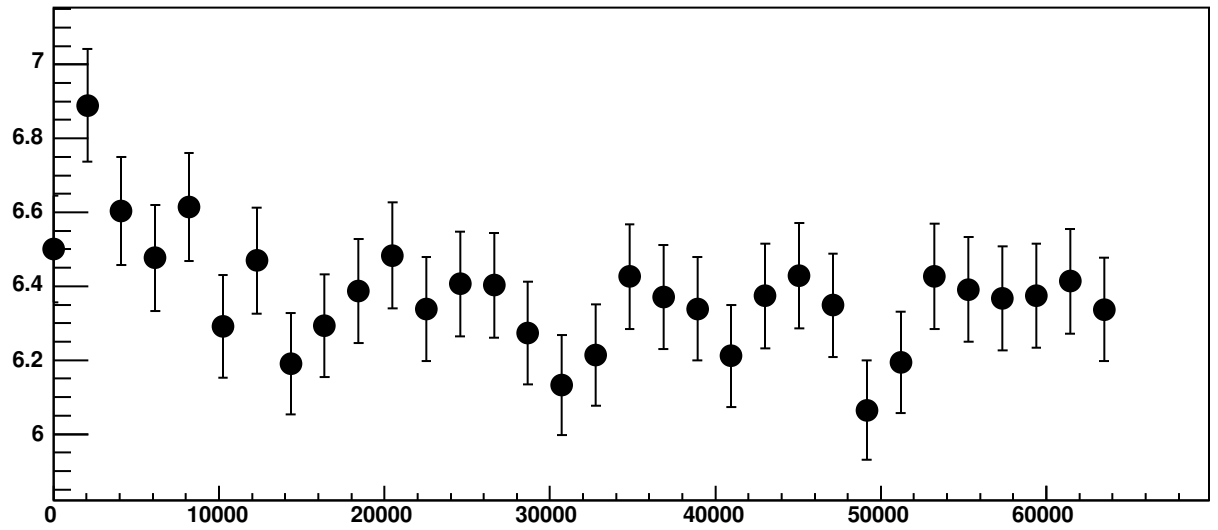

Chip 4, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

Chip 4, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC

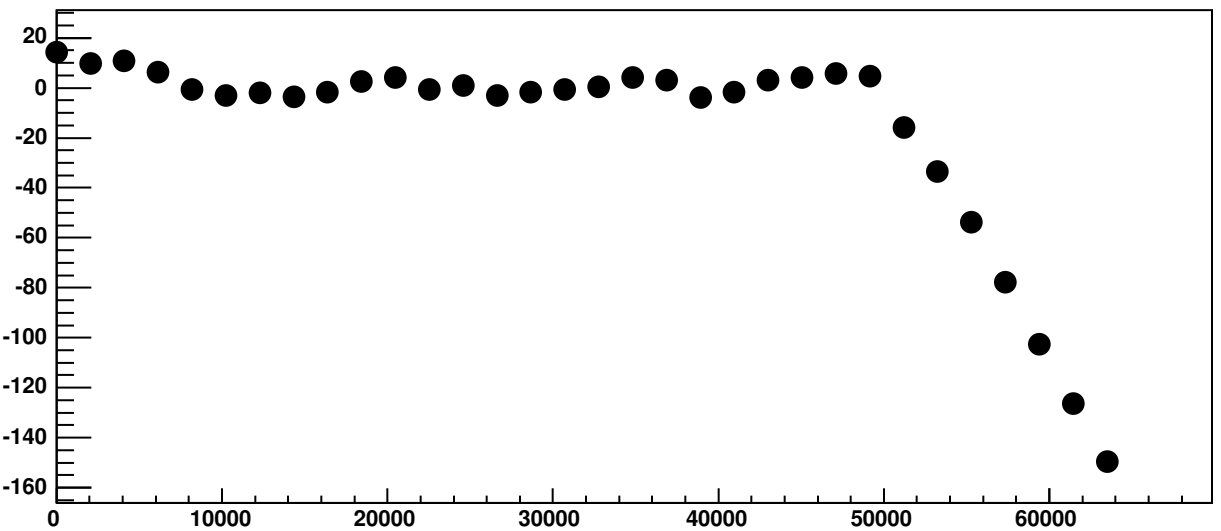
χ^2 / ndf	3775 / 15
p0	940.6 ± 0.1194
p1	-0.001031 ± 4.822e-06

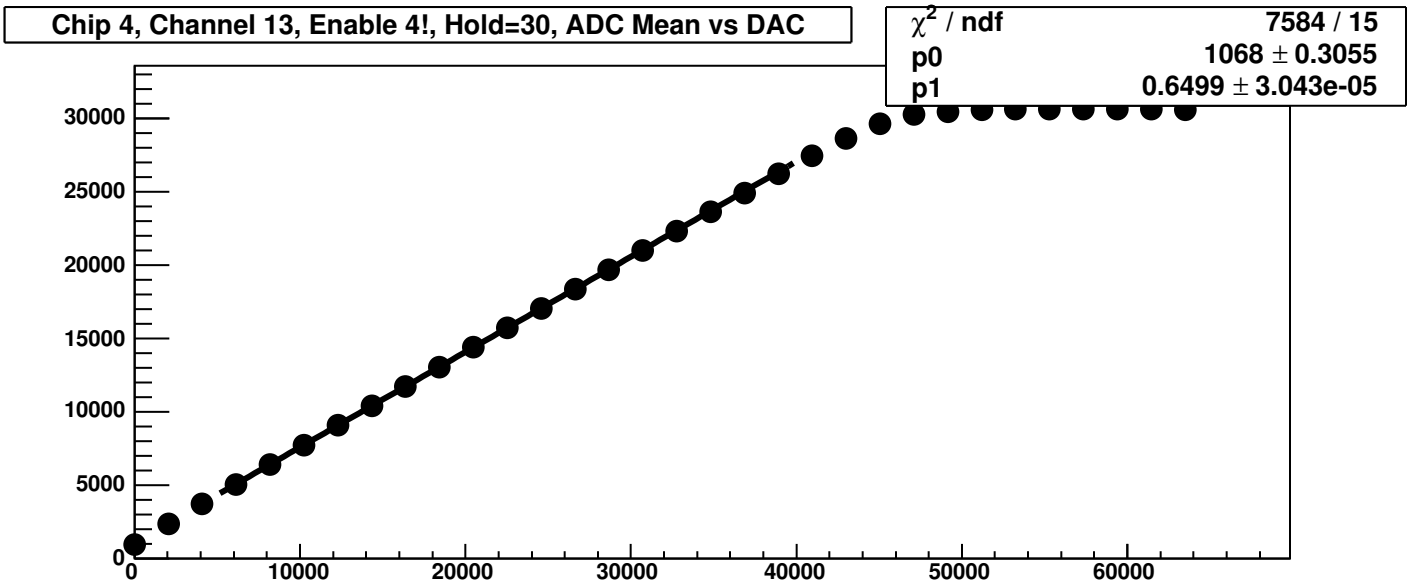


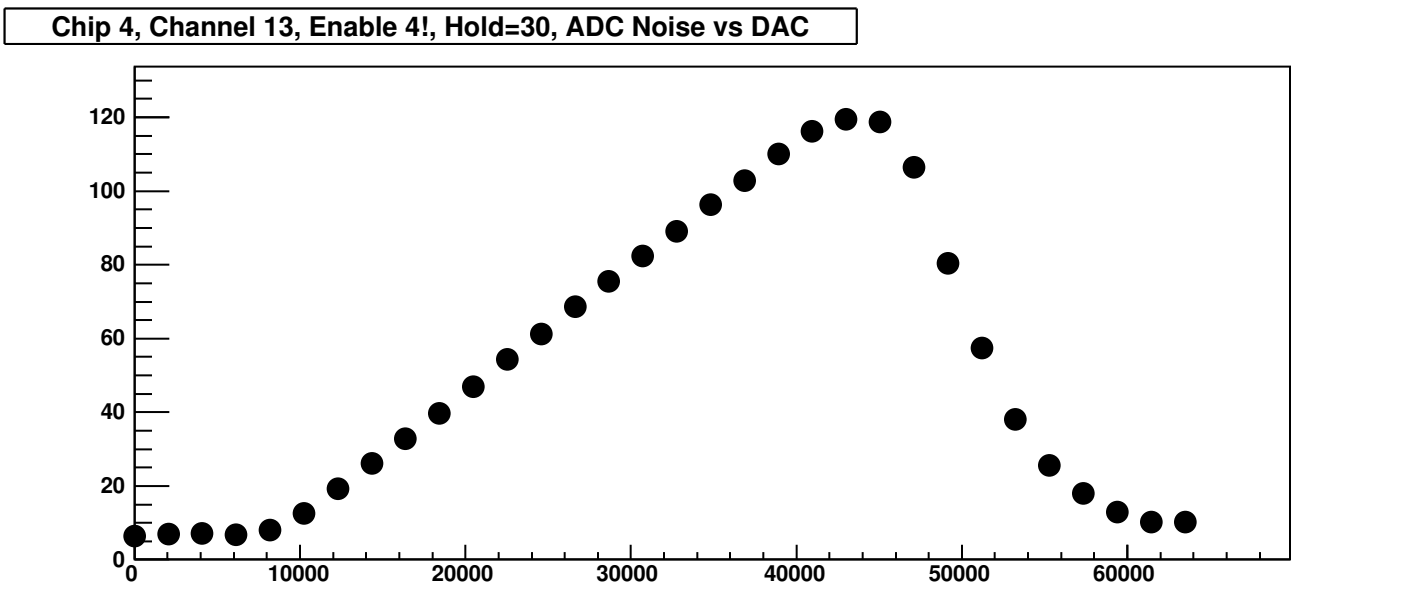
Chip 4, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC

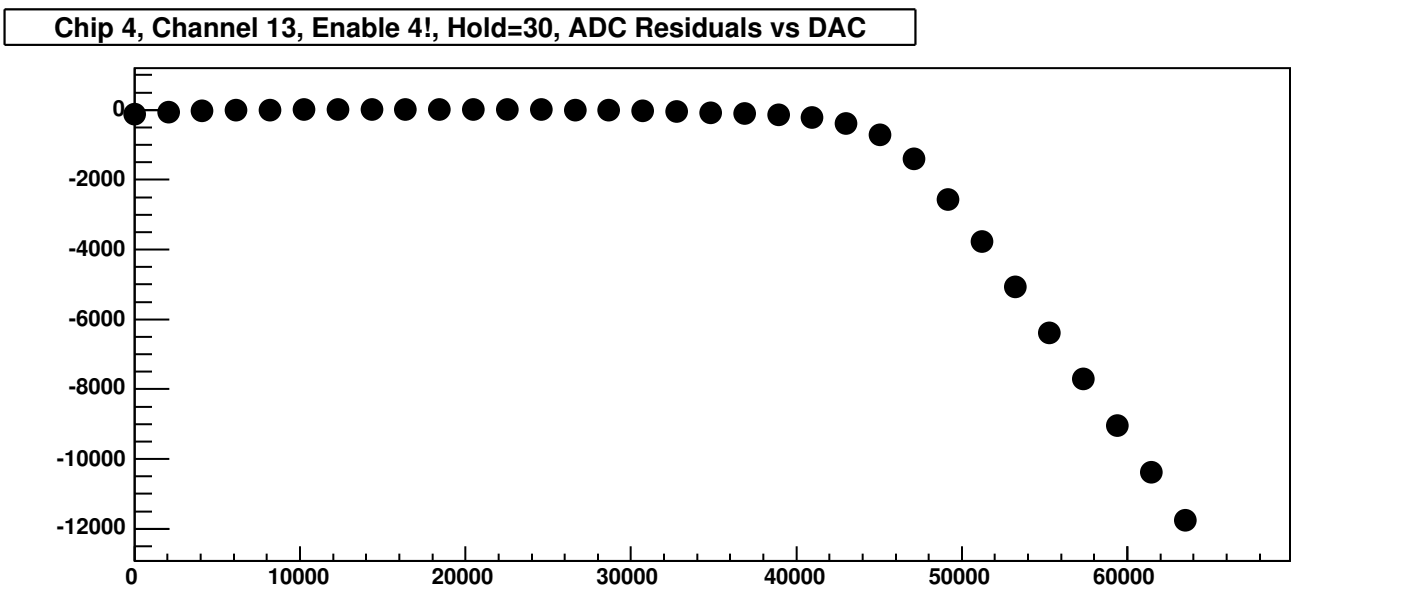


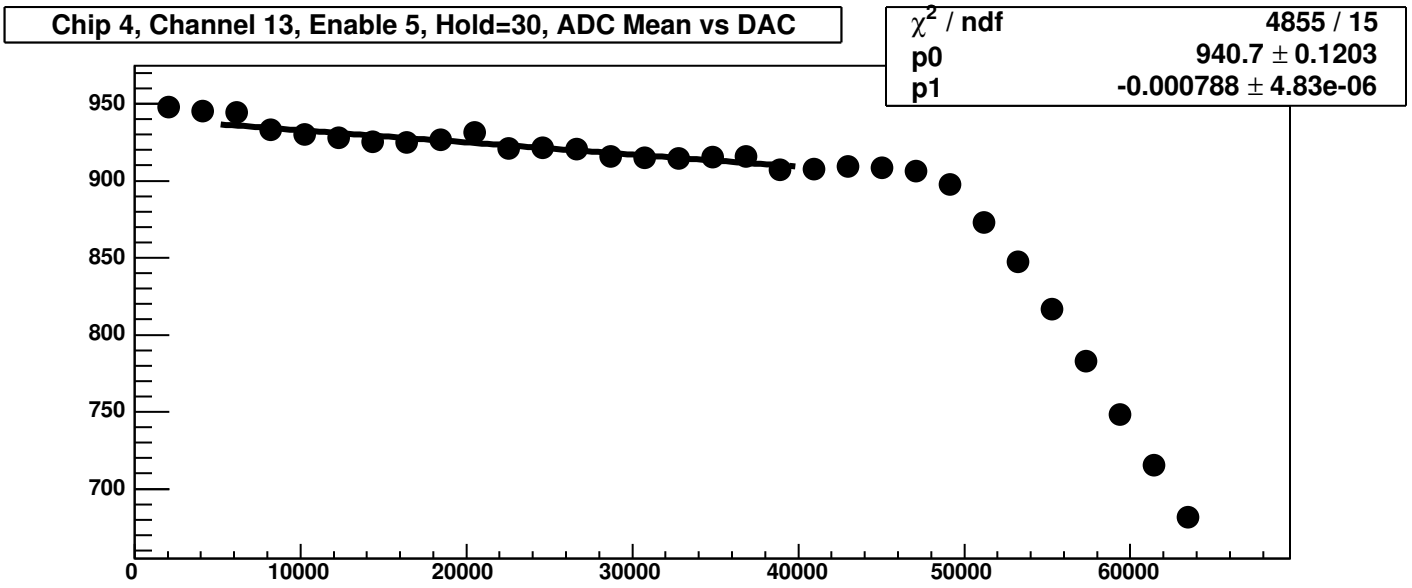
Chip 4, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC

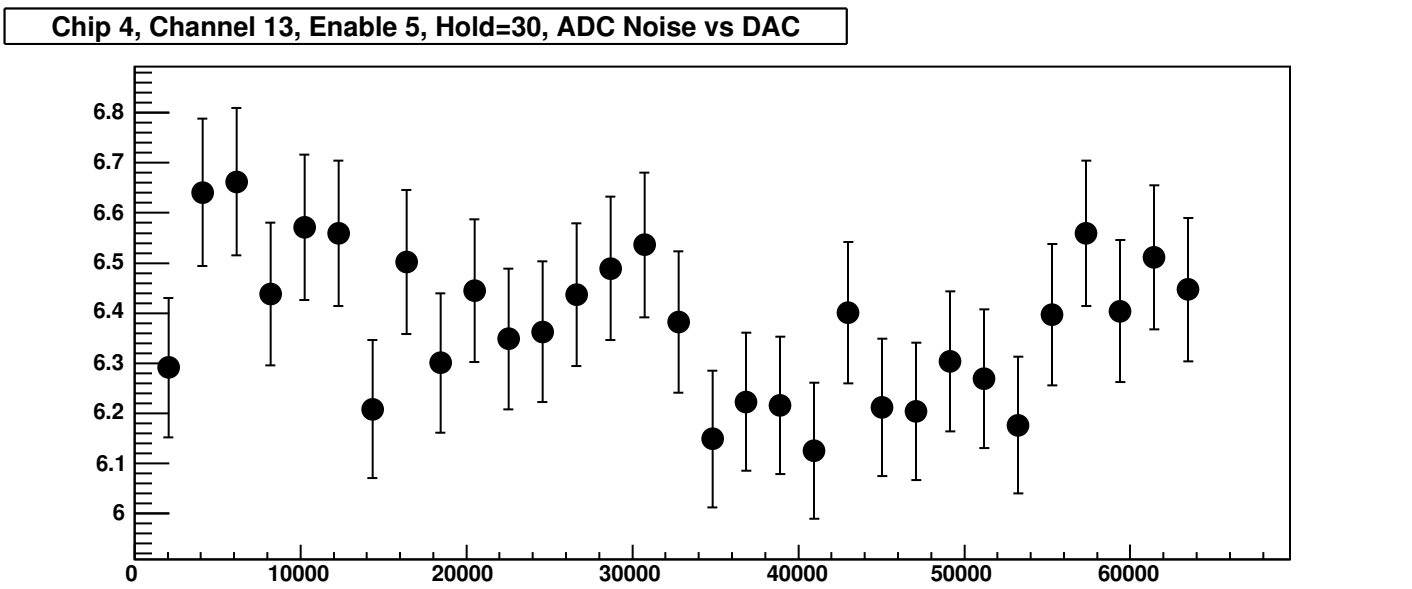


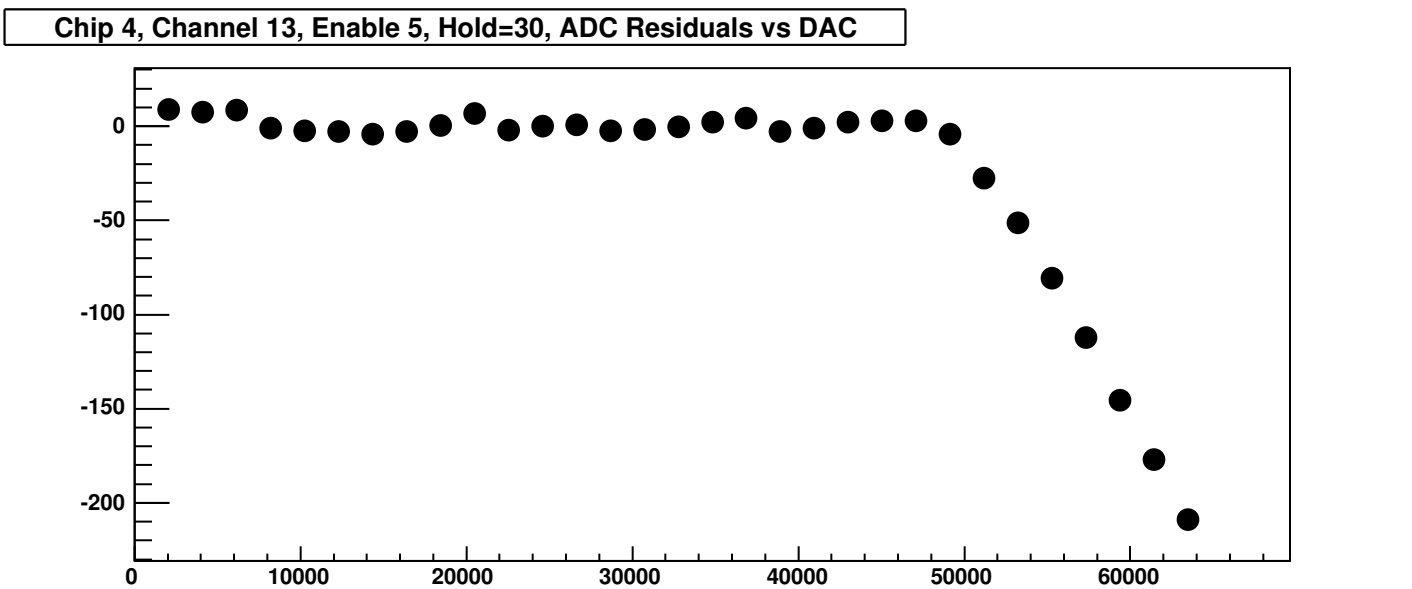

Chip 4, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 7584 / 15
p0 1068 ± 0.3055
p1 0.6499 ± 3.043e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

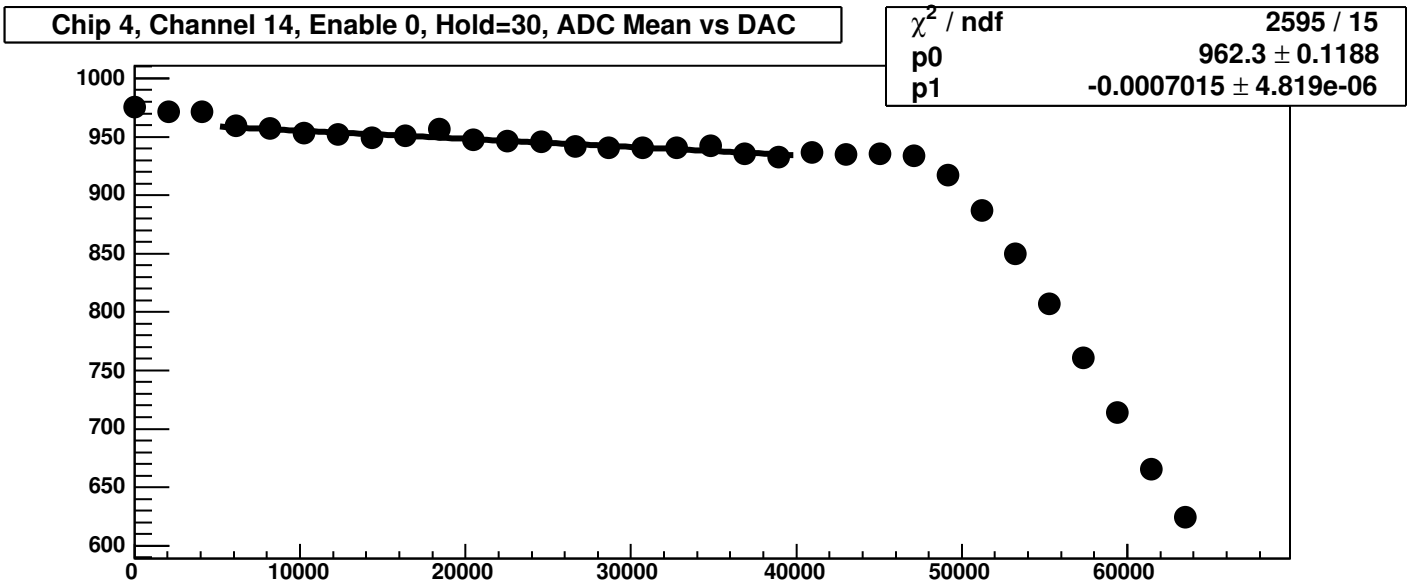

Chip 4, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

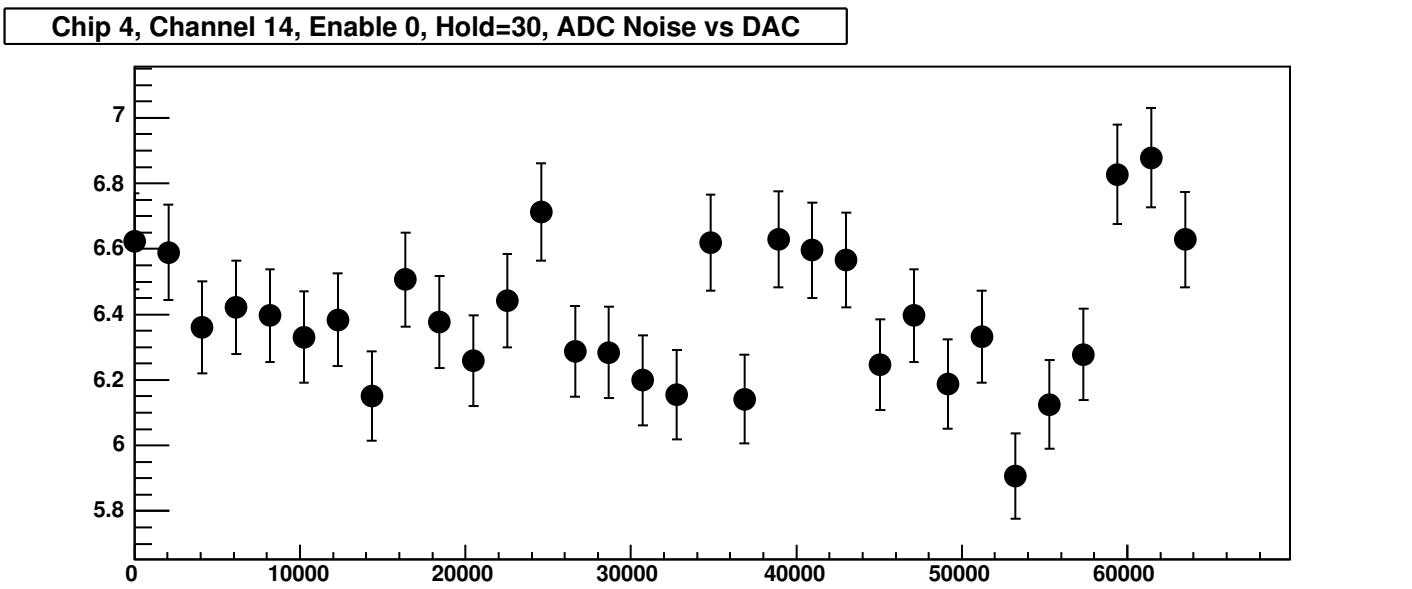

Chip 4, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

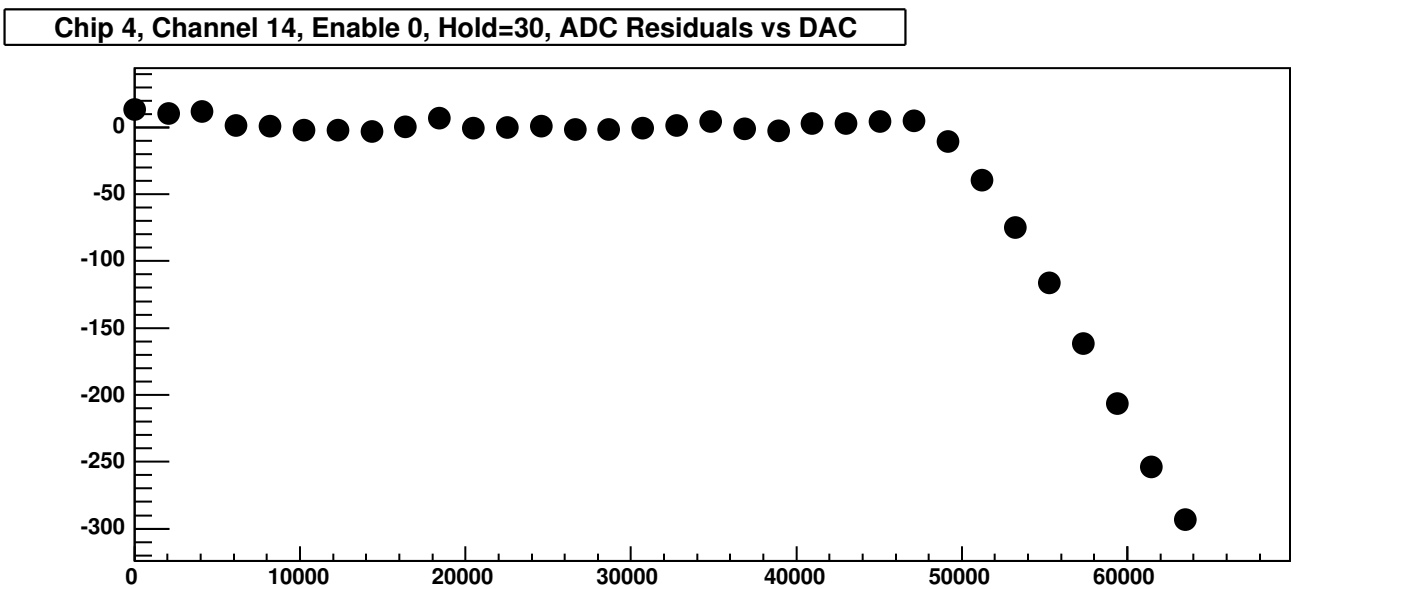

Chip 4, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4855 / 15
p0 940.7 ± 0.1203
p1 -0.000788 ± 4.83e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

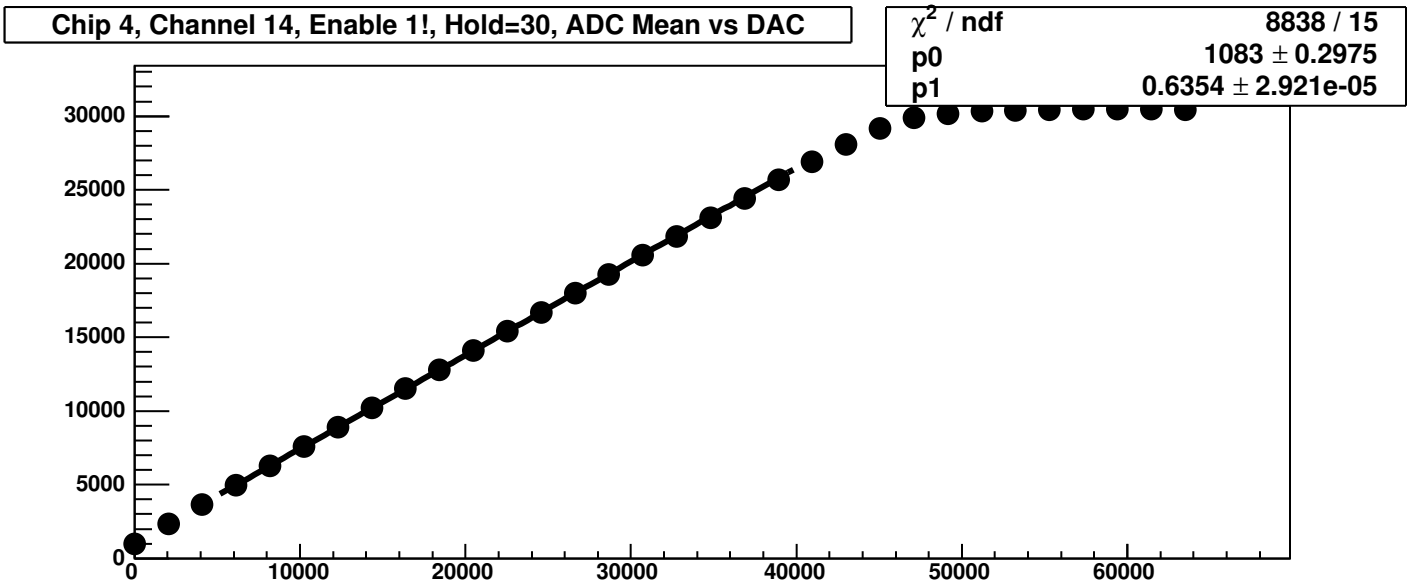

Chip 4, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.7
6.6
6.5
6.4
6.3
6.2
6.1
6
0
10000
20000
30000
40000
50000
60000

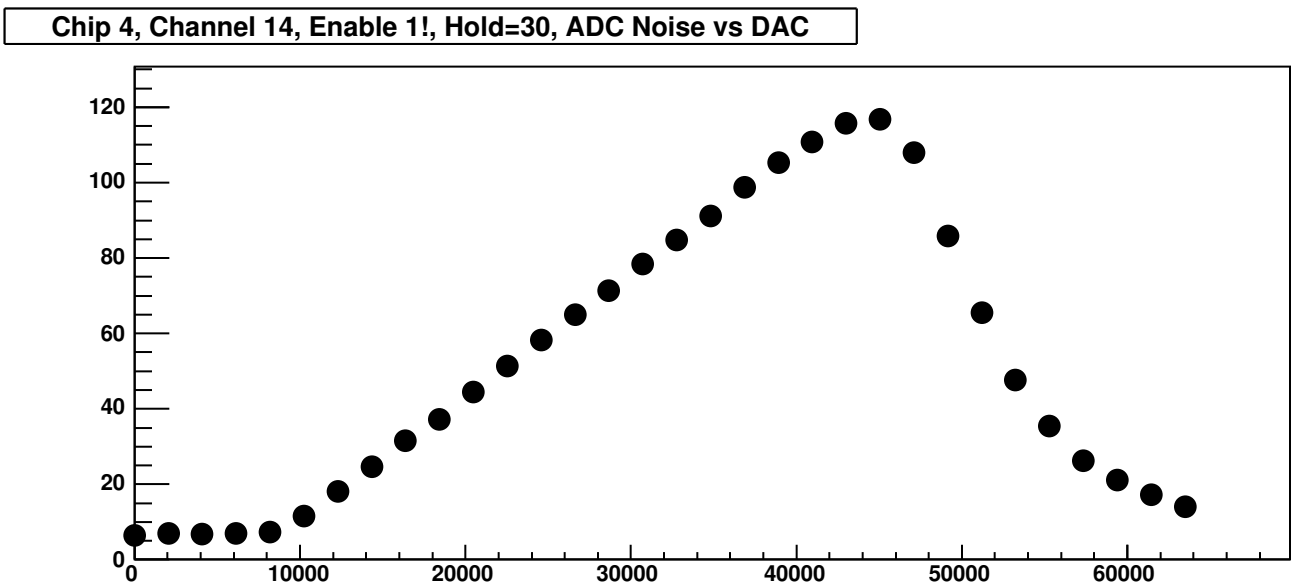

Chip 4, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

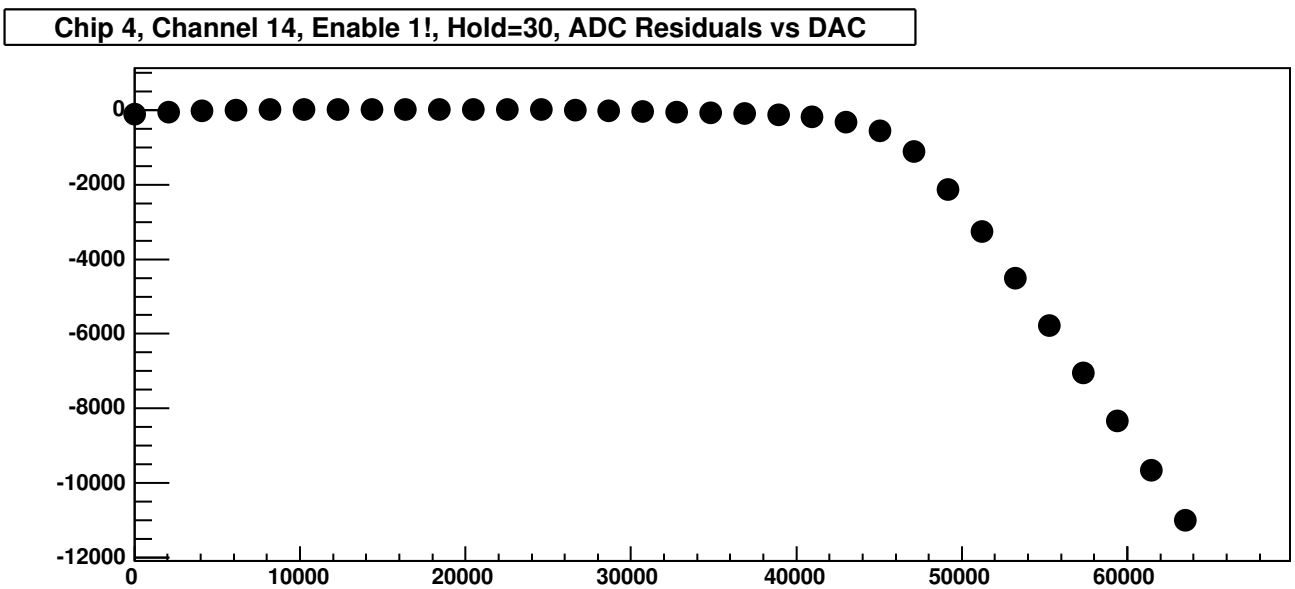

Chip 4, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2595 / 15
p0 962.3 ± 0.1188
p1 -0.0007015 ± 4.819e-06
1000
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

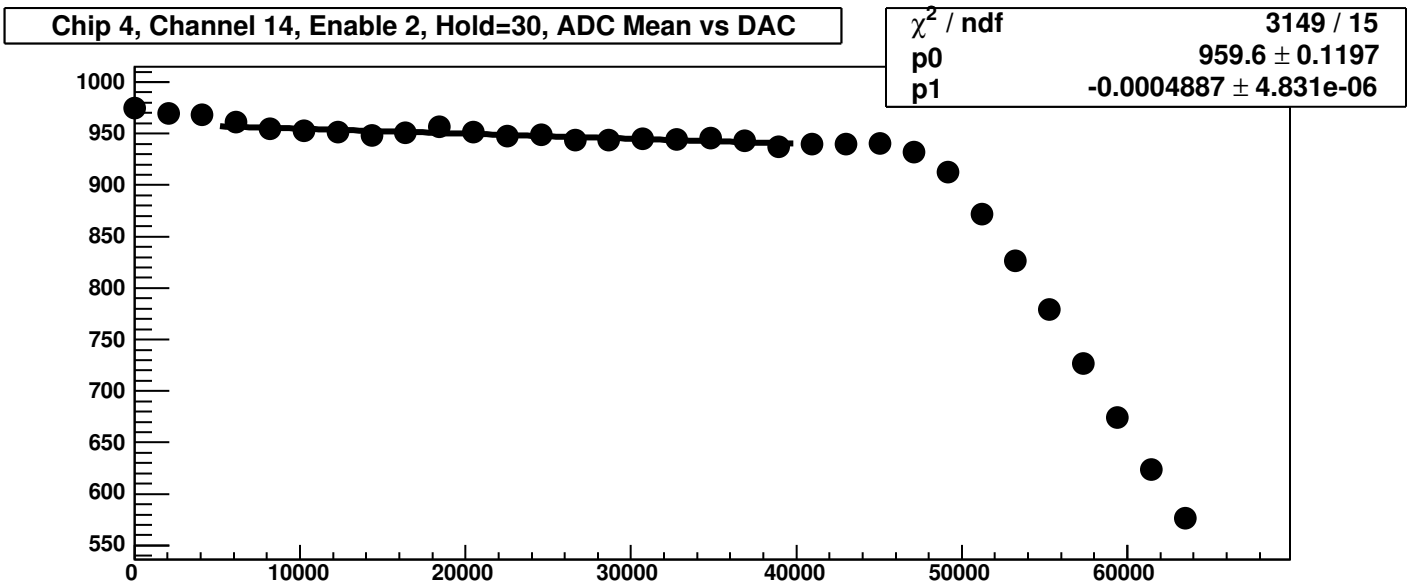

Chip 4, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

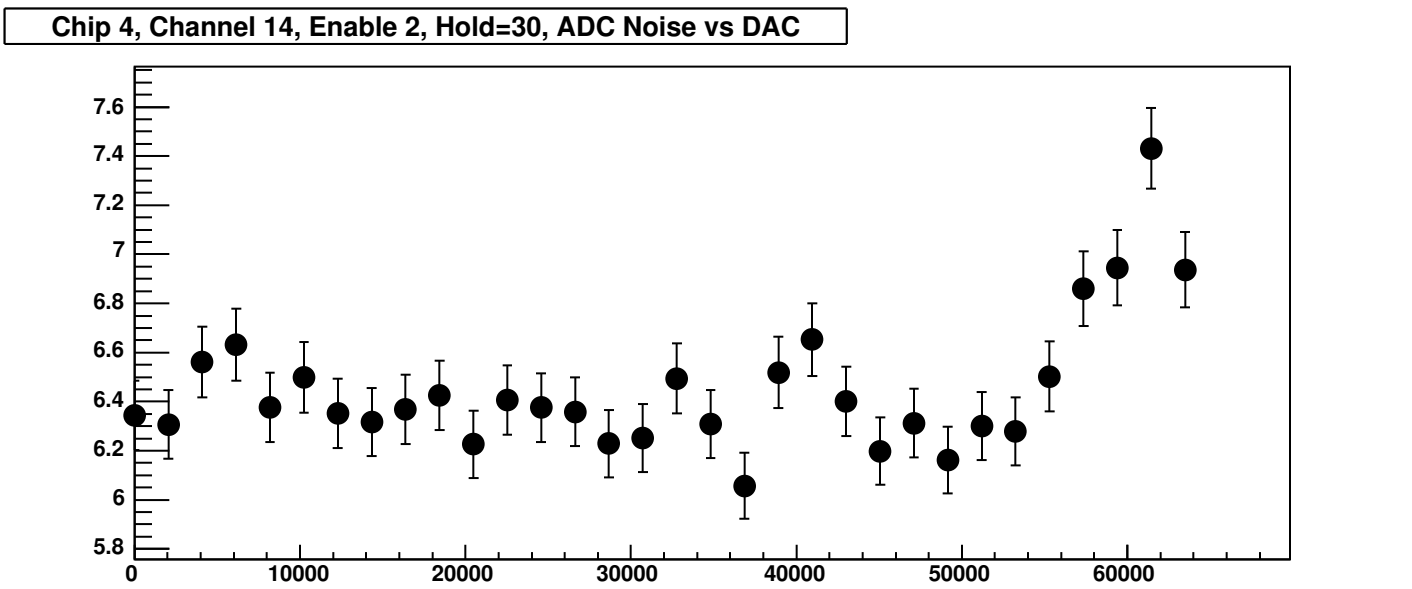

Chip 4, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

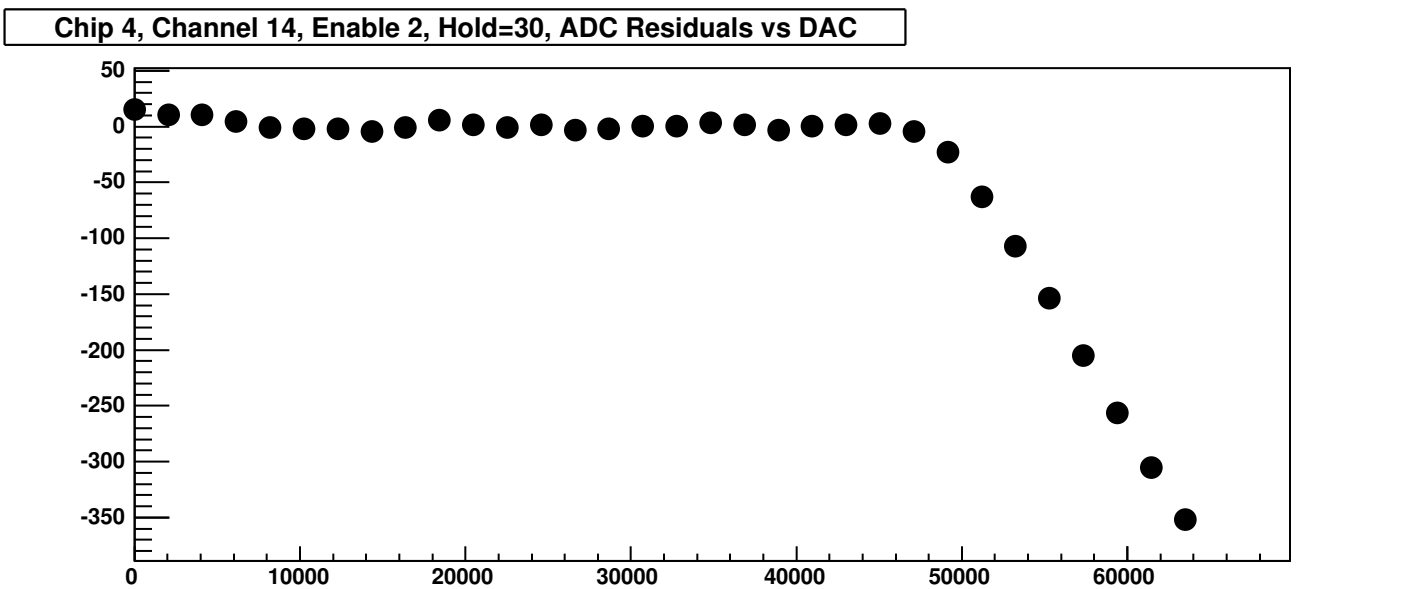

Chip 4, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 8838 / 15
p0 1083 ± 0.2975
p1 0.6354 ± 2.921e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

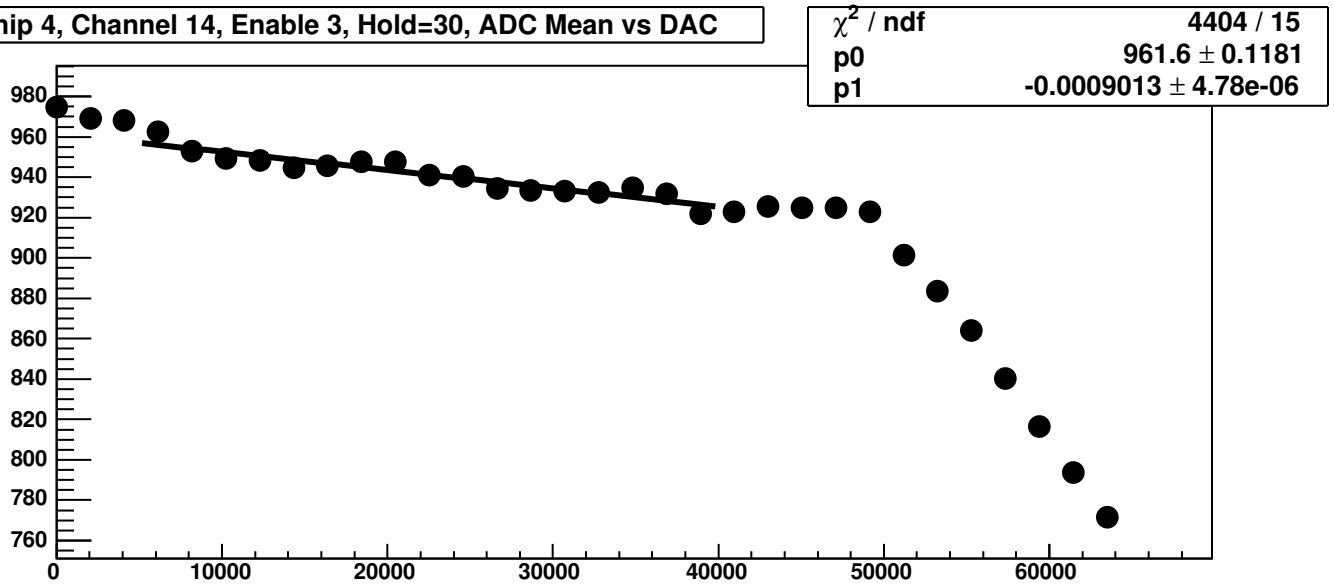

Chip 4, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3149 / 15
p0 959.6 ± 0.1197
p1 -0.0004887 ± 4.831e-06
1000
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

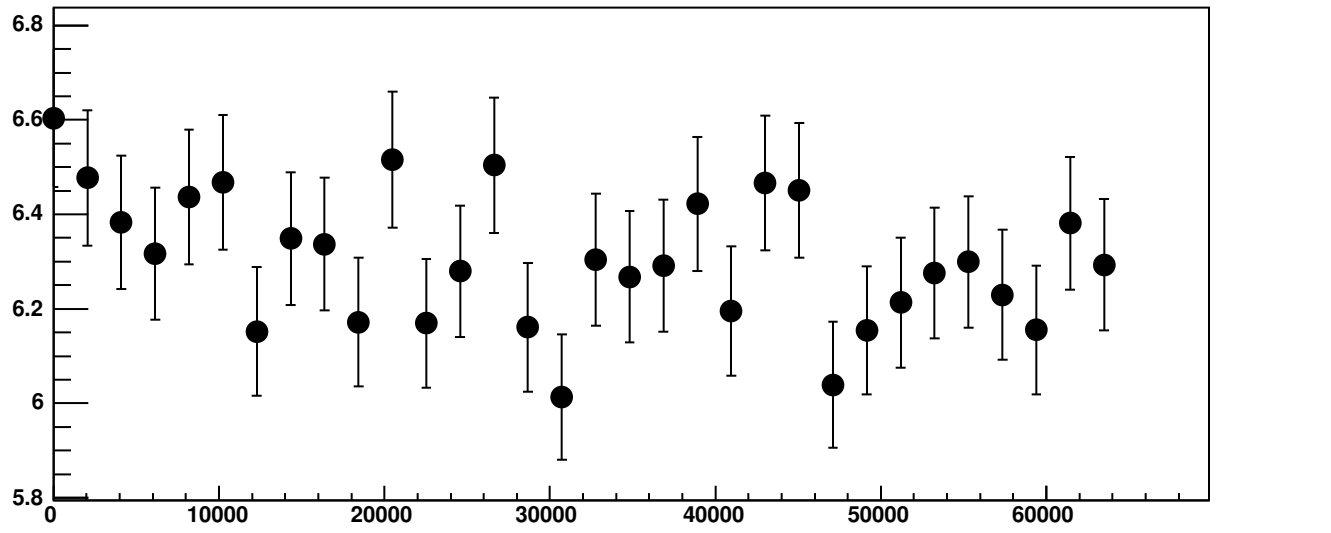

Chip 4, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

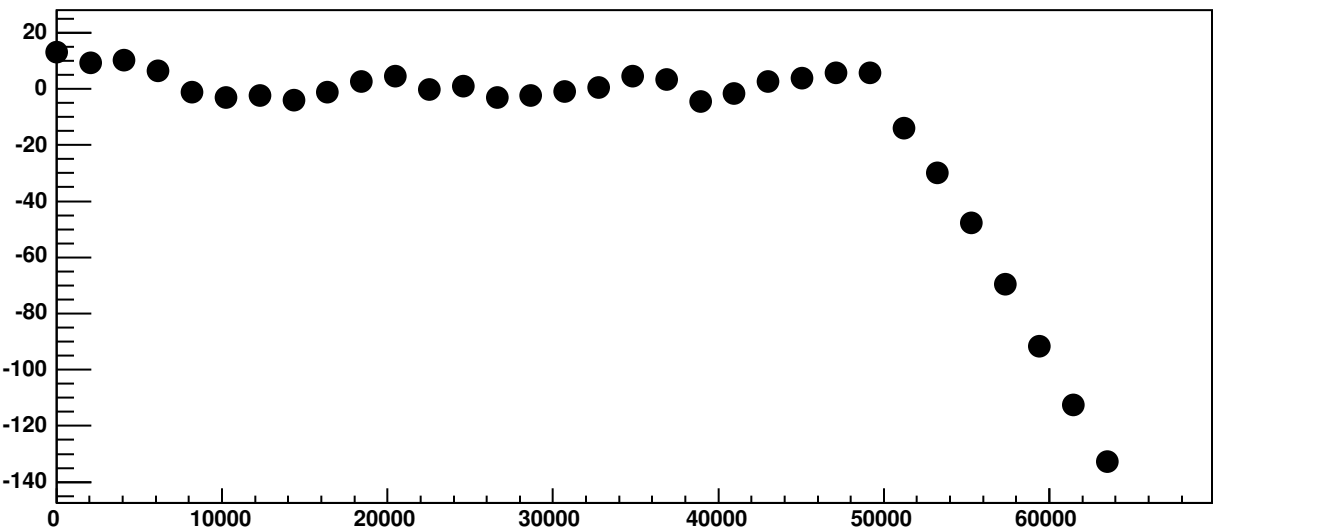
Chip 4, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC



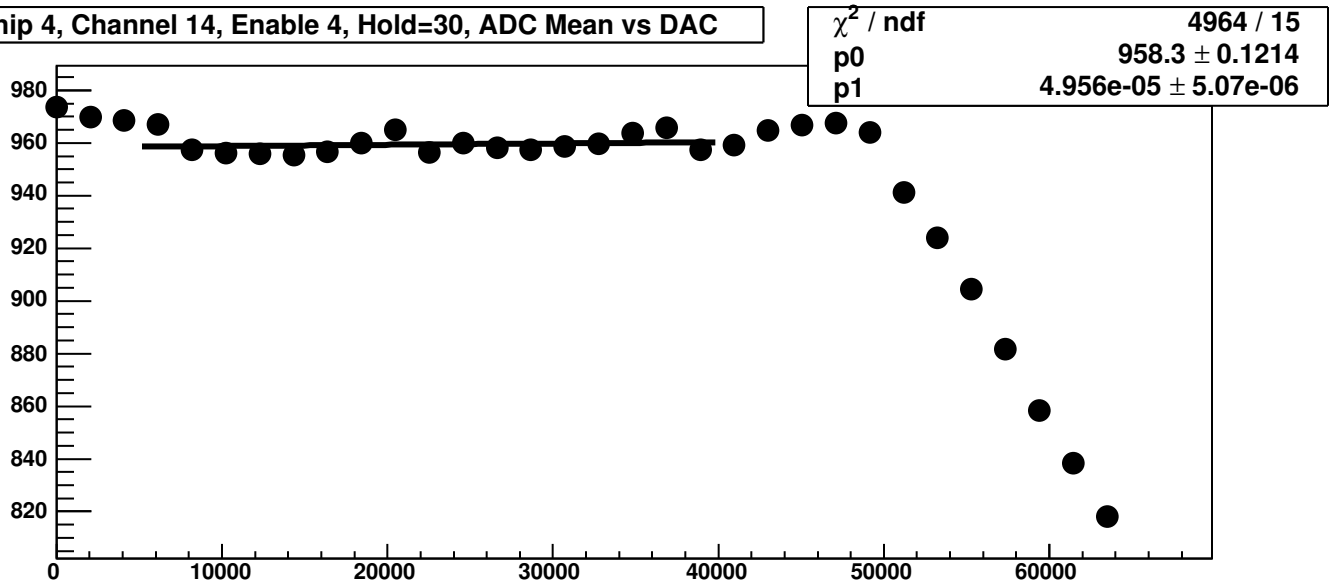
Chip 4, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



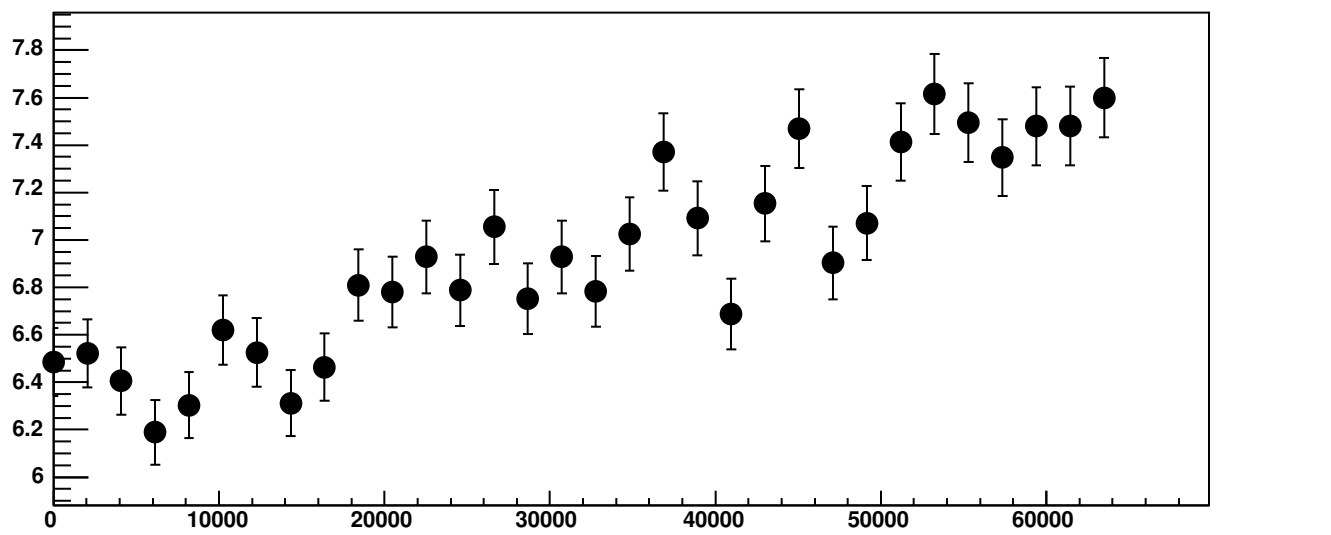
Chip 4, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC



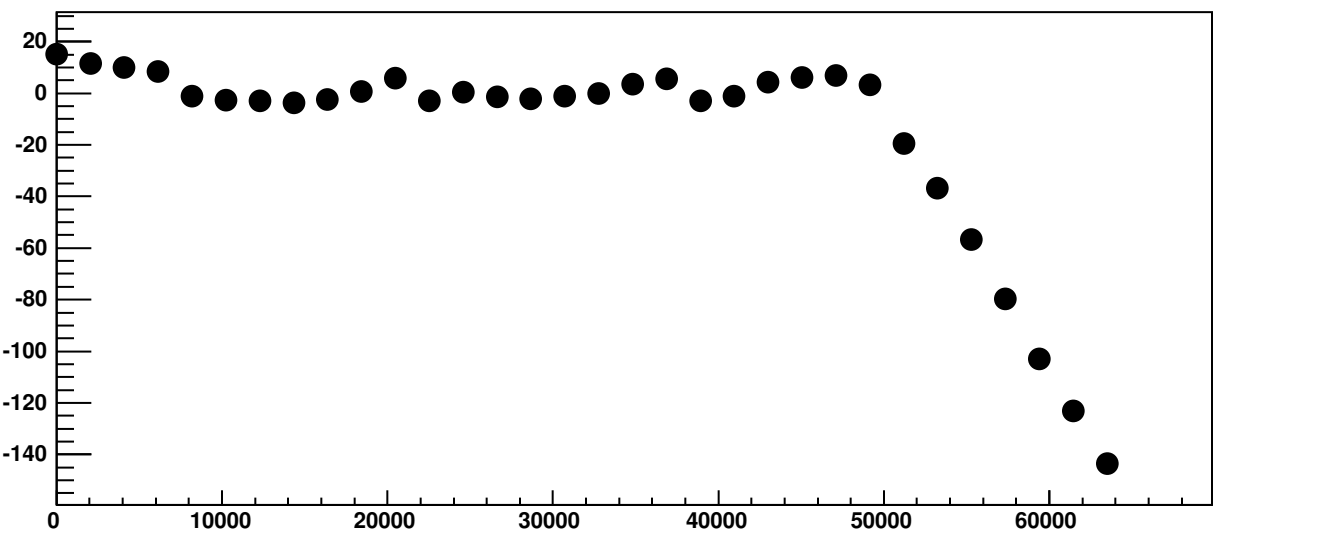
Chip 4, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC

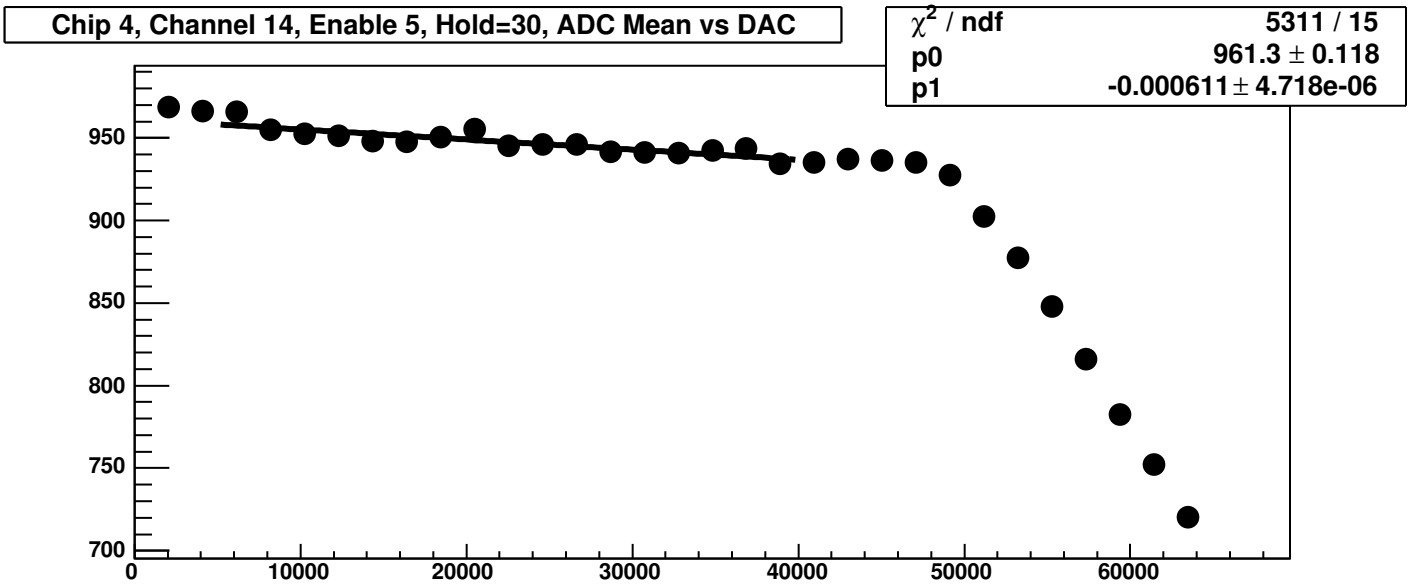


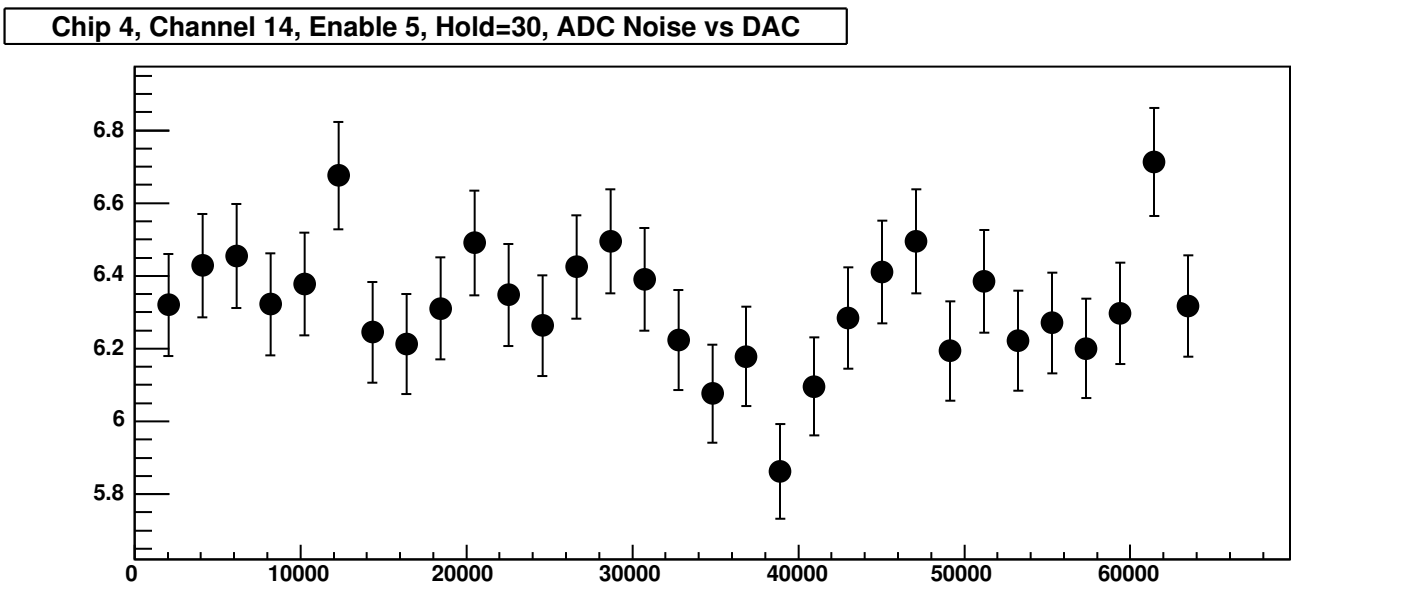
Chip 4, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC

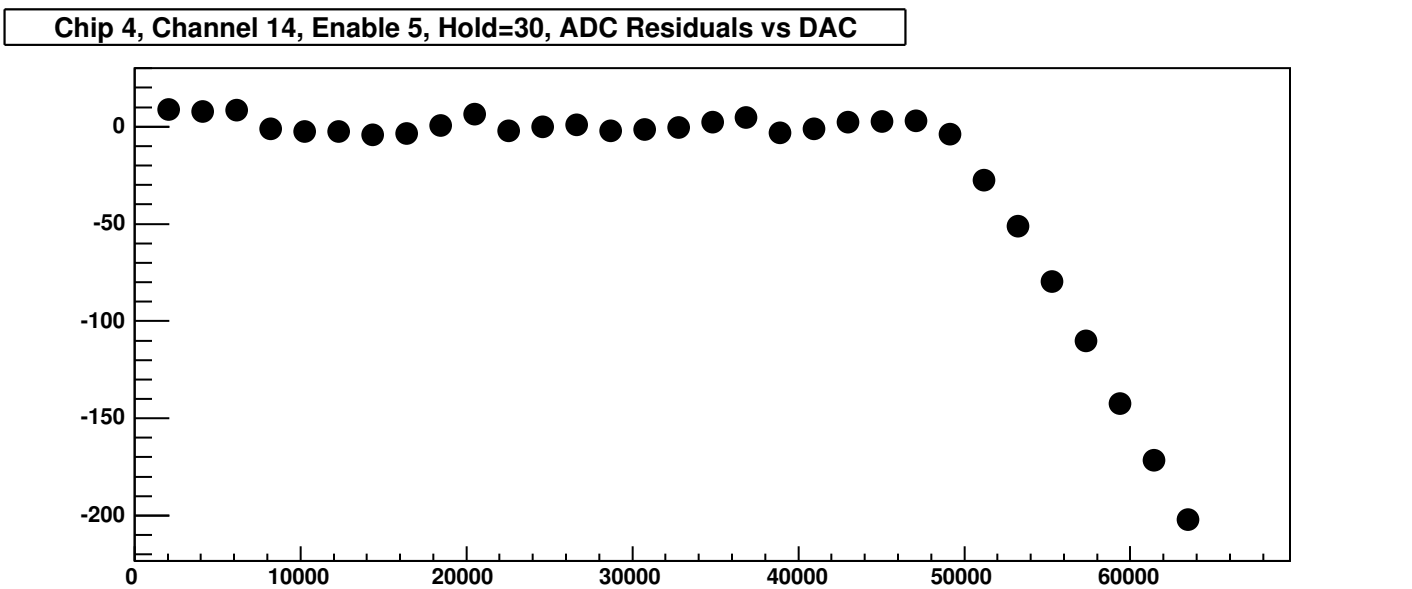


Chip 4, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC



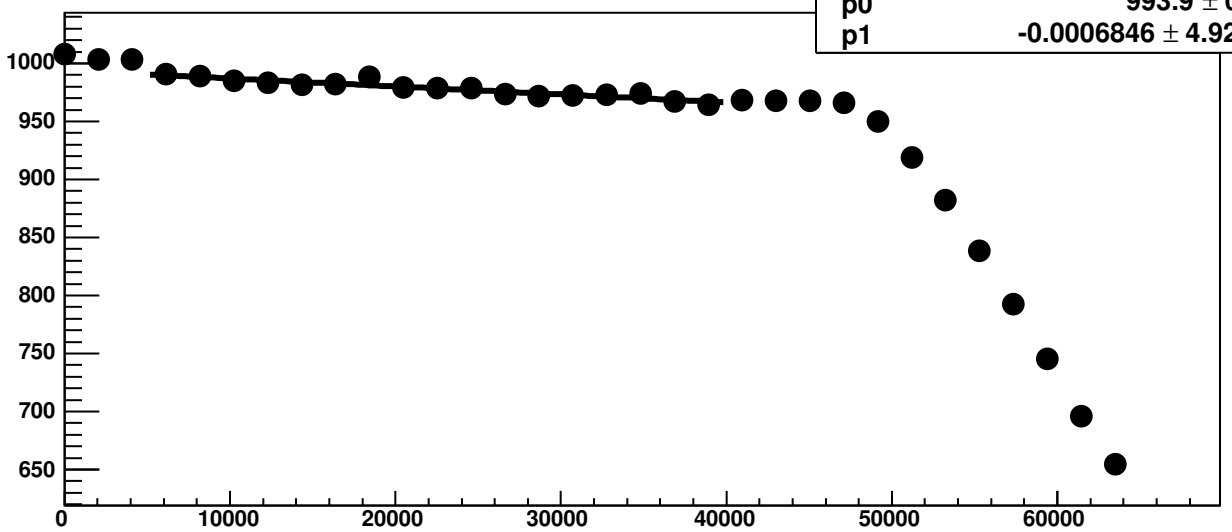

Chip 4, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5311 / 15
p0 961.3 ± 0.118
p1 -0.000611 ± 4.718e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

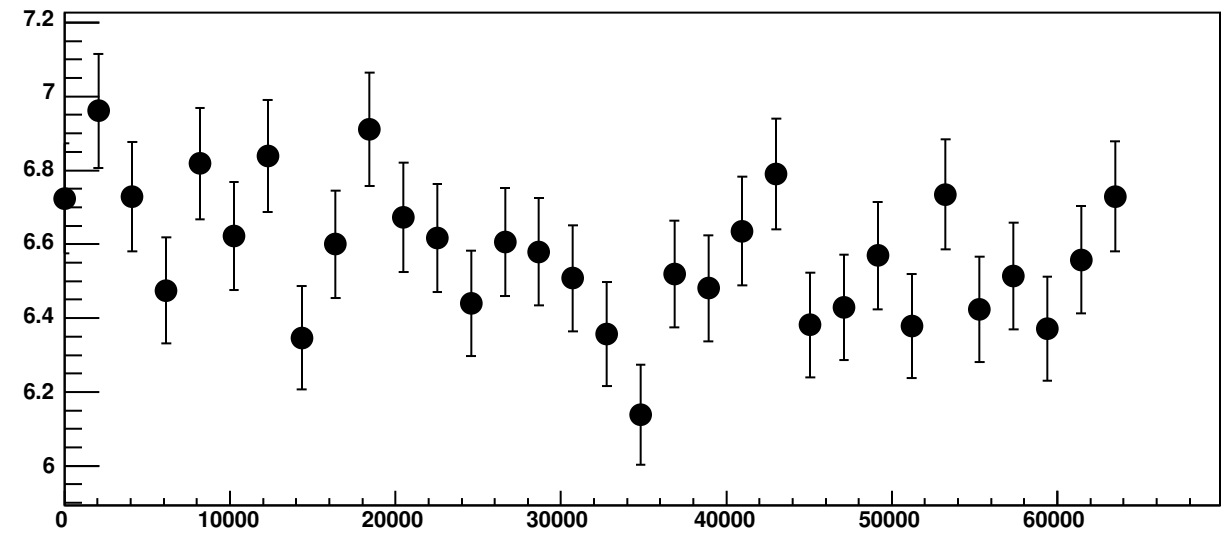

Chip 4, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

Chip 4, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC

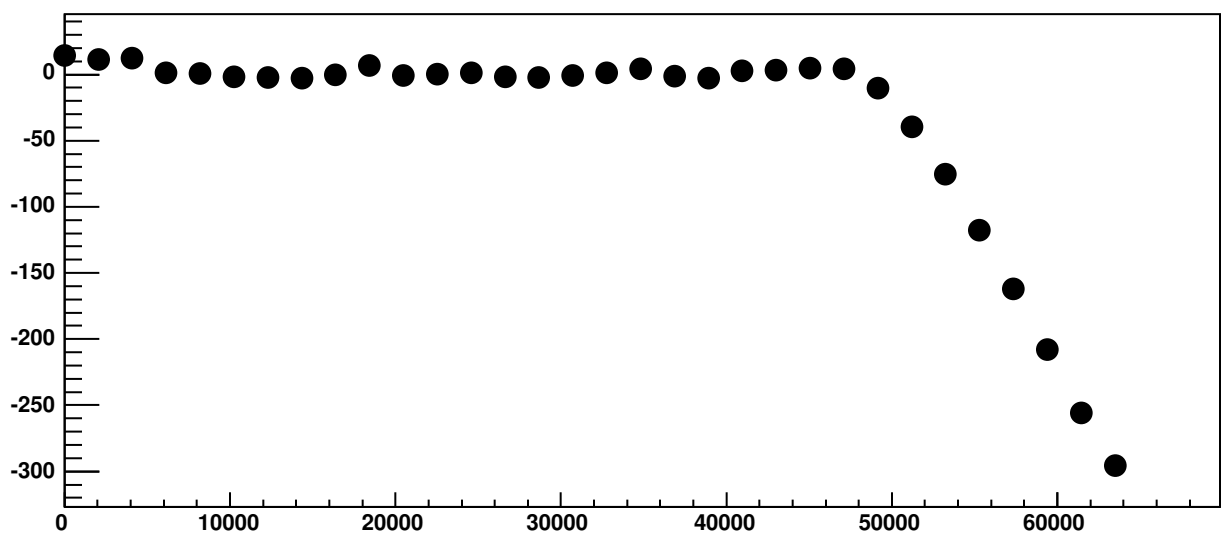
χ^2 / ndf	2496 / 15
p0	993.9 ± 0.1229
p1	-0.0006846 ± 4.927e-06



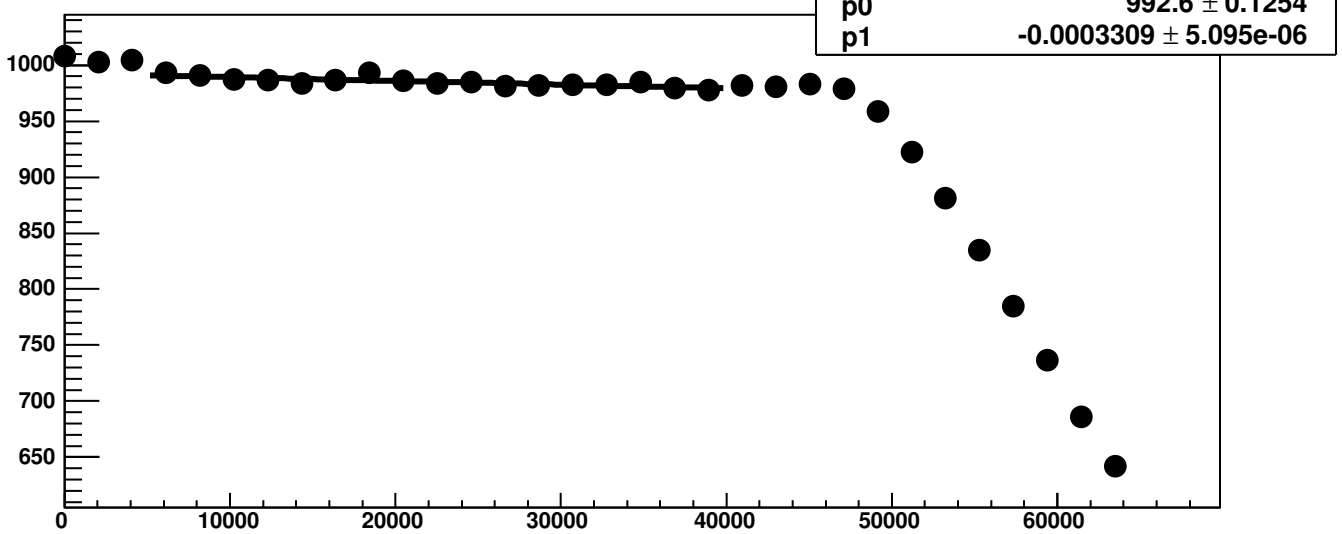
Chip 4, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



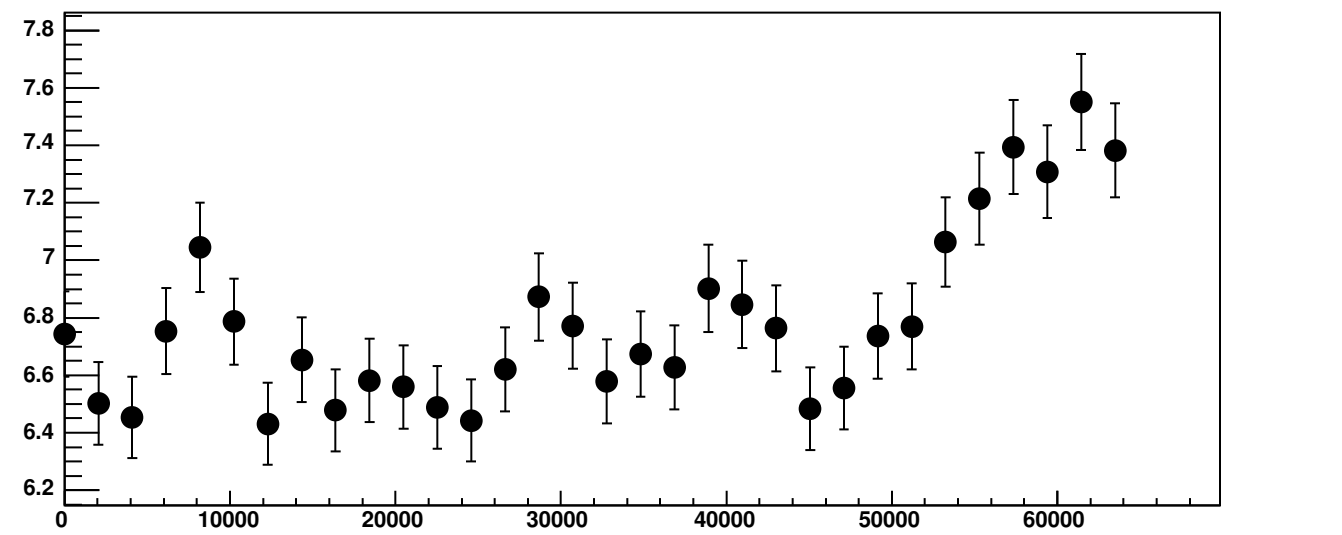
Chip 4, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC



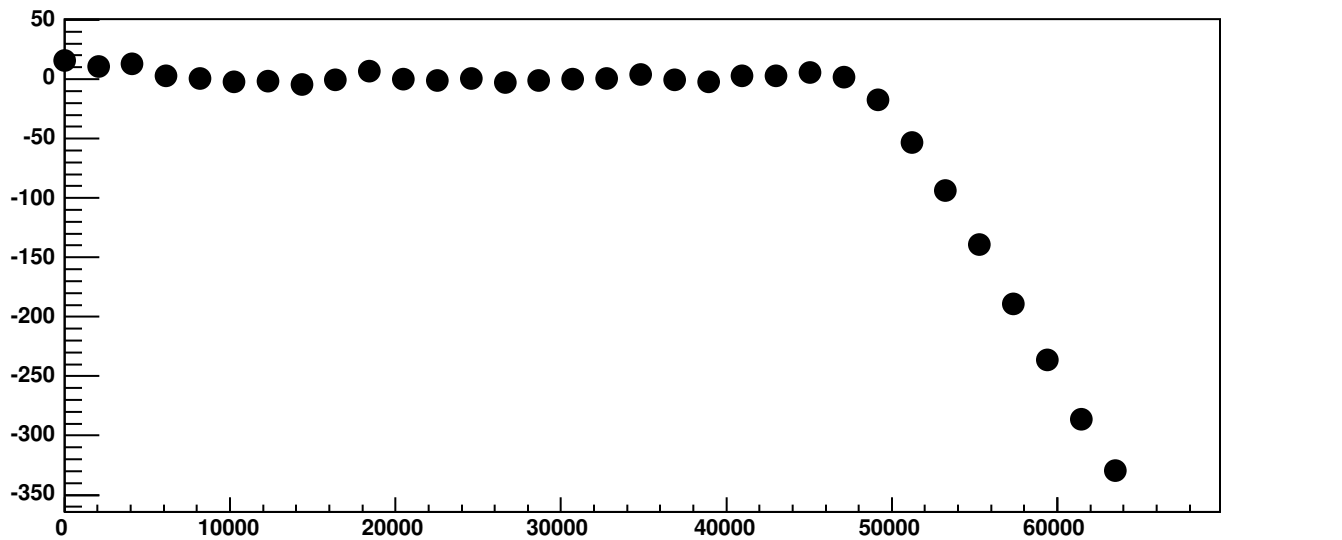
Chip 4, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

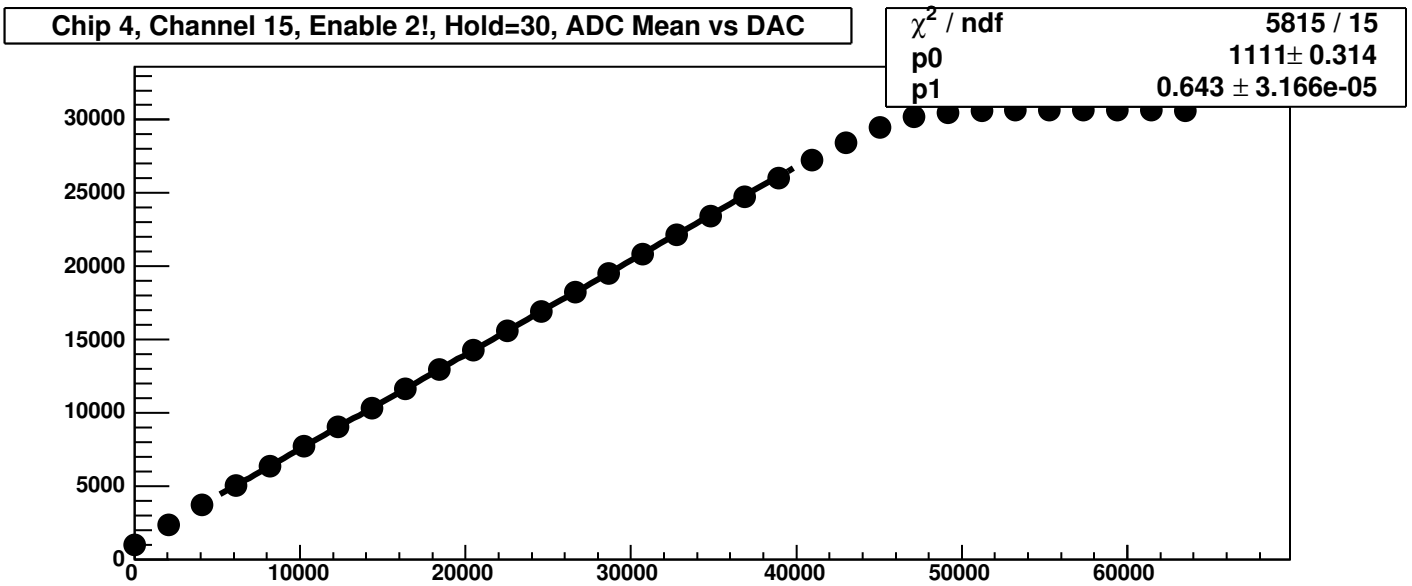


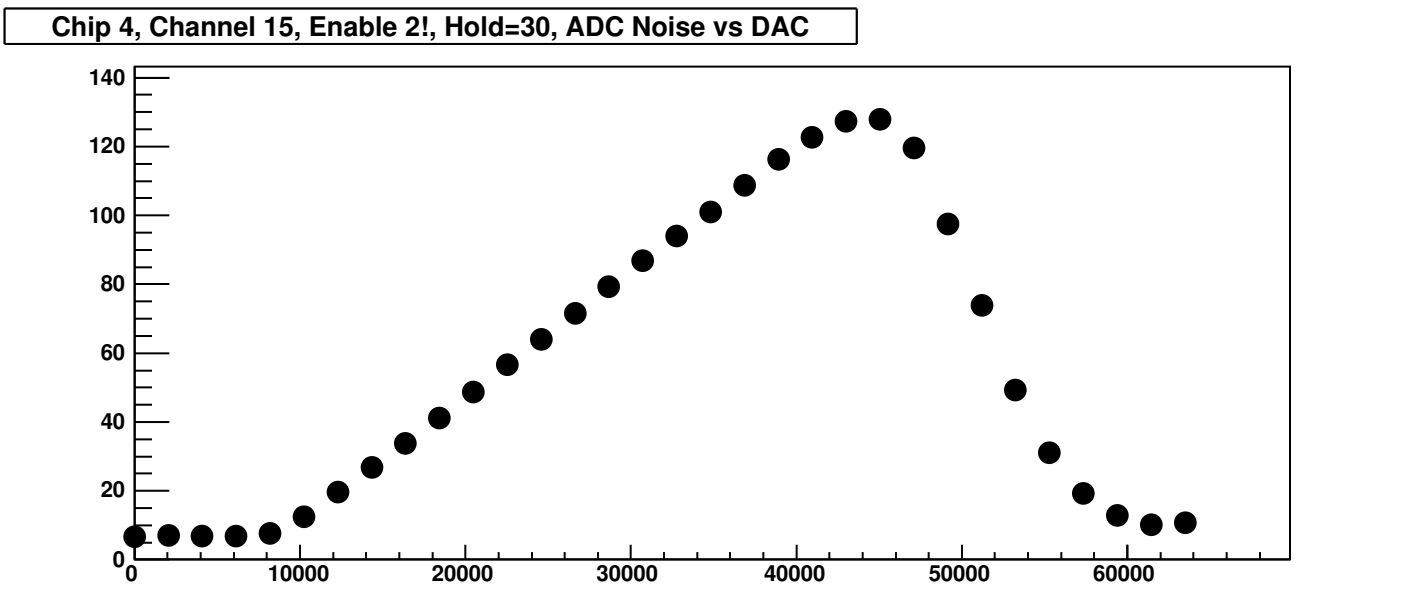
Chip 4, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC

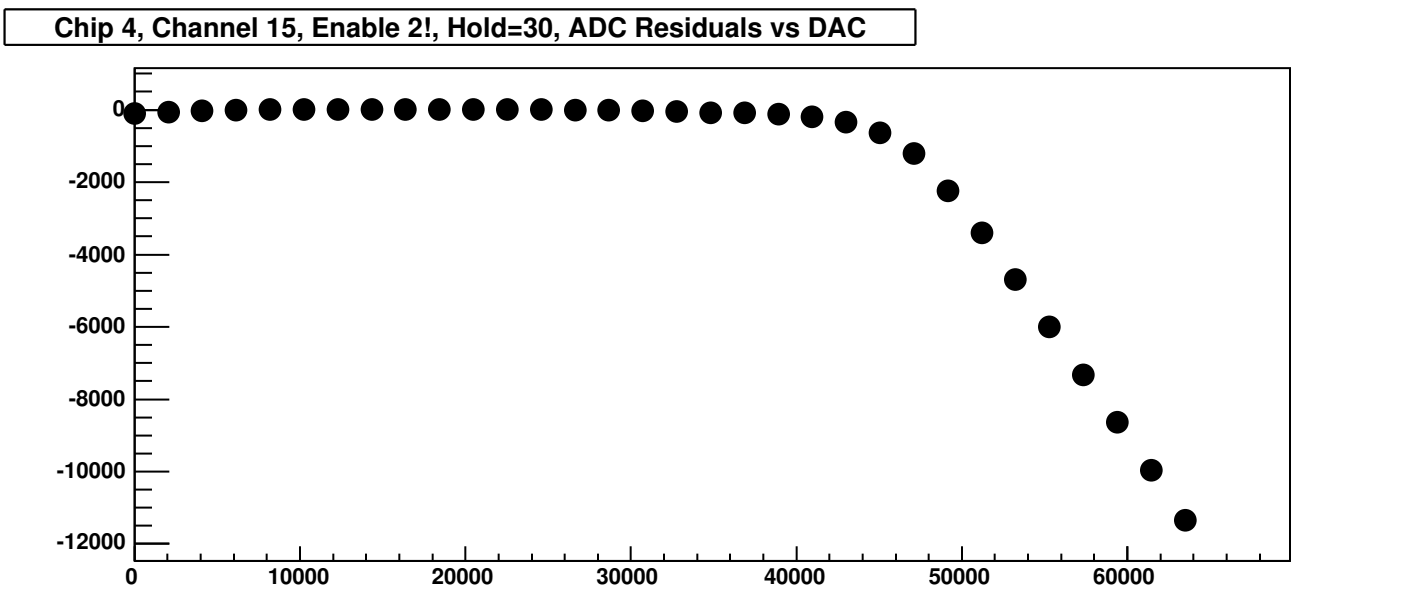


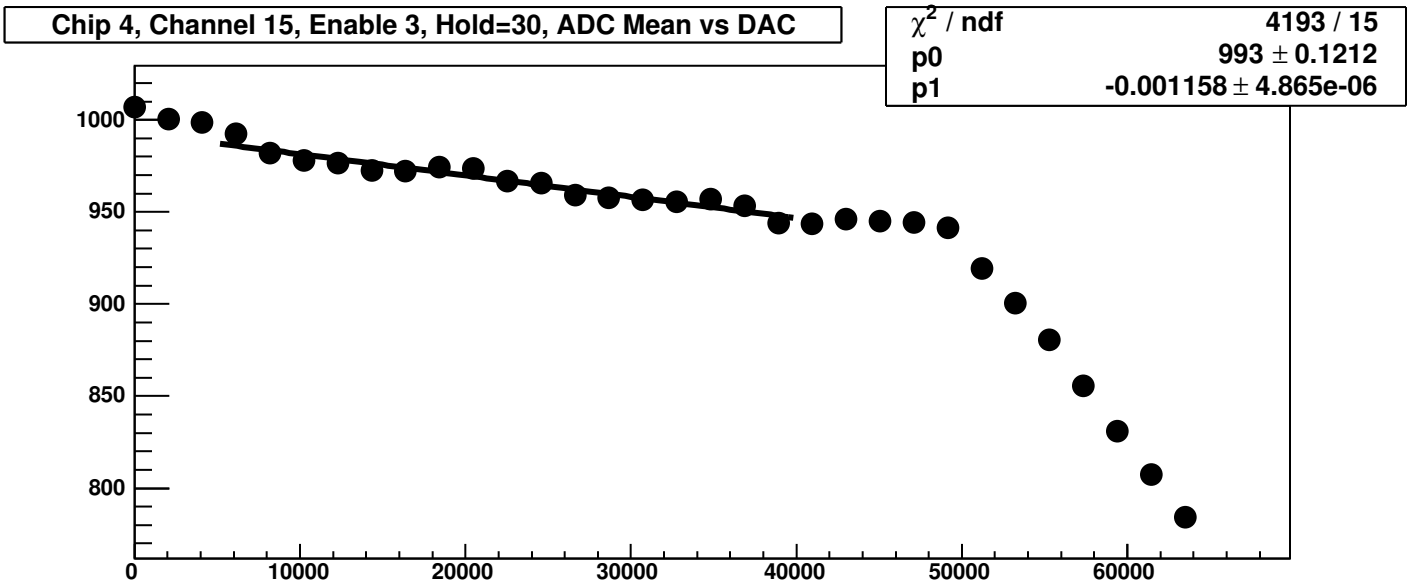
Chip 4, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

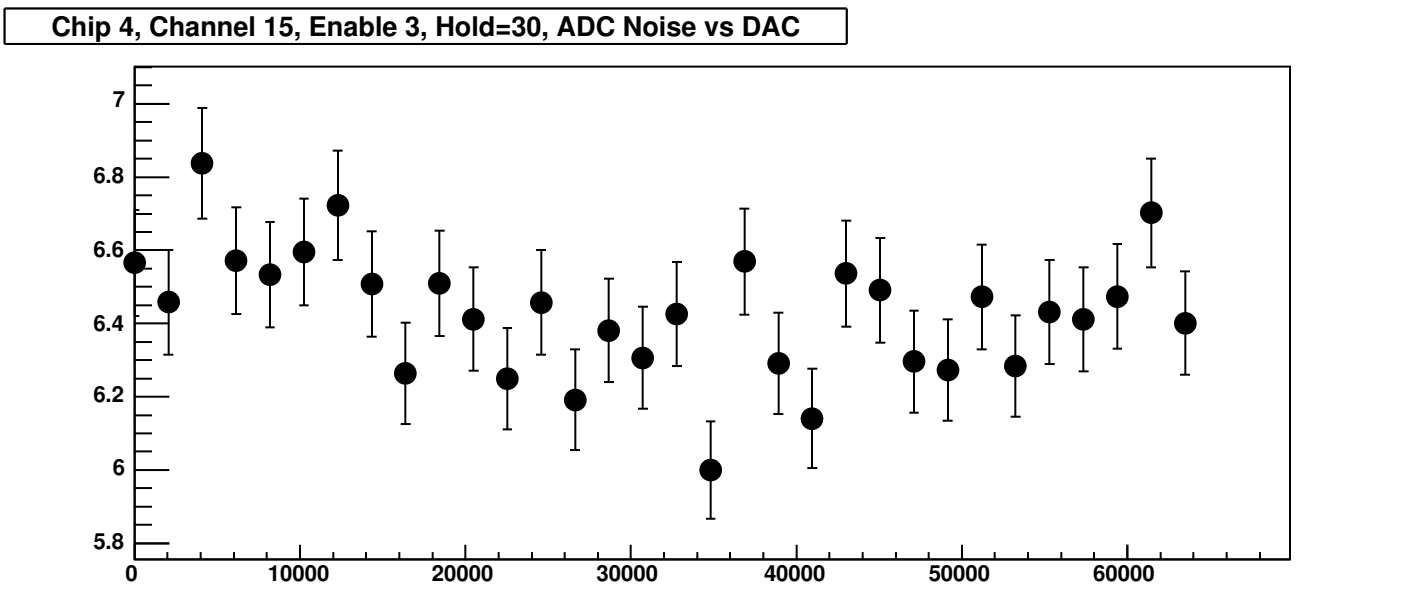


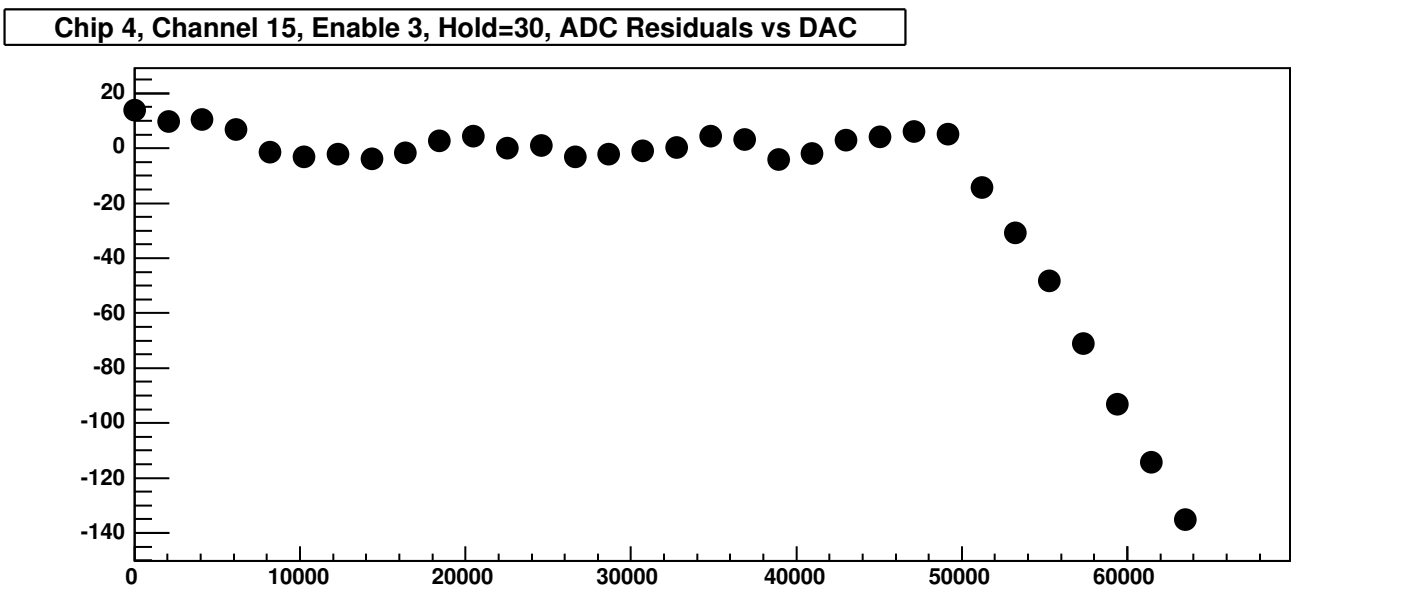

Chip 4, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 5815 / 15
p0 1111 ± 0.314
p1 0.643 ± 3.166e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

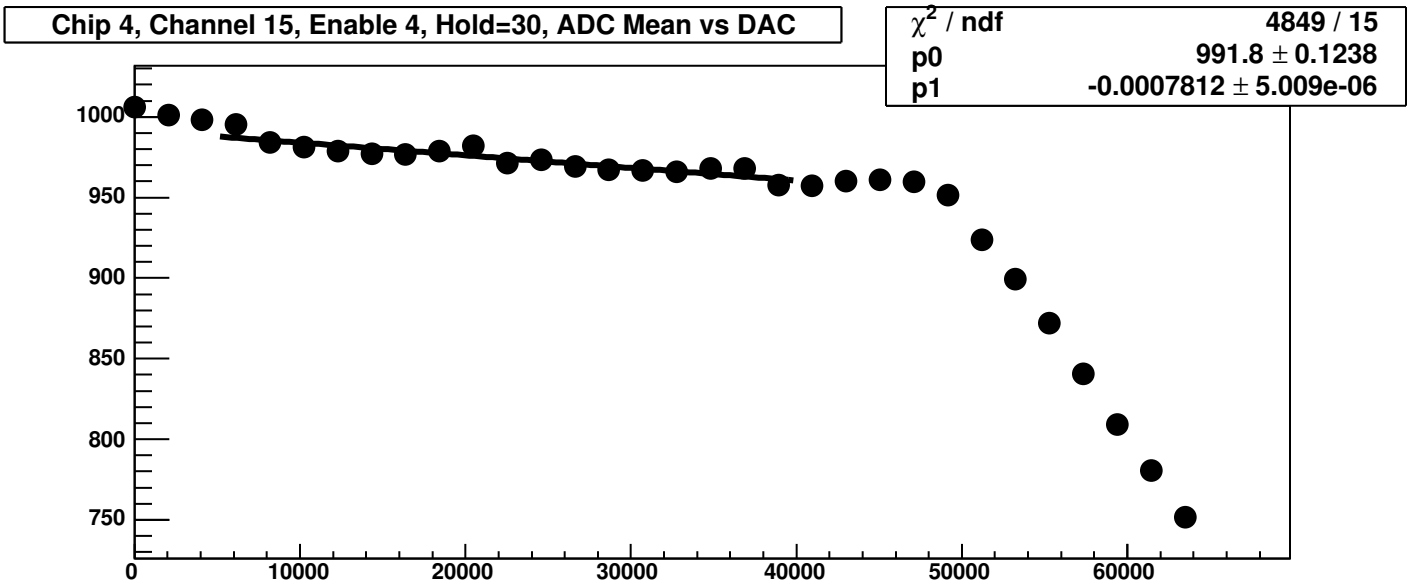

Chip 4, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

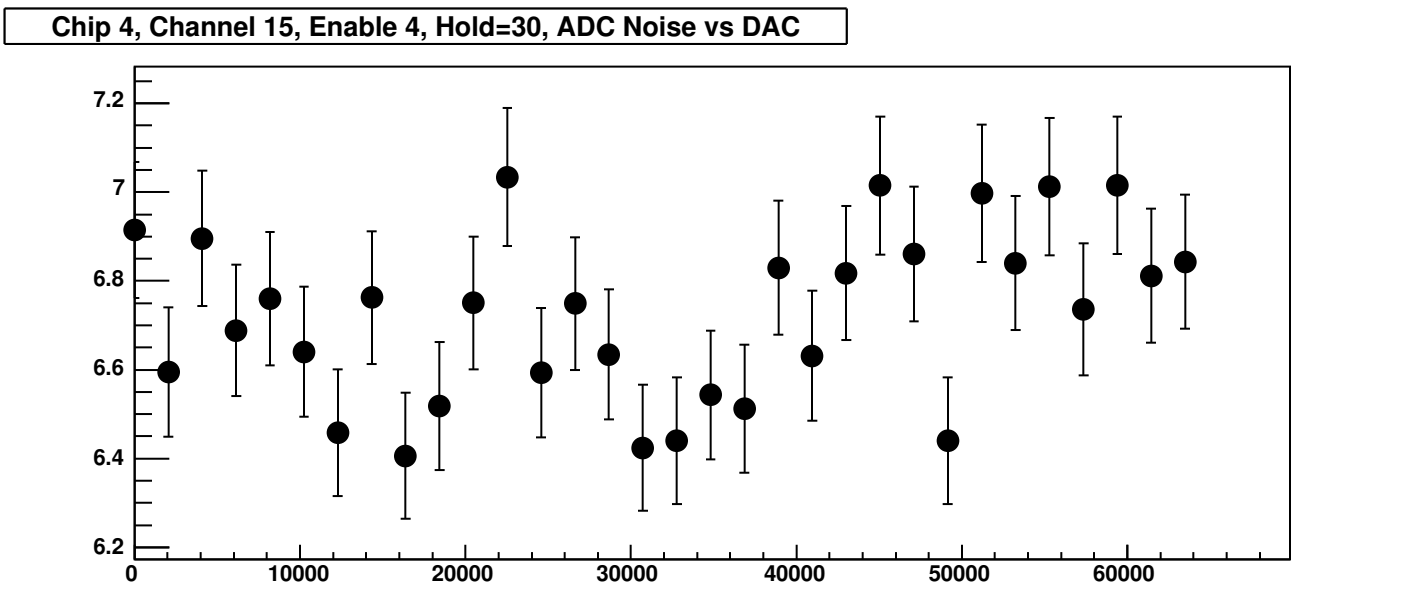

Chip 4, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

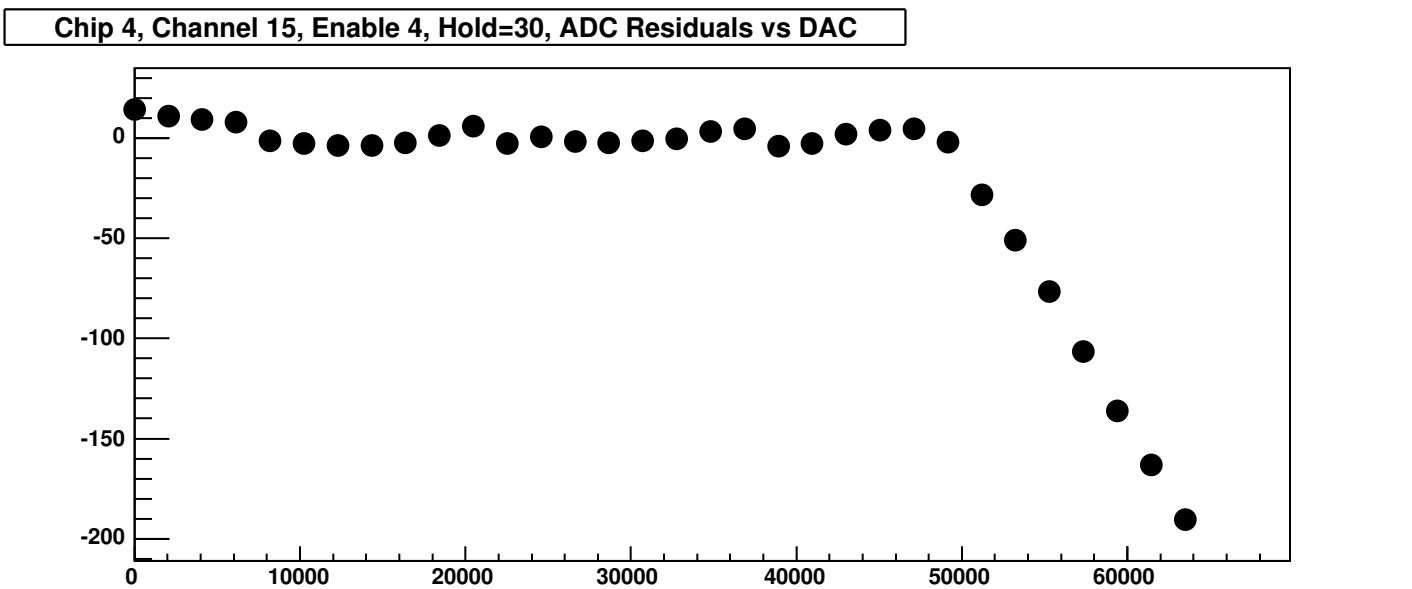

Chip 4, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4193 / 15
p0 993 ± 0.1212
p1 -0.001158 ± 4.865e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

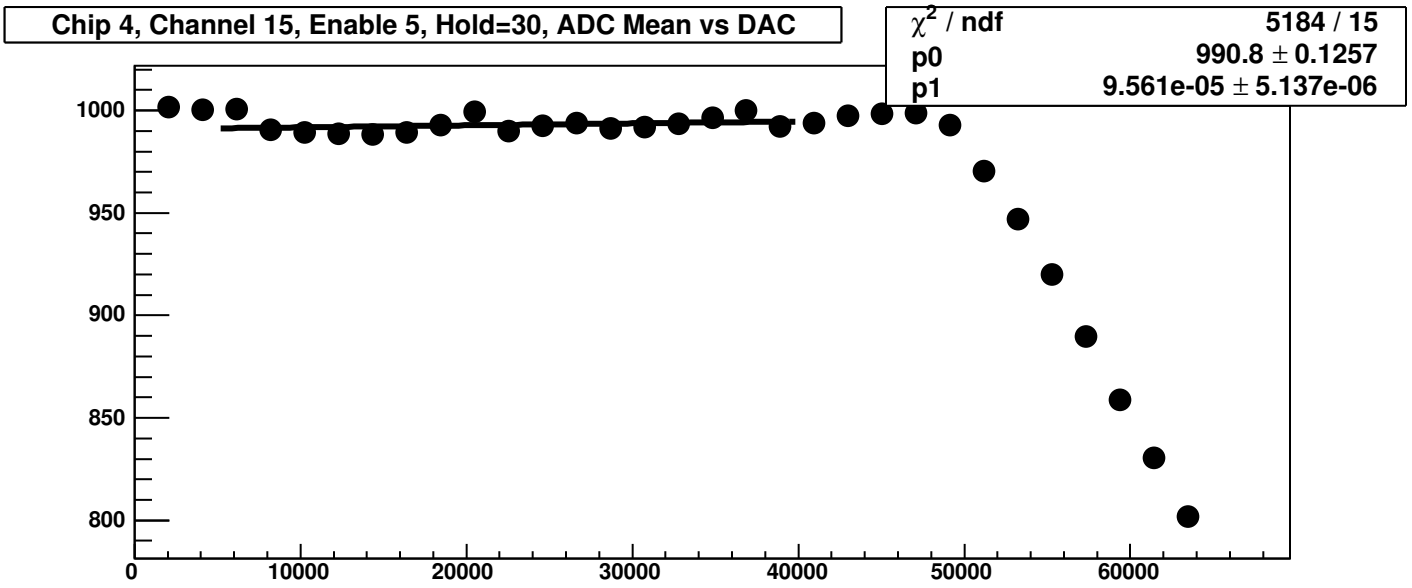

Chip 4, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

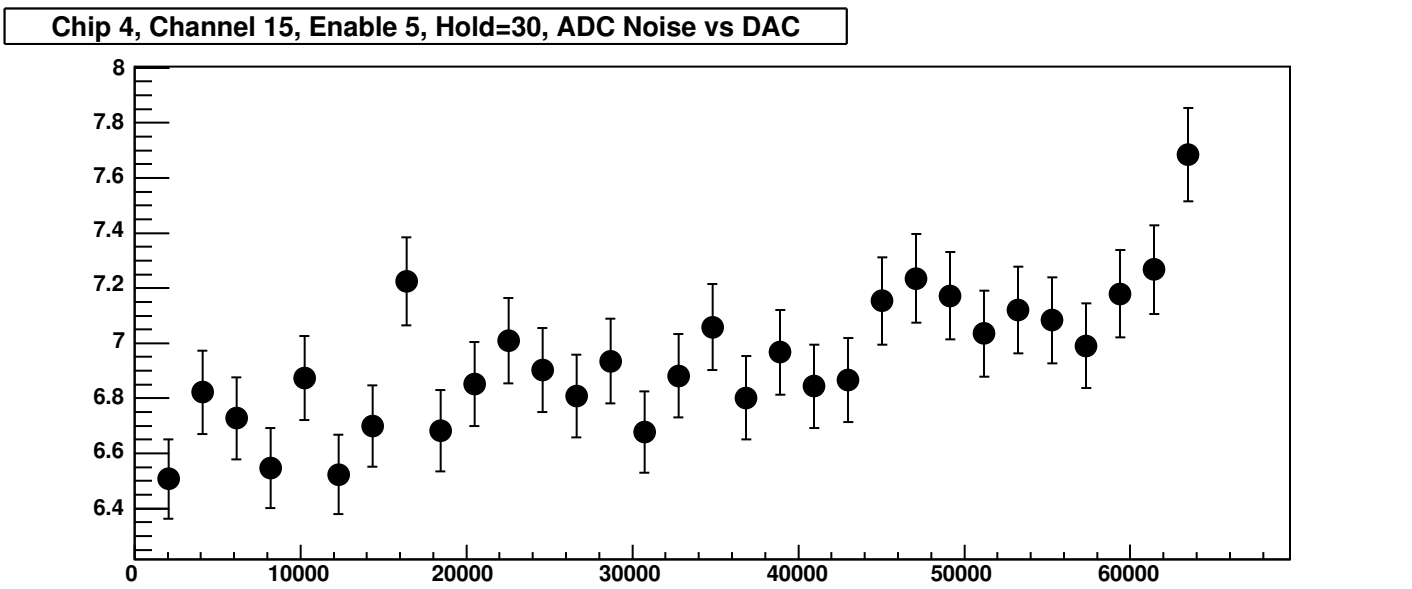

Chip 4, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

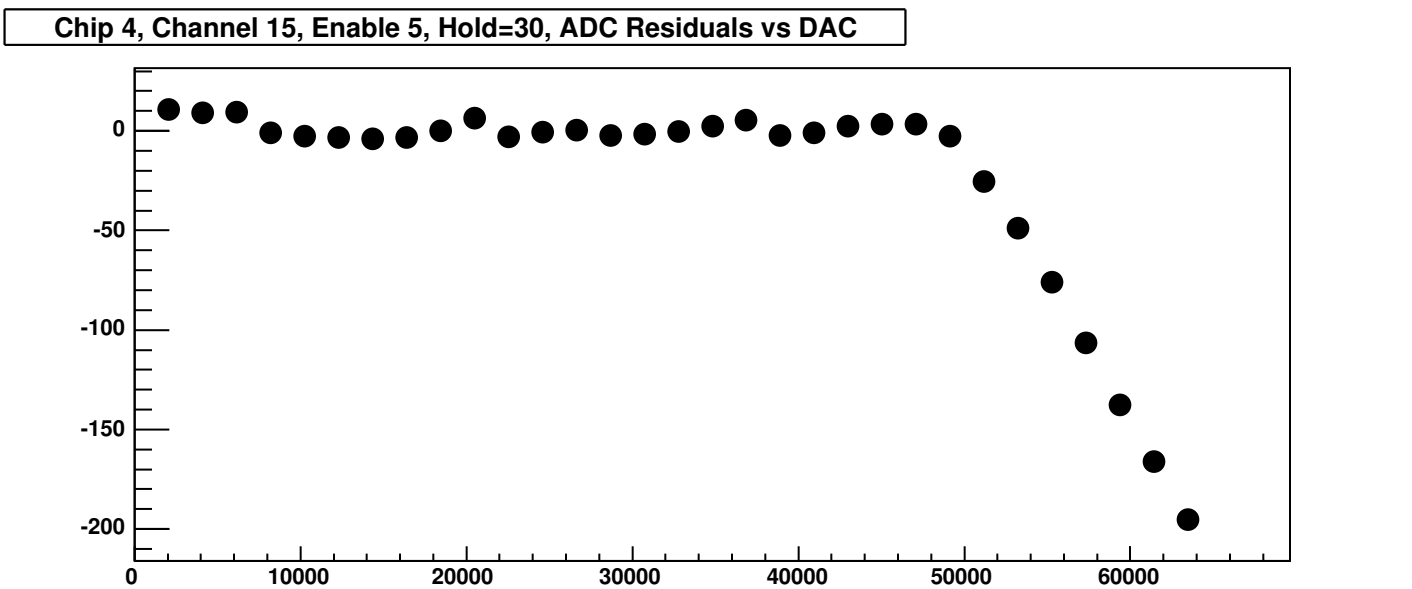

Chip 4, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4849 / 15
p0 991.8 ± 0.1238
p1 -0.0007812 ± 5.009e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

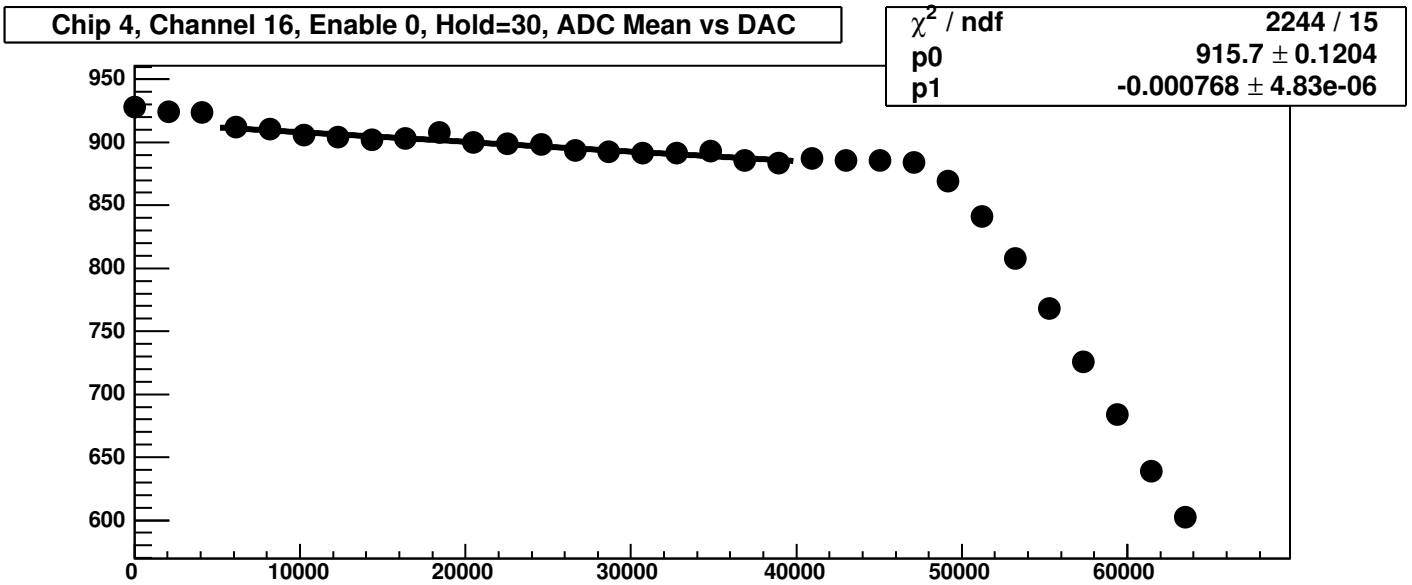

Chip 4, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

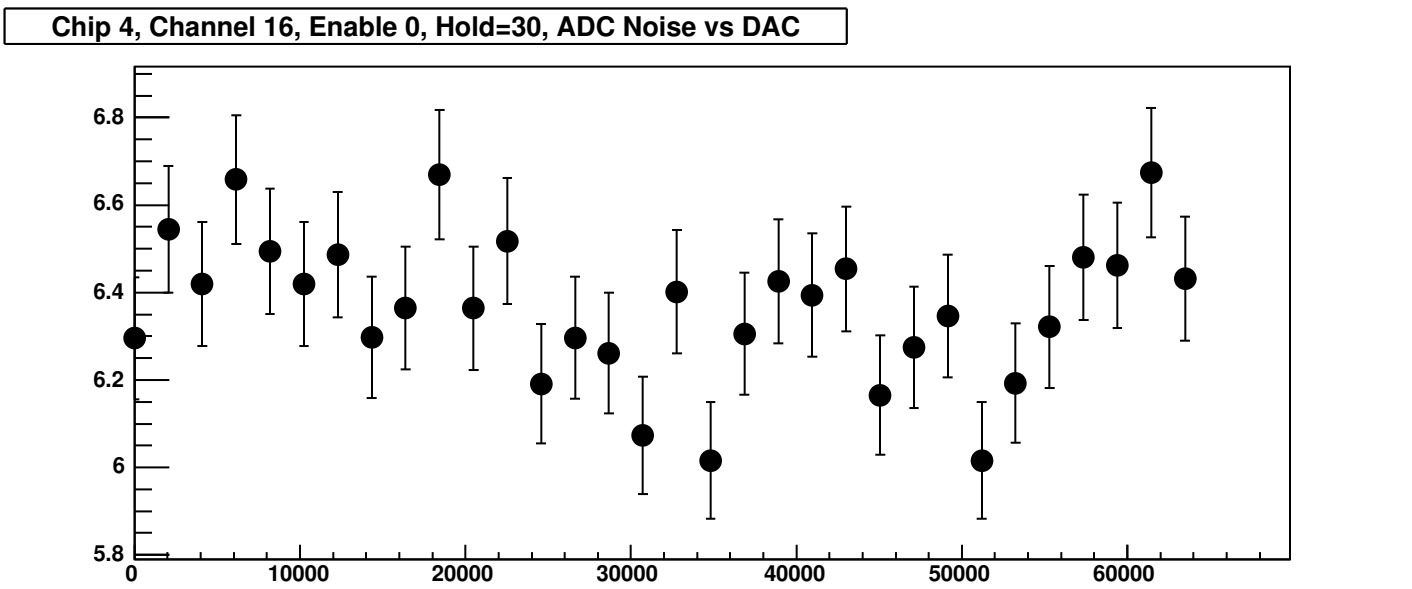

Chip 4, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

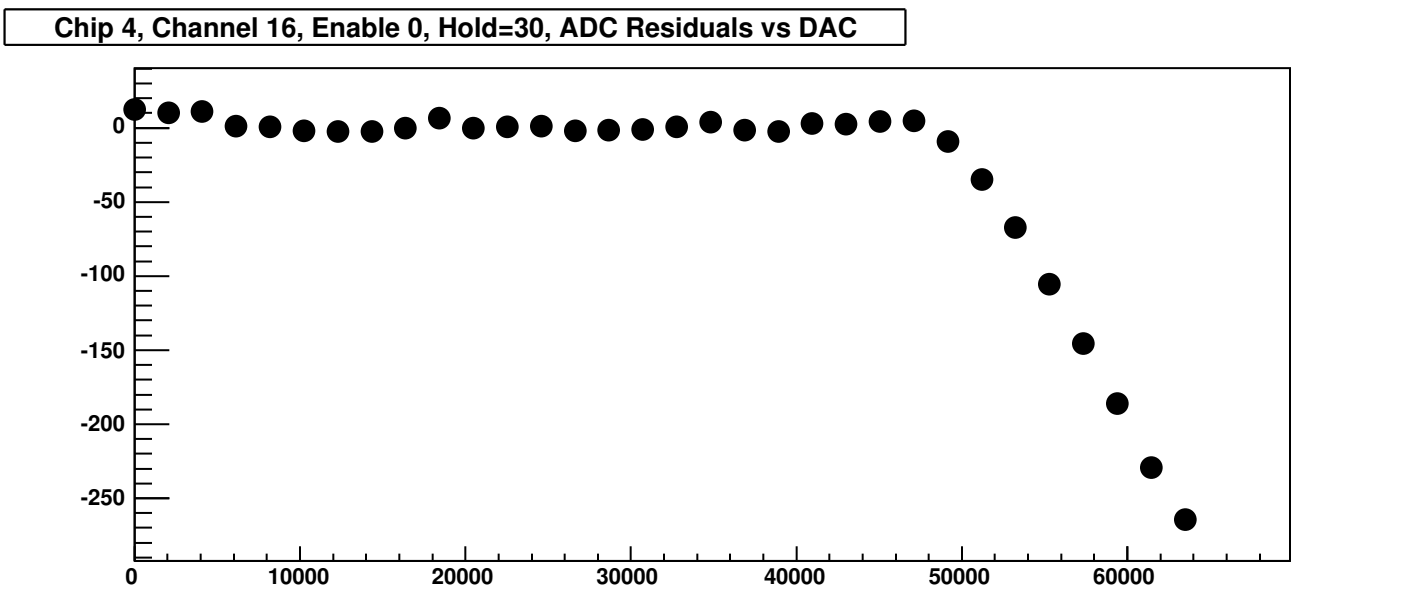

Chip 4, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5184 / 15
p0 990.8 ± 0.1257
p1 9.561e-05 ± 5.137e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC
8
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

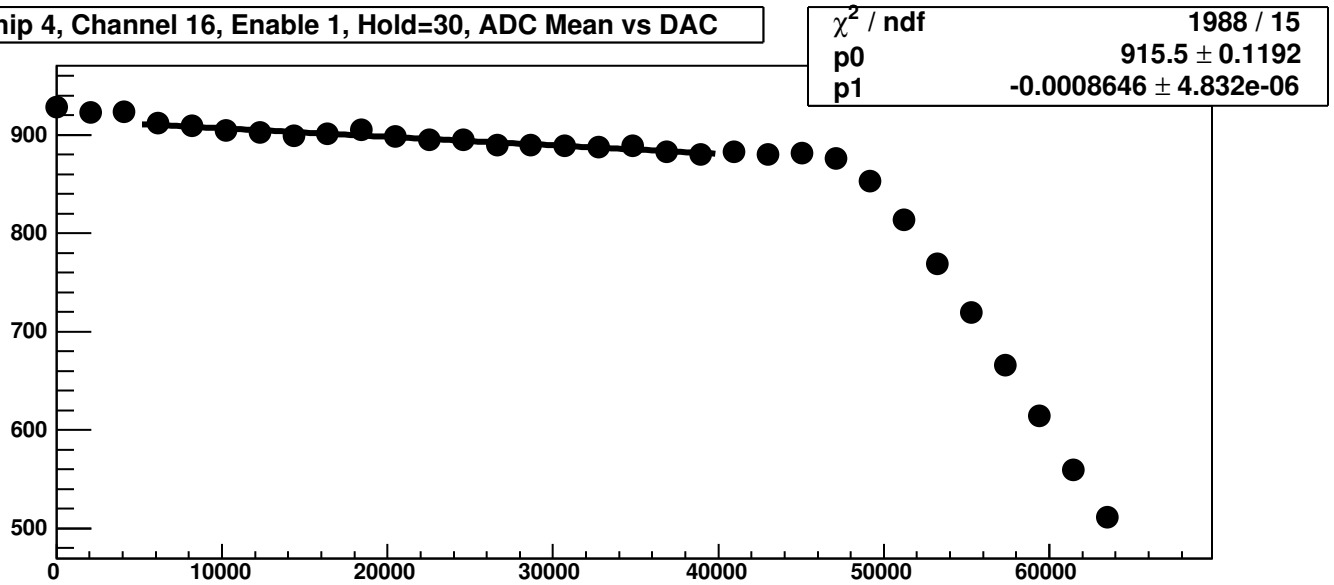

Chip 4, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2244 / 15
p0 915.7 ± 0.1204
p1 -0.000768 ± 4.83e-06
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

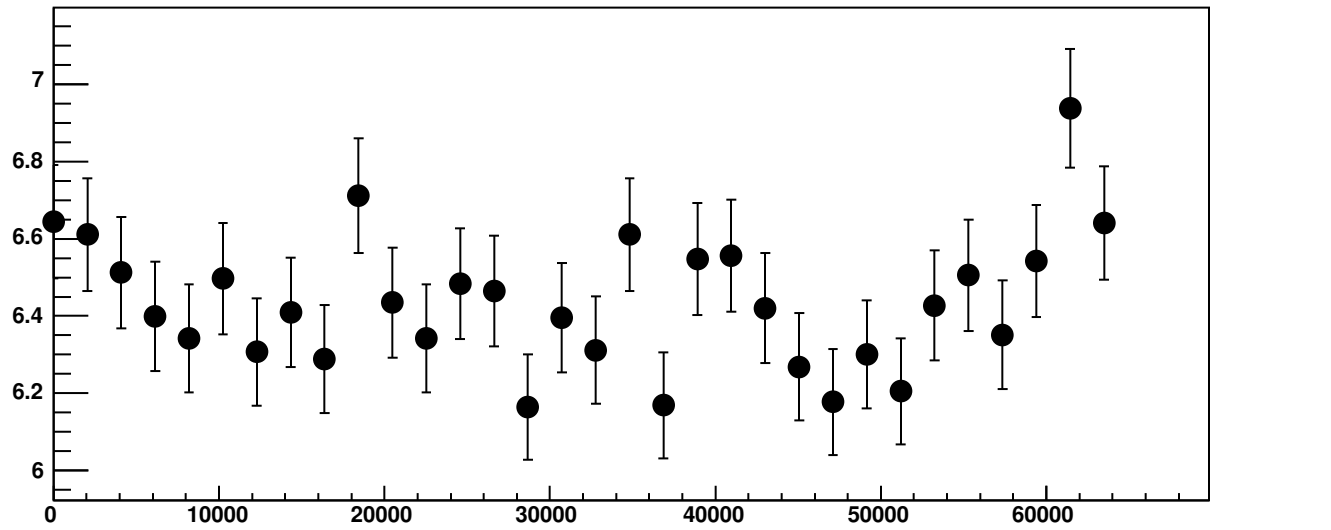

Chip 4, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

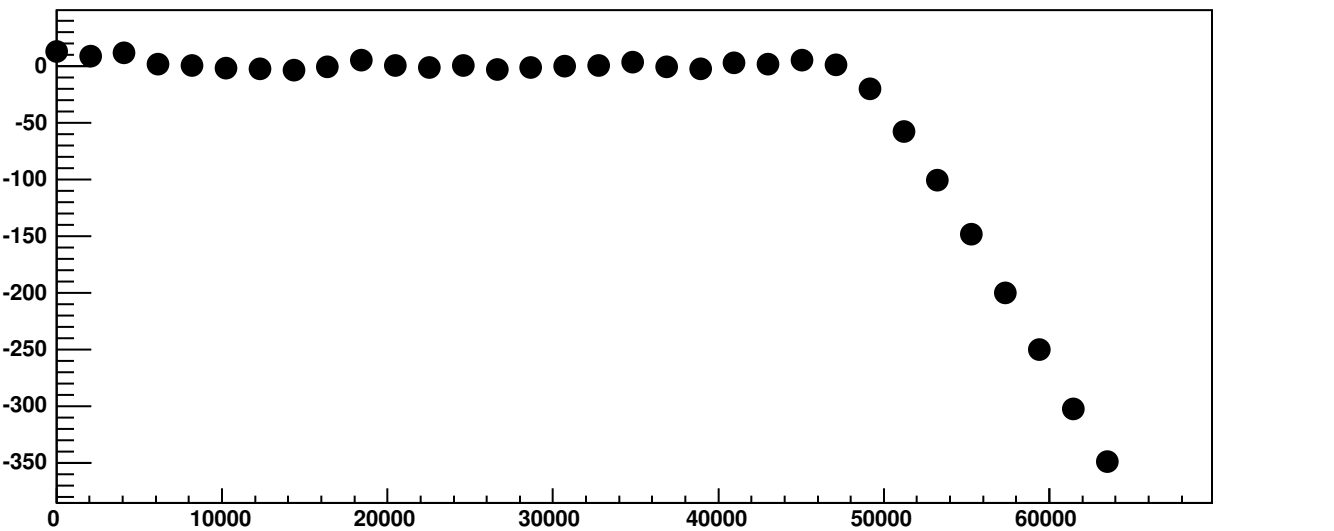
Chip 4, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC

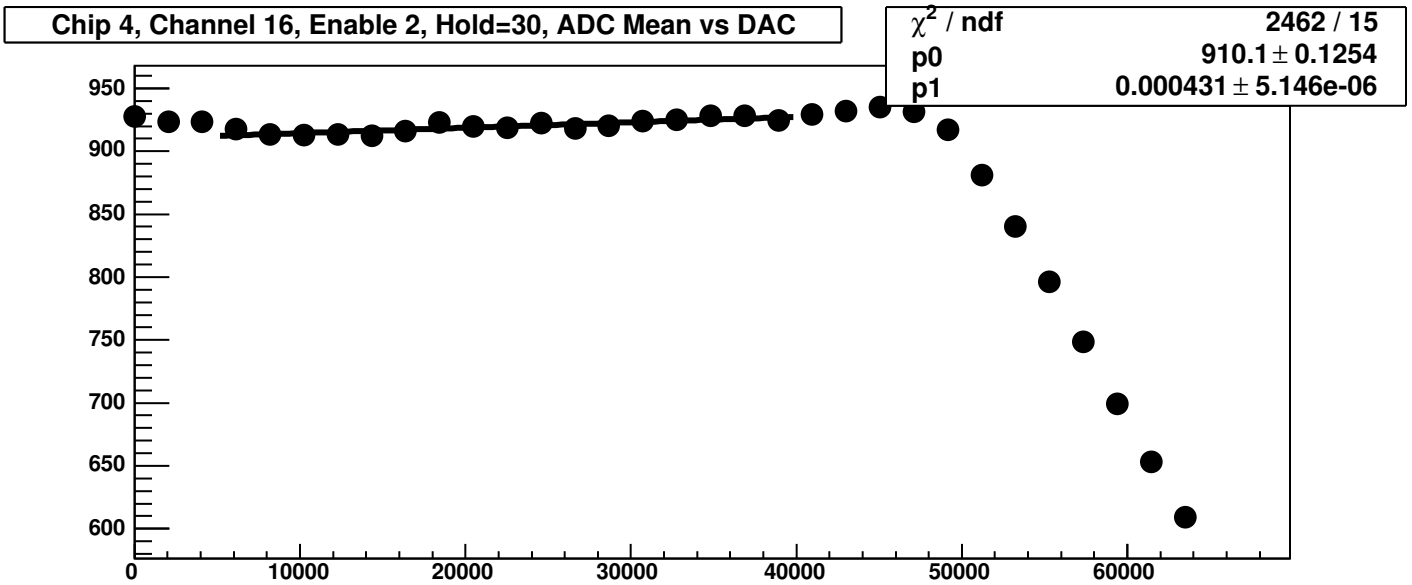


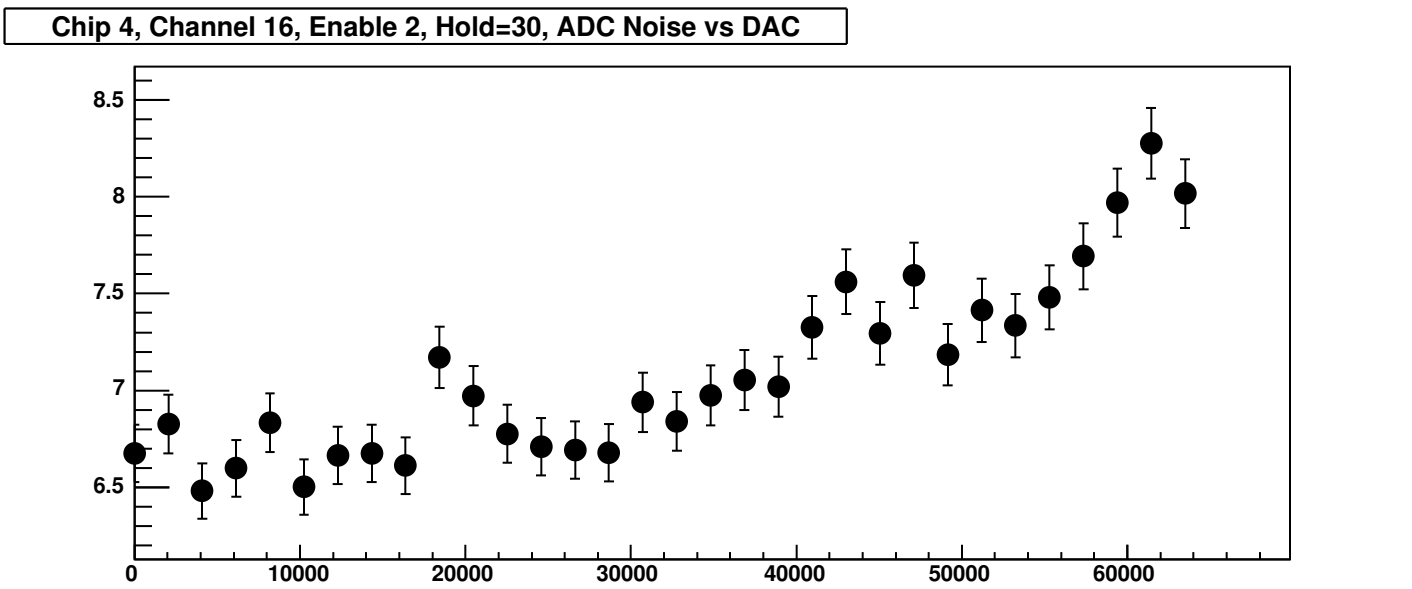
Chip 4, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC

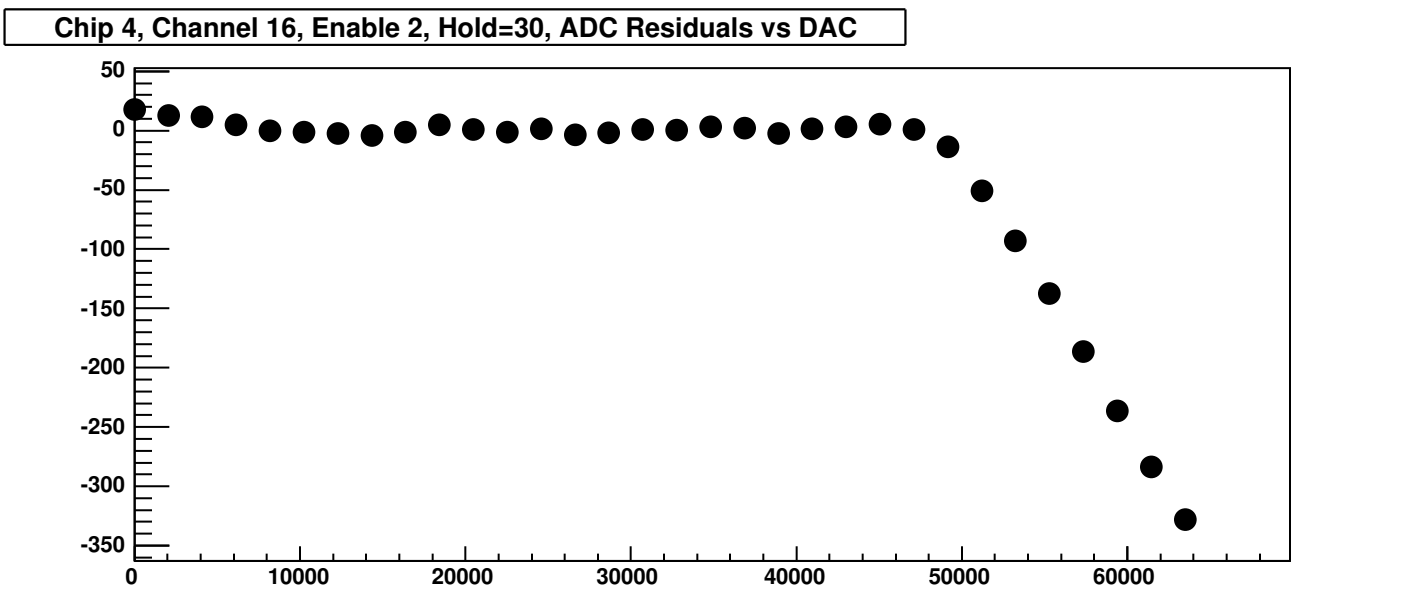


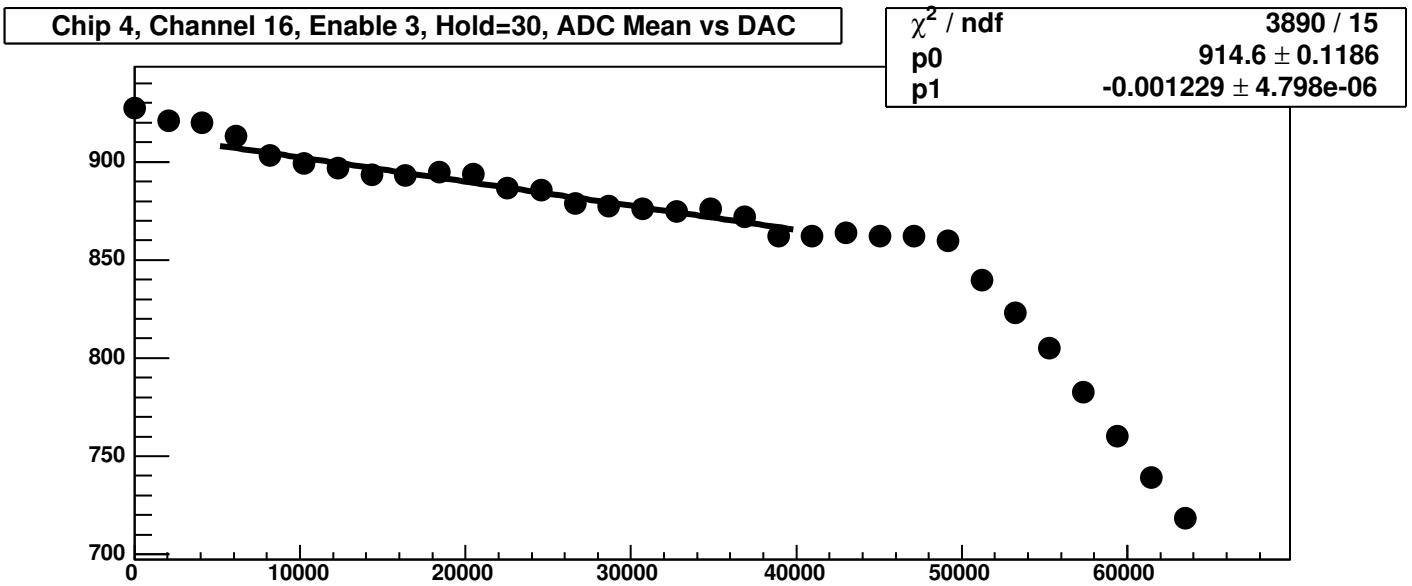
Chip 4, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC

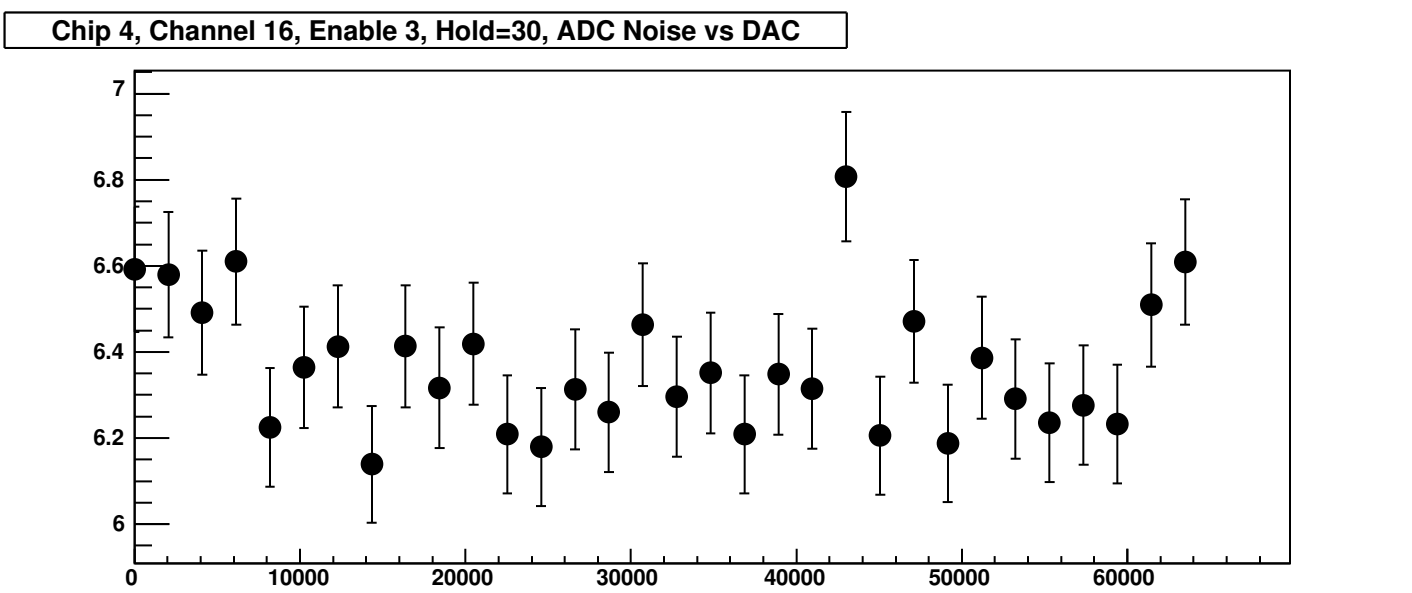


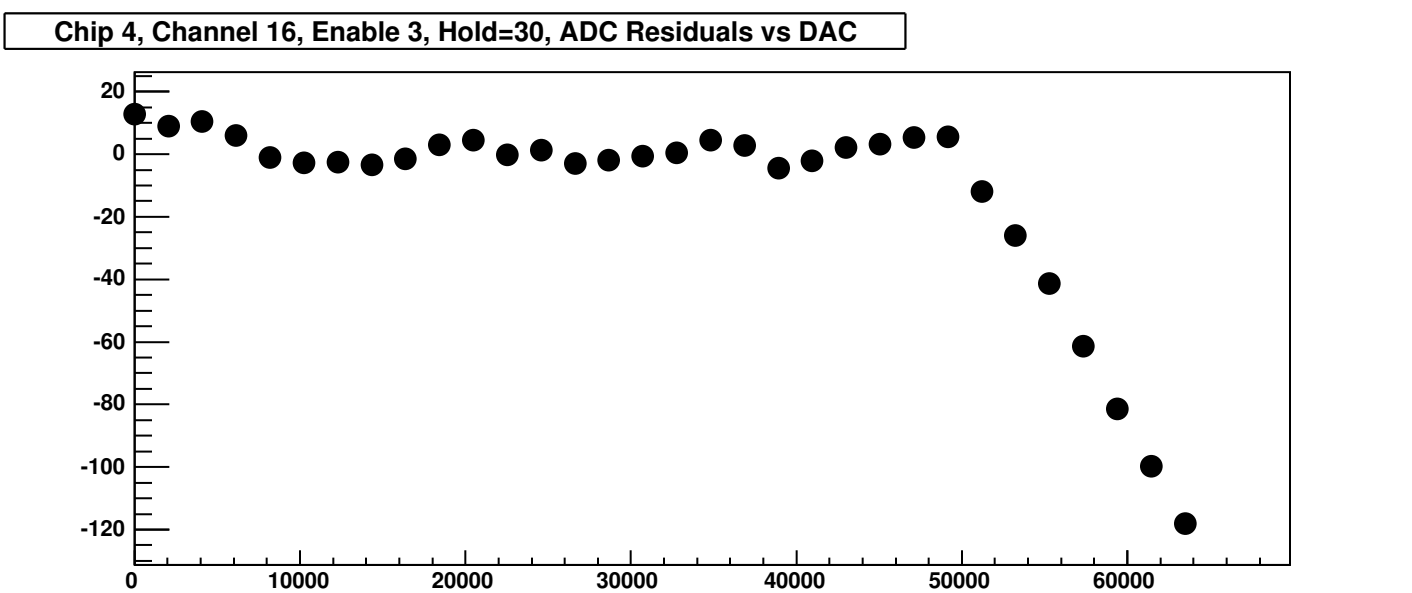

Chip 4, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2462 / 15
p0 910.1 ± 0.1254
p1 0.000431 ± 5.146e-06
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

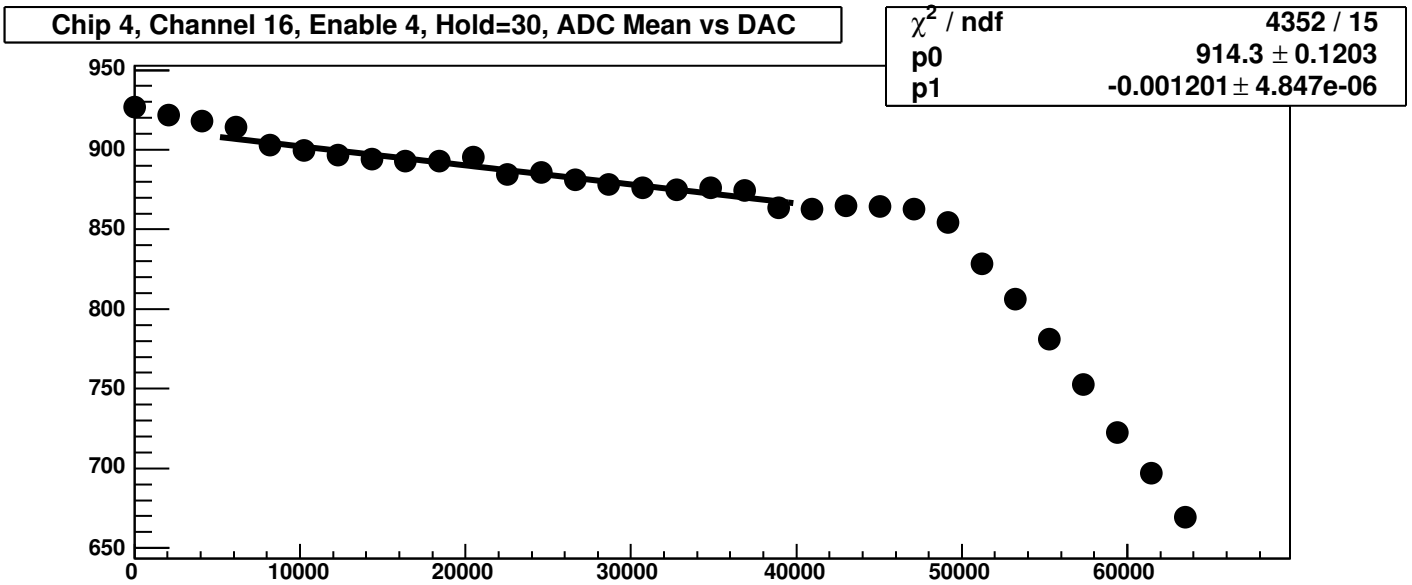

Chip 4, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
0
10000
20000
30000
40000
50000
60000

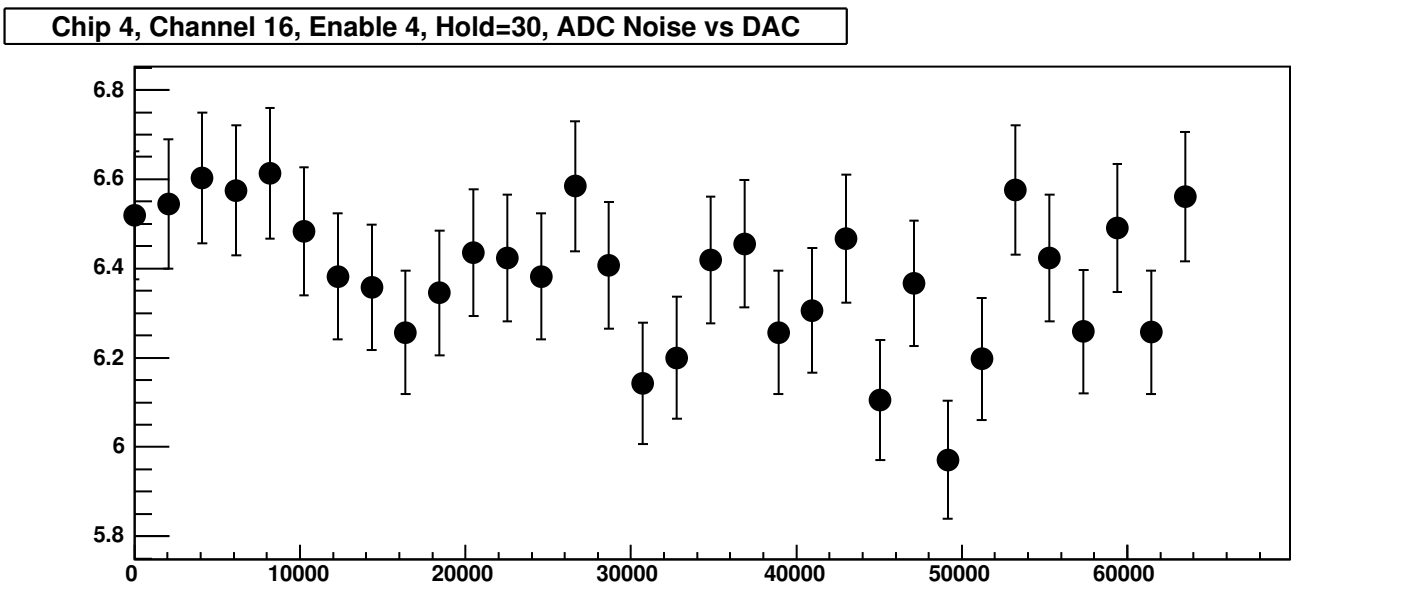

Chip 4, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

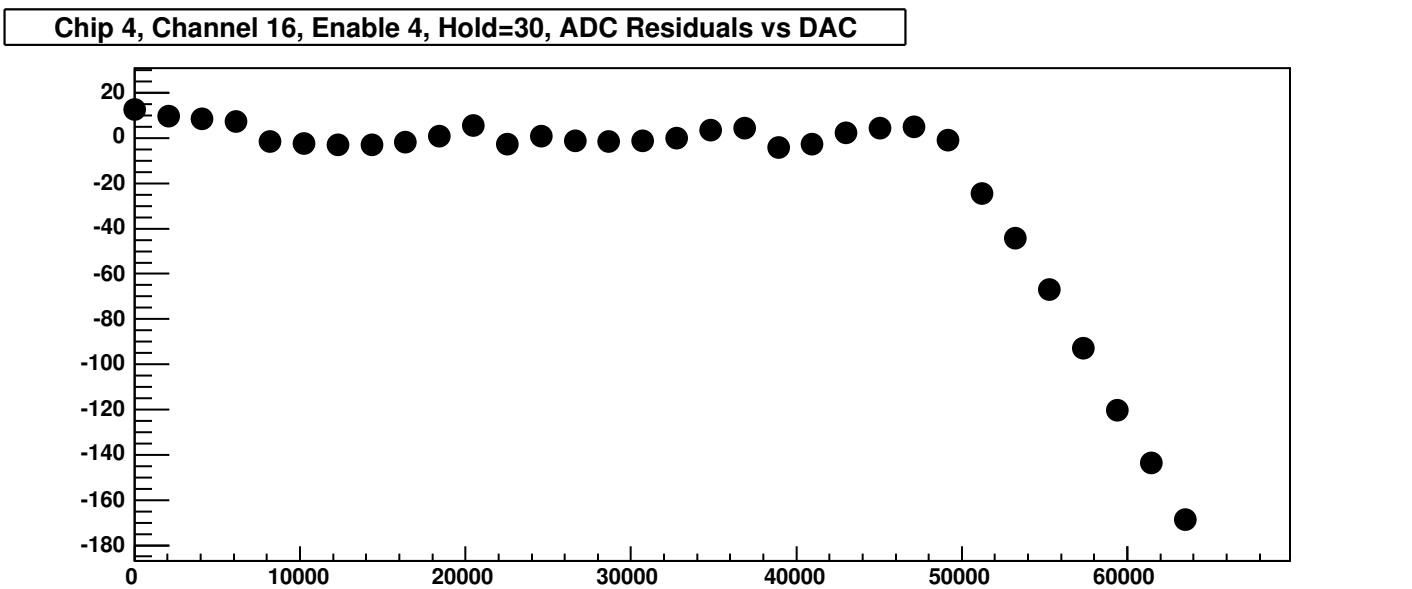

Chip 4, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3890 / 15
p0 914.6 ± 0.1186
p1 -0.001229 ± 4.798e-06
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

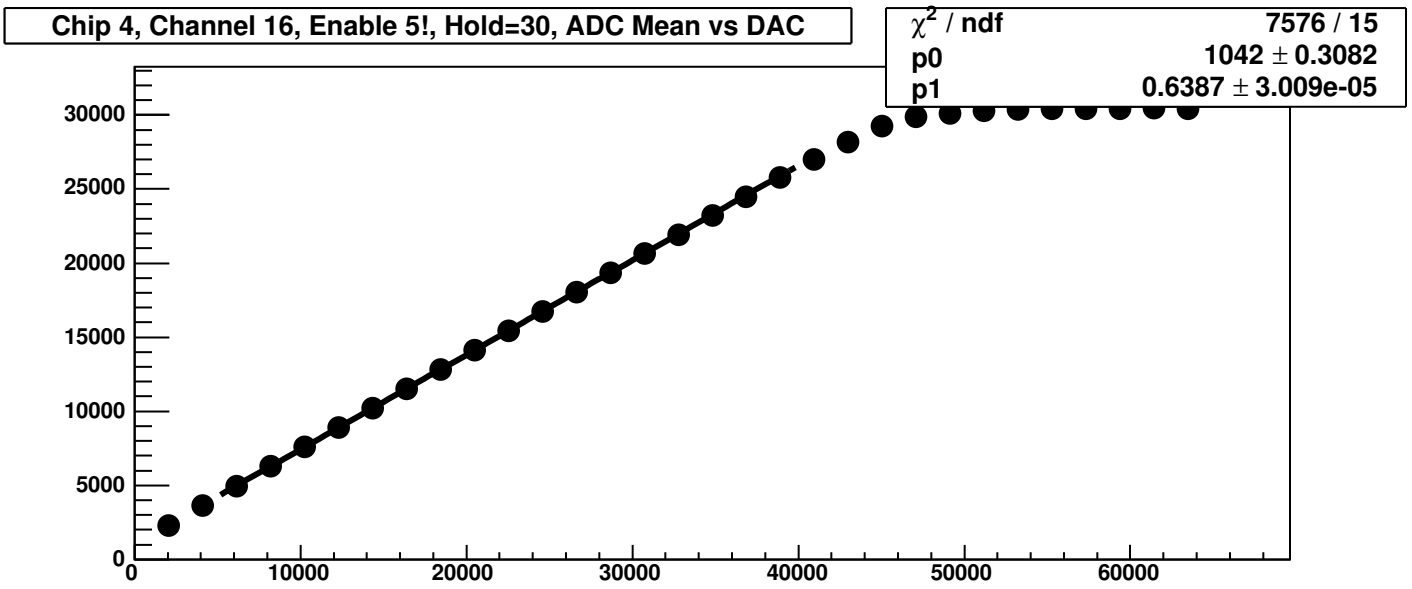

Chip 4, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

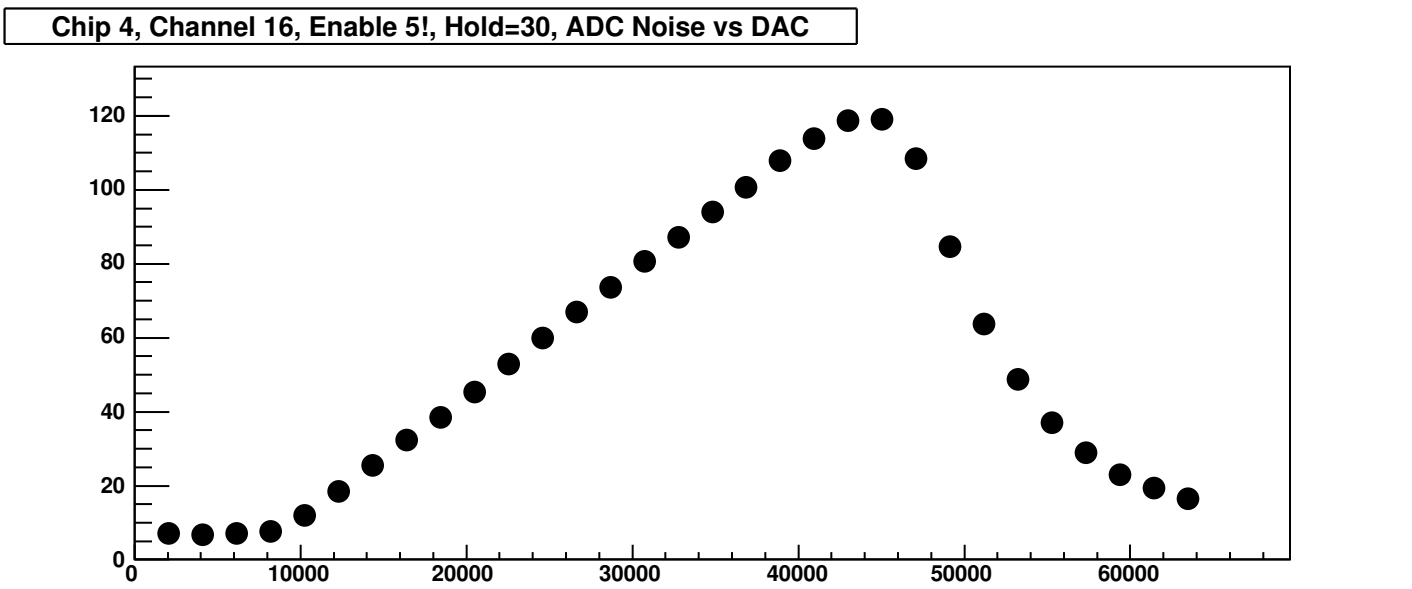

Chip 4, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

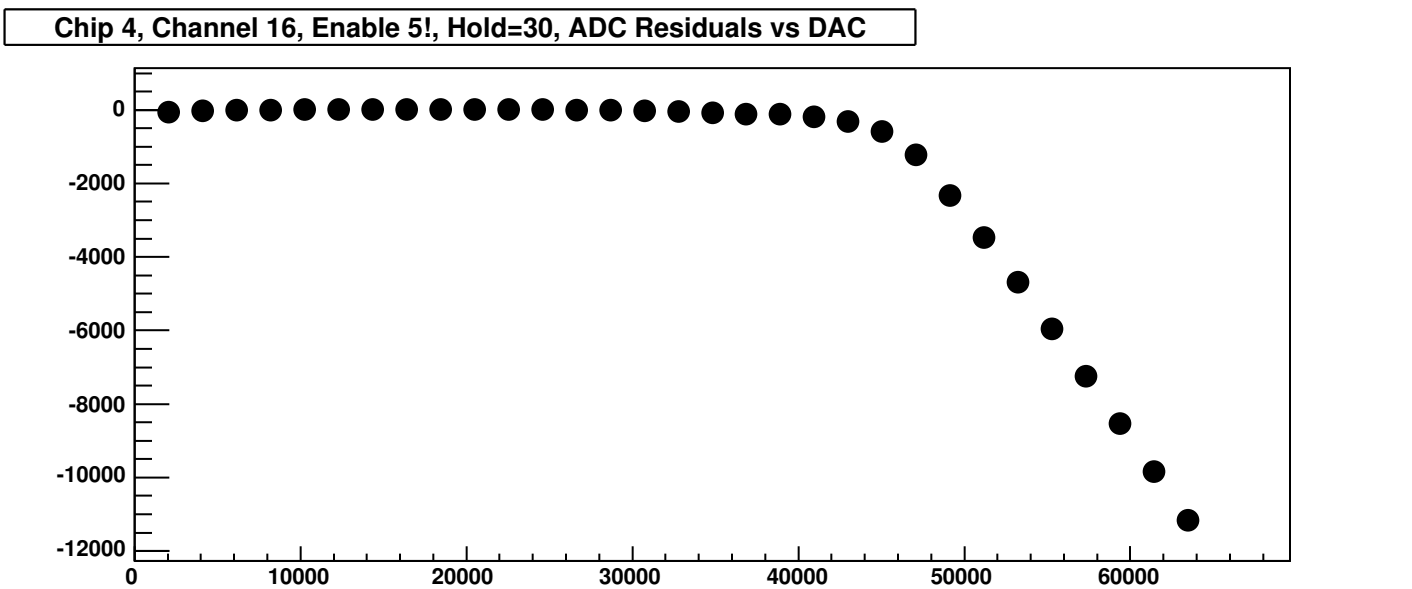

Chip 4, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4352 / 15
p0 914.3 ± 0.1203
p1 -0.001201 ± 4.847e-06
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

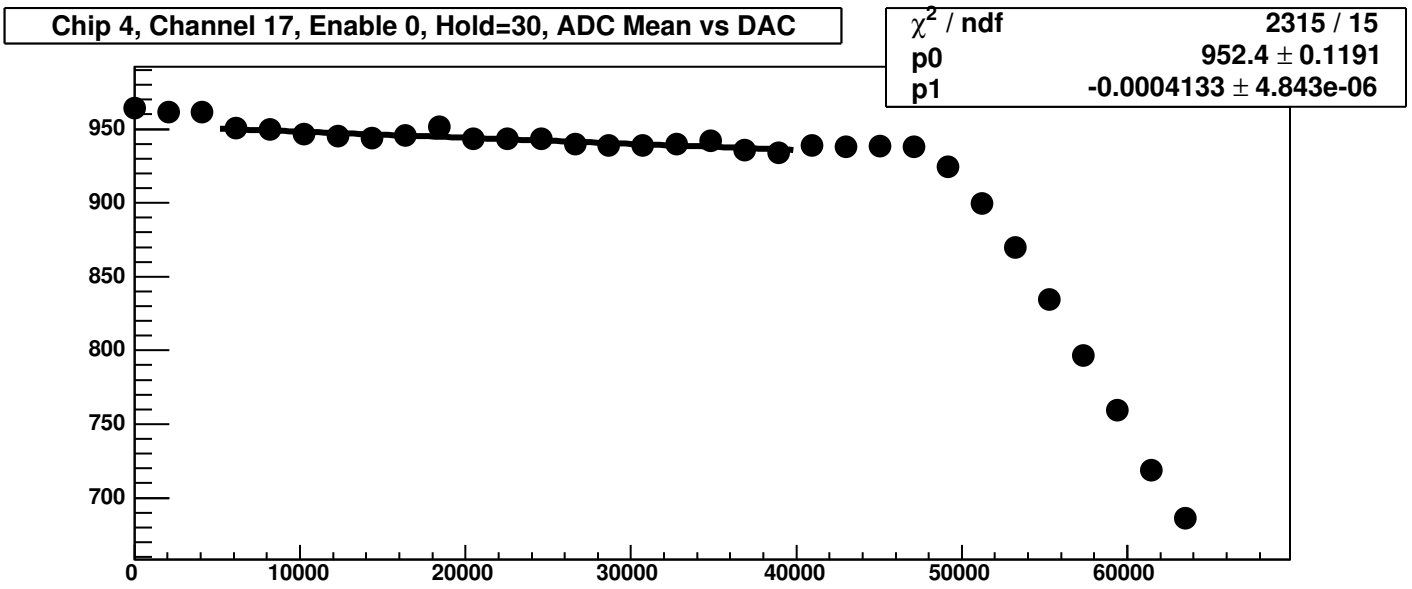

Chip 4, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

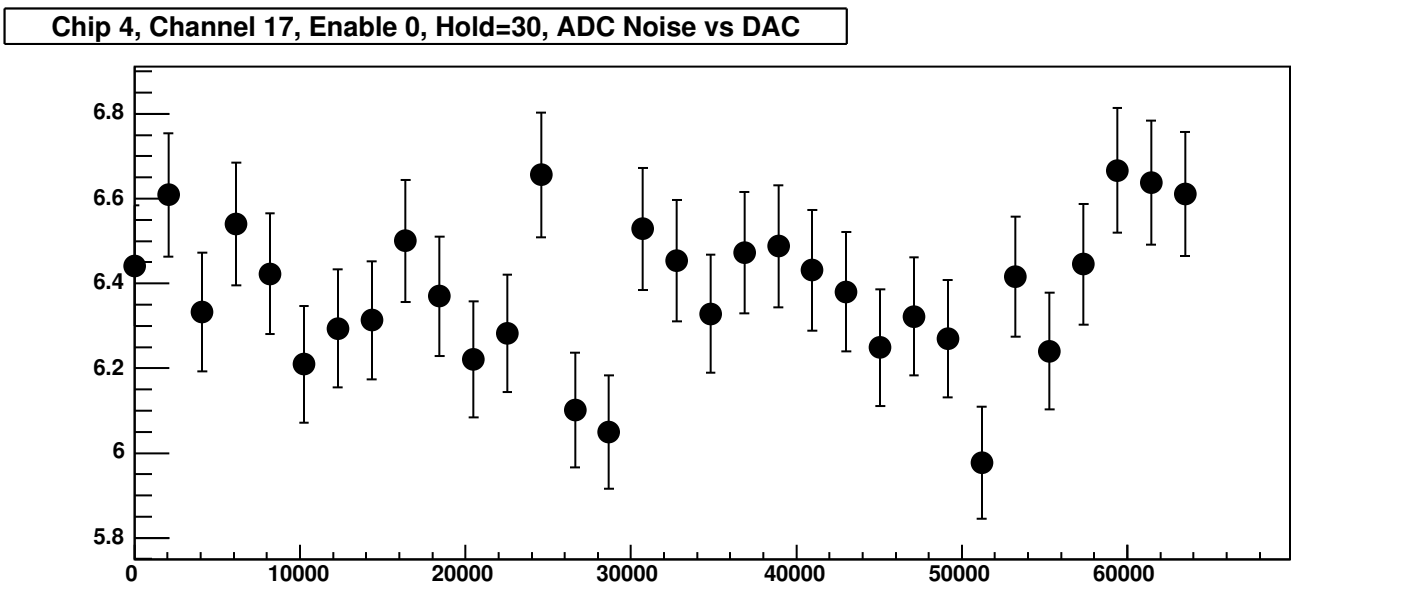

Chip 4, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

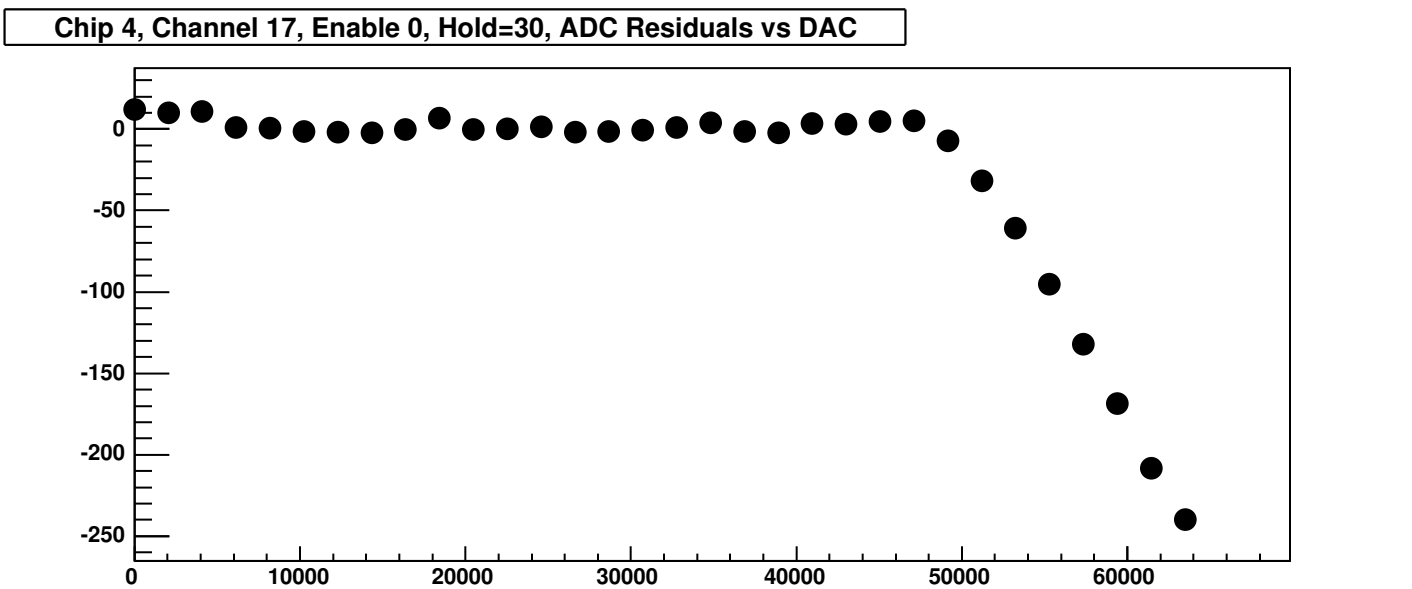

Chip 4, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7576 / 15
p0 1042 ± 0.3082
p1 0.6387 ± 3.009e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

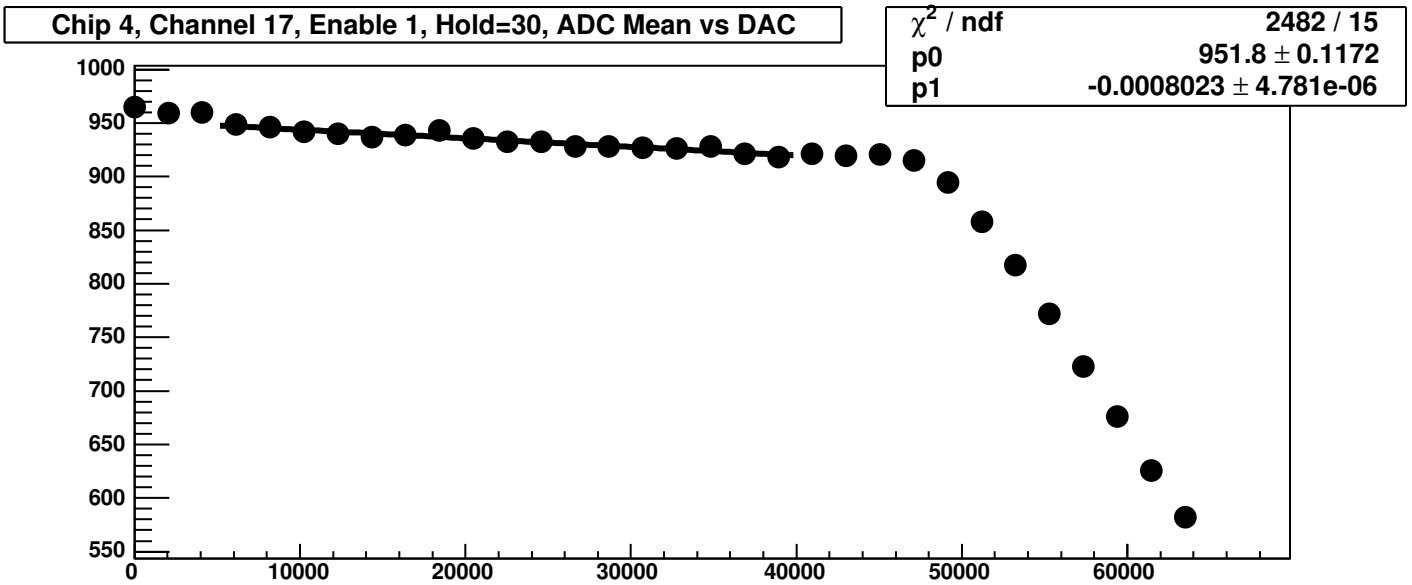

Chip 4, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

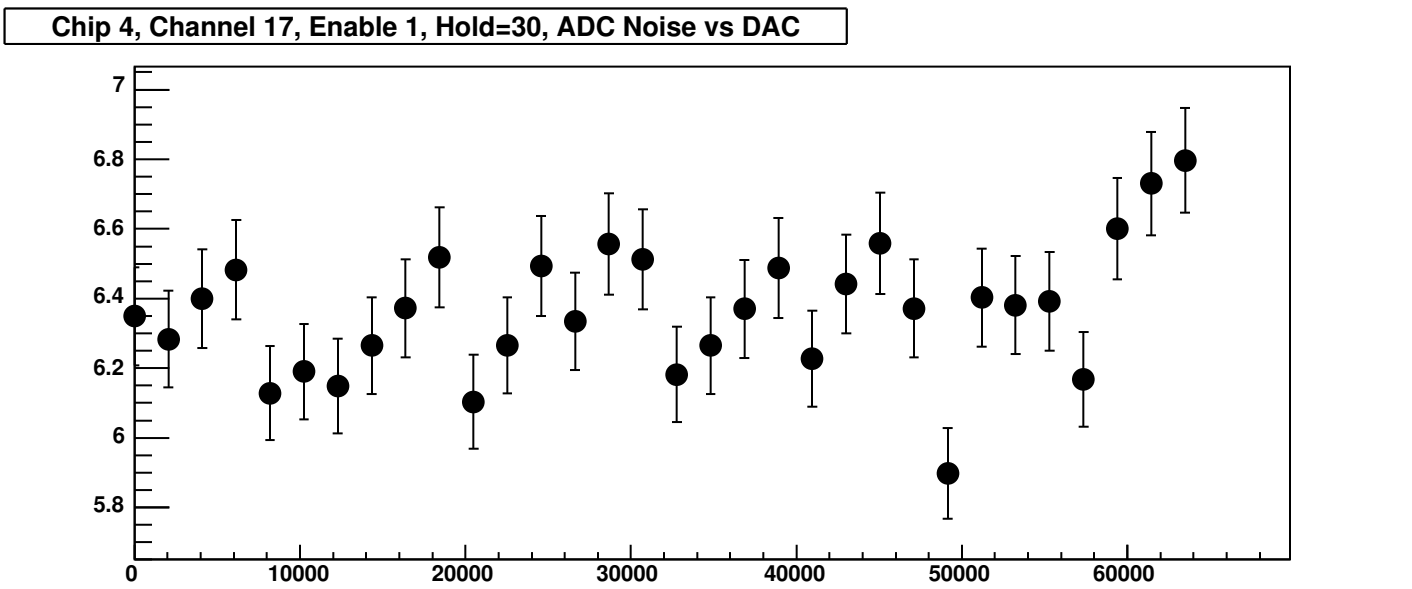

Chip 4, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

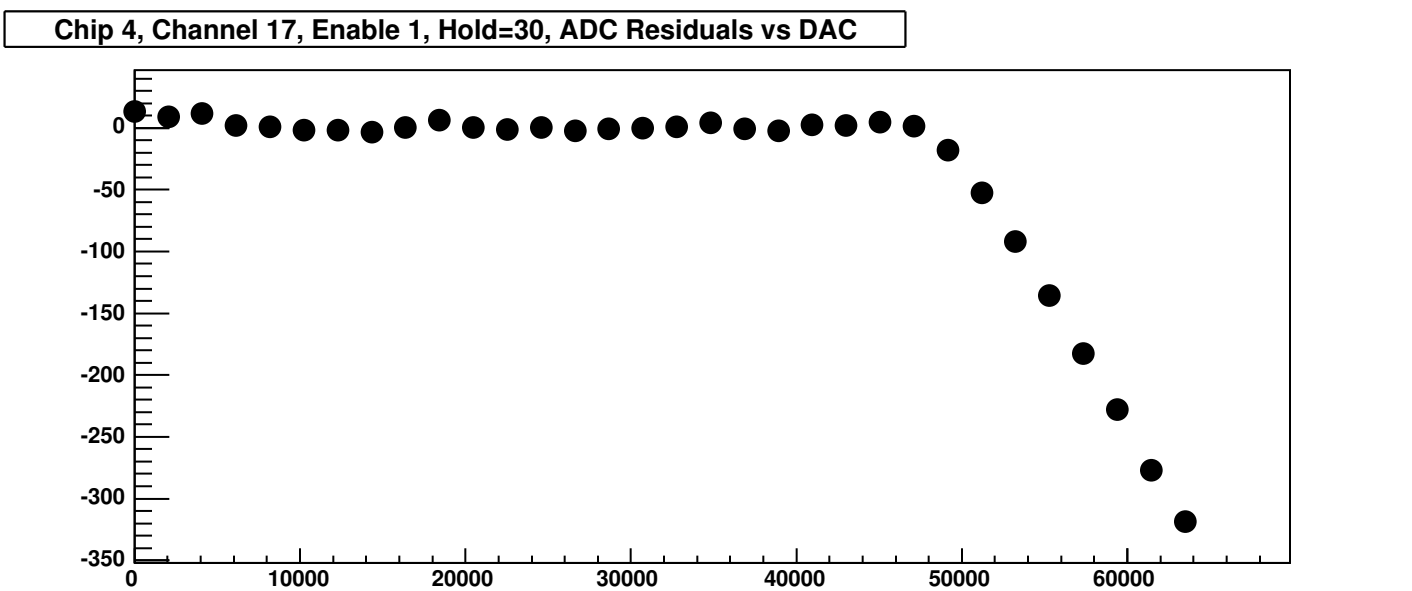

Chip 4, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2315 / 15
p0 952.4 ± 0.1191
p1 -0.0004133 ± 4.843e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

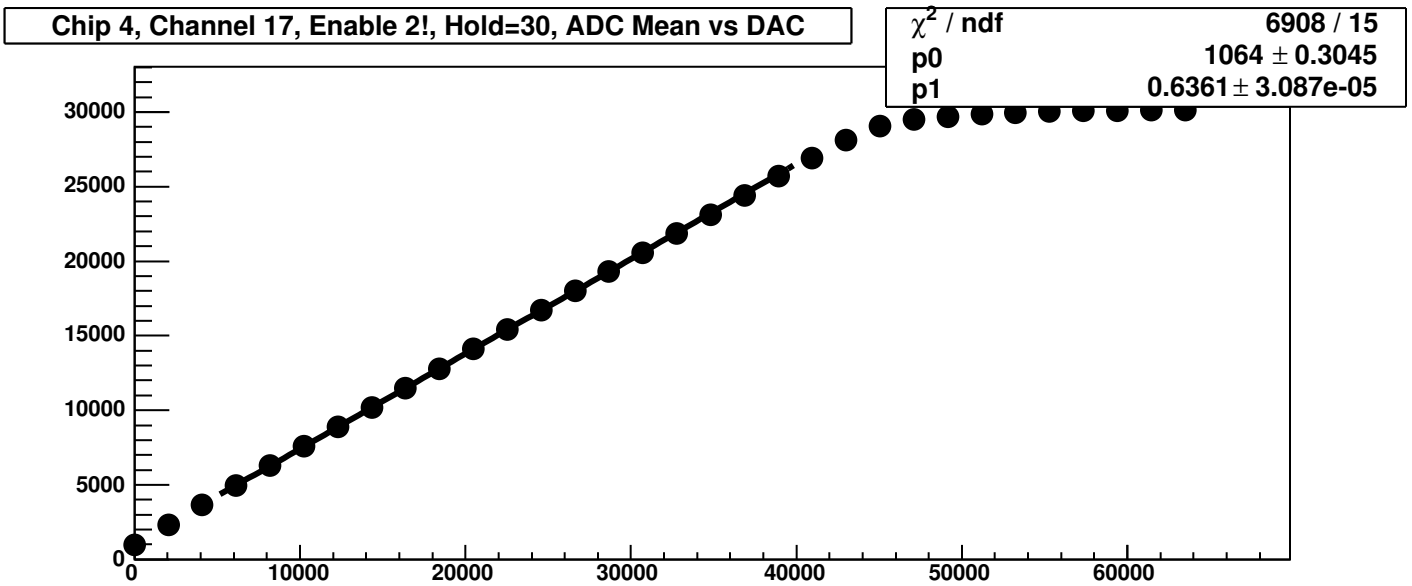

Chip 4, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

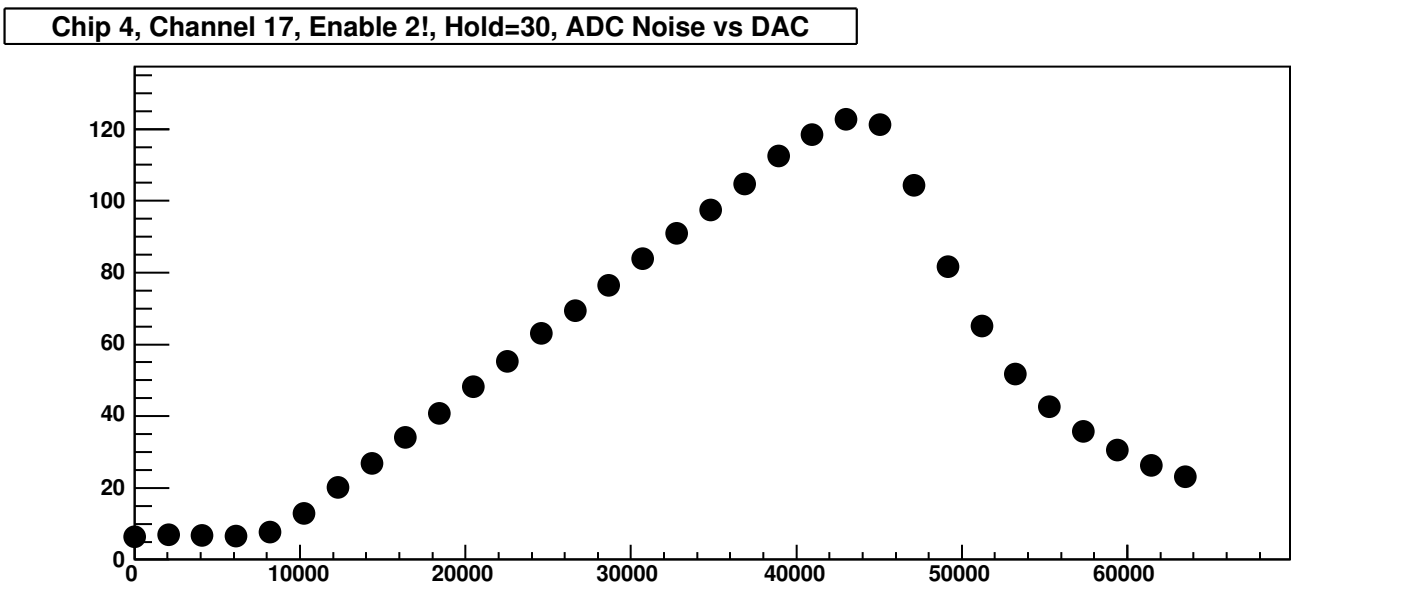

Chip 4, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

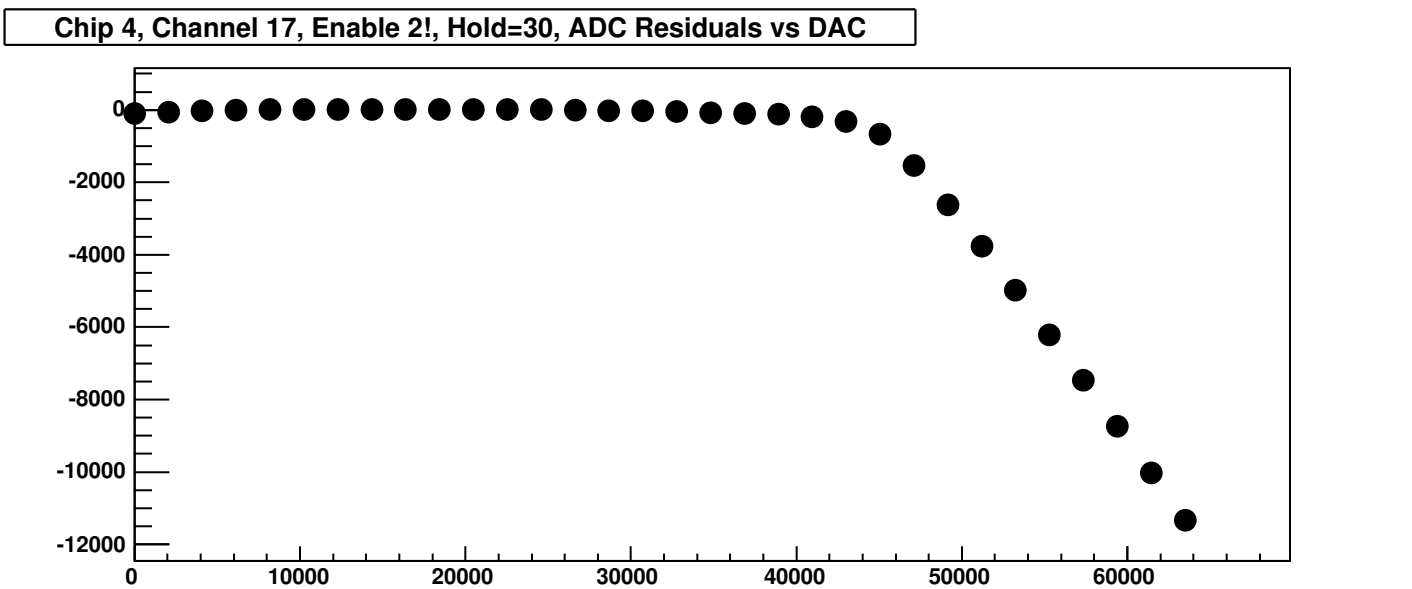

Chip 4, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2482 / 15
p0 951.8 ± 0.1172
p1 -0.0008023 ± 4.781e-06
1000
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

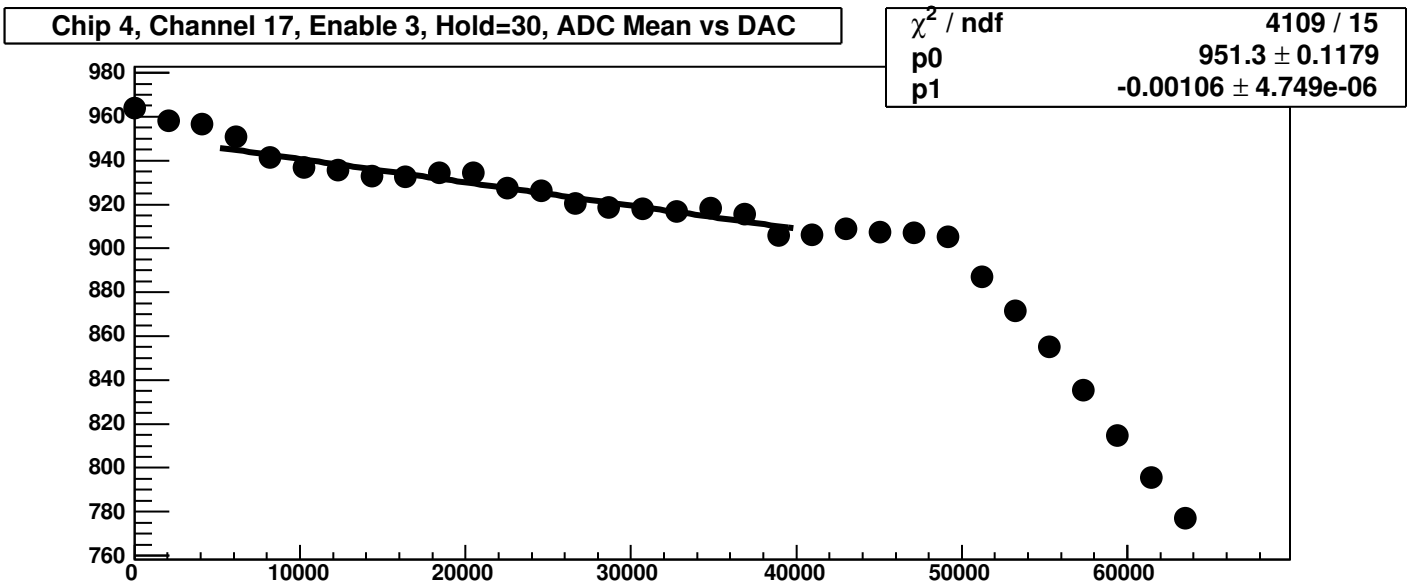

Chip 4, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

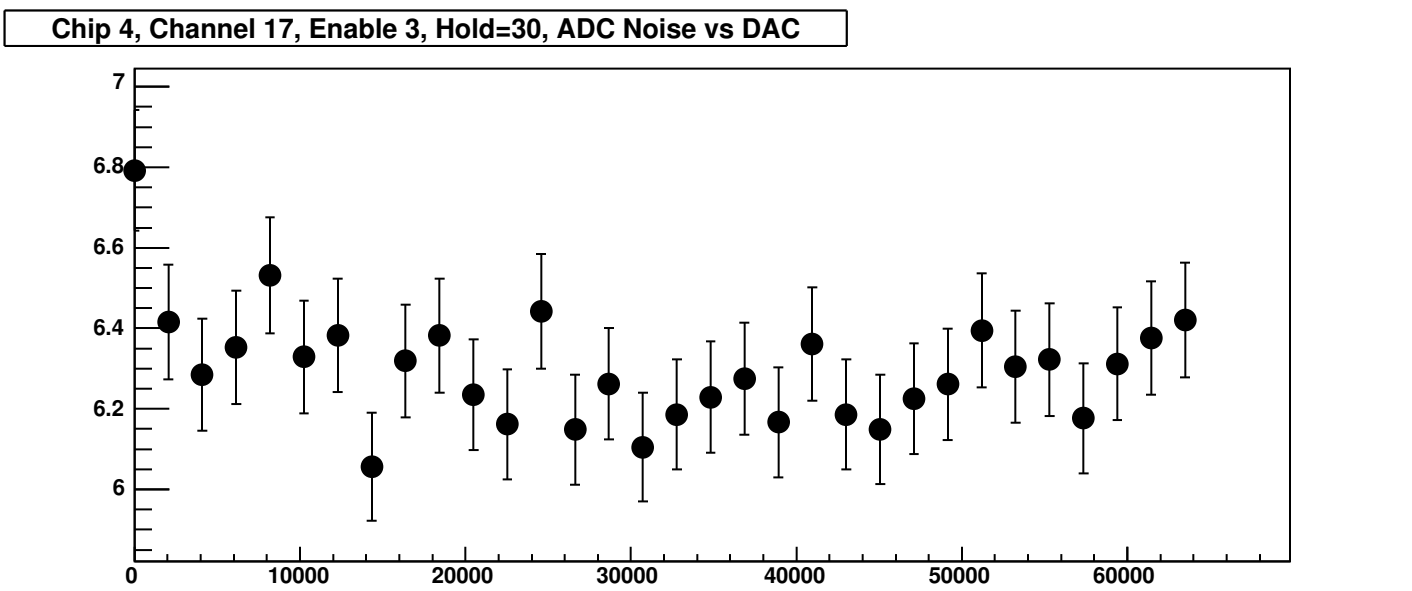

Chip 4, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

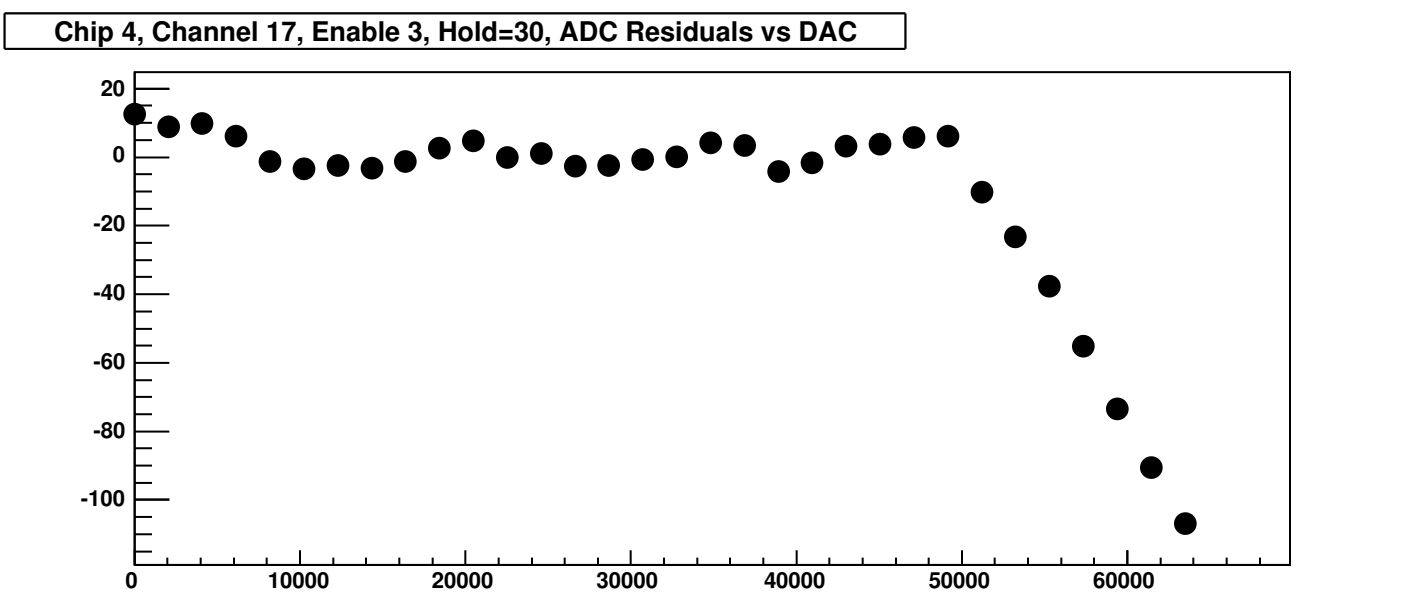

Chip 4, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 6908 / 15
p0 1064 ± 0.3045
p1 0.6361 ± 3.087e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

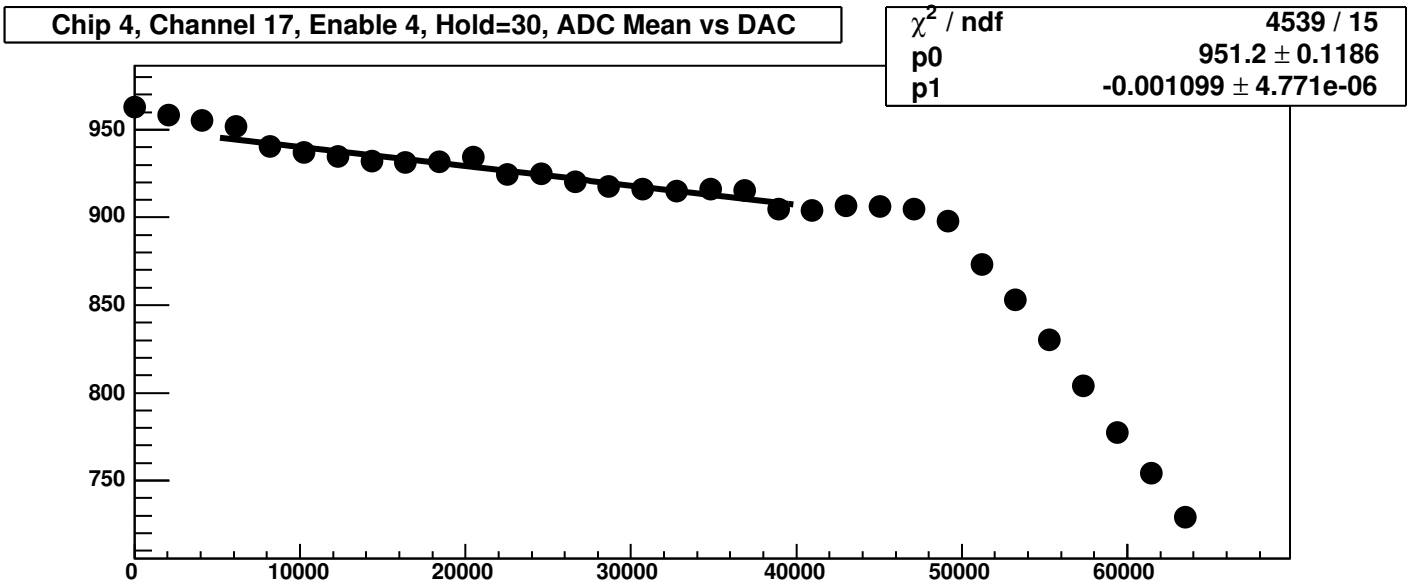

Chip 4, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

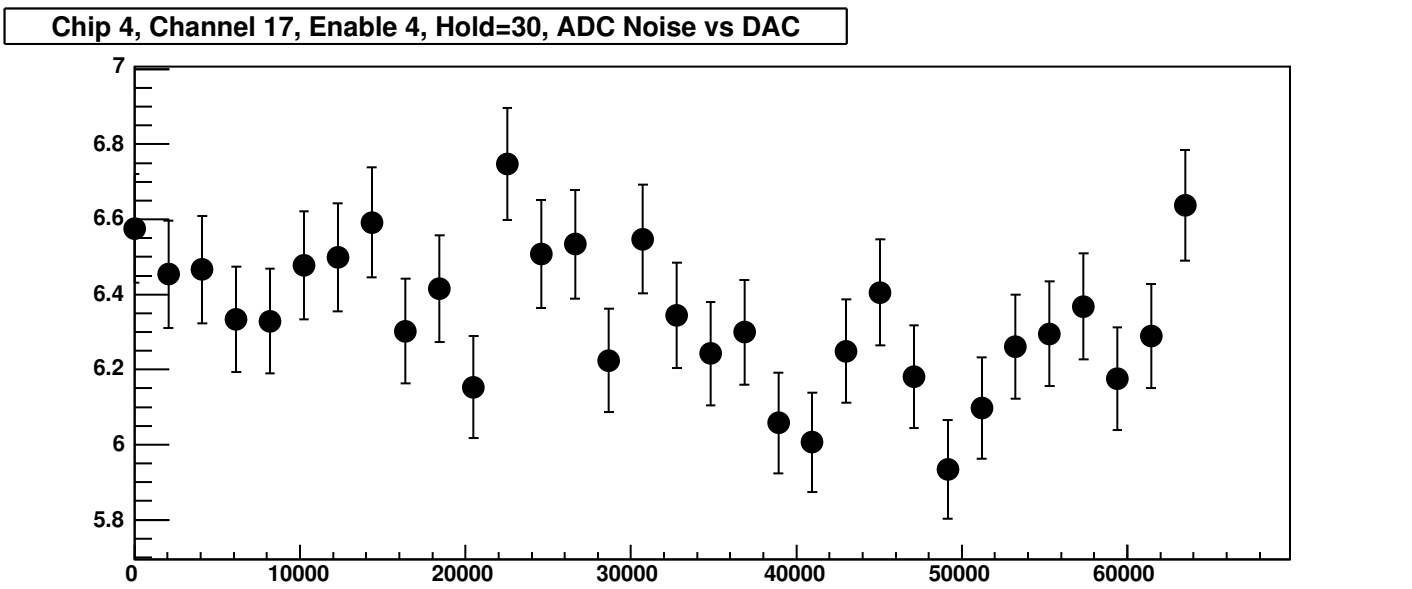

Chip 4, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

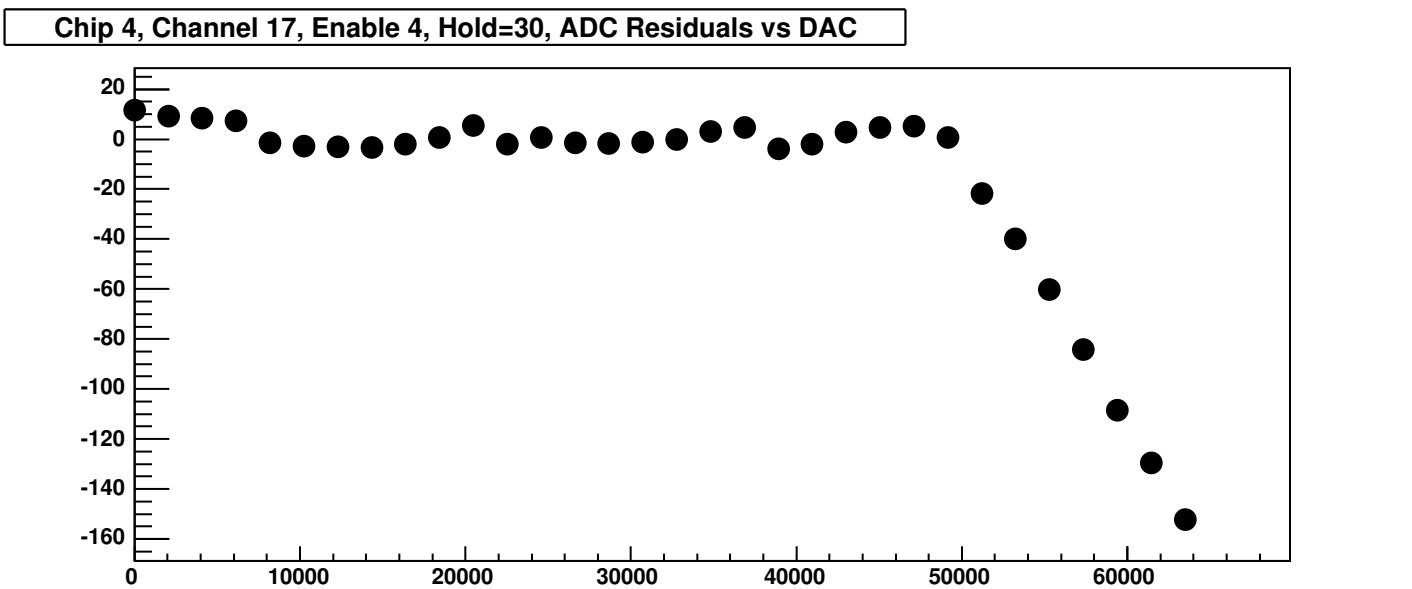

Chip 4, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4109 / 15
p0 951.3 ± 0.1179
p1 -0.00106 ± 4.749e-06
980
960
940
920
900
880
860
840
820
800
780
760
0
10000
20000
30000
40000
50000
60000

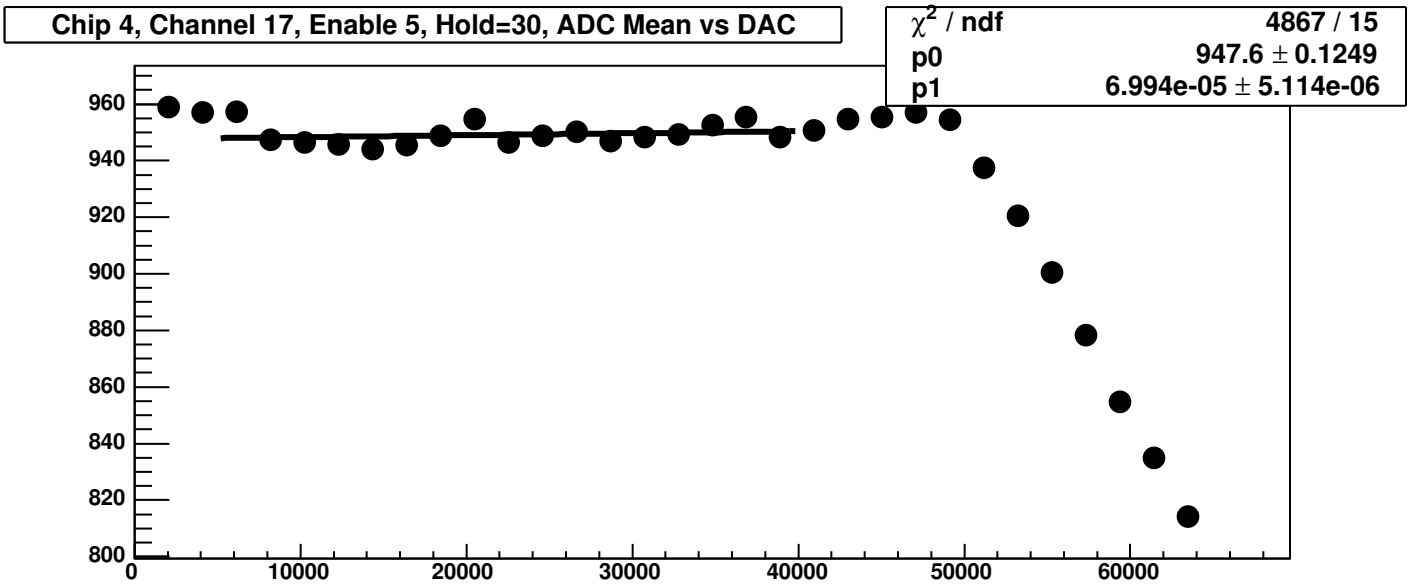

Chip 4, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

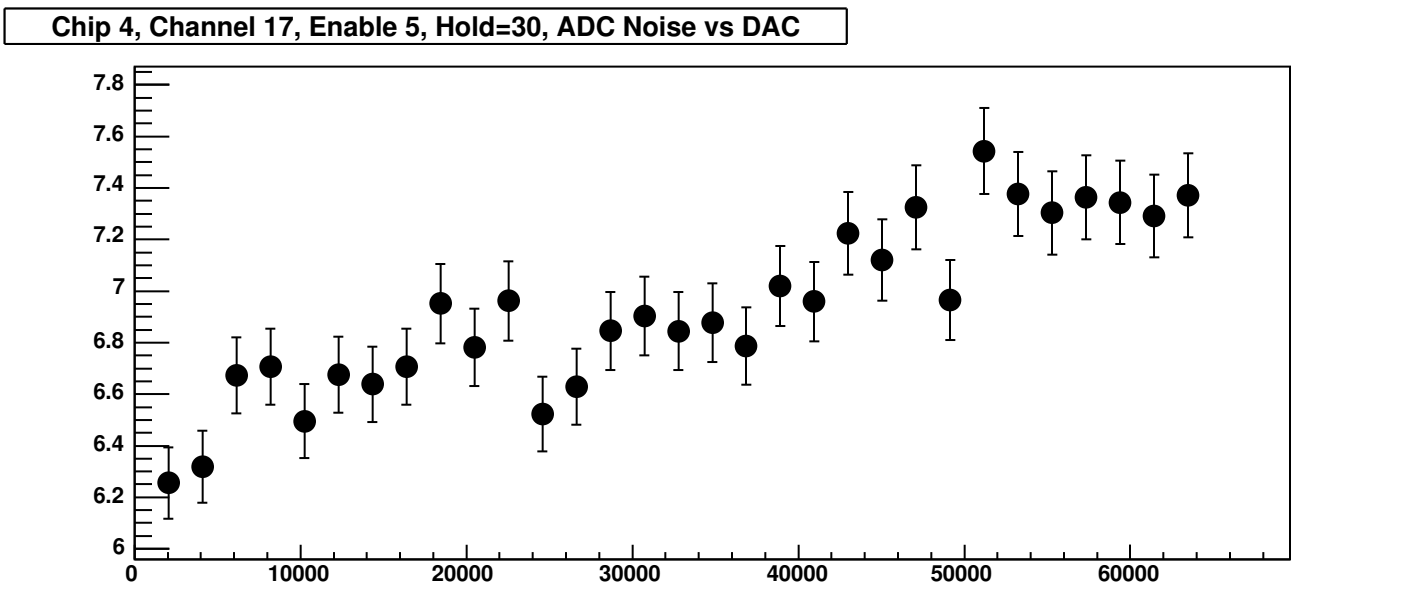

Chip 4, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

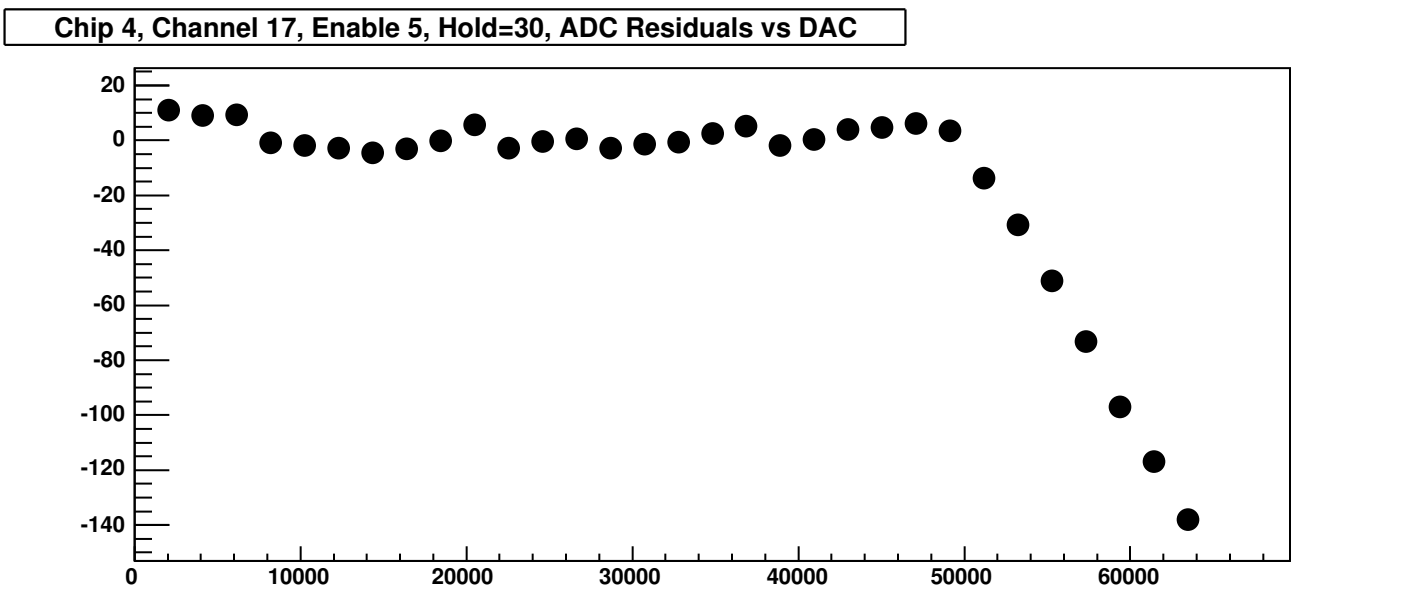

Chip 4, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4539 / 15
p0 951.2 ± 0.1186
p1 -0.001099 ± 4.771e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

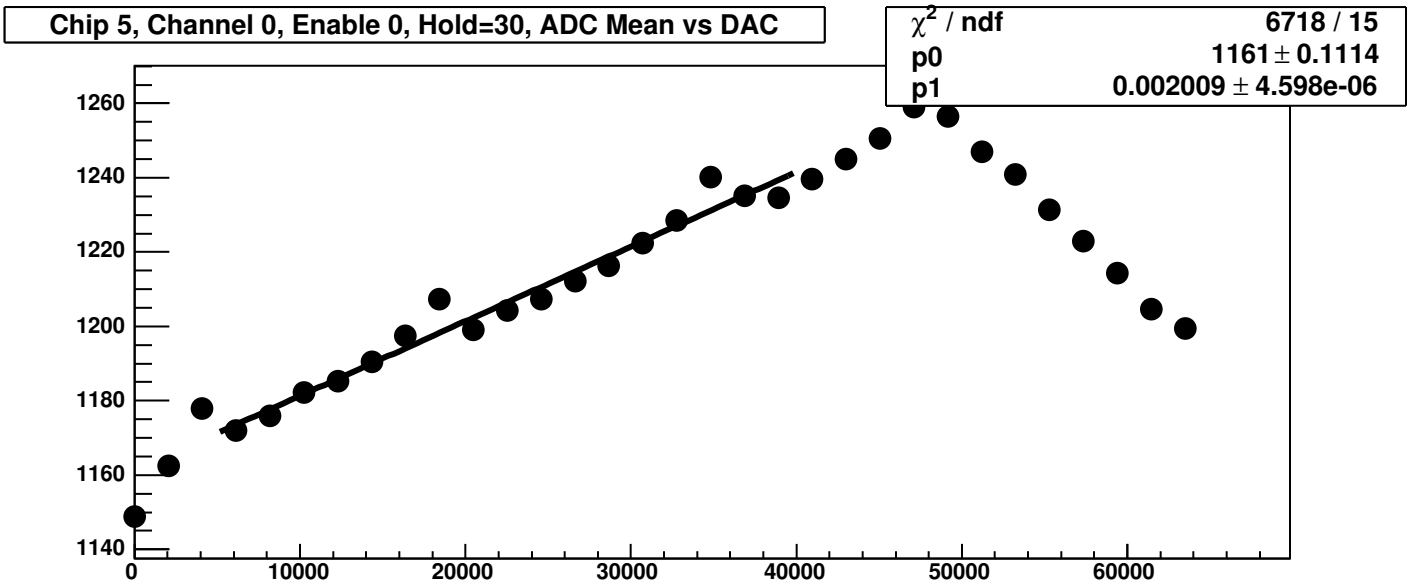

Chip 4, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

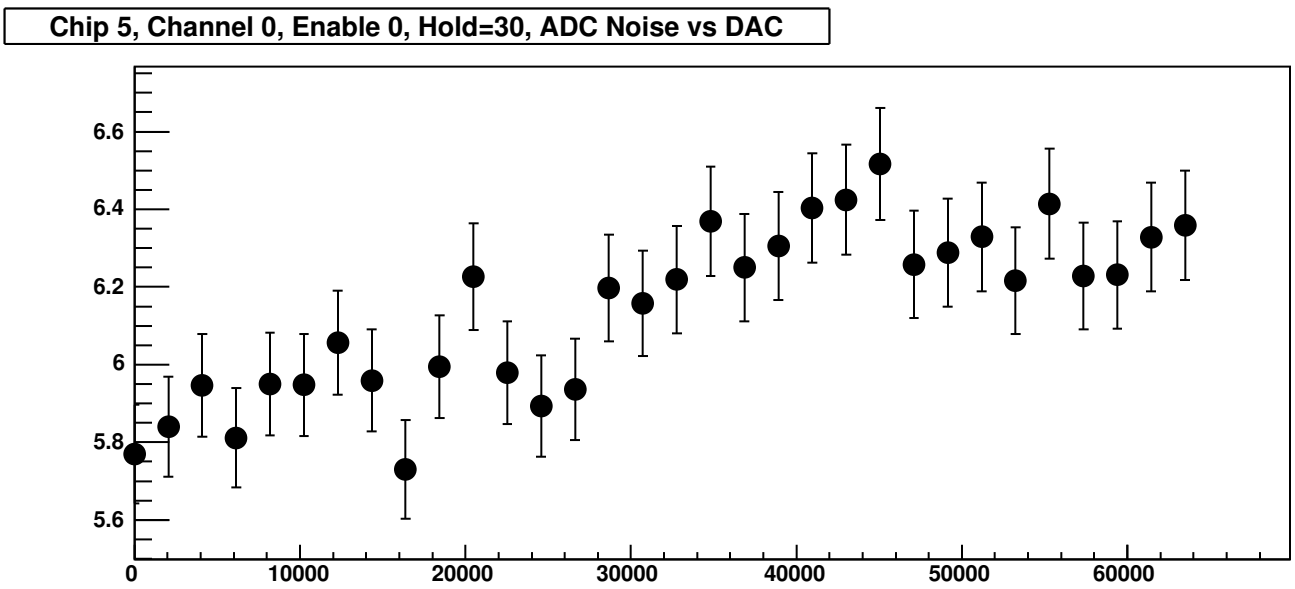

Chip 4, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

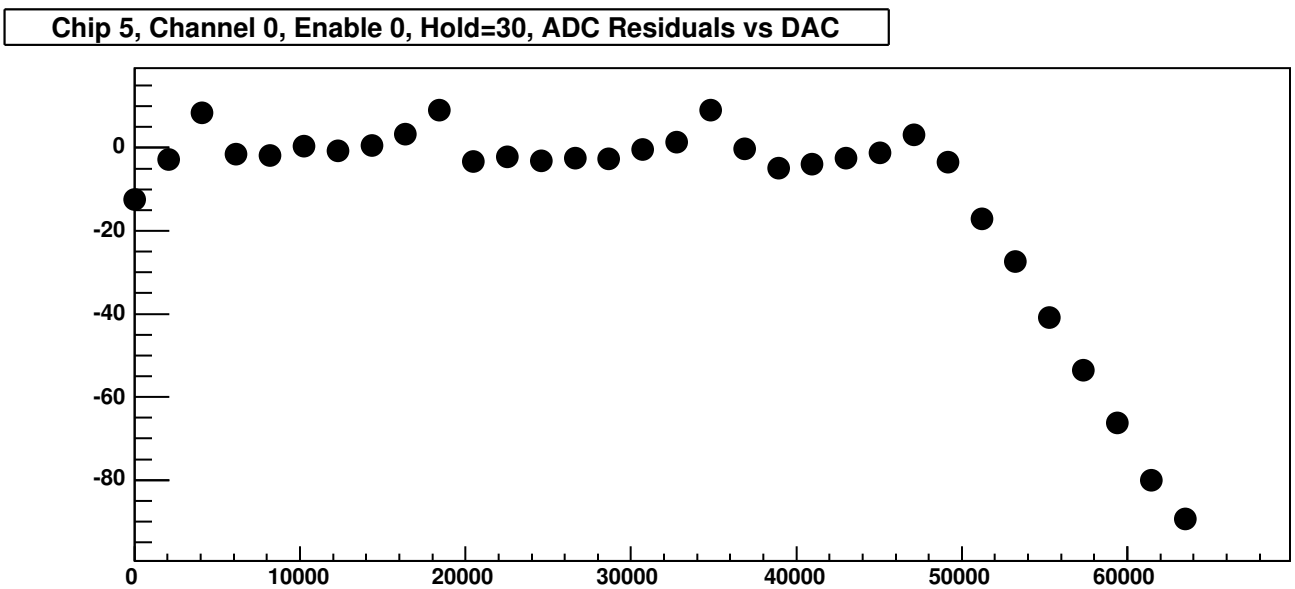

Chip 4, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4867 / 15
p0 947.6 ± 0.1249
p1 6.994e-05 ± 5.114e-06
960
940
920
900
880
860
840
820
800
0
10000
20000
30000
40000
50000
60000

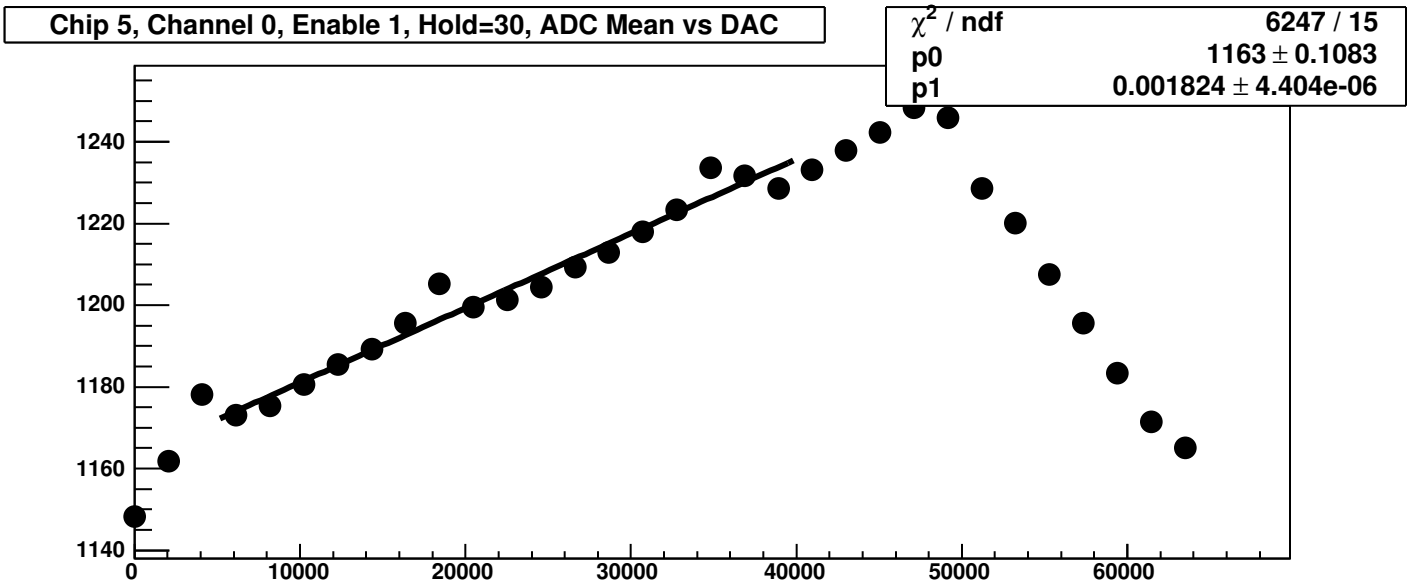

Chip 4, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

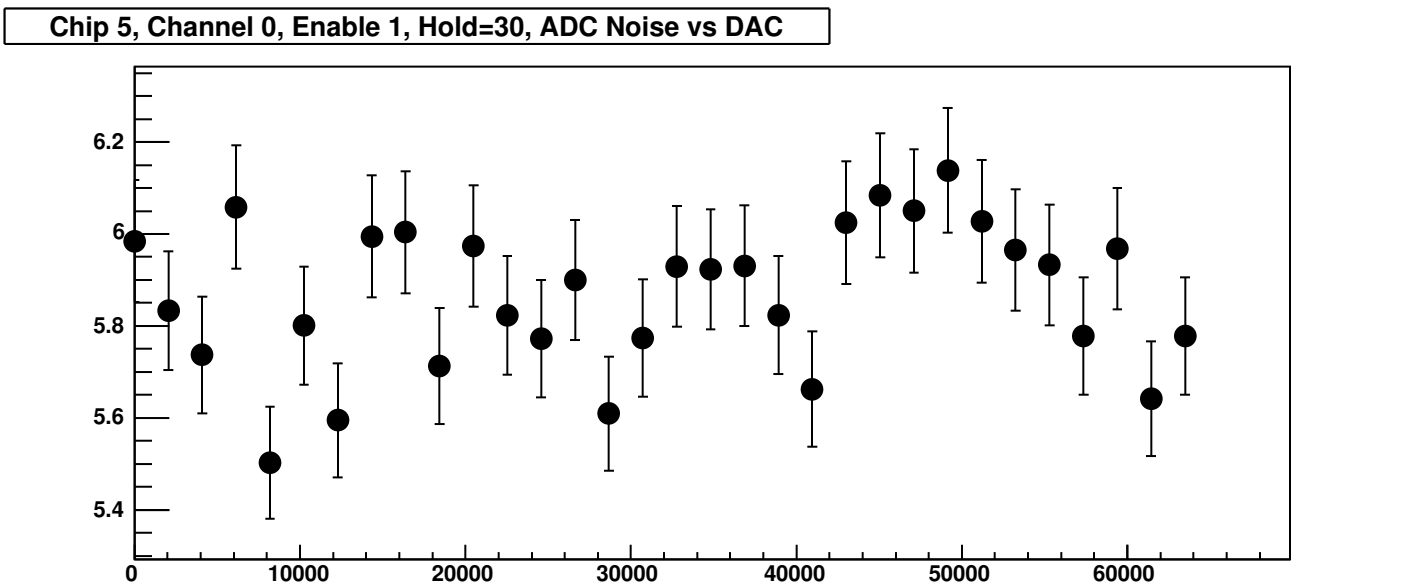

Chip 4, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

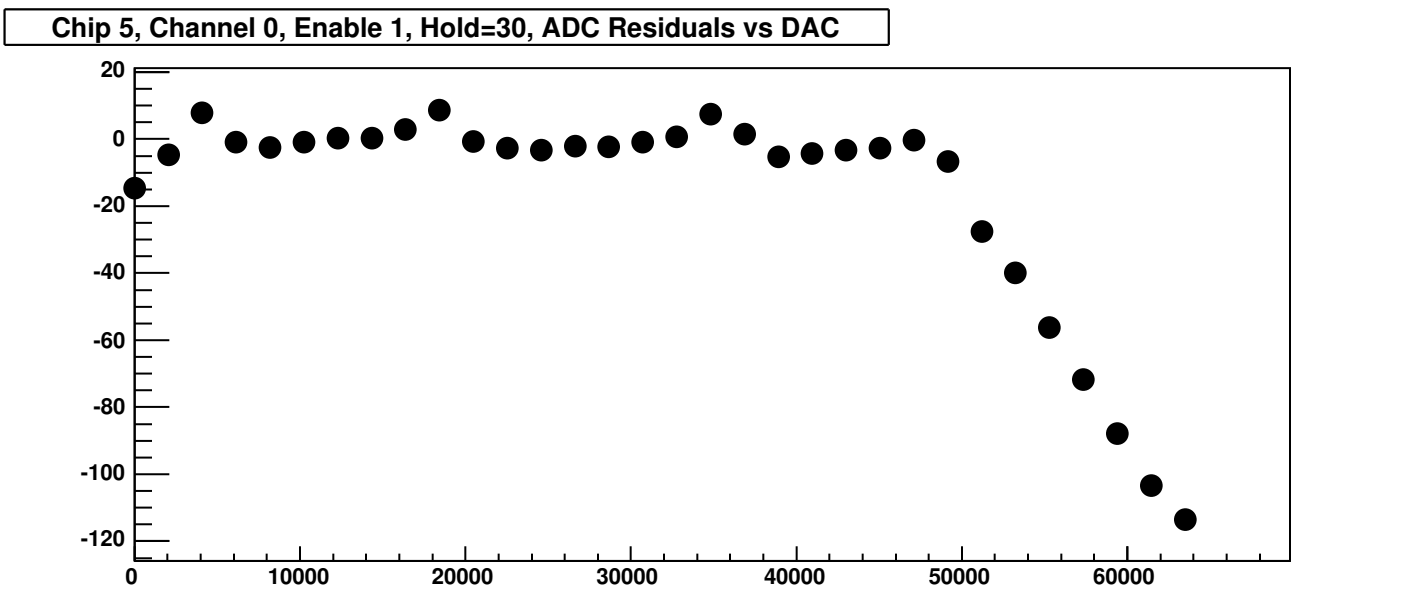

Chip 5, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6718 / 15
p0 1161 ± 0.1114
p1 0.002009 ± 4.598e-06
1260
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

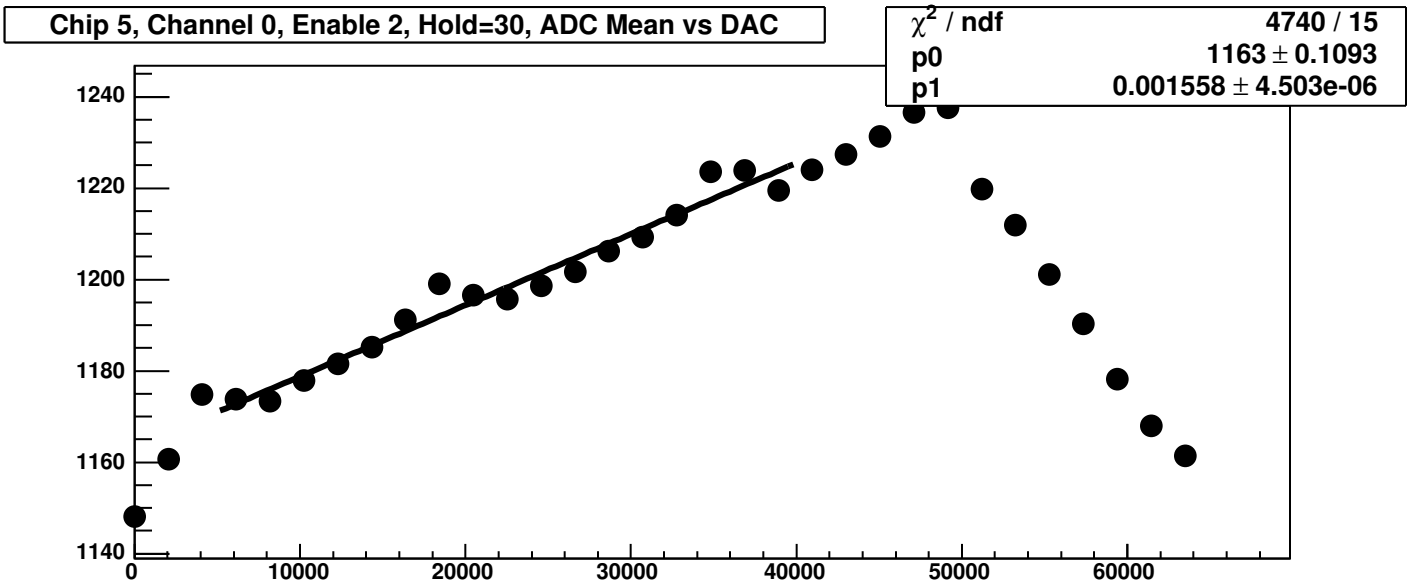

Chip 5, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

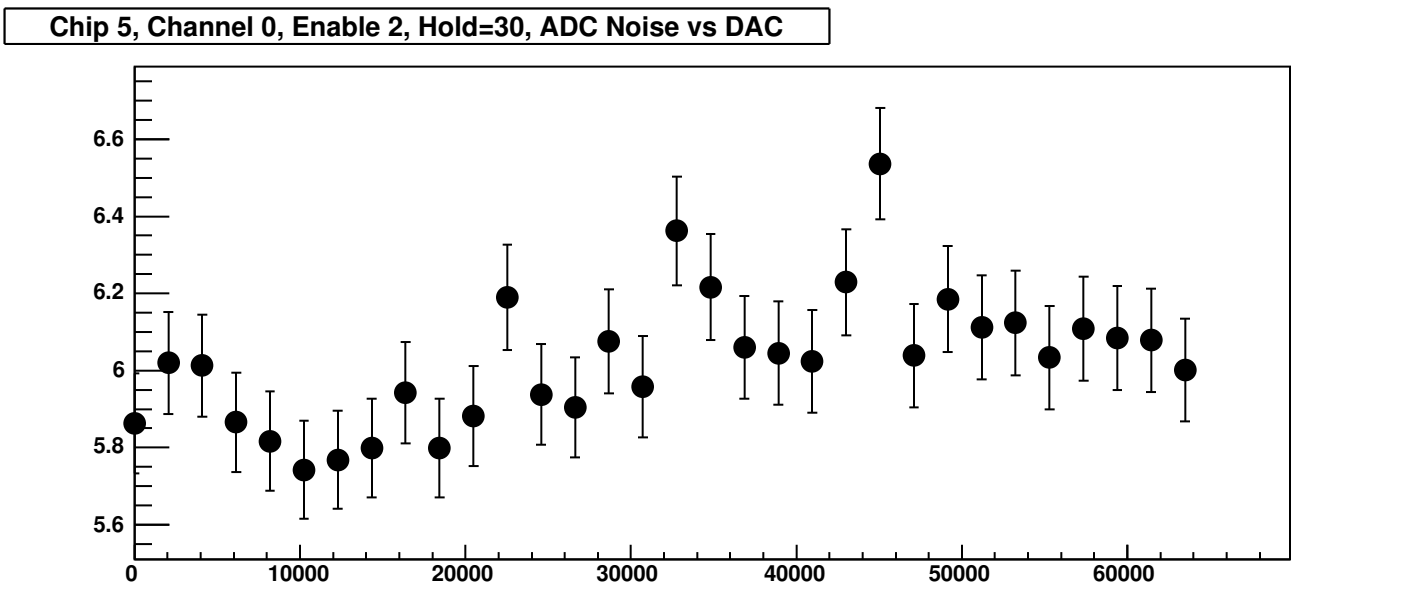

Chip 5, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

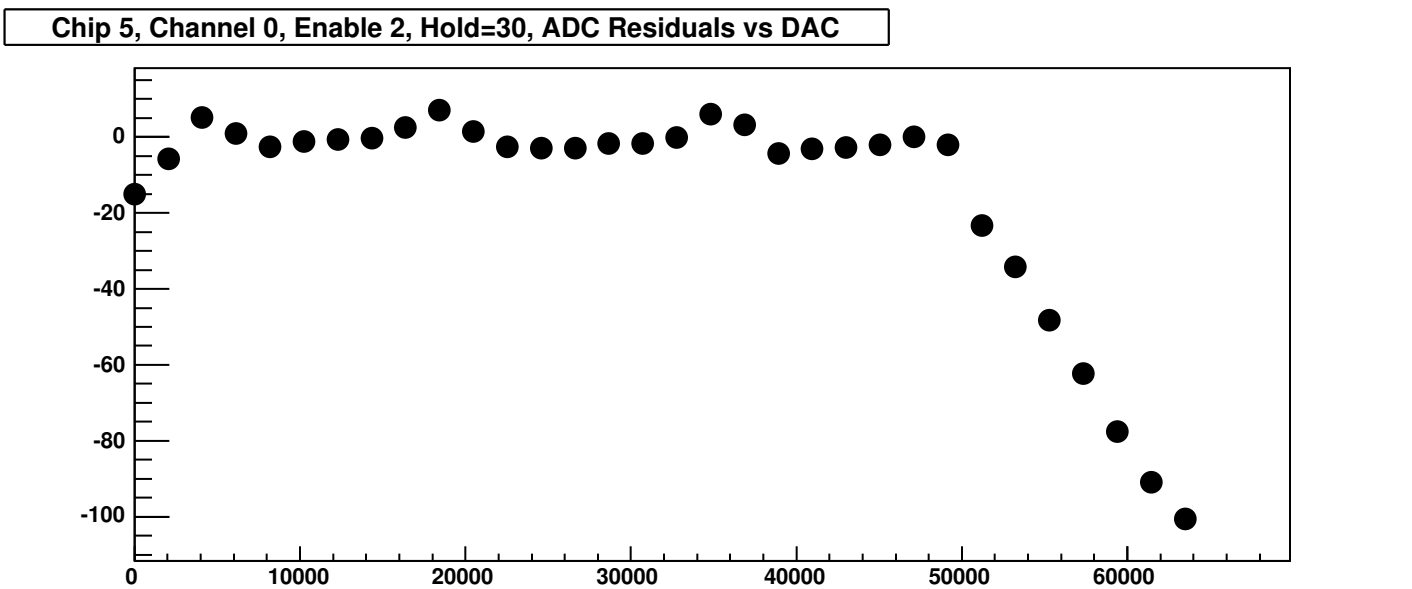

Chip 5, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6247 / 15
p0 1163 ± 0.1083
p1 0.001824 ± 4.404e-06
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

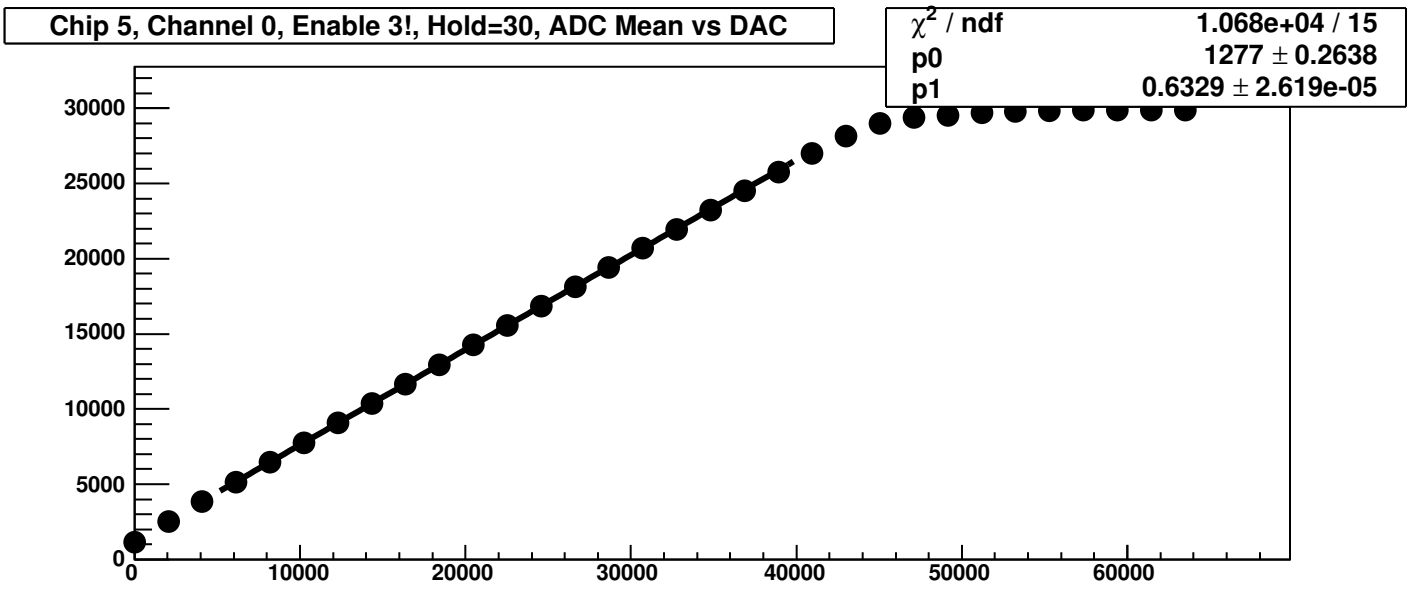

Chip 5, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

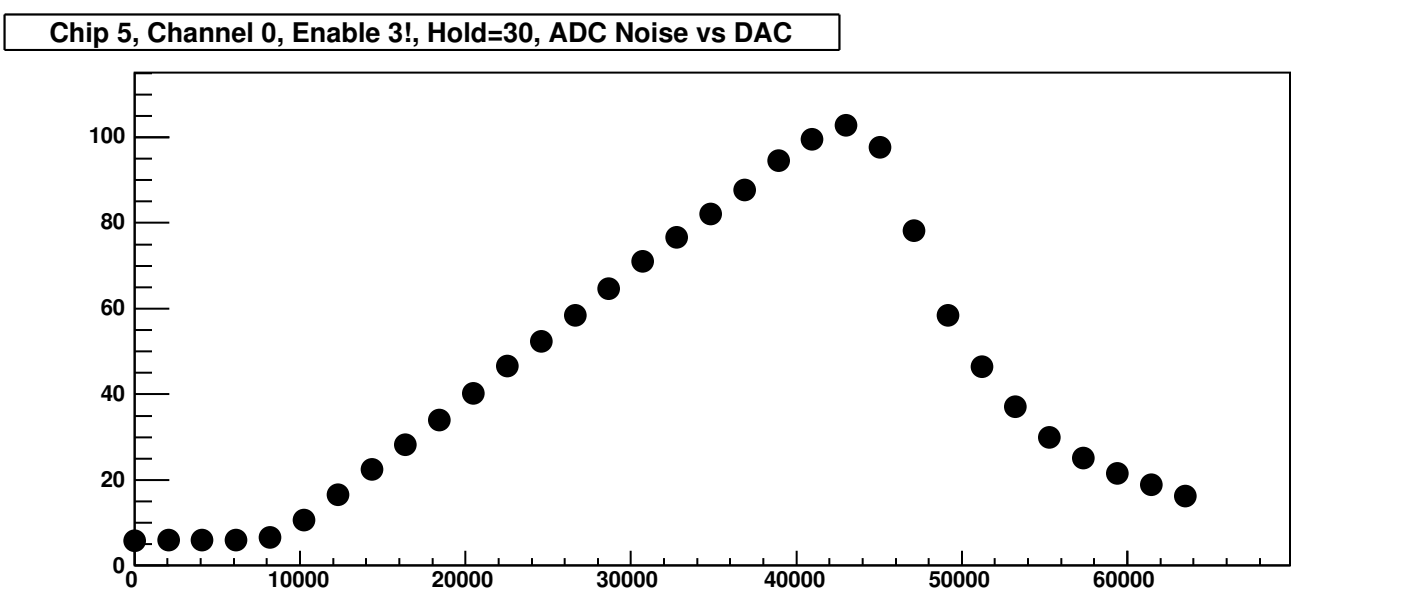

Chip 5, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

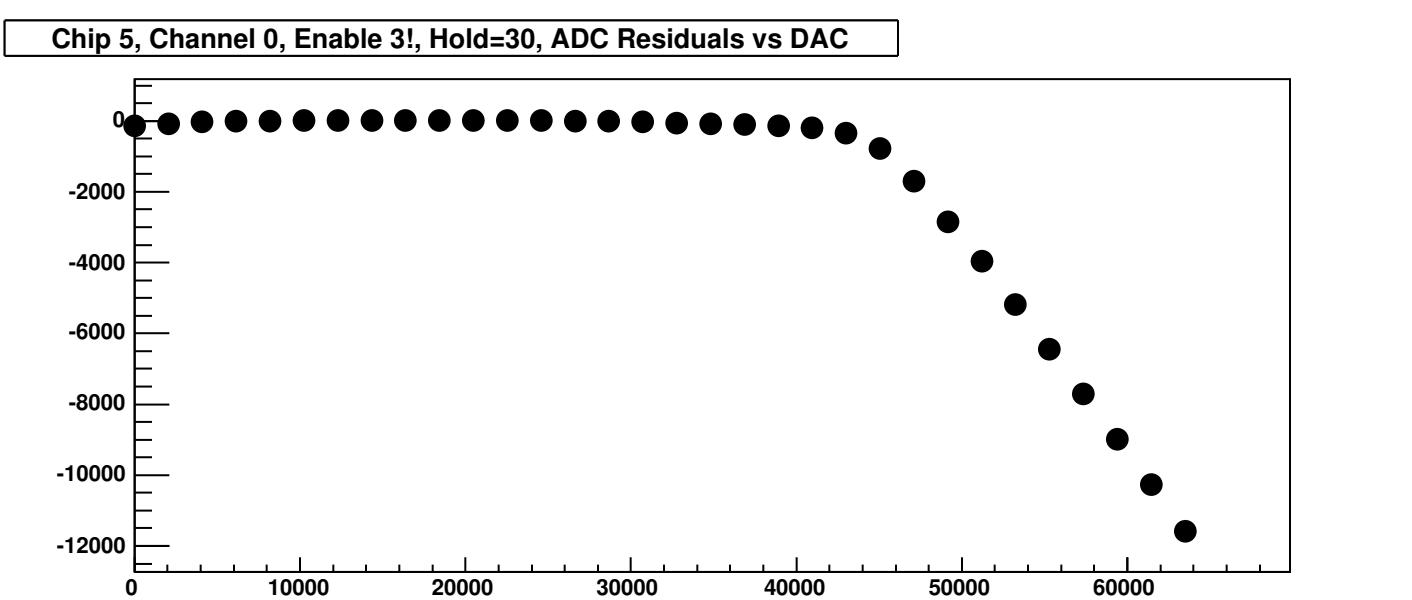

Chip 5, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4740 / 15
p0 1163 ± 0.1093
p1 0.001558 ± 4.503e-06
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

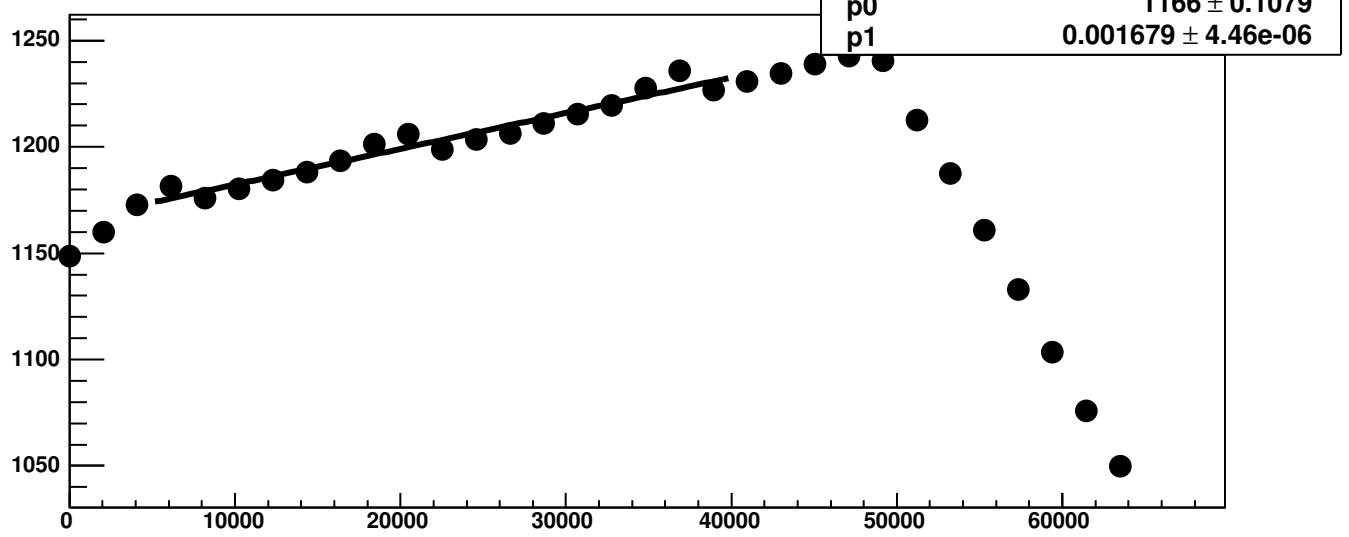

Chip 5, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.068e+04 / 15
p0 1277 ± 0.2638
p1 0.6329 ± 2.619e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

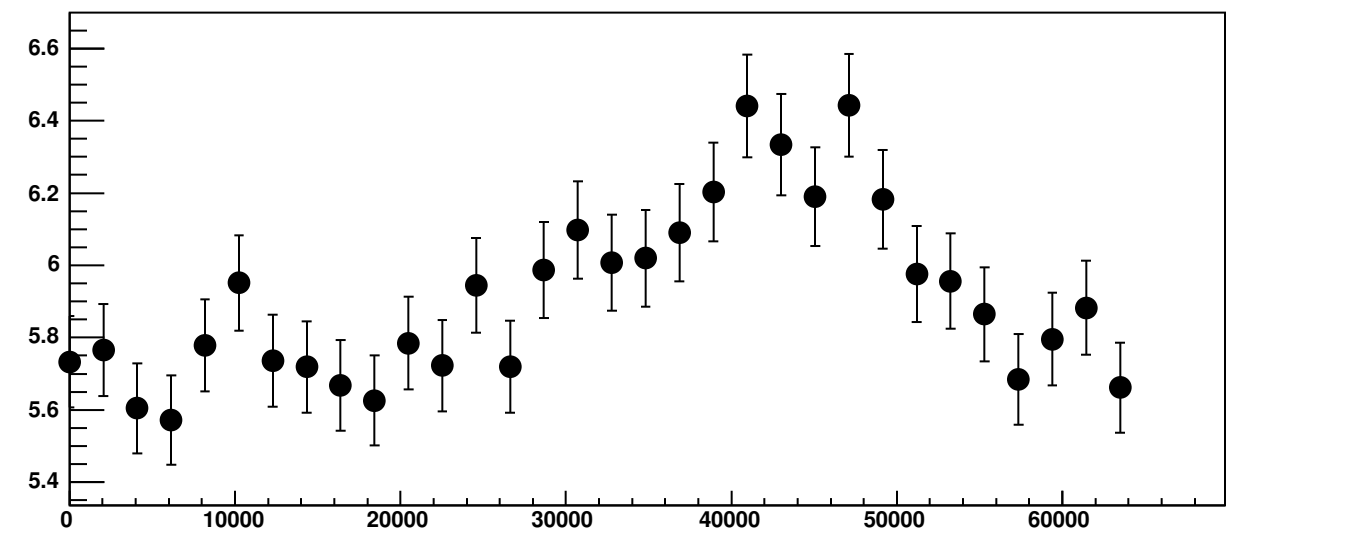

Chip 5, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

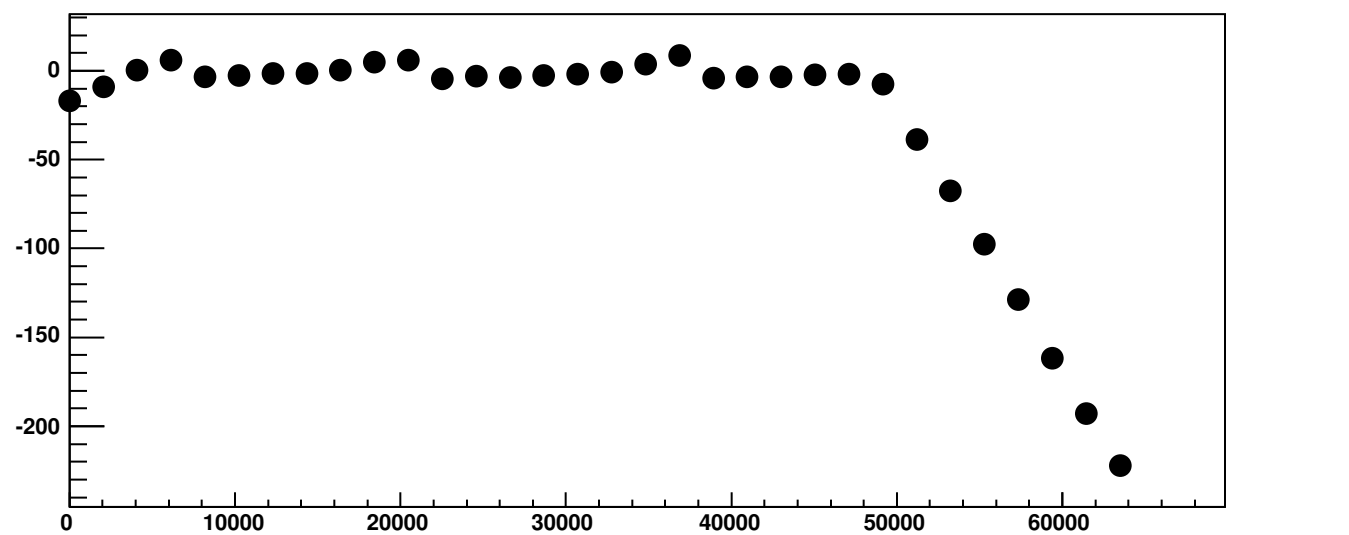
Chip 5, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC

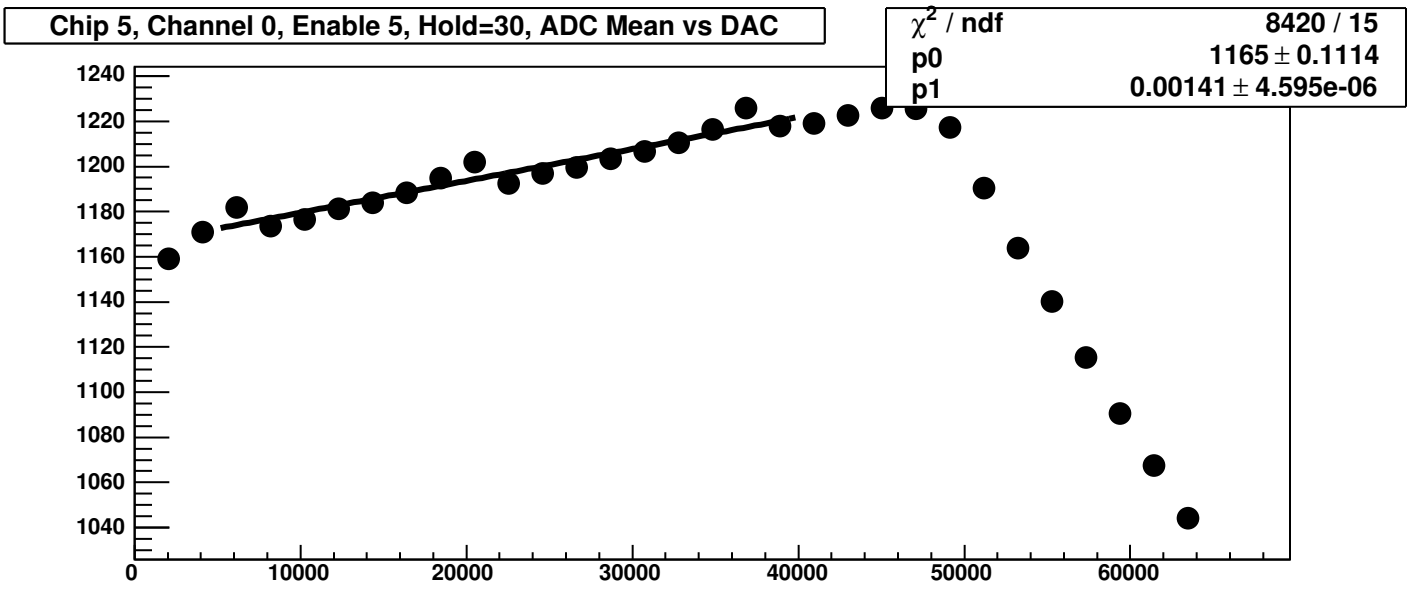


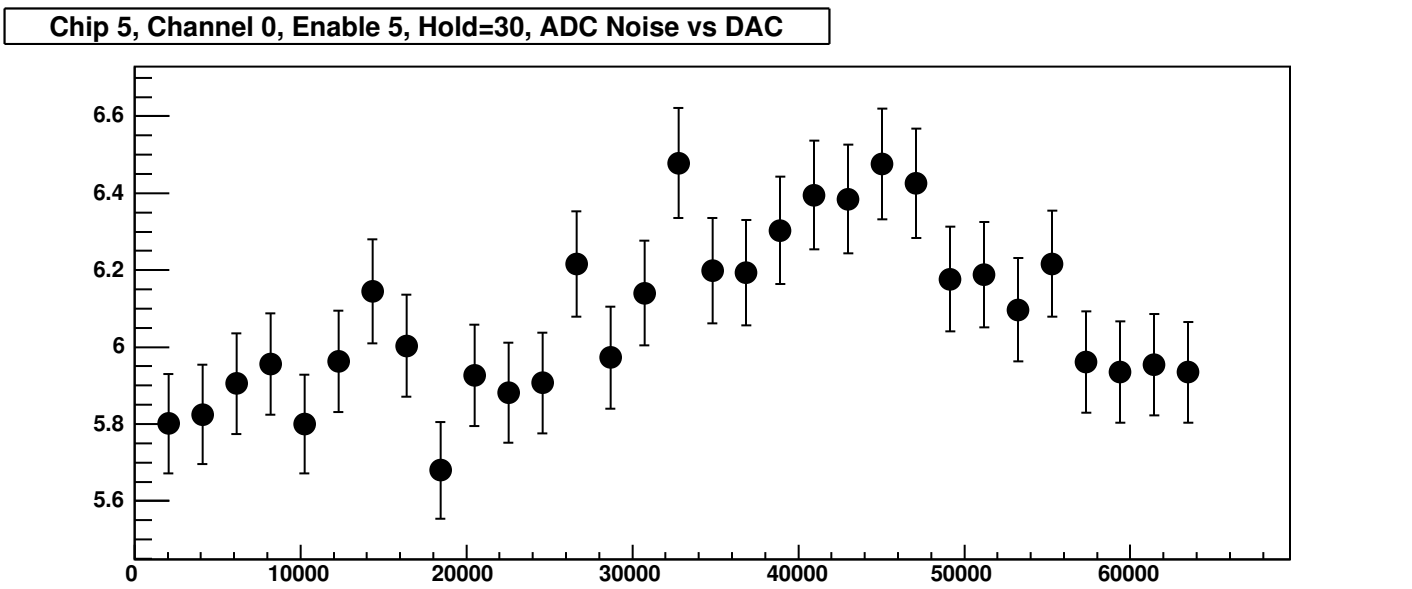
Chip 5, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC

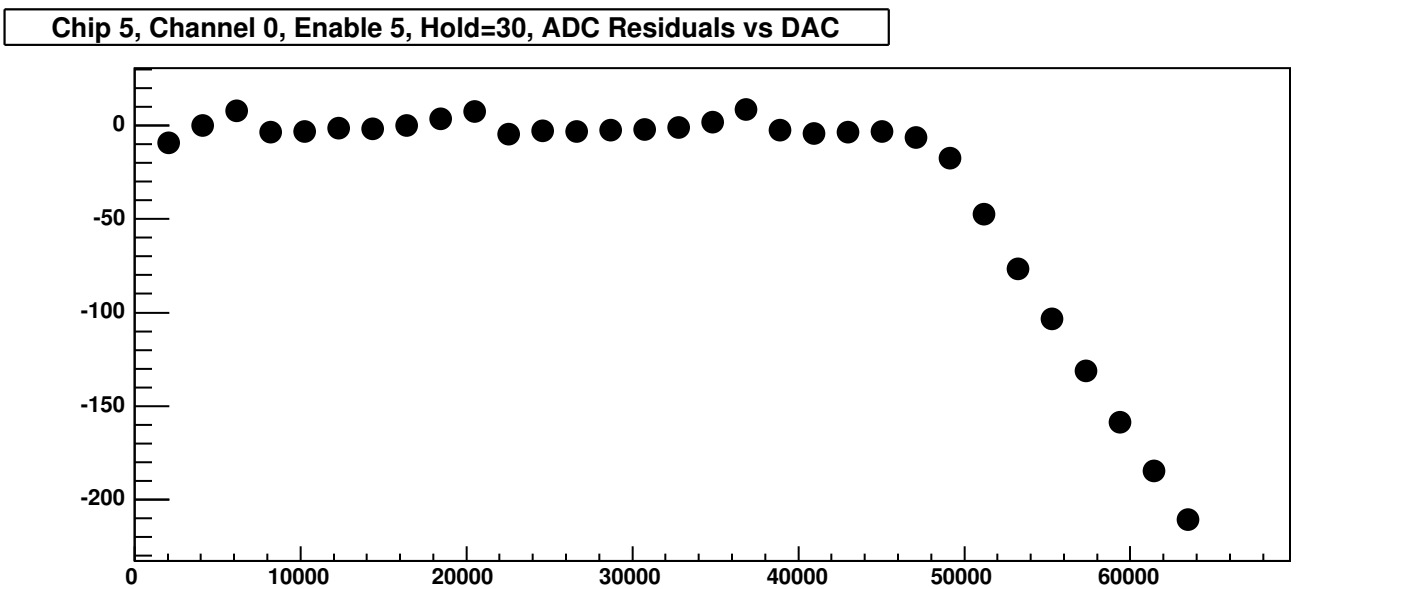


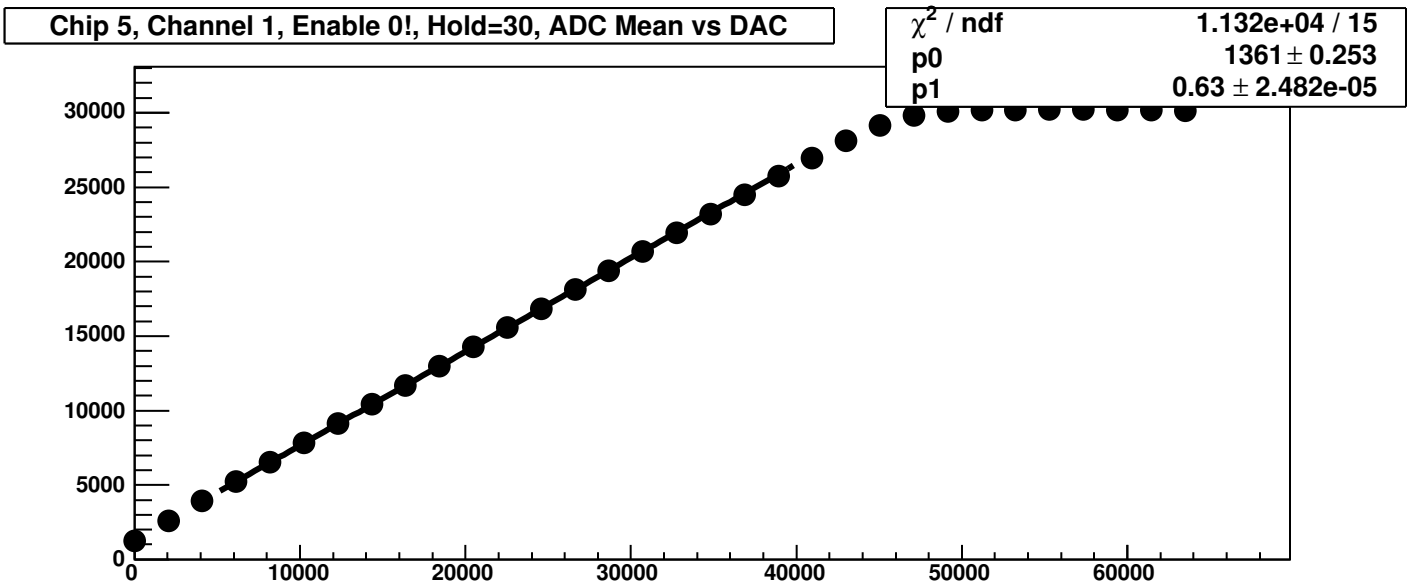
Chip 5, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC

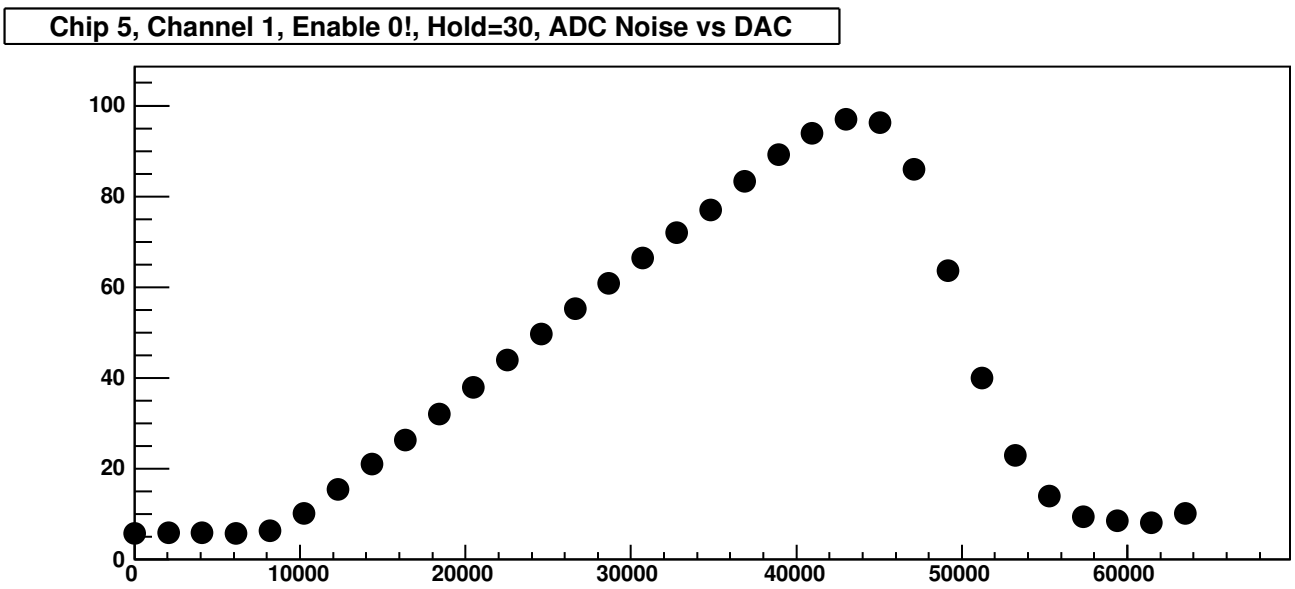


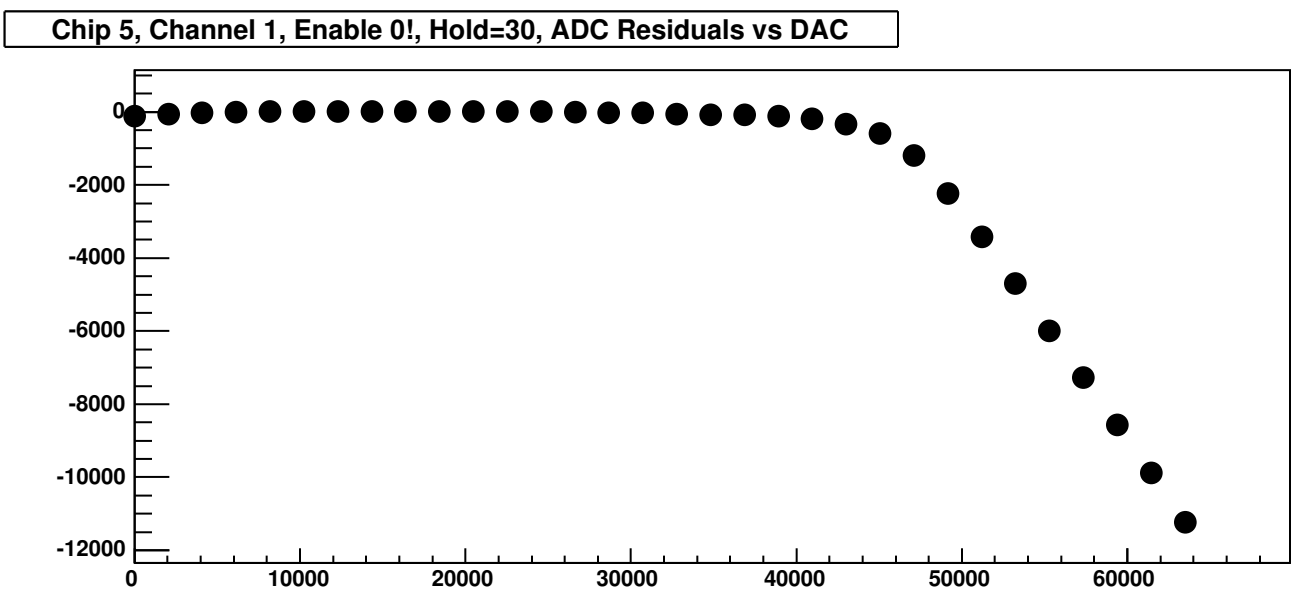

Chip 5, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8420 / 15
p0 1165 ± 0.1114
p1 0.00141 ± 4.595e-06
1240
1220
1200
1180
1160
1140
1120
1100
1080
1060
1040
0
10000
20000
30000
40000
50000
60000

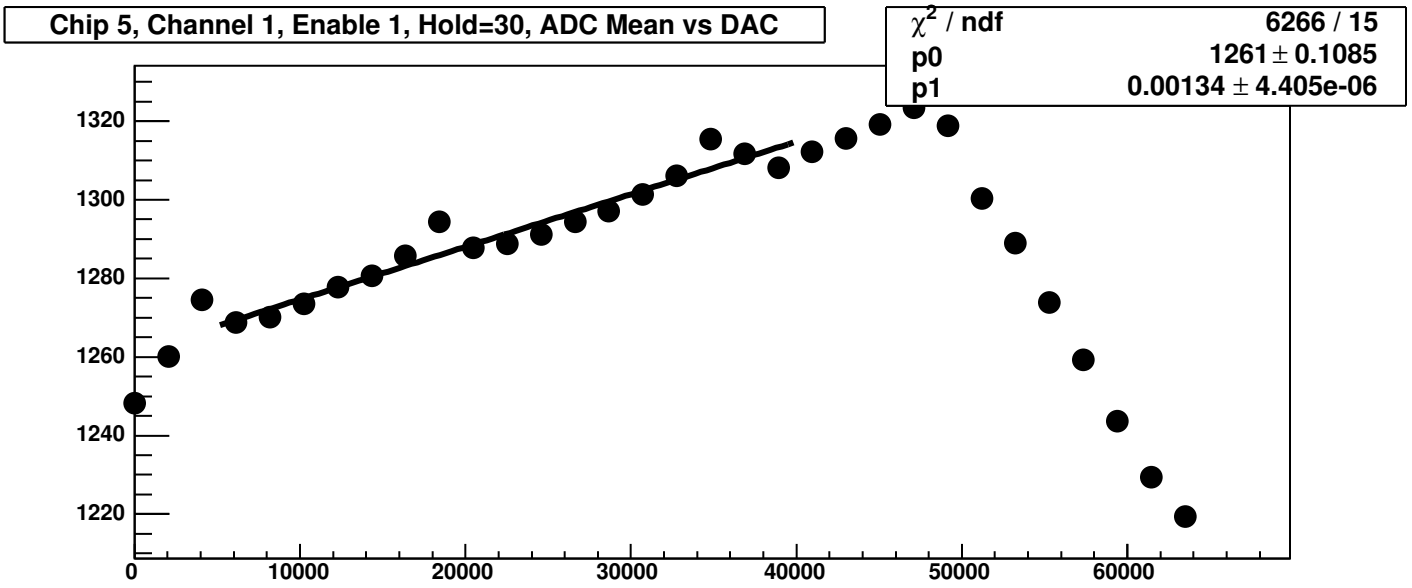

Chip 5, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

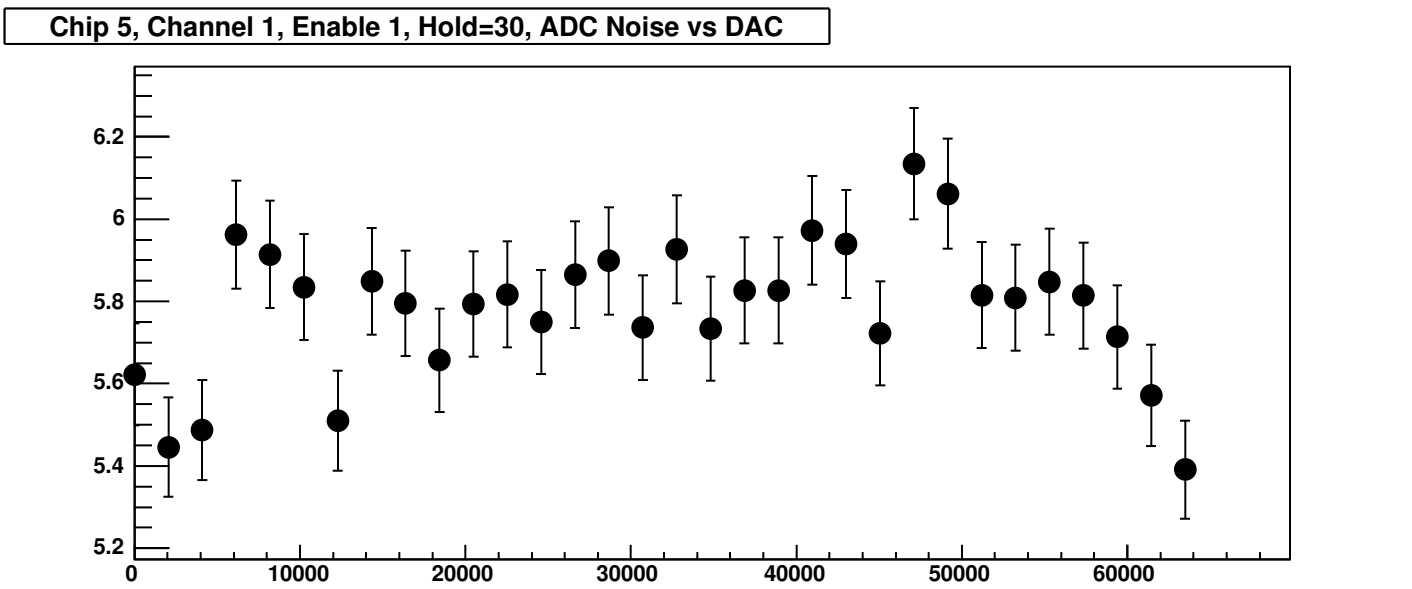

Chip 5, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

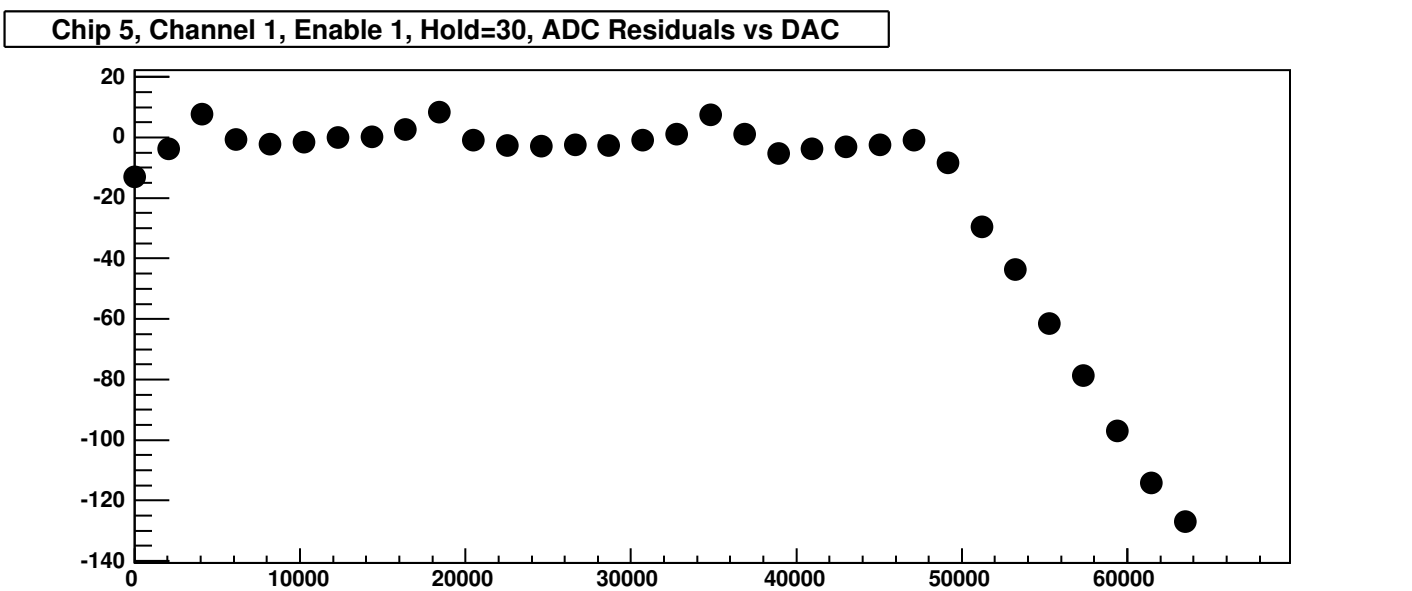

Chip 5, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.132e+04 / 15
p0 1361 ± 0.253
p1 0.63 ± 2.482e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

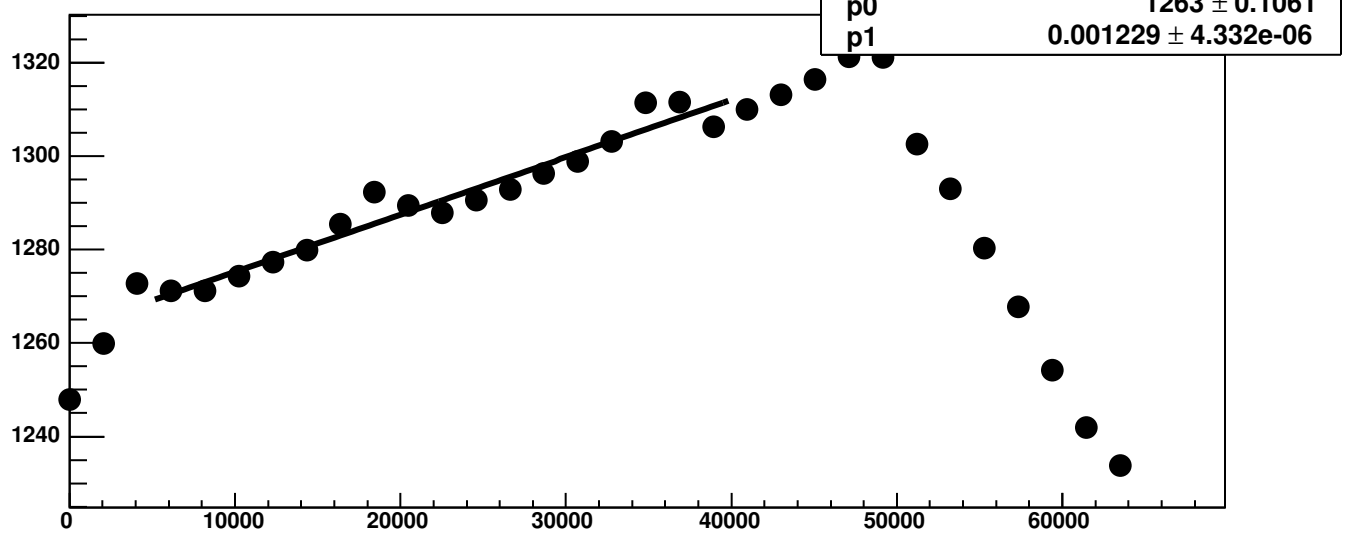

Chip 5, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6266 / 15
p0 1261 ± 0.1085
p1 0.00134 ± 4.405e-06
1320
1300
1280
1260
1240
1220
0
10000
20000
30000
40000
50000
60000

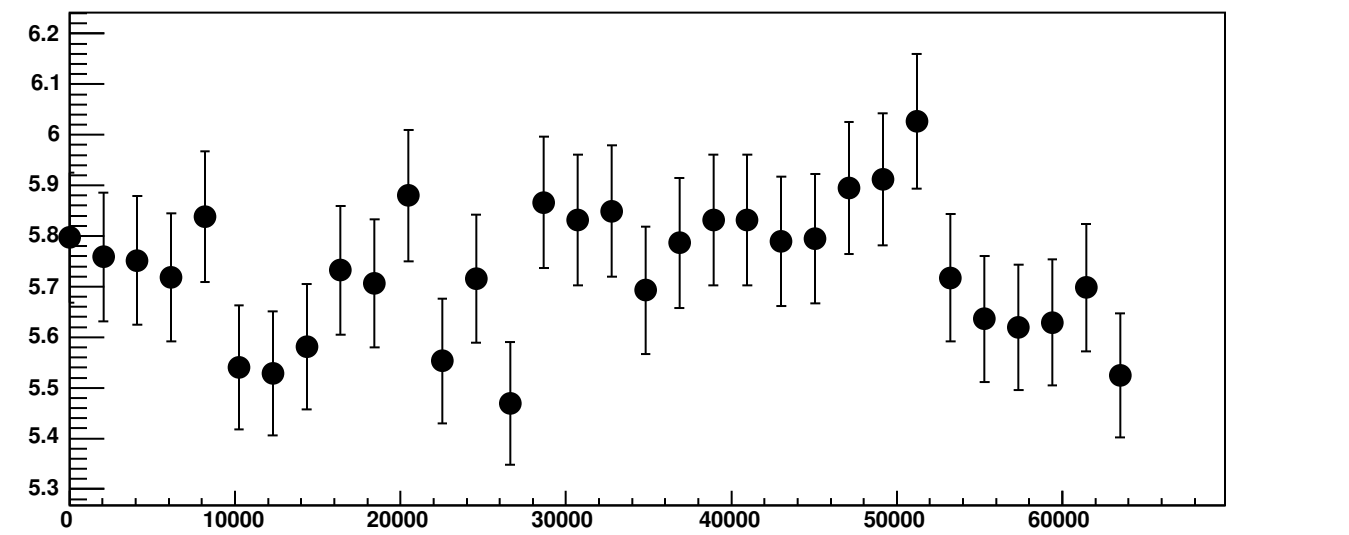

Chip 5, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

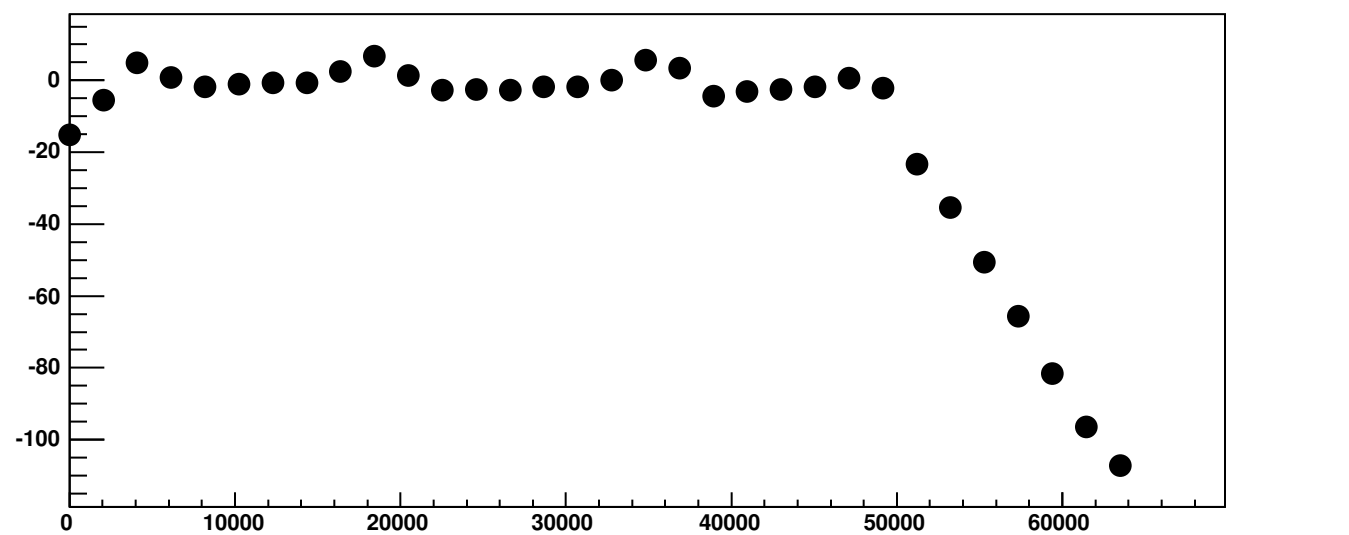
Chip 5, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC

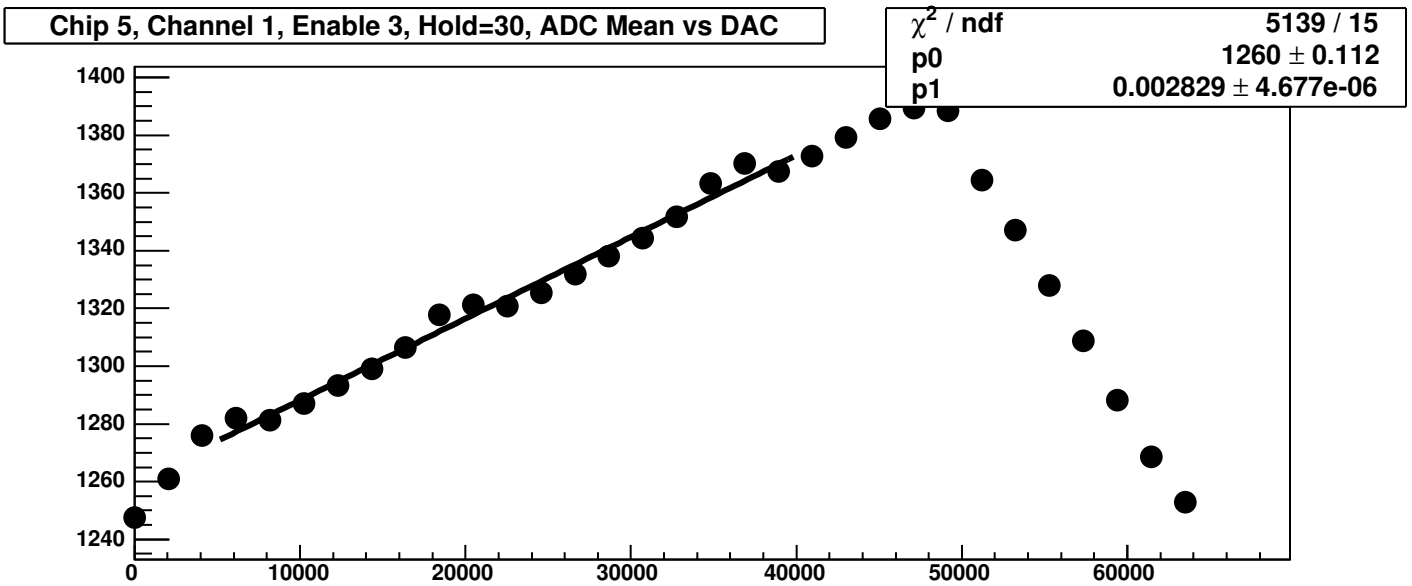


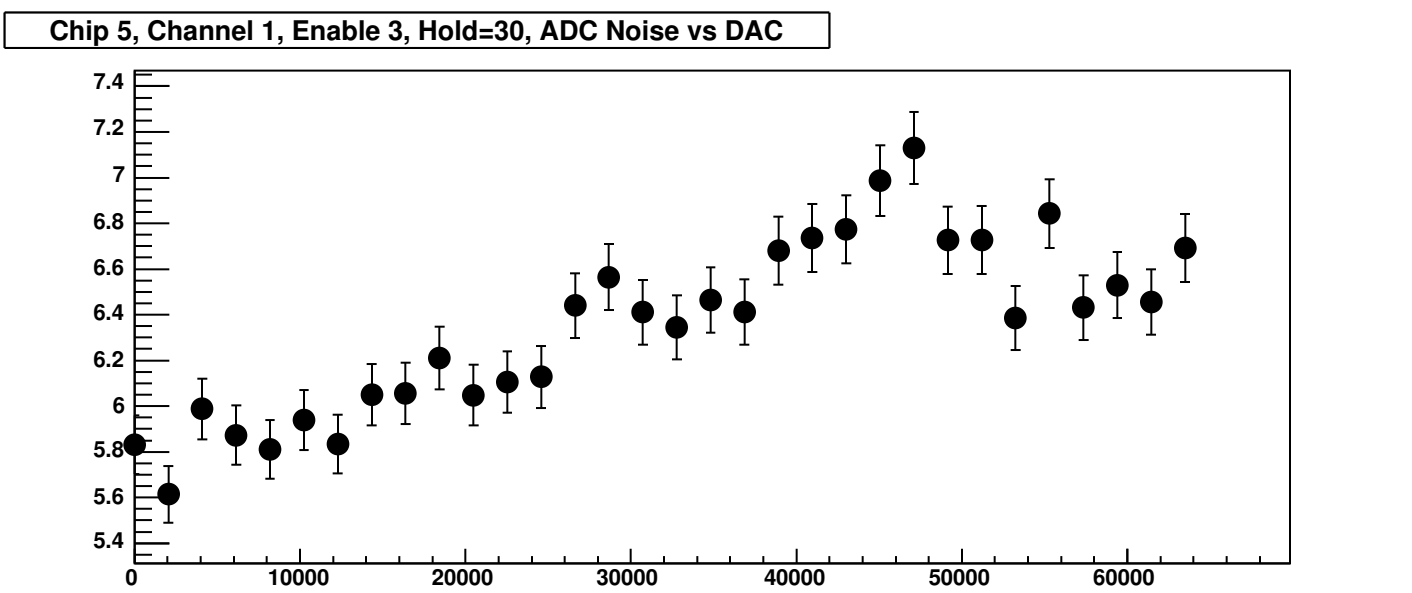
Chip 5, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC

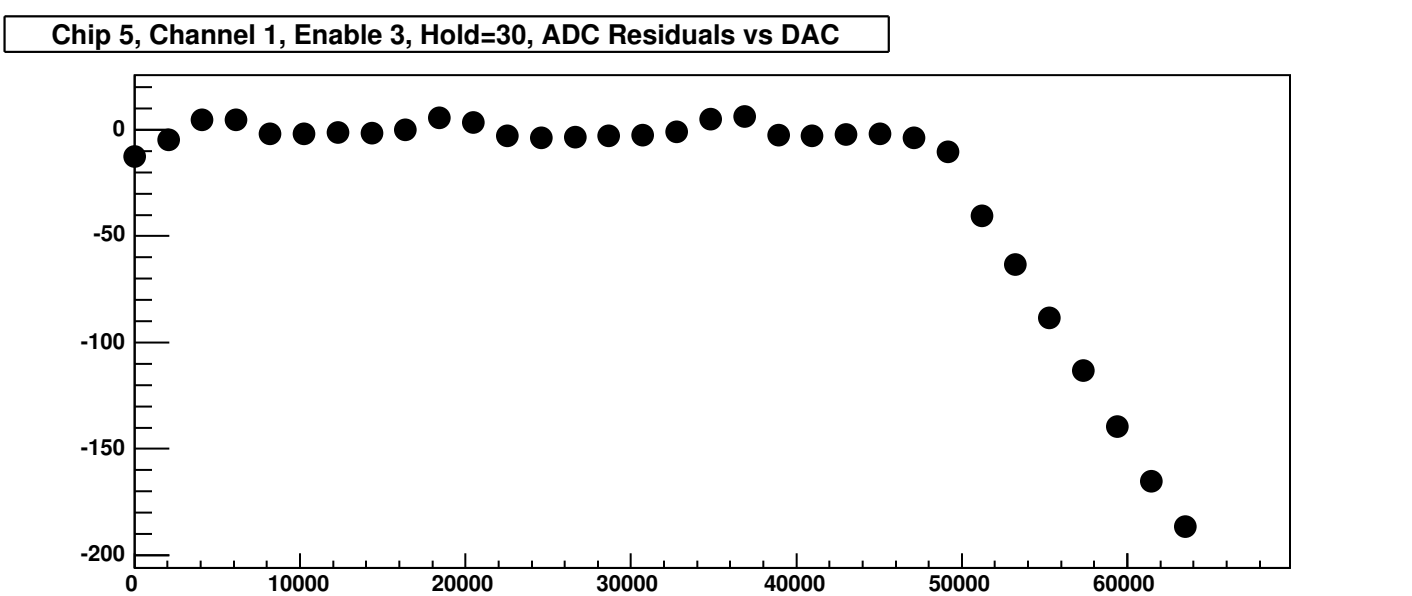


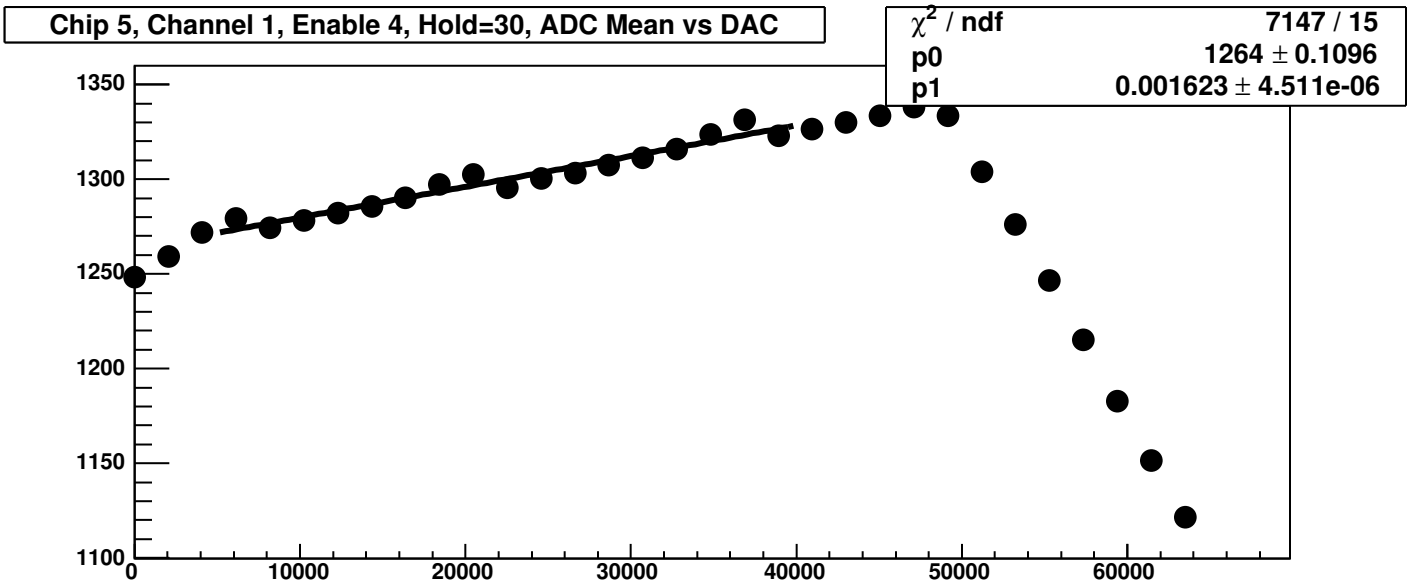
Chip 5, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC

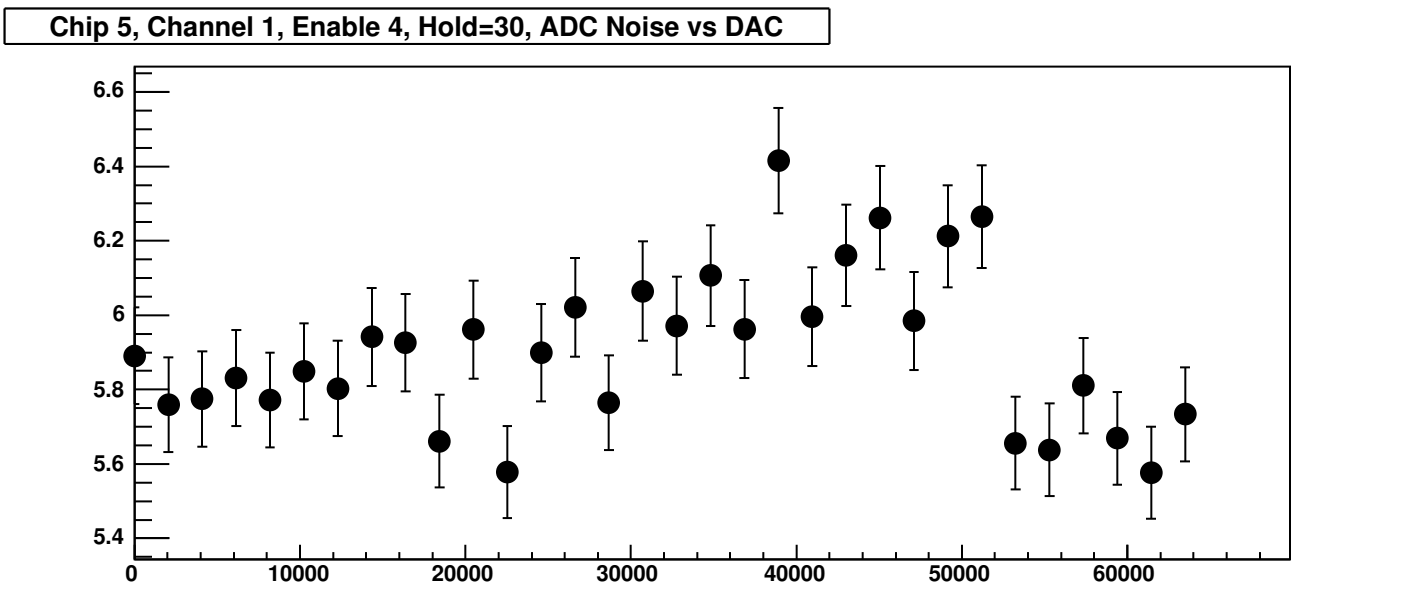


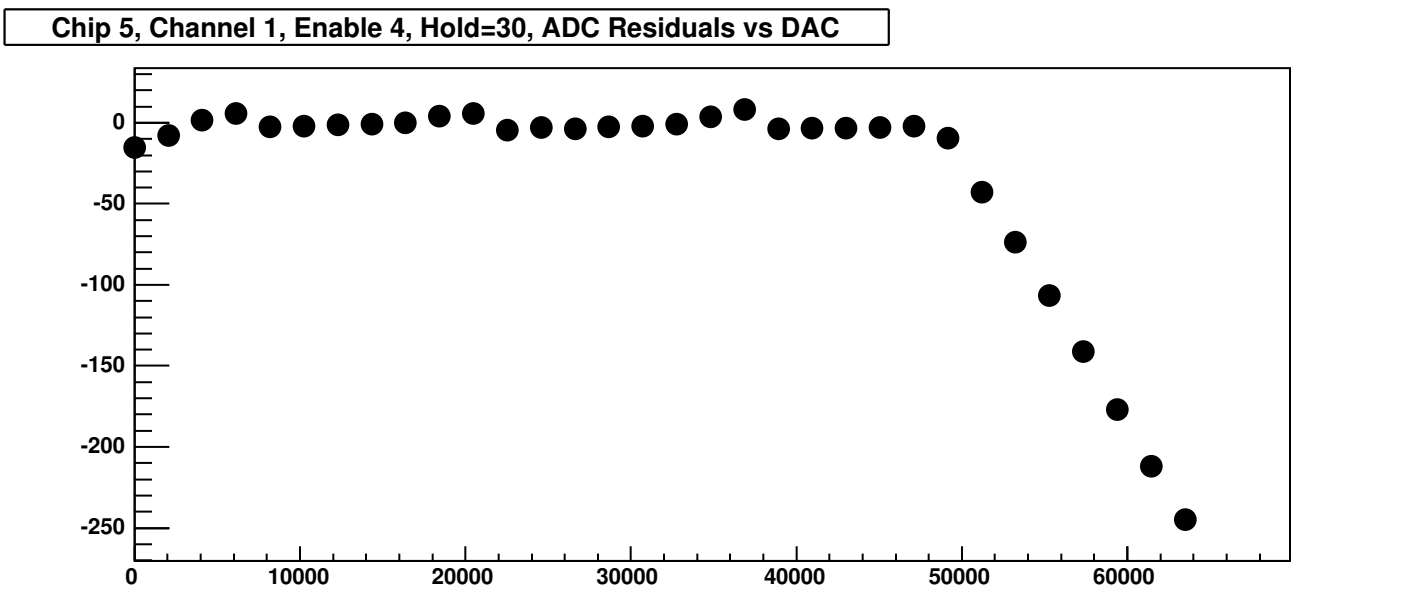

Chip 5, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5139 / 15
p0 1260 ± 0.112
p1 0.002829 ± 4.677e-06
1400
1380
1360
1340
1320
1300
1280
1260
1240
0
10000
20000
30000
40000
50000
60000

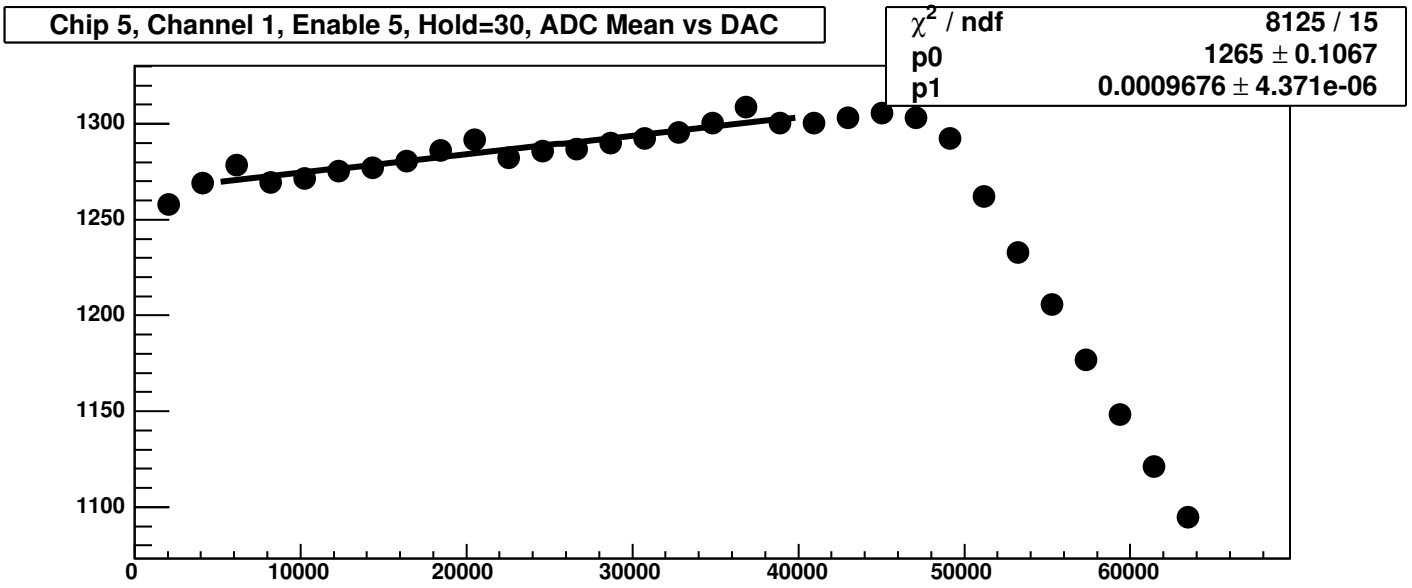

Chip 5, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

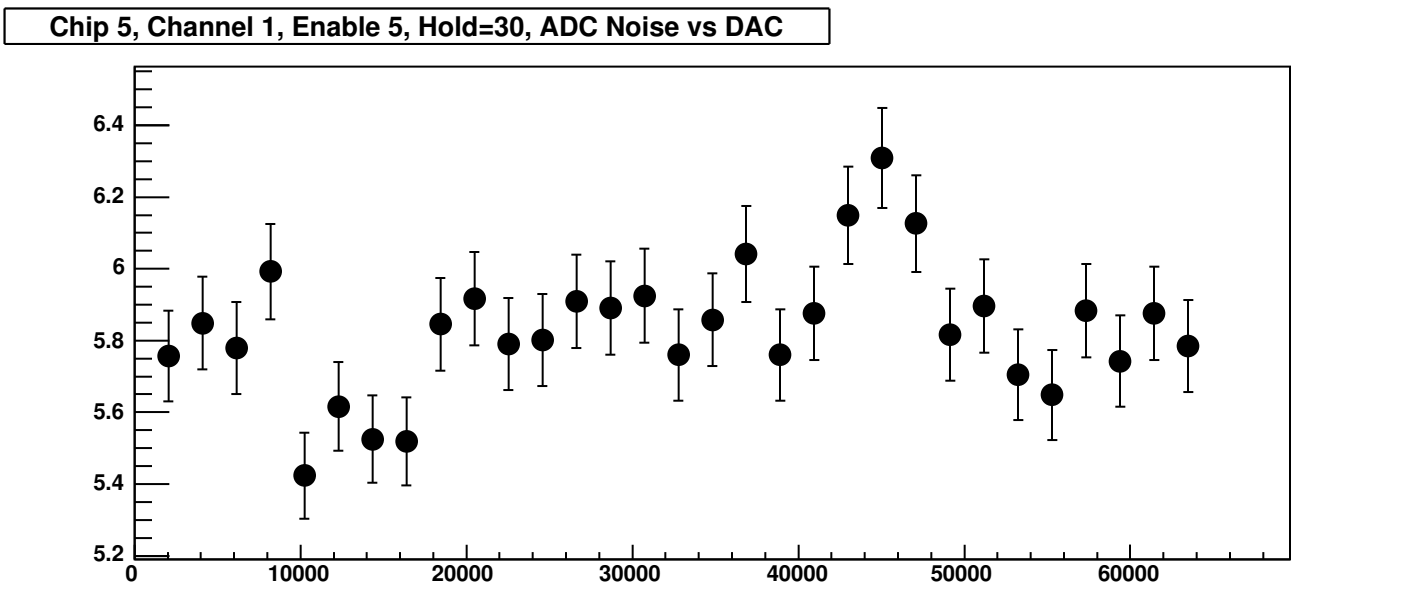

Chip 5, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

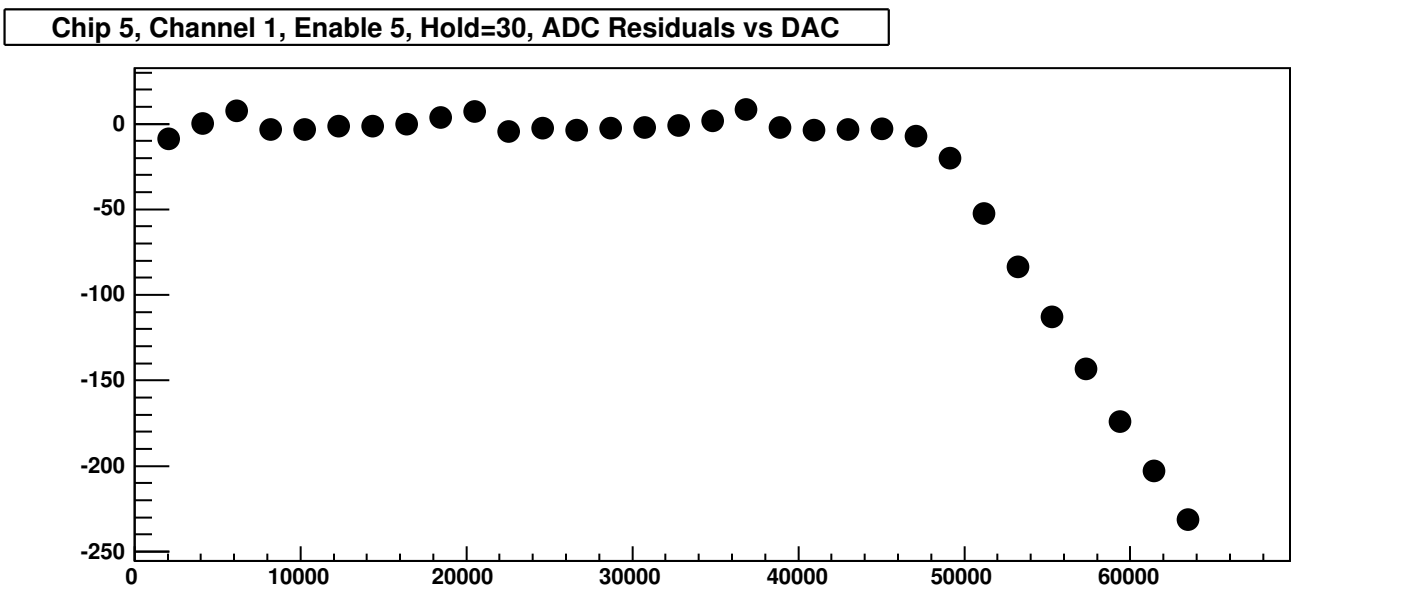

Chip 5, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7147 / 15
p0 1264 ± 0.1096
p1 0.001623 ± 4.511e-06
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

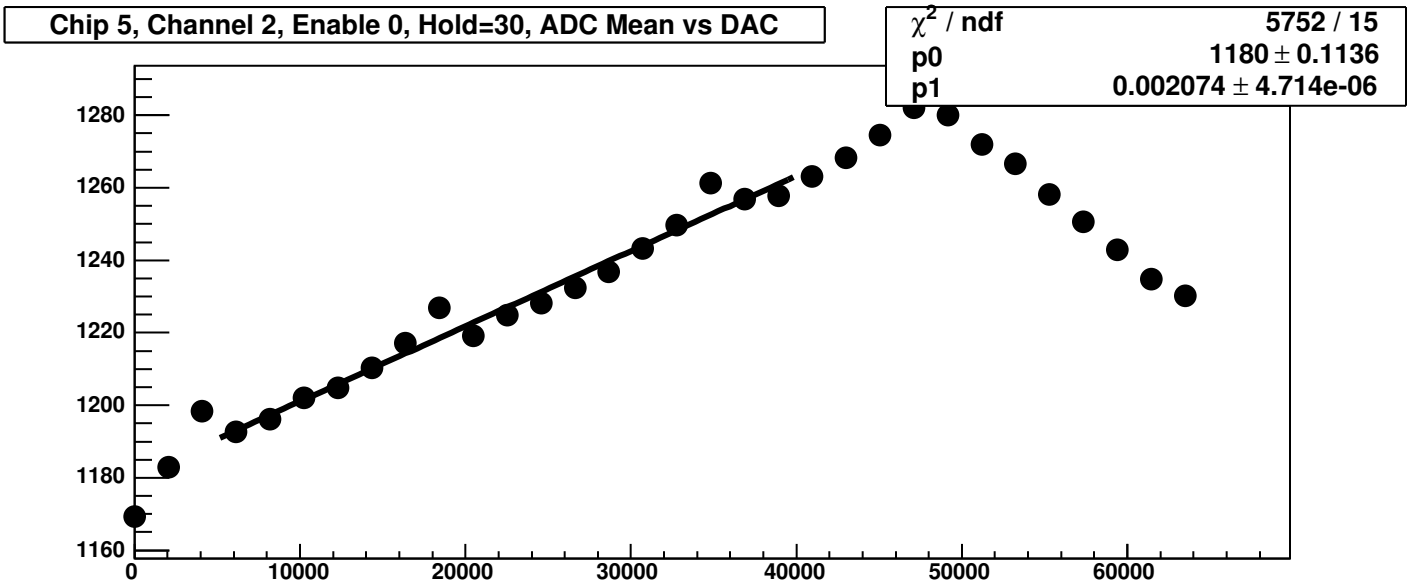

Chip 5, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

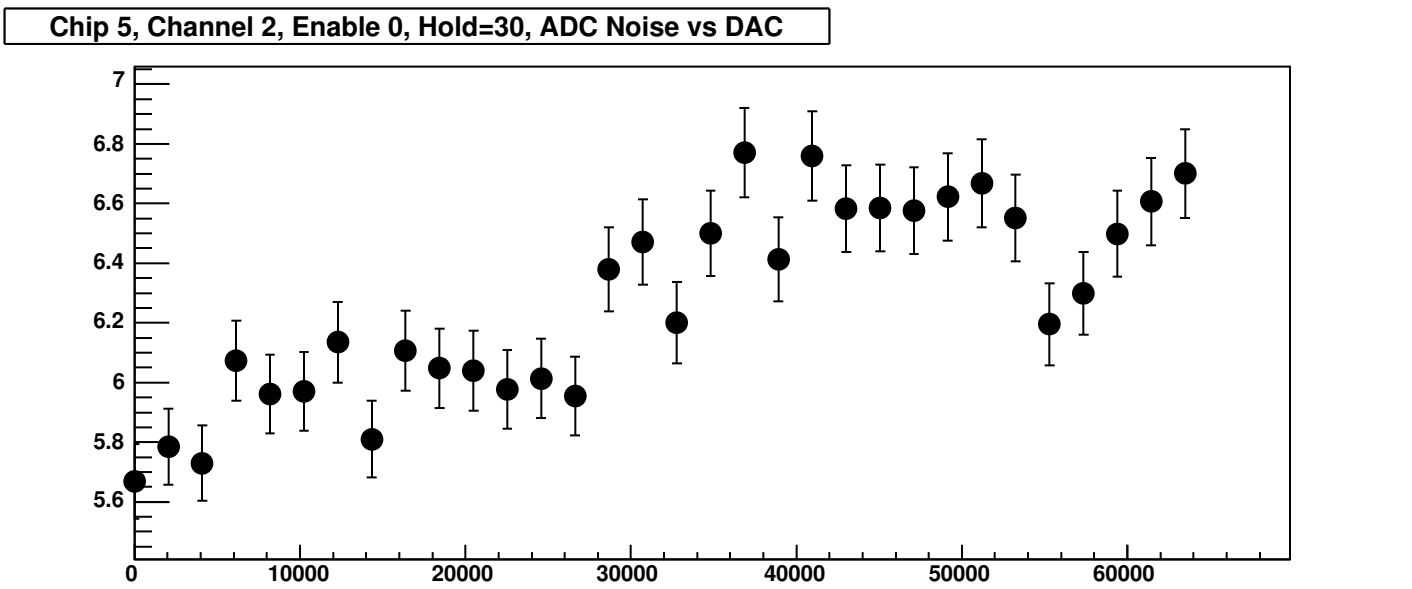

Chip 5, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

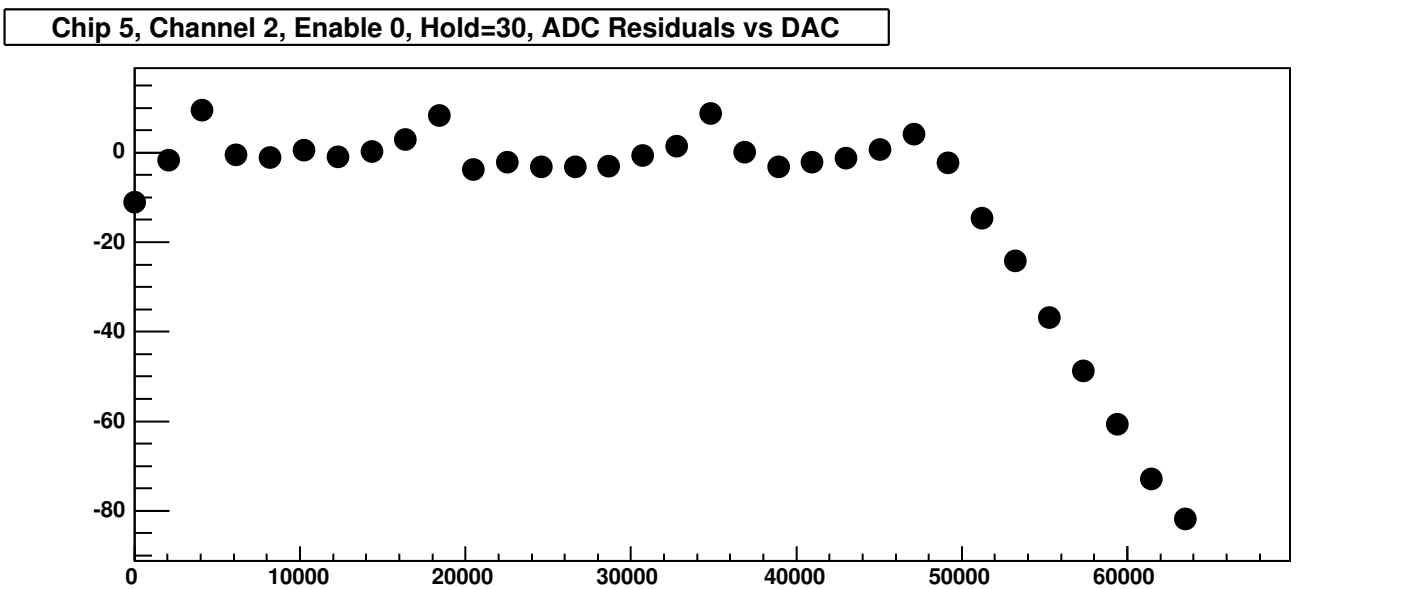

Chip 5, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8125 / 15
p0 1265 ± 0.1067
p1 0.0009676 ± 4.371e-06
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

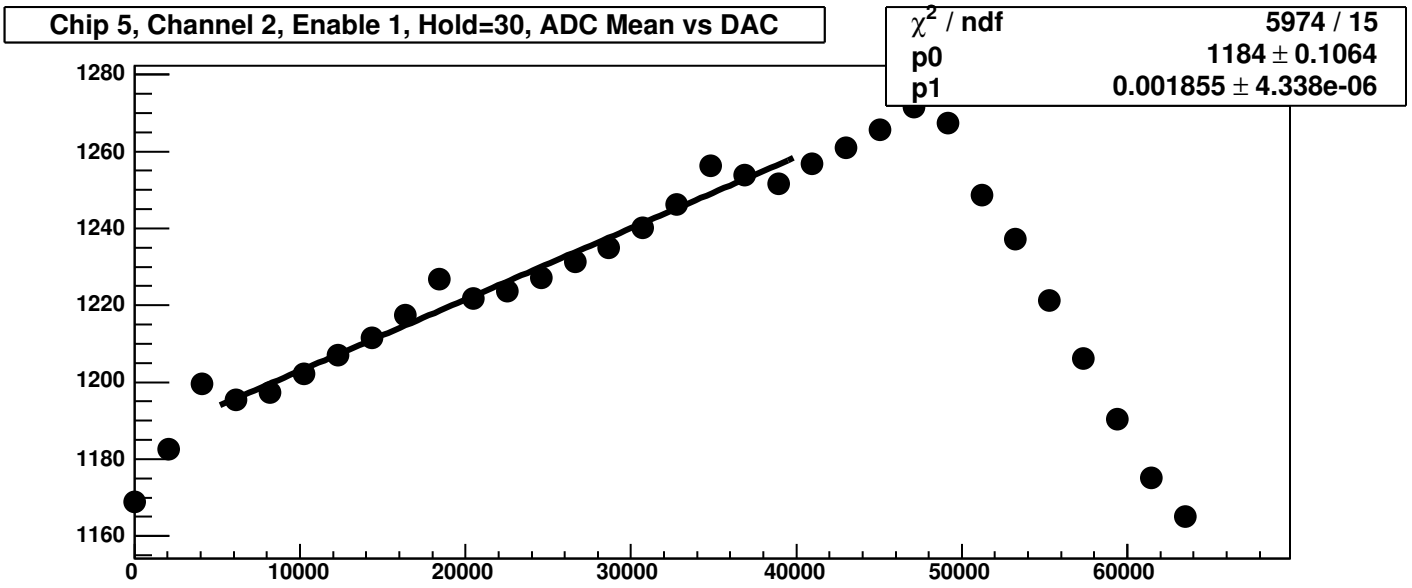

Chip 5, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

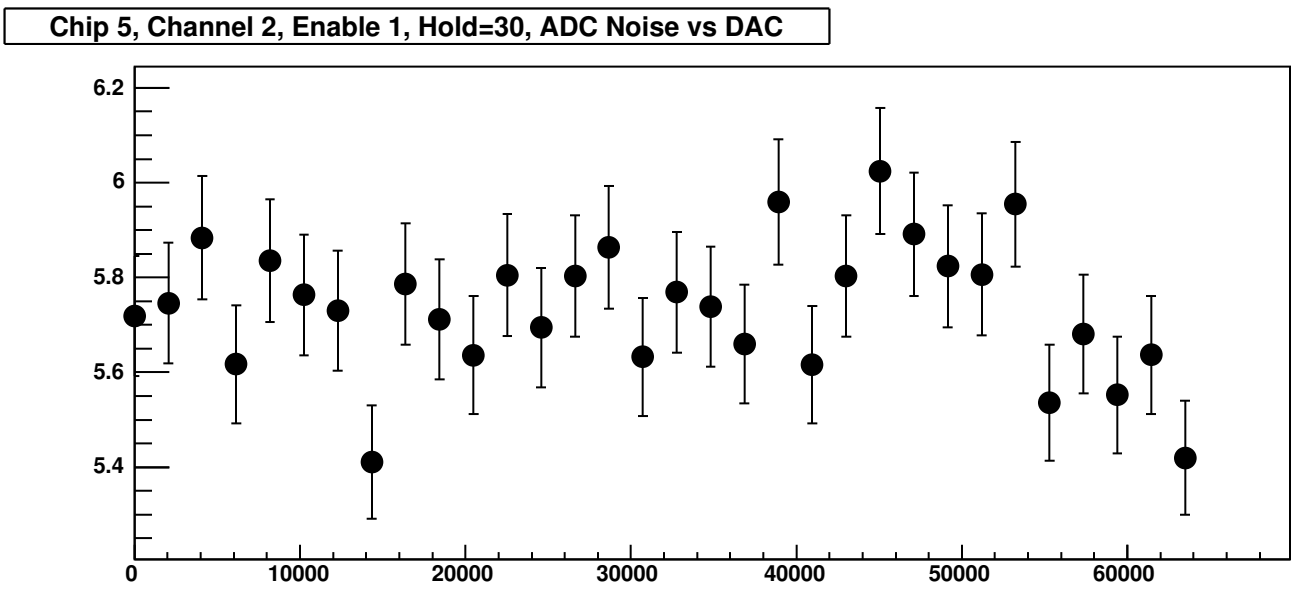

Chip 5, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

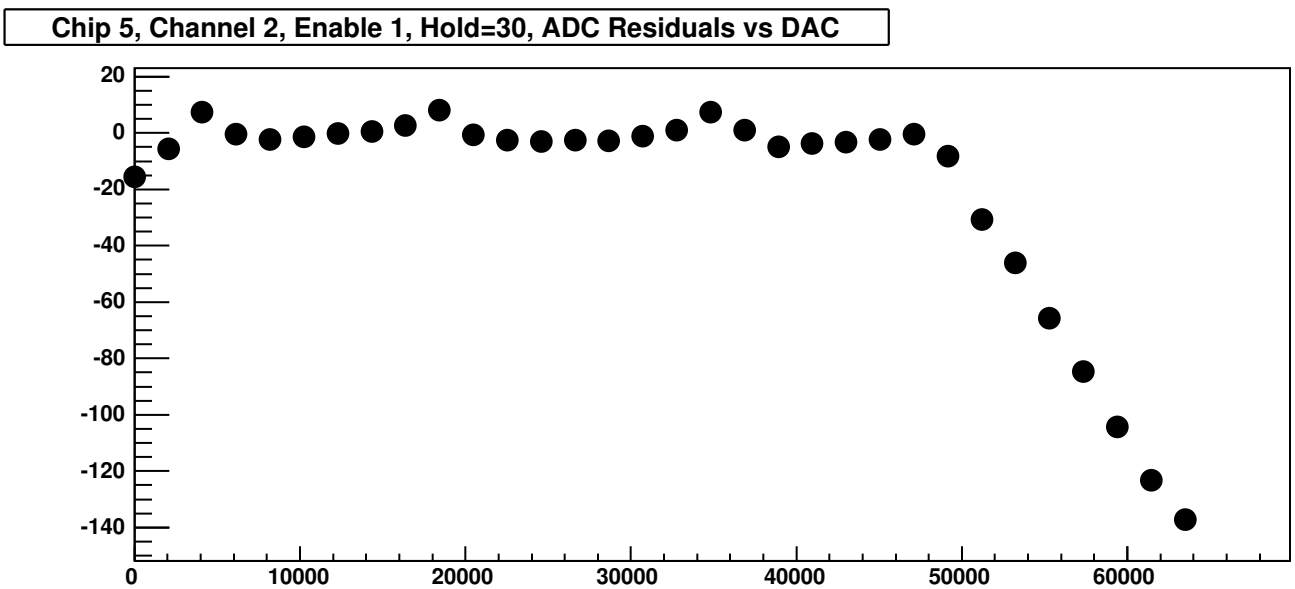

Chip 5, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5752 / 15
p0 1180 ± 0.1136
p1 0.002074 ± 4.714e-06
1280
1260
1240
1220
1200
1180
1160
0
10000
20000
30000
40000
50000
60000

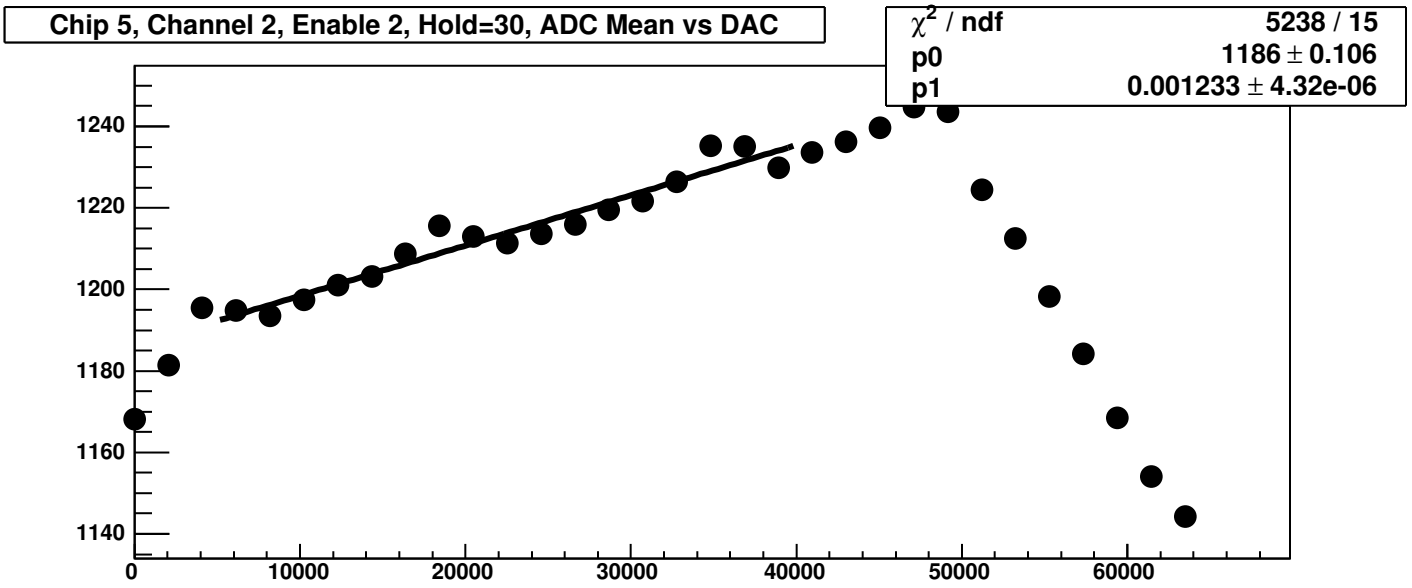

Chip 5, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

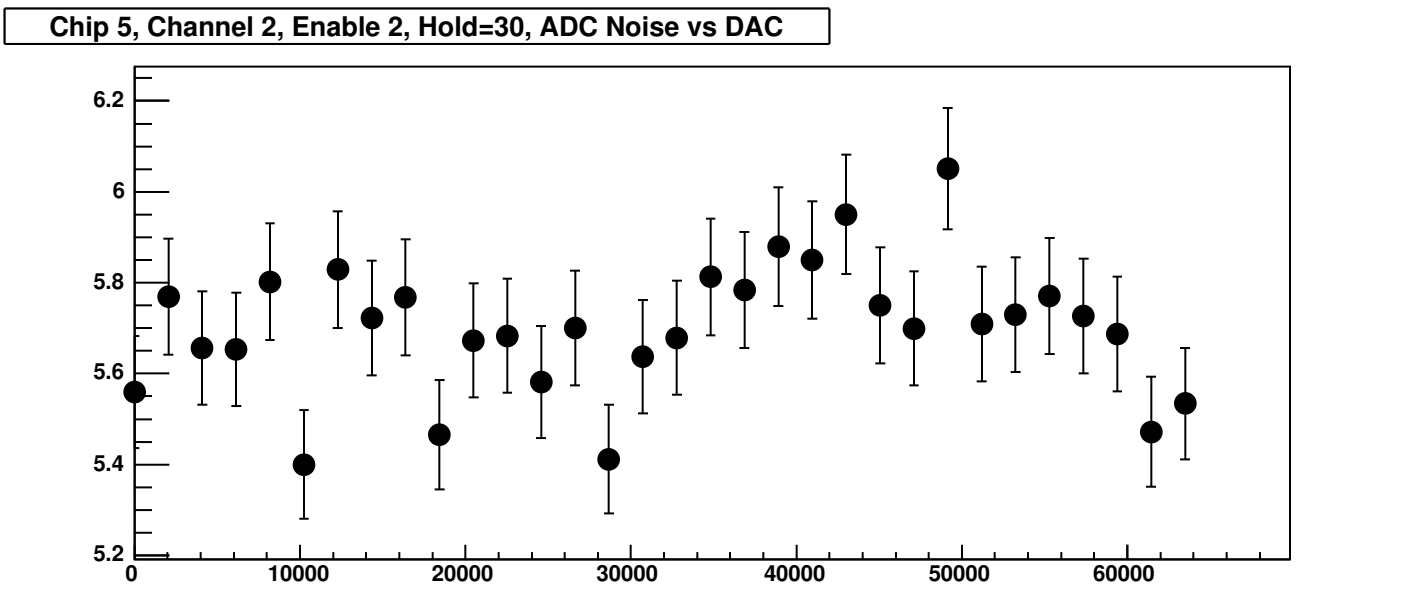

Chip 5, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

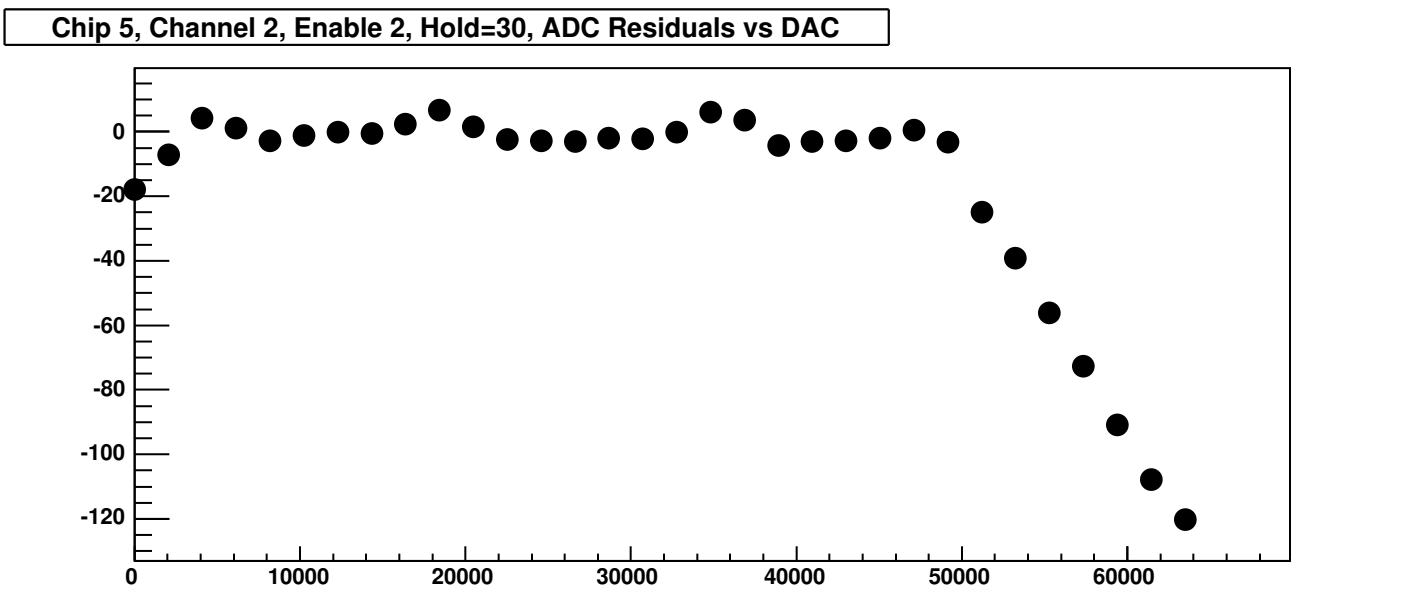

Chip 5, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5974 / 15
p0 1184 ± 0.1064
p1 0.001855 ± 4.338e-06
1280
1260
1240
1220
1200
1180
1160
0
10000
20000
30000
40000
50000
60000

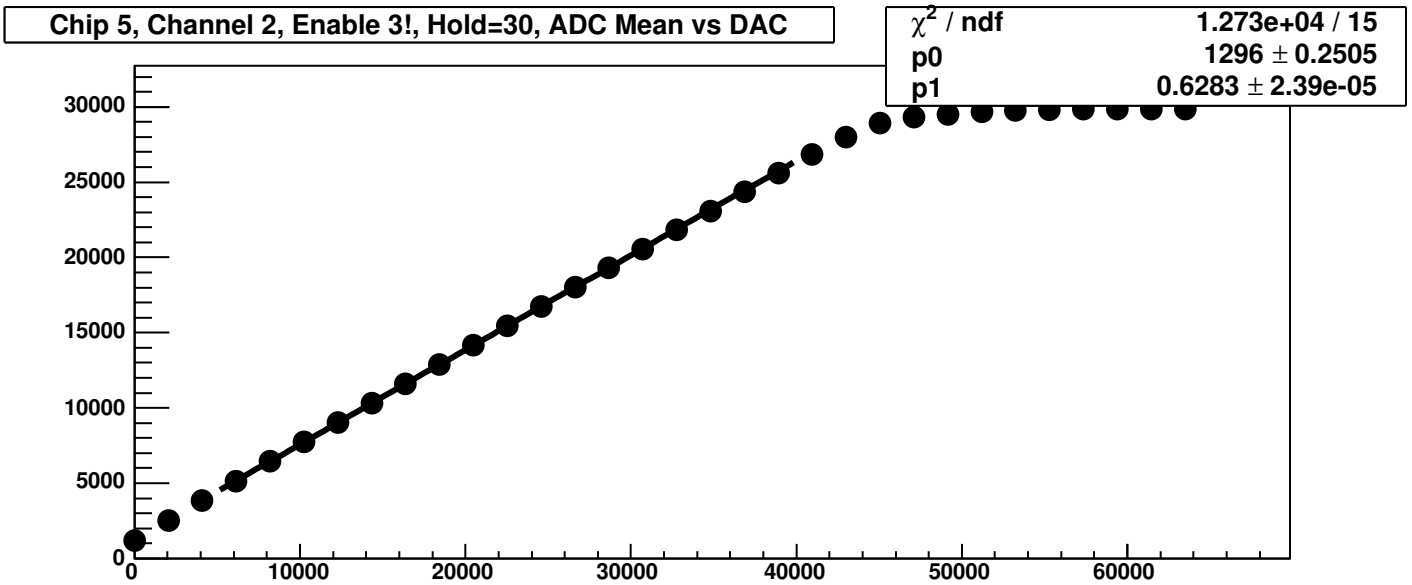

Chip 5, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

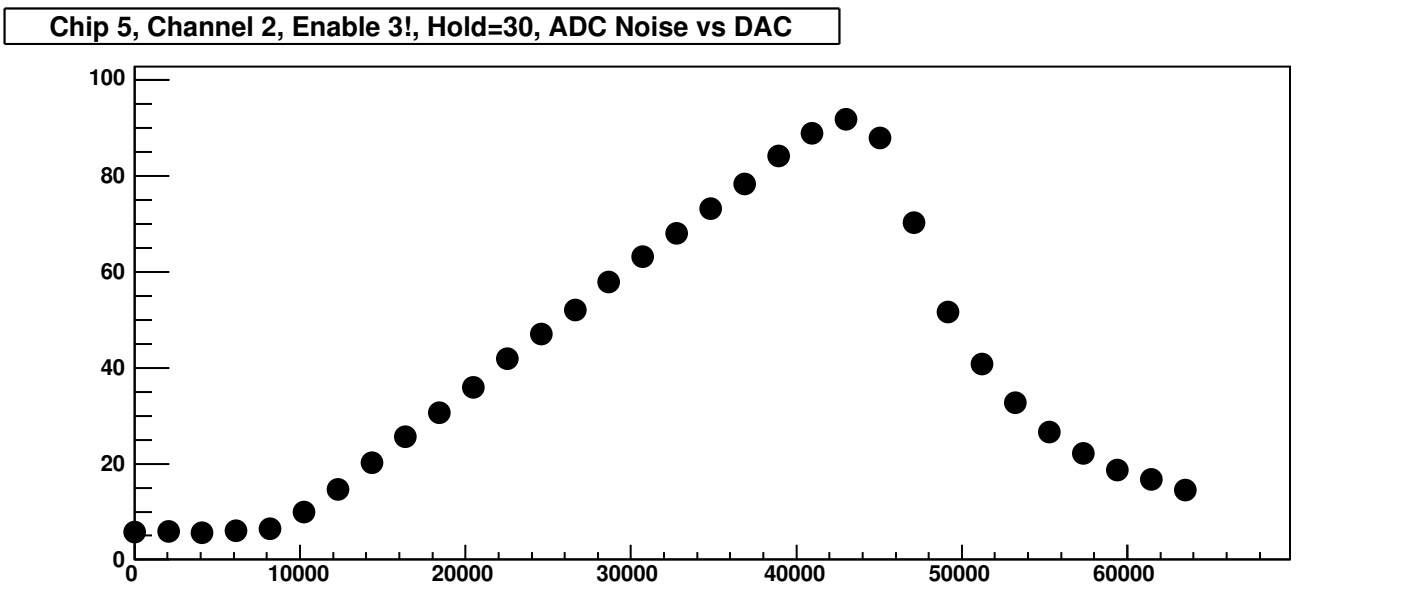

Chip 5, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

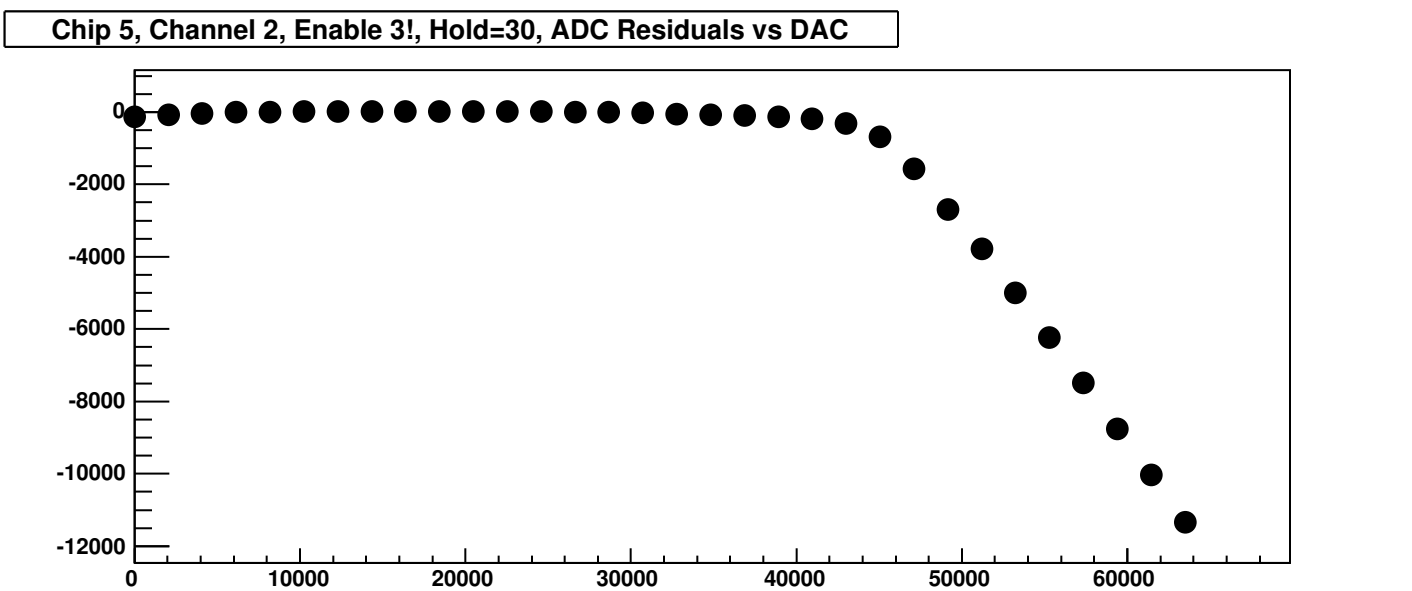

Chip 5, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5238 / 15
p0 1186 ± 0.106
p1 0.001233 ± 4.32e-06
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

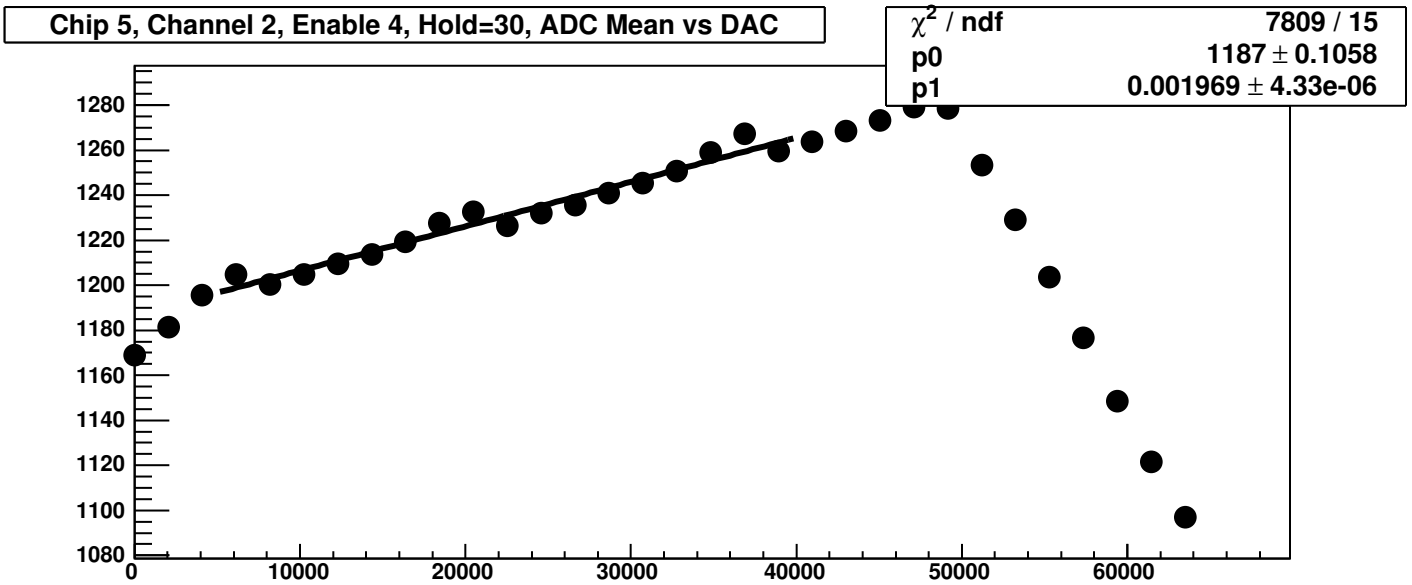

Chip 5, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

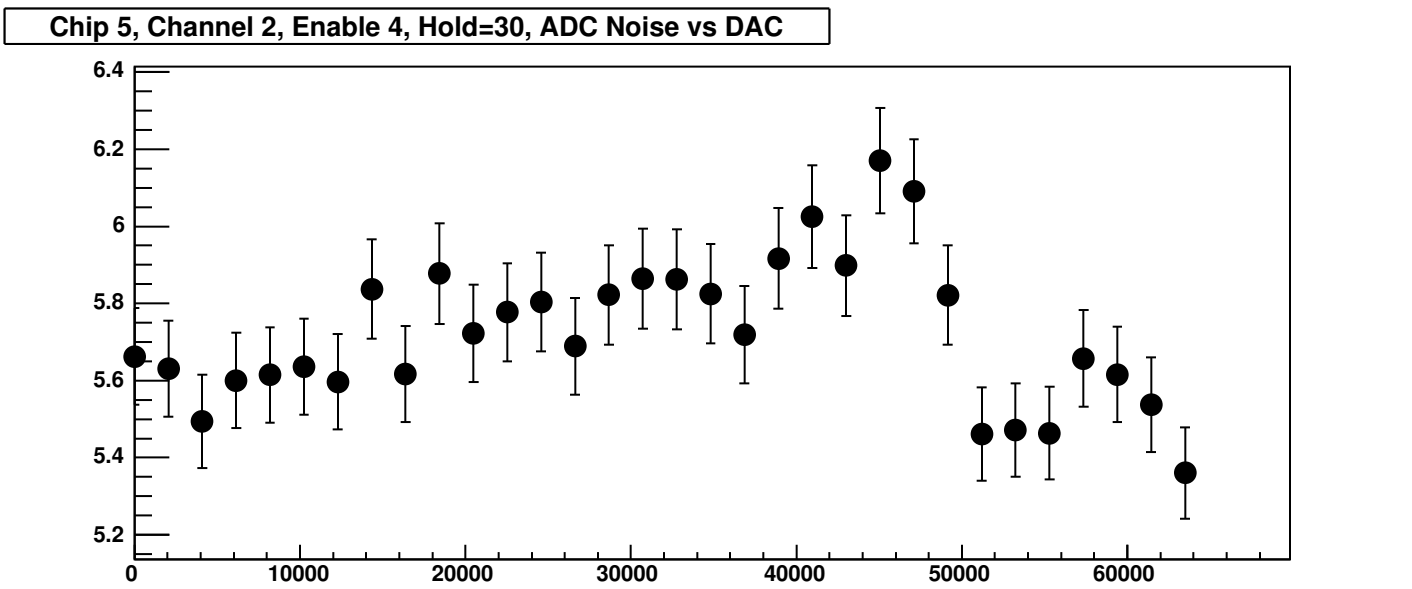

Chip 5, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

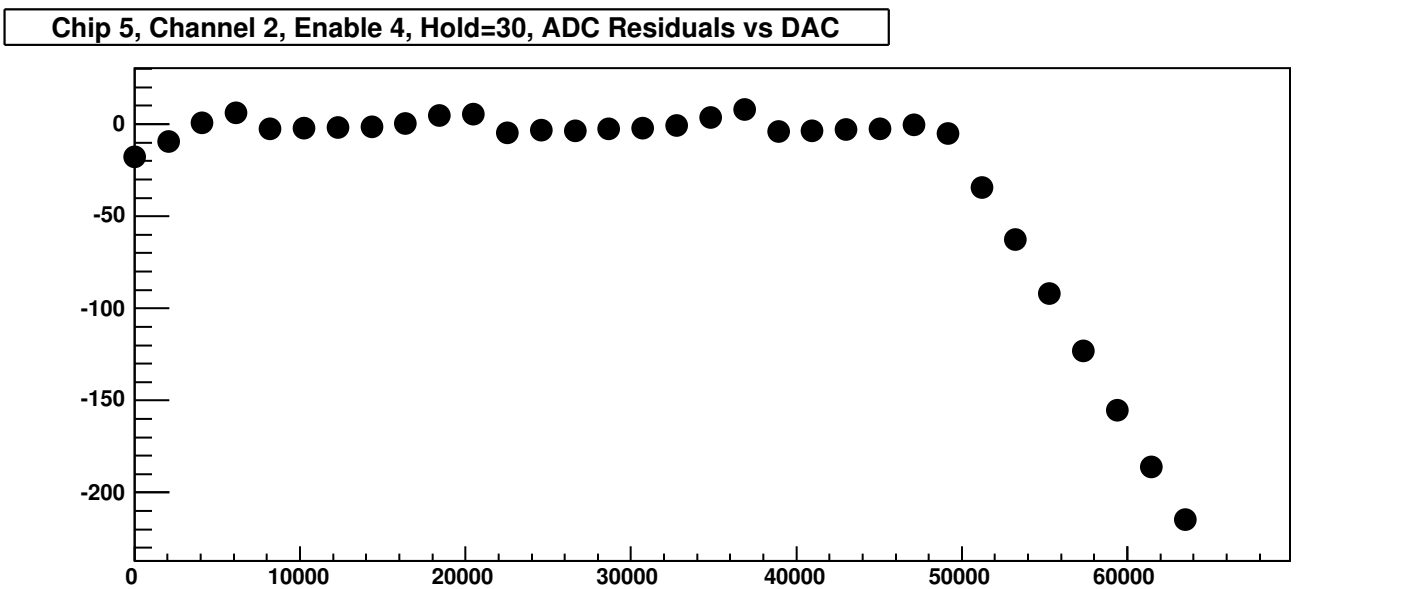

Chip 5, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.273e+04 / 15
p0 1296 ± 0.2505
p1 0.6283 ± 2.39e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

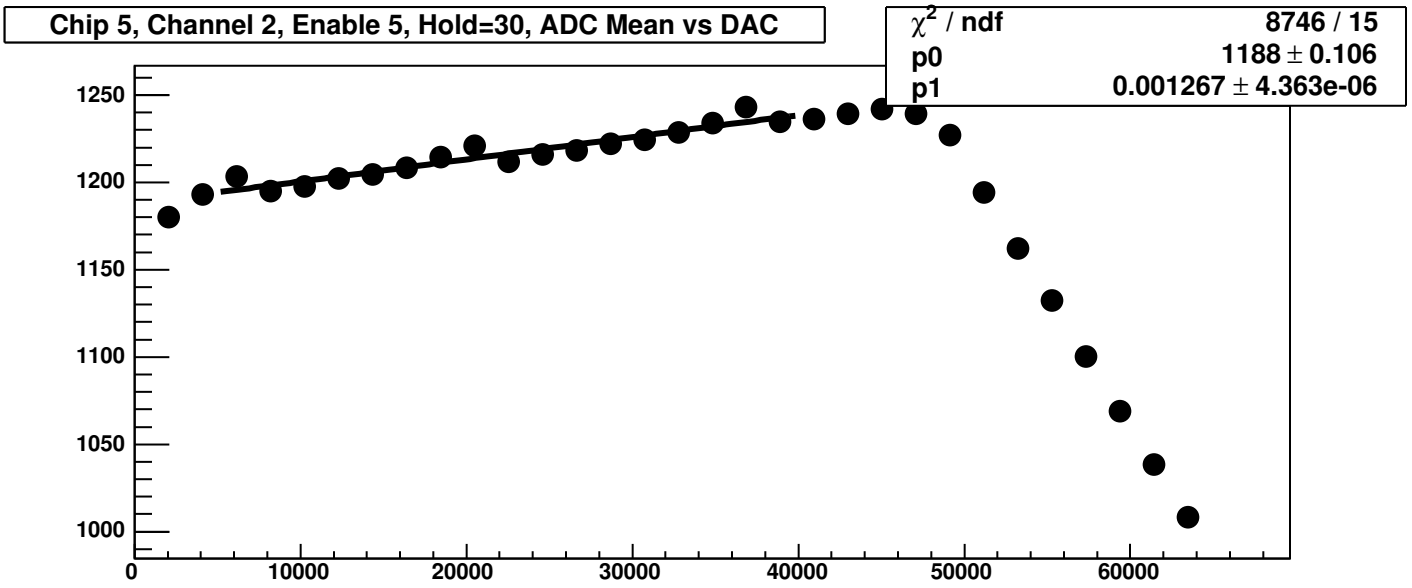

Chip 5, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

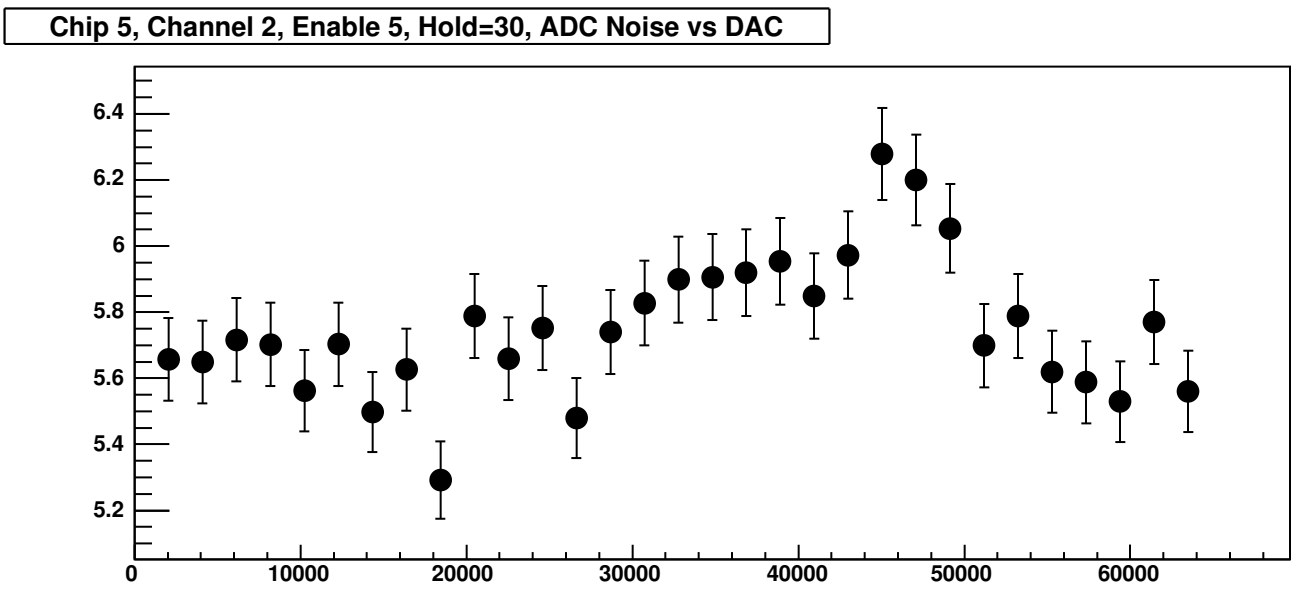

Chip 5, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

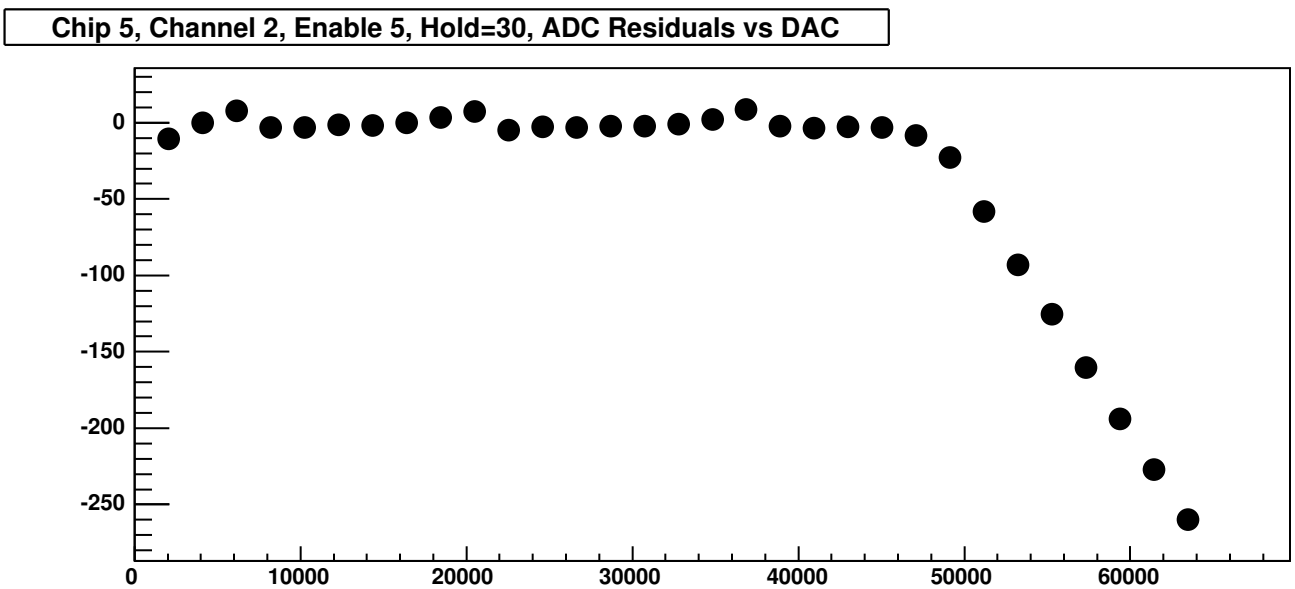

Chip 5, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7809 / 15
p0 1187 ± 0.1058
p1 0.001969 ± 4.33e-06
1280
1260
1240
1220
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

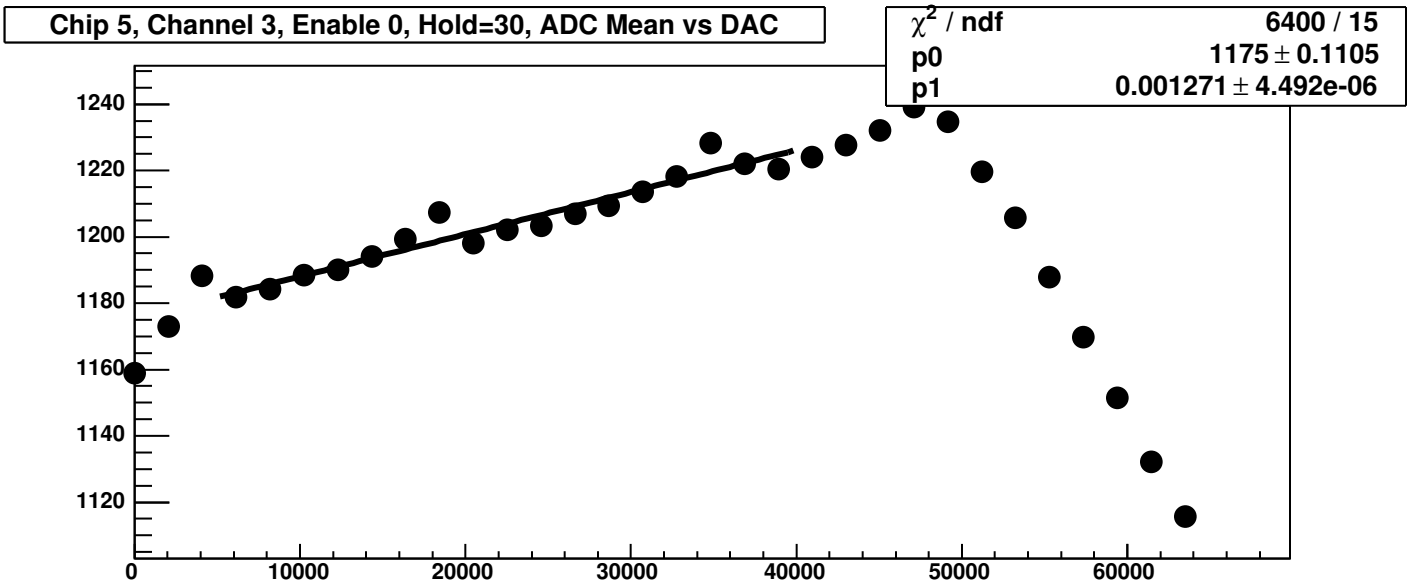

Chip 5, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

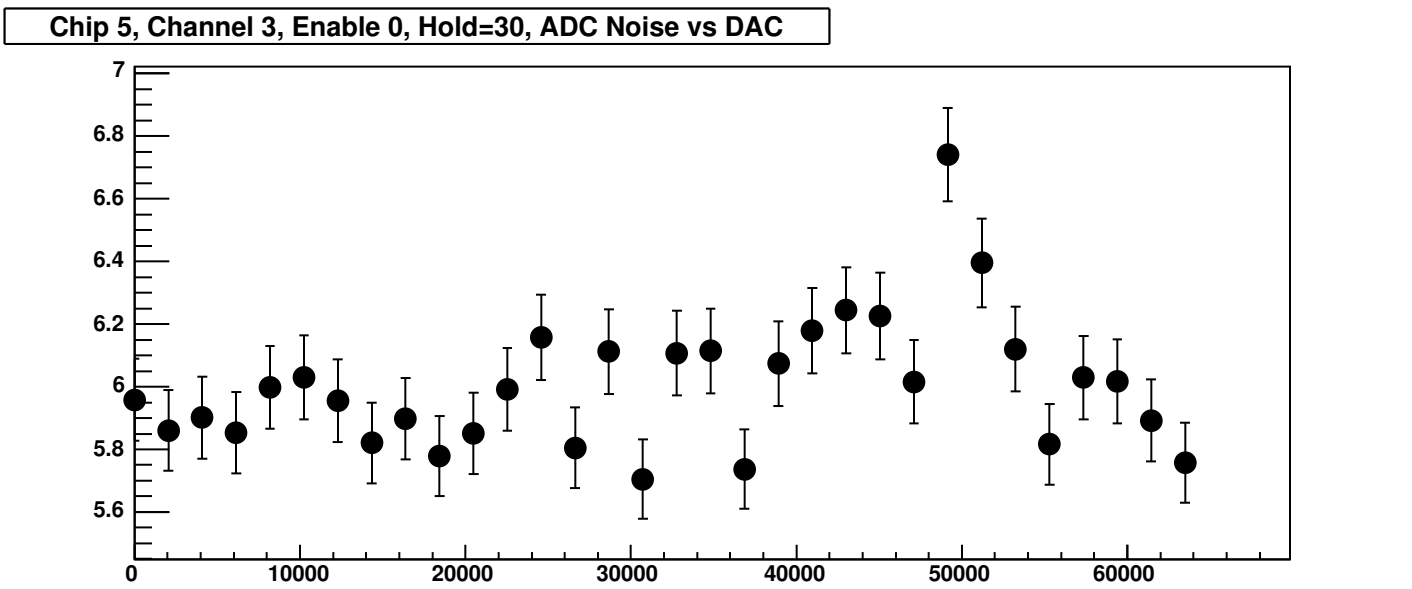

Chip 5, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

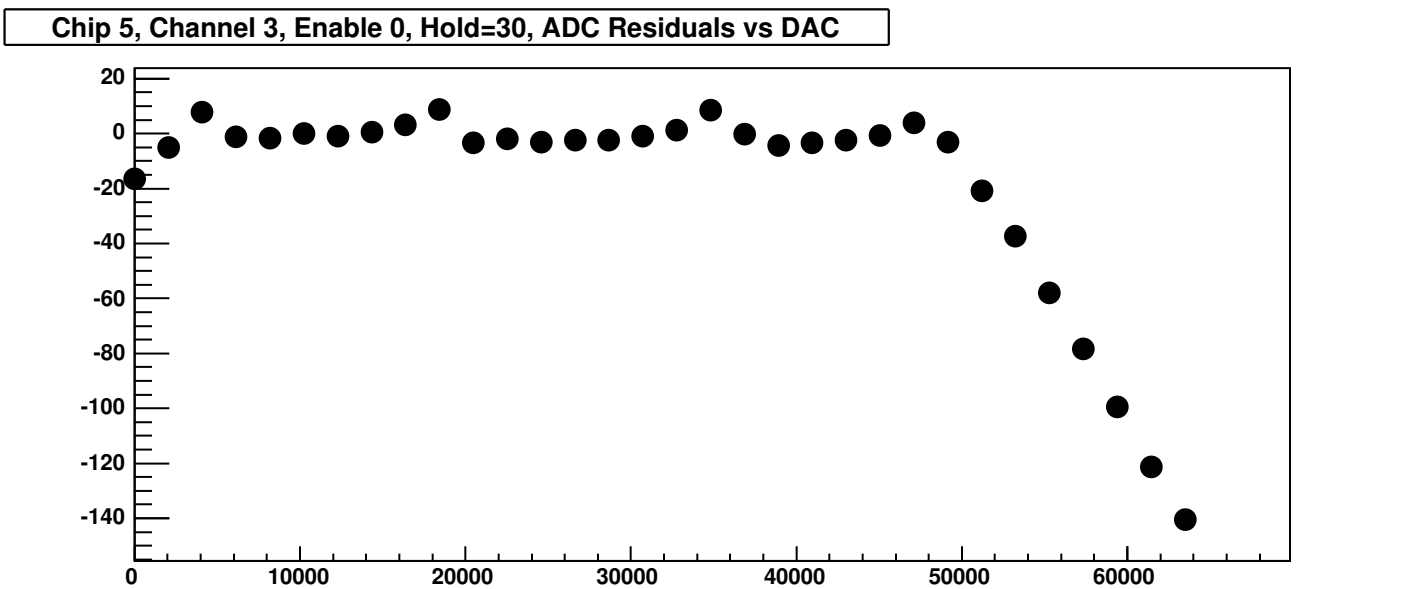

Chip 5, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8746 / 15
p0 1188 ± 0.106
p1 0.001267 ± 4.363e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

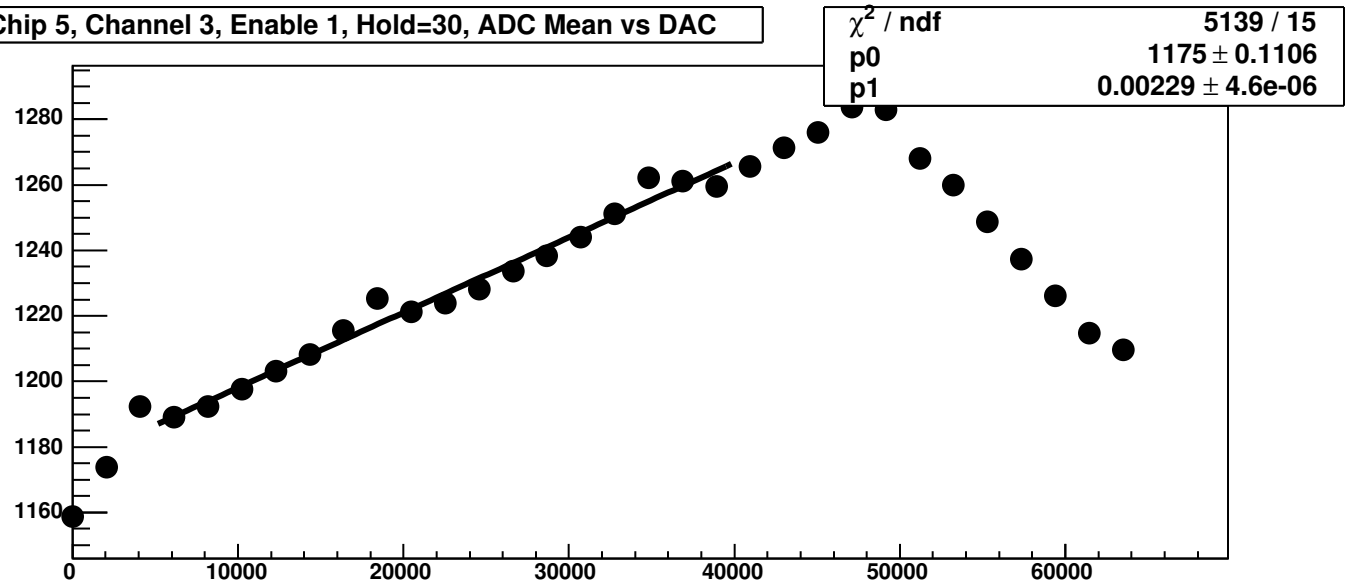

Chip 5, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6400 / 15
p0 1175 ± 0.1105
p1 0.001271 ± 4.492e-06
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000

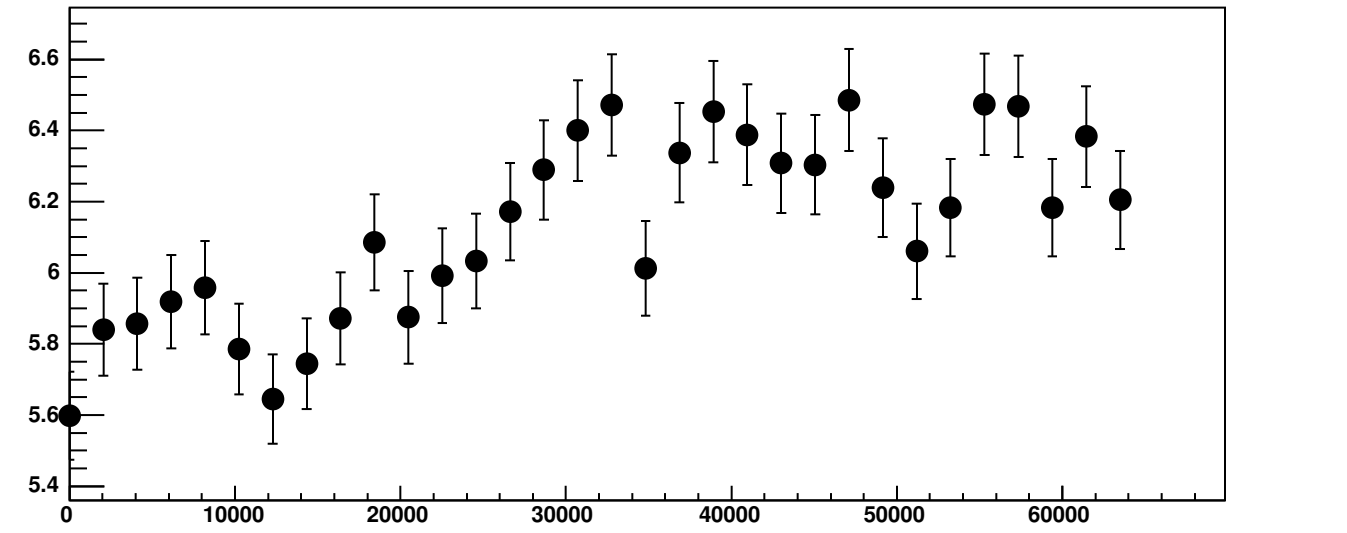

Chip 5, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

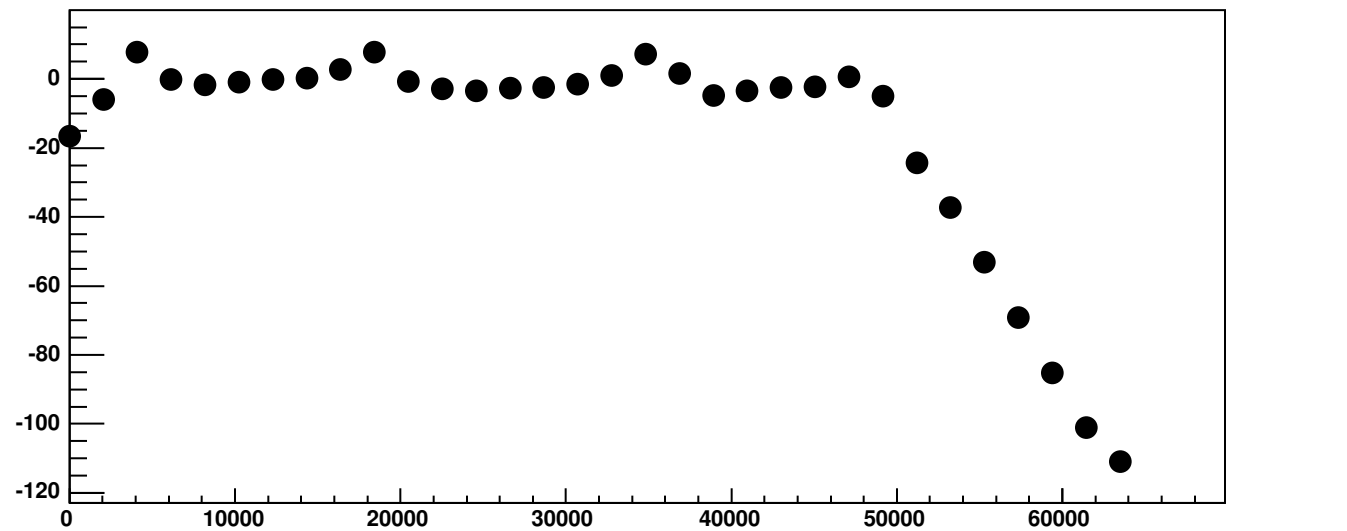
Chip 5, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC

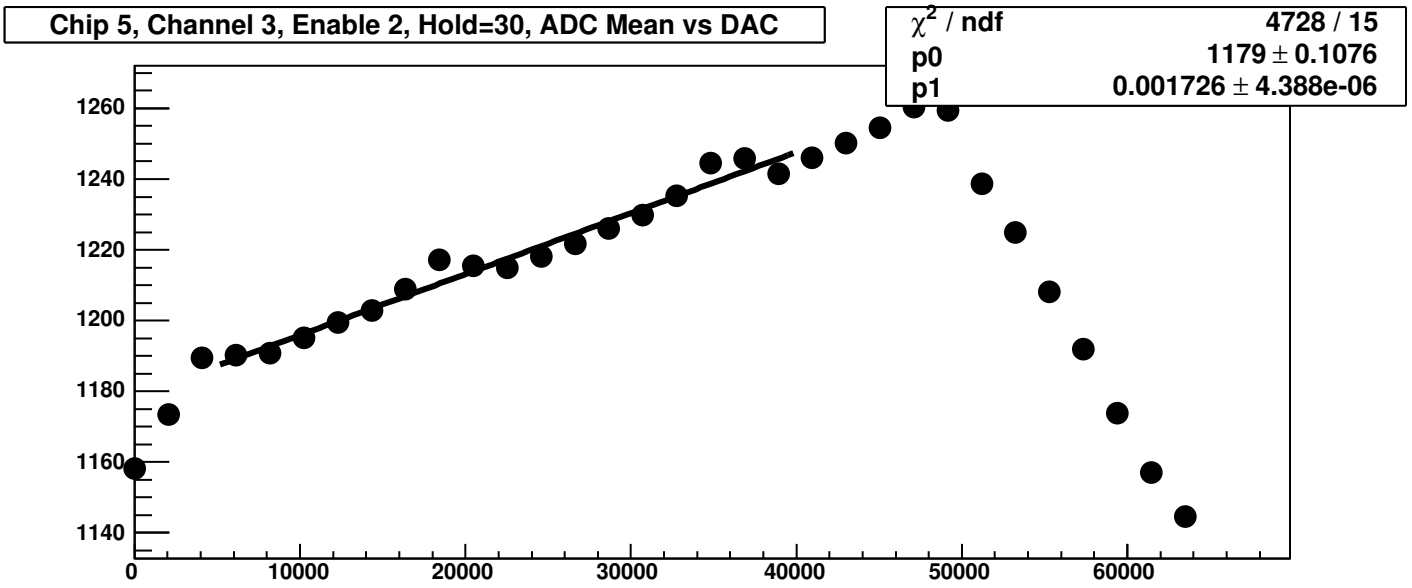


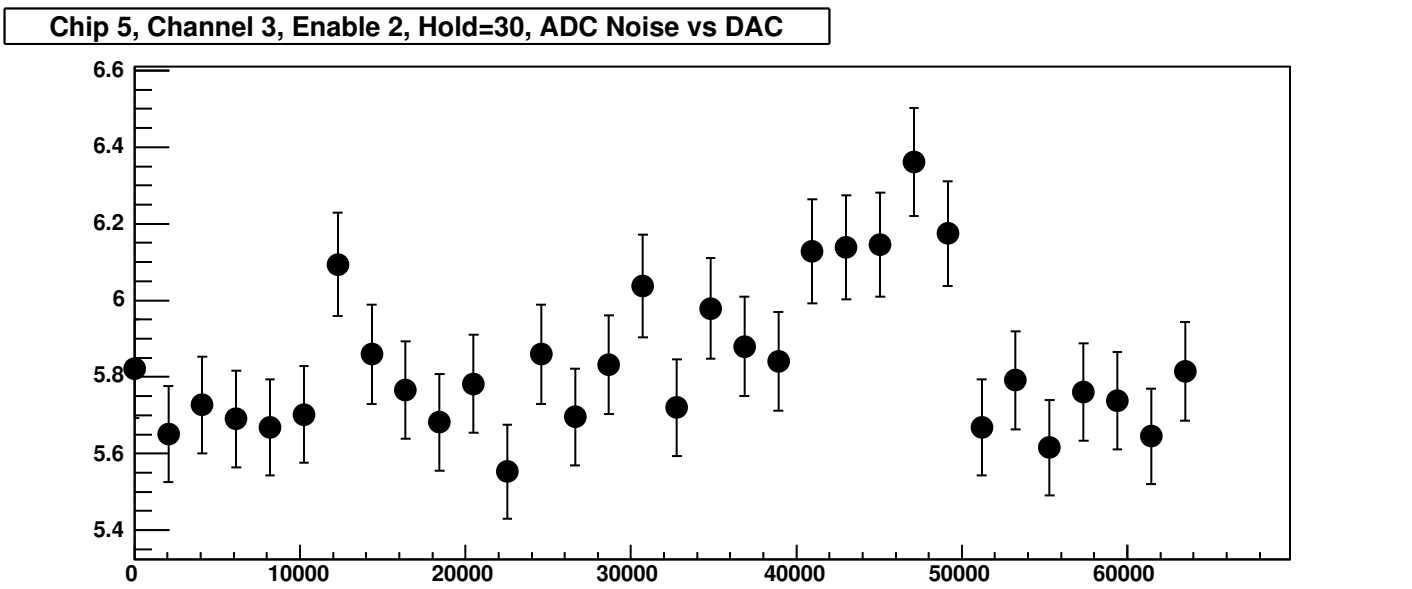
Chip 5, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC

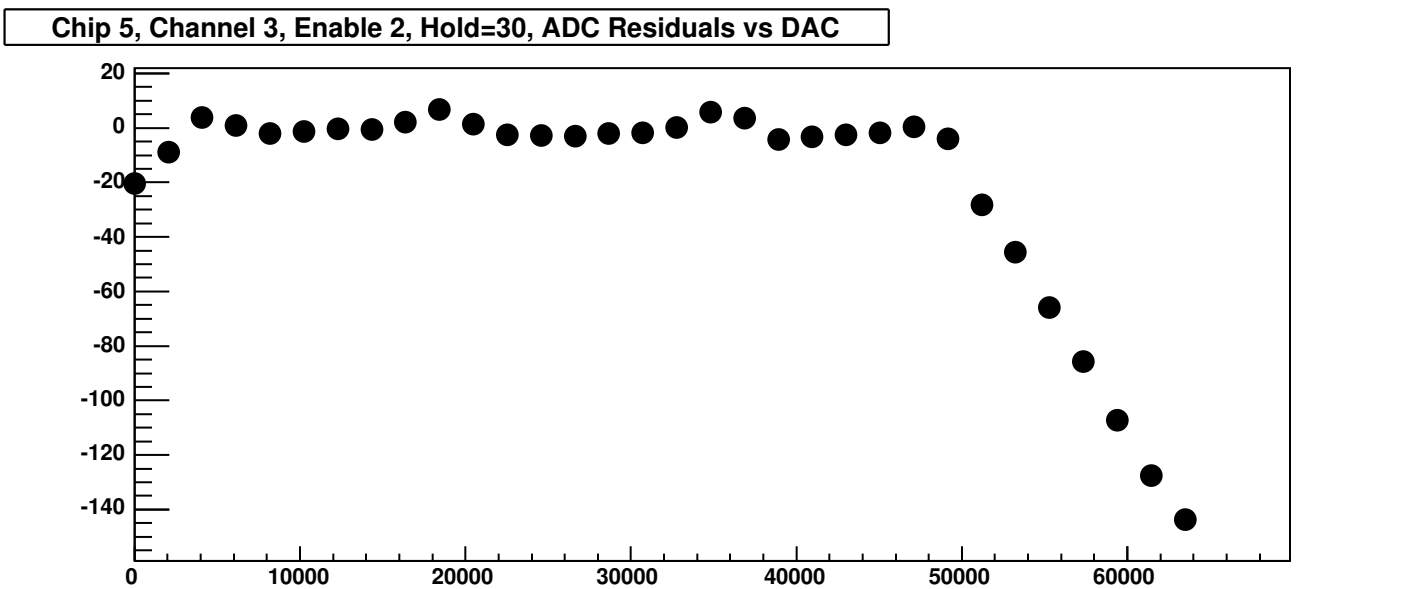


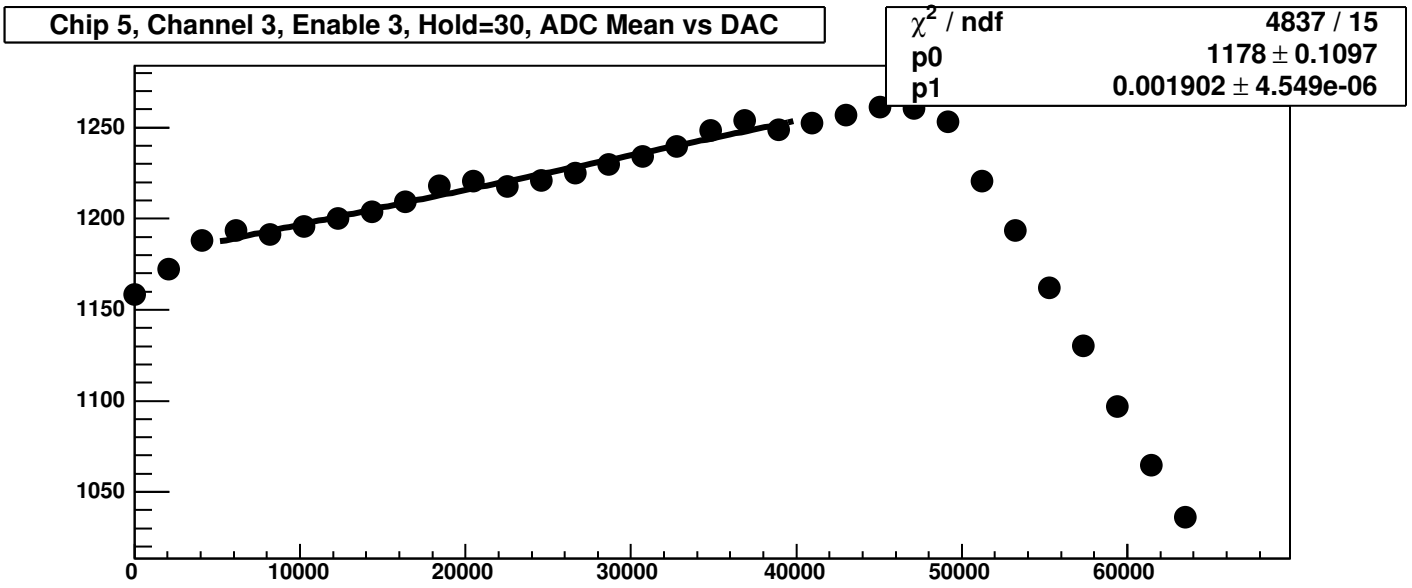
Chip 5, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC

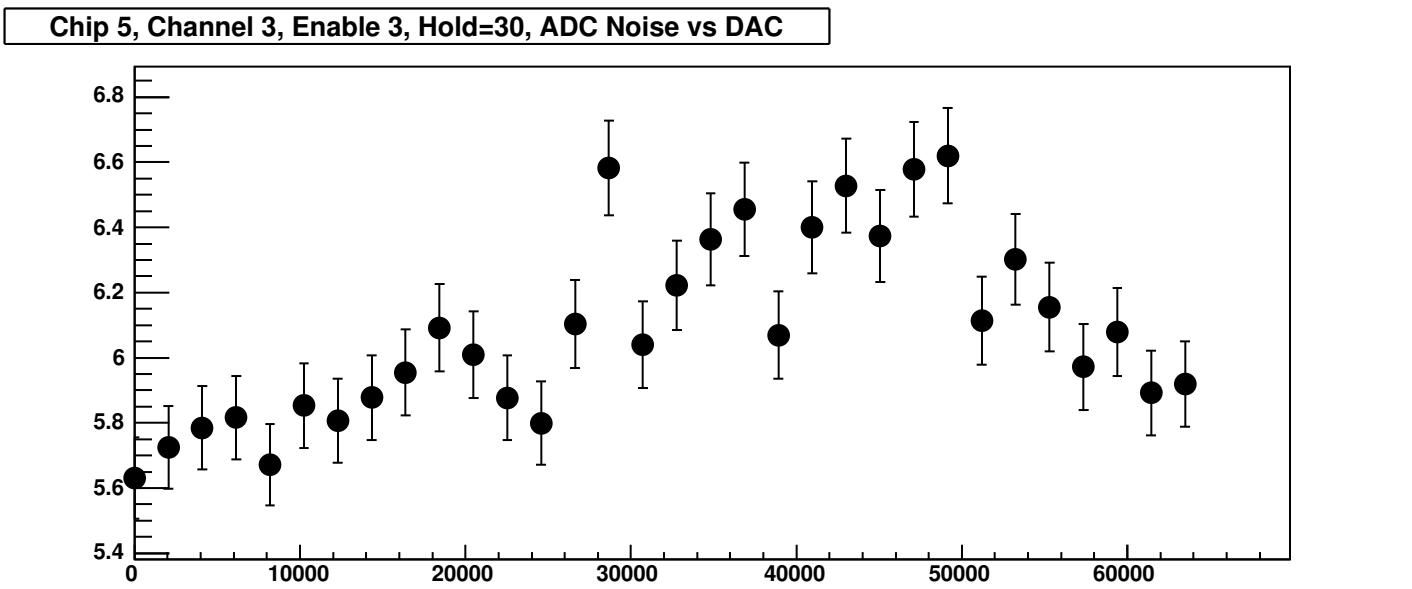


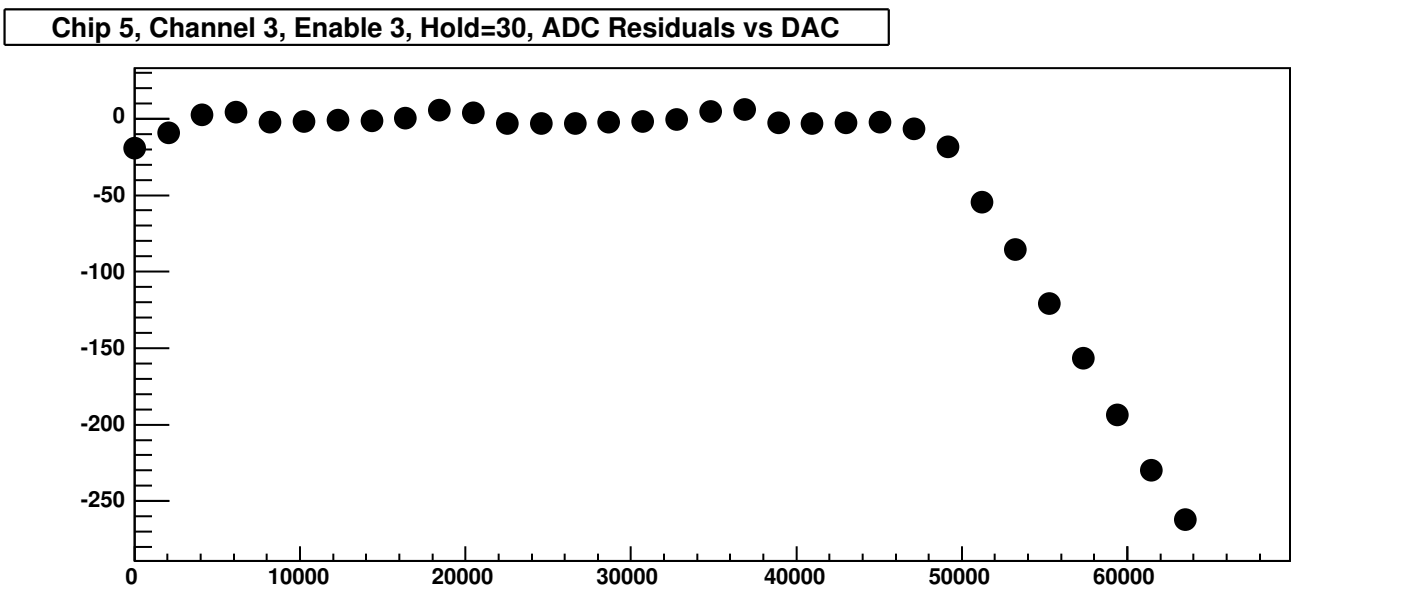

Chip 5, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4728 / 15
p0 1179 ± 0.1076
p1 0.001726 ± 4.388e-06
1260
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

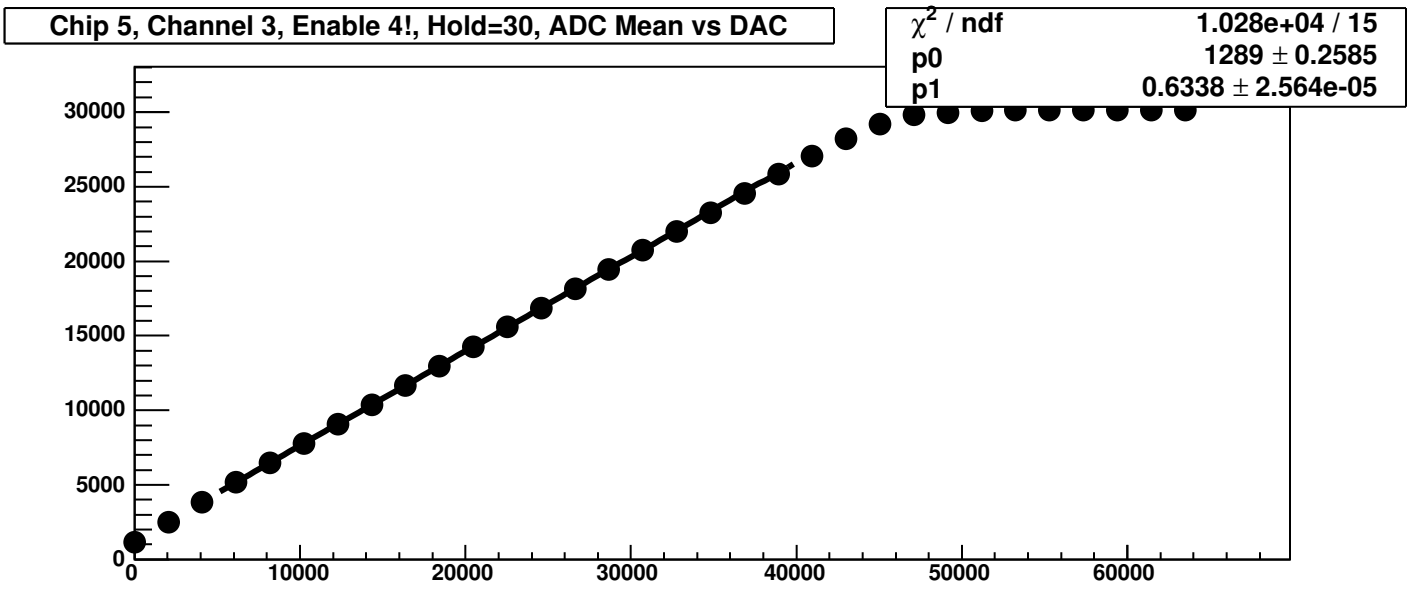

Chip 5, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

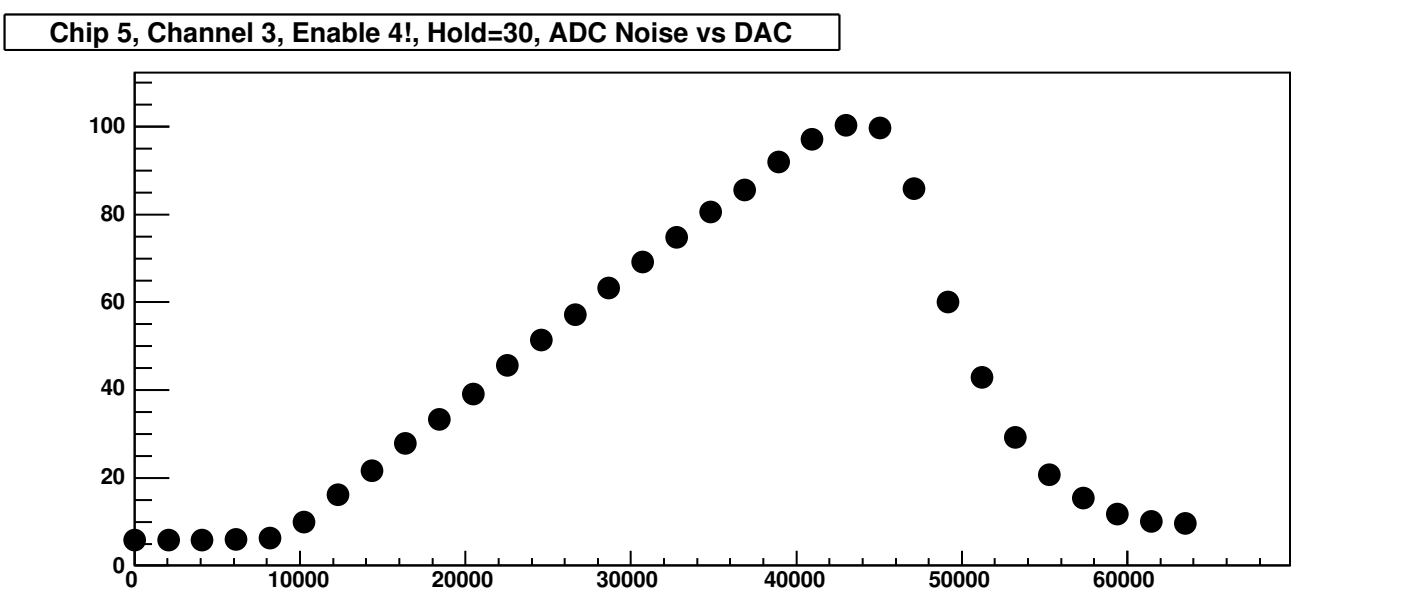

Chip 5, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

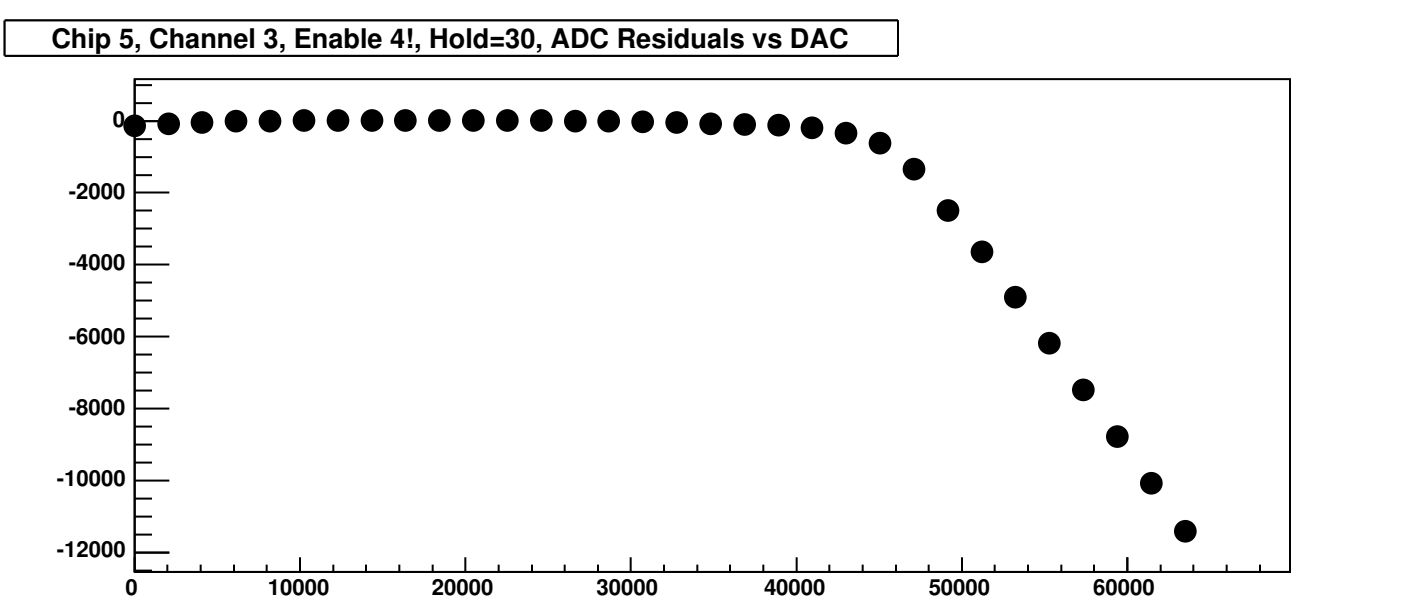

Chip 5, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4837 / 15
p0 1178 ± 0.1097
p1 0.001902 ± 4.549e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

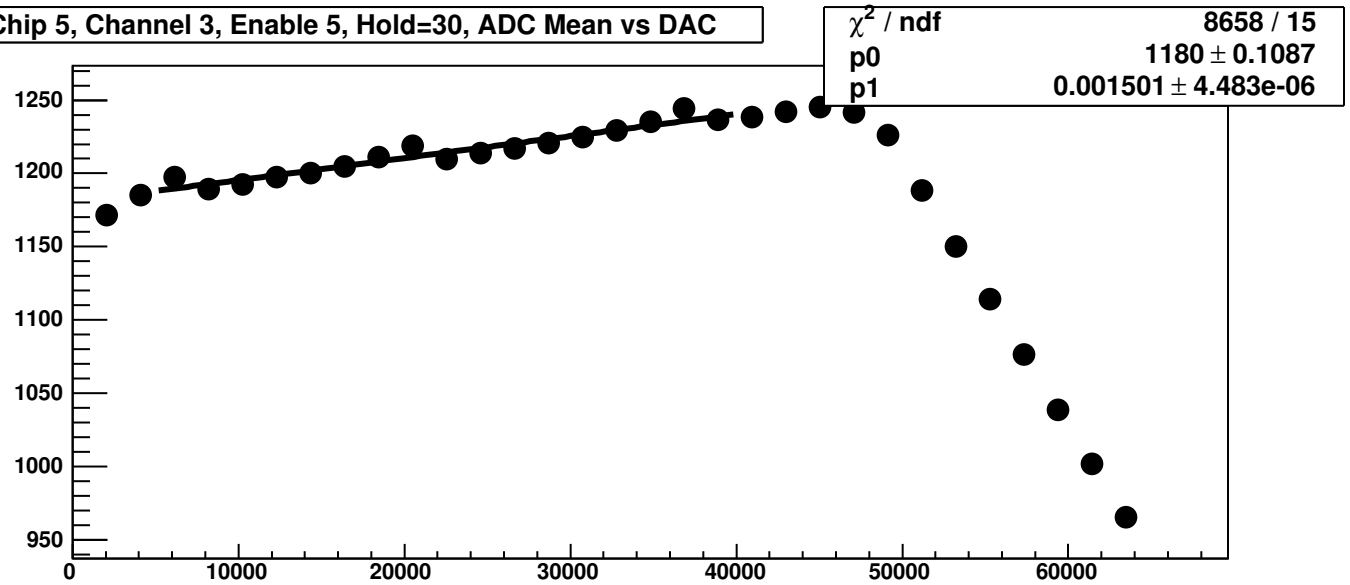

Chip 5, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.028e+04 / 15
p0 1289 ± 0.2585
p1 0.6338 ± 2.564e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

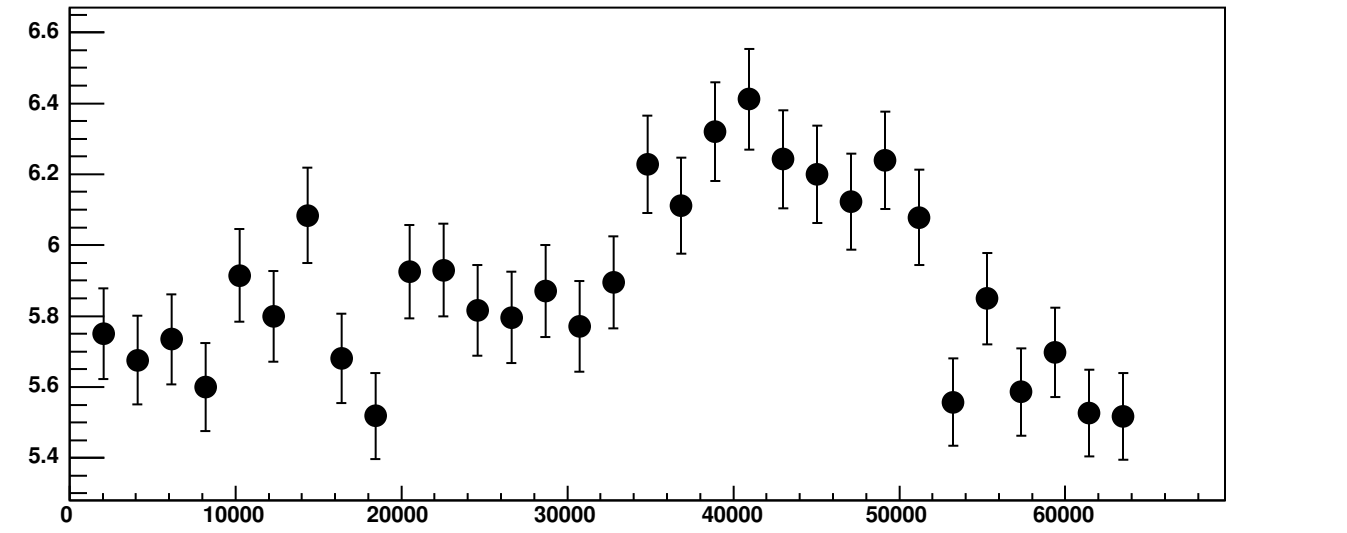

Chip 5, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

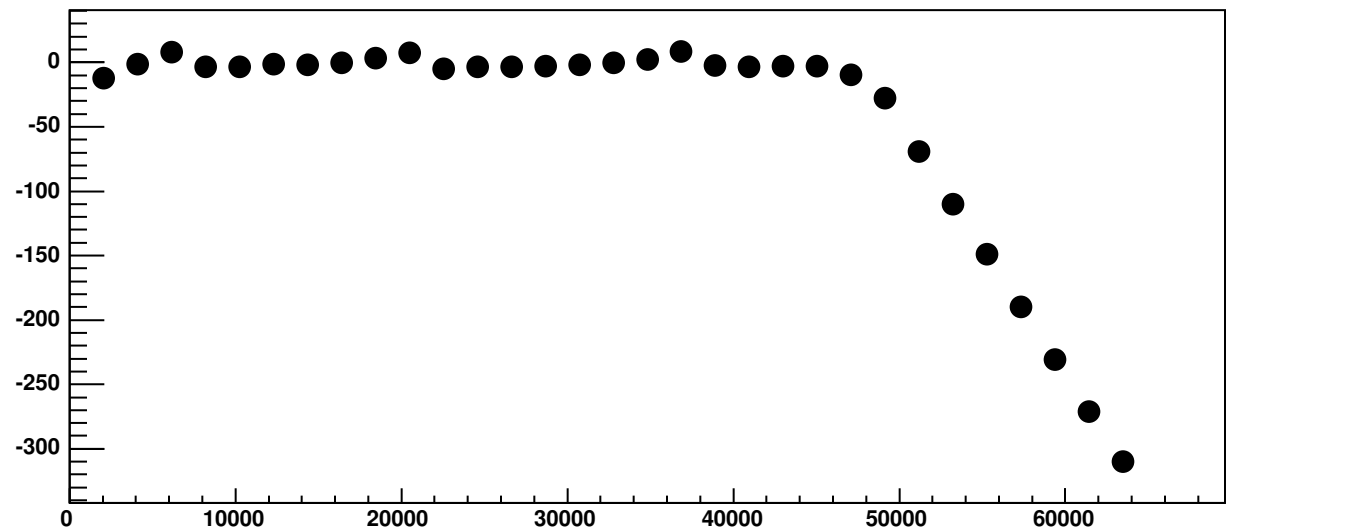
Chip 5, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC

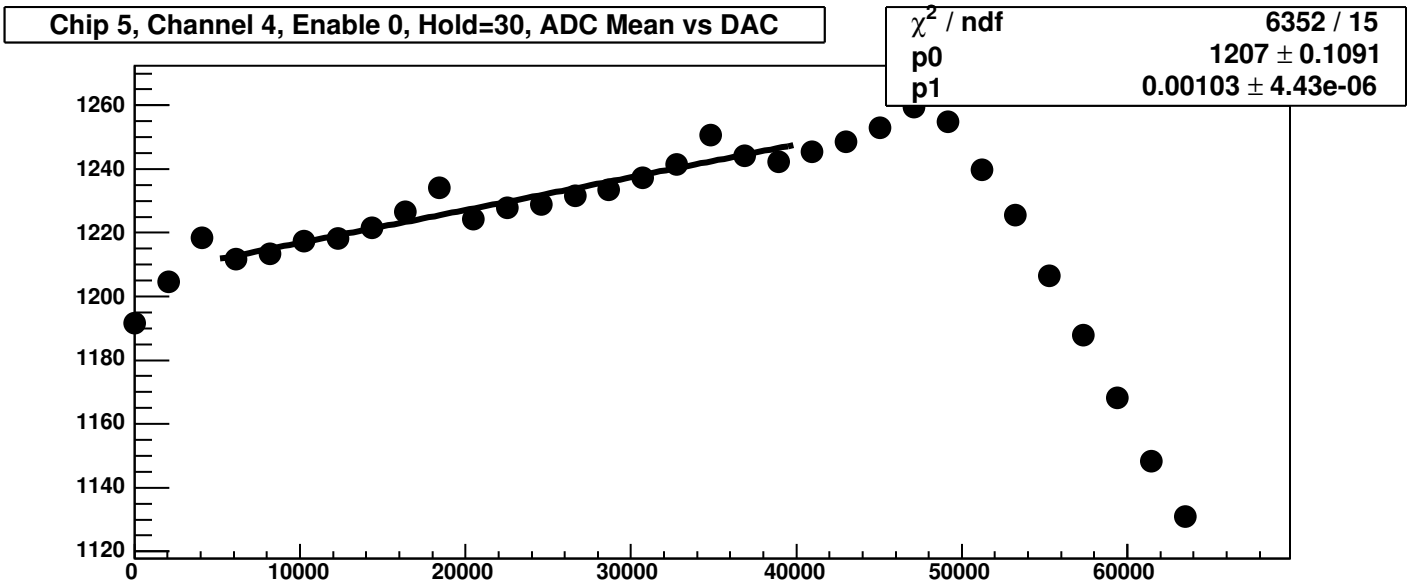


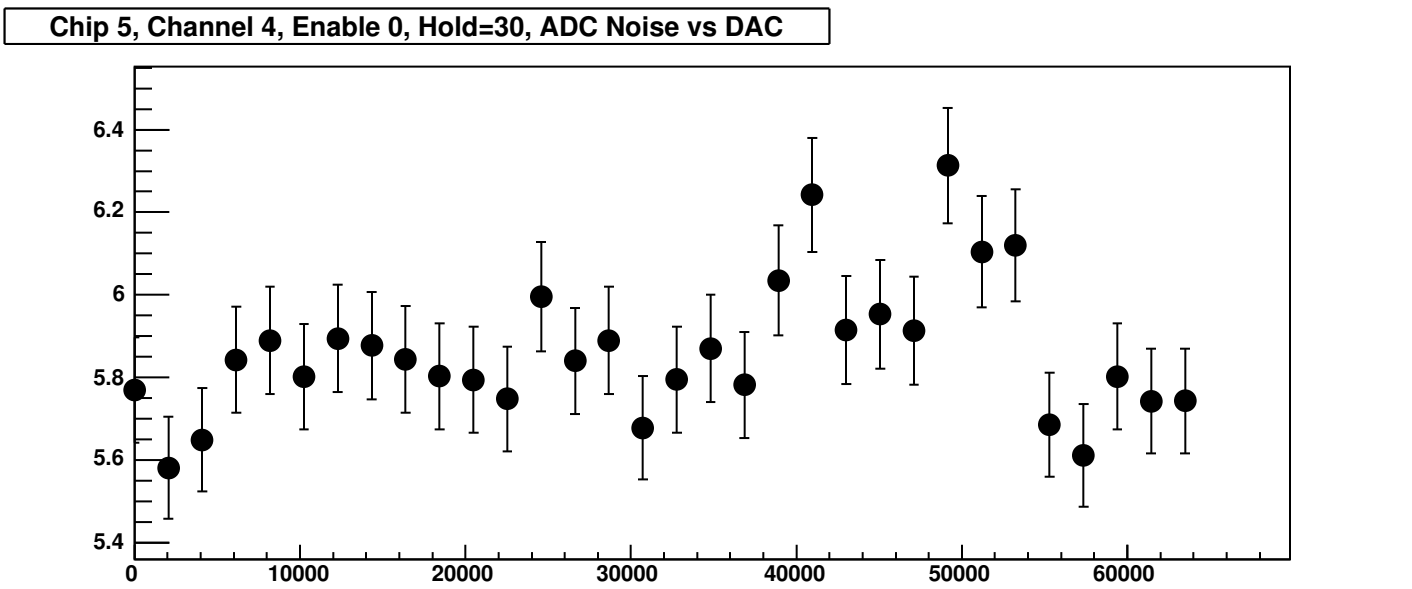
Chip 5, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC

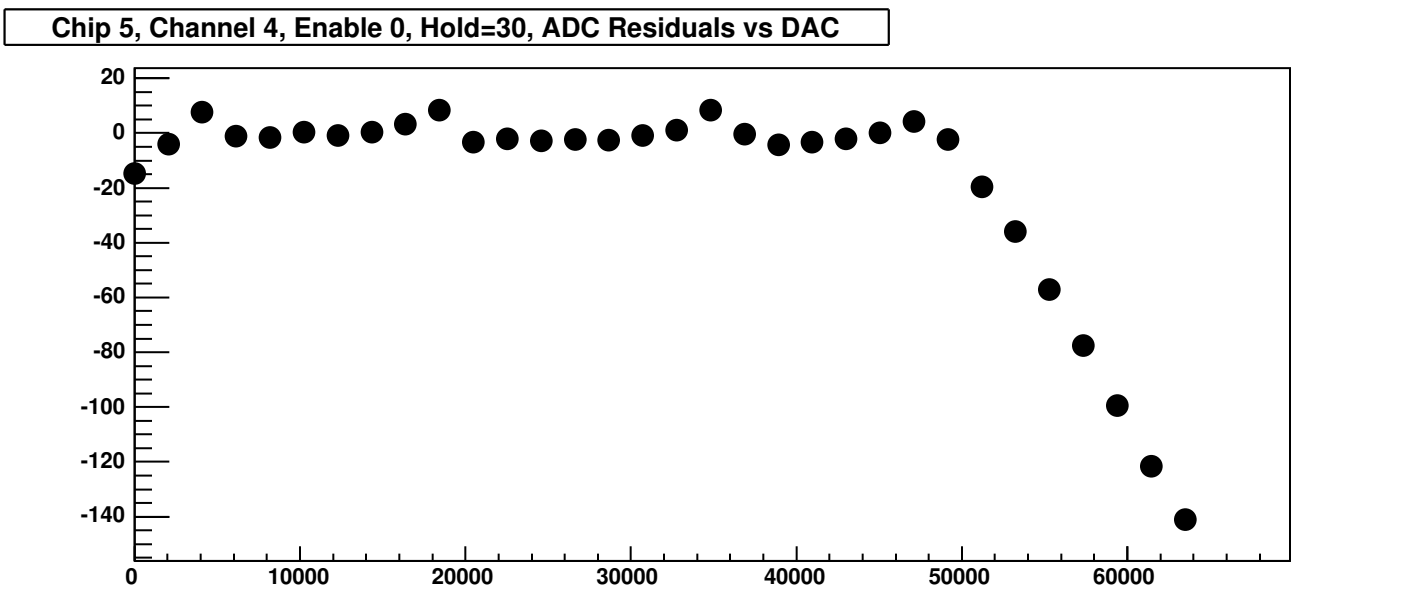


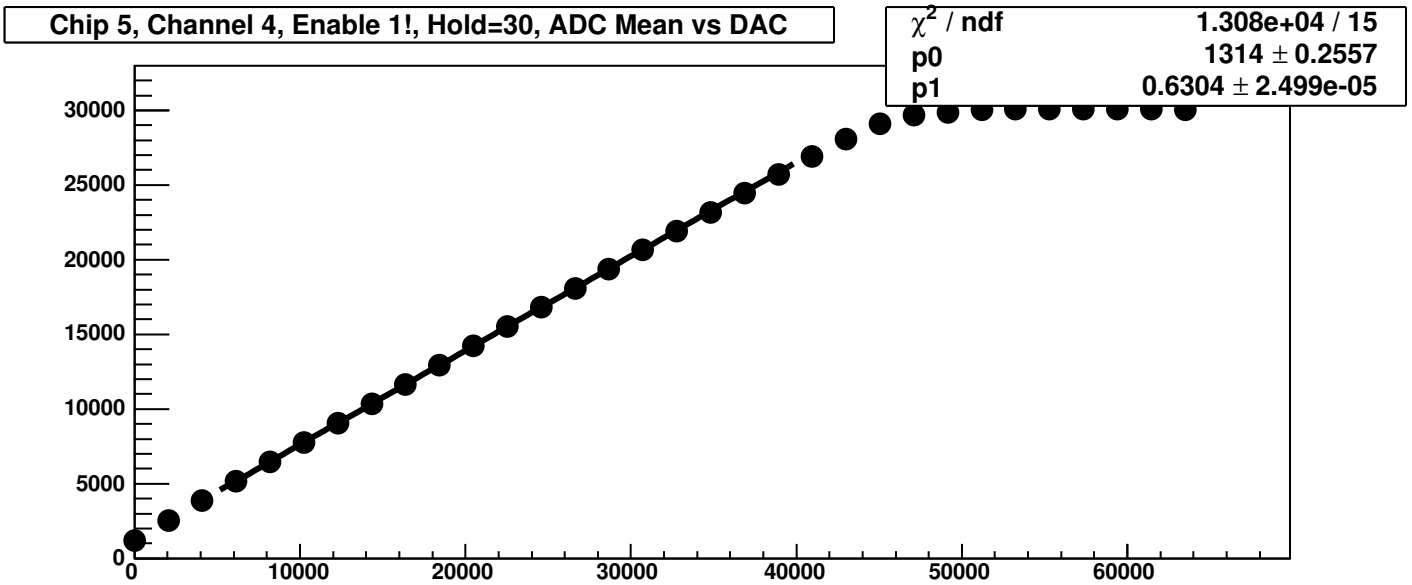
Chip 5, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC

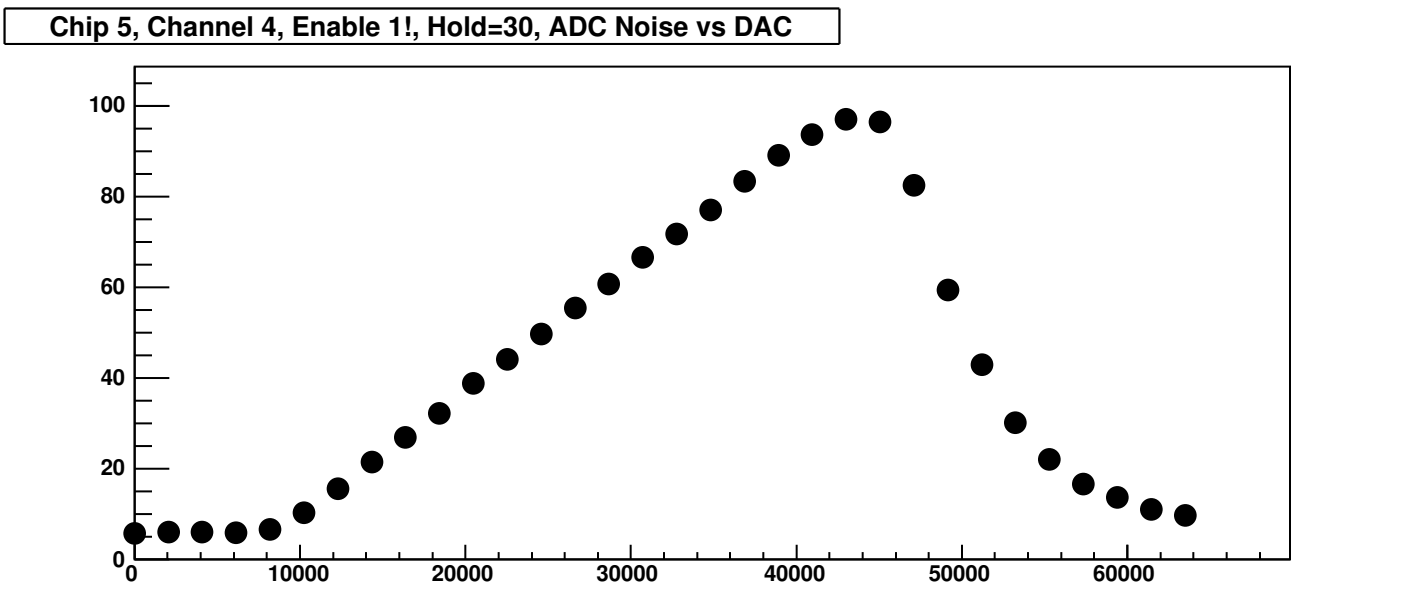


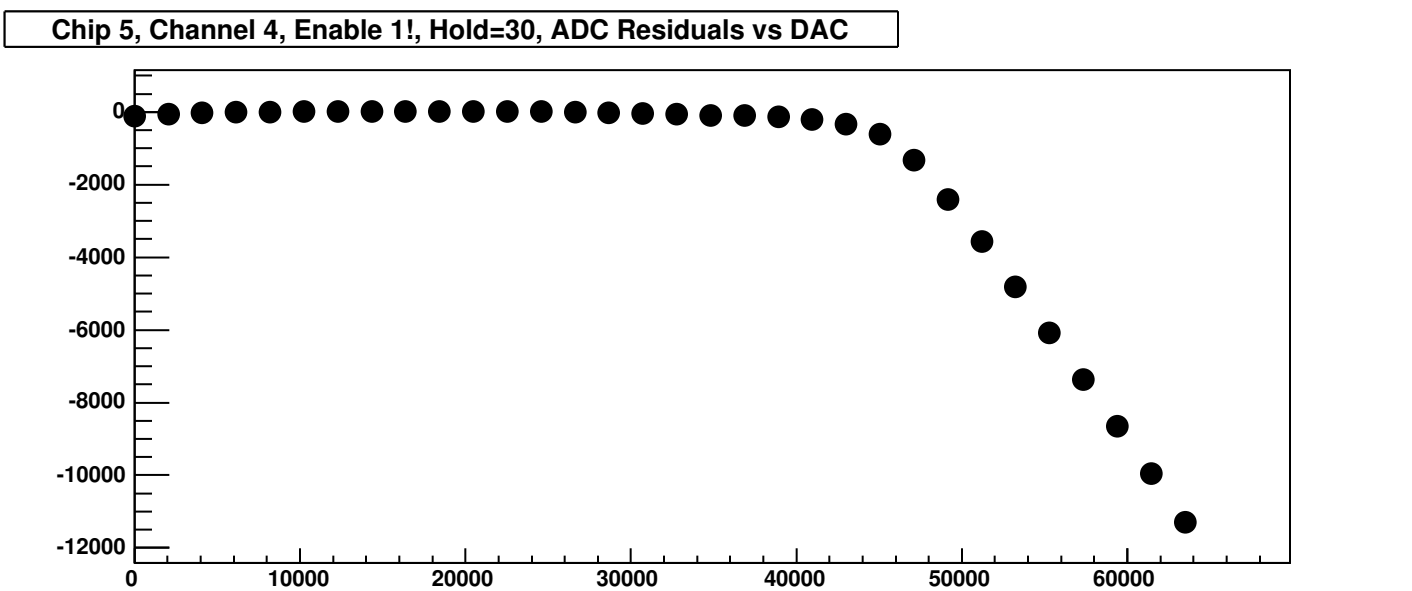

Chip 5, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6352 / 15
p0 1207 ± 0.1091
p1 0.00103 ± 4.43e-06
1260
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000

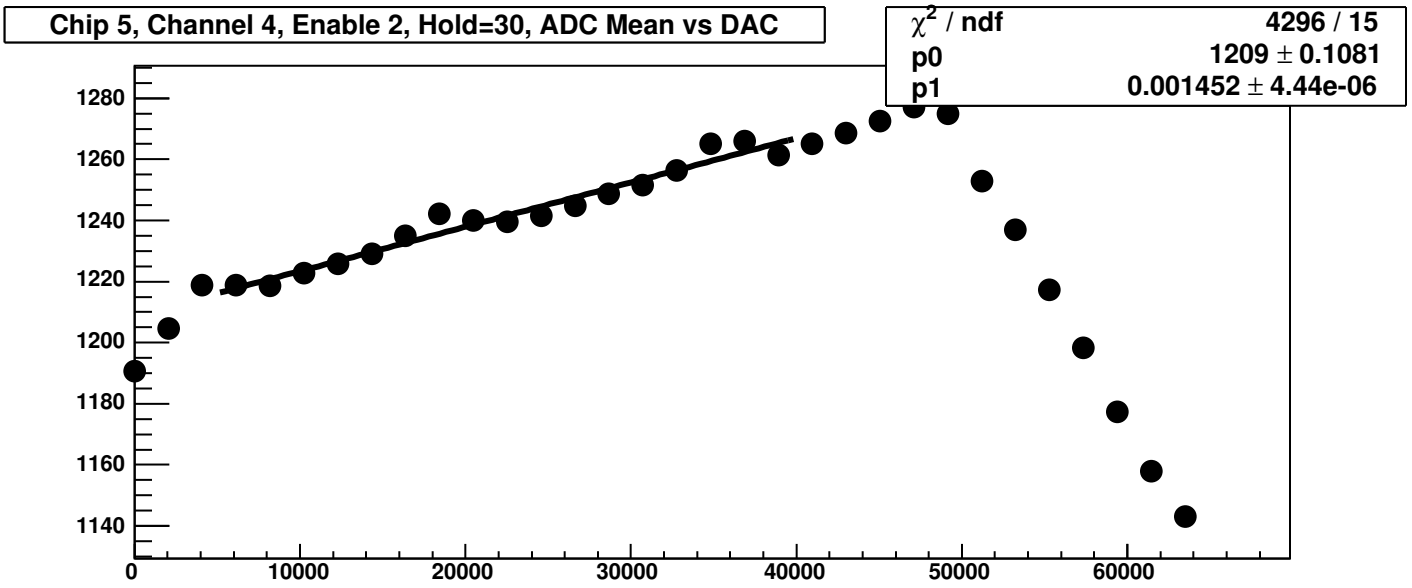

Chip 5, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

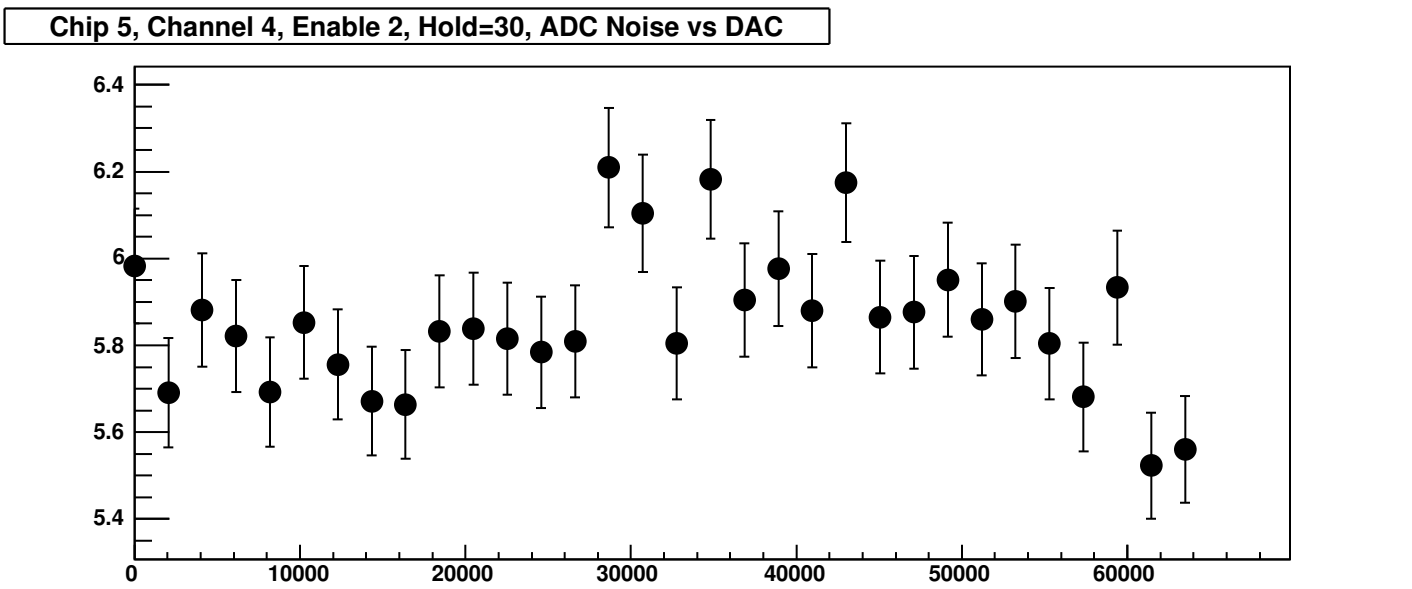

Chip 5, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

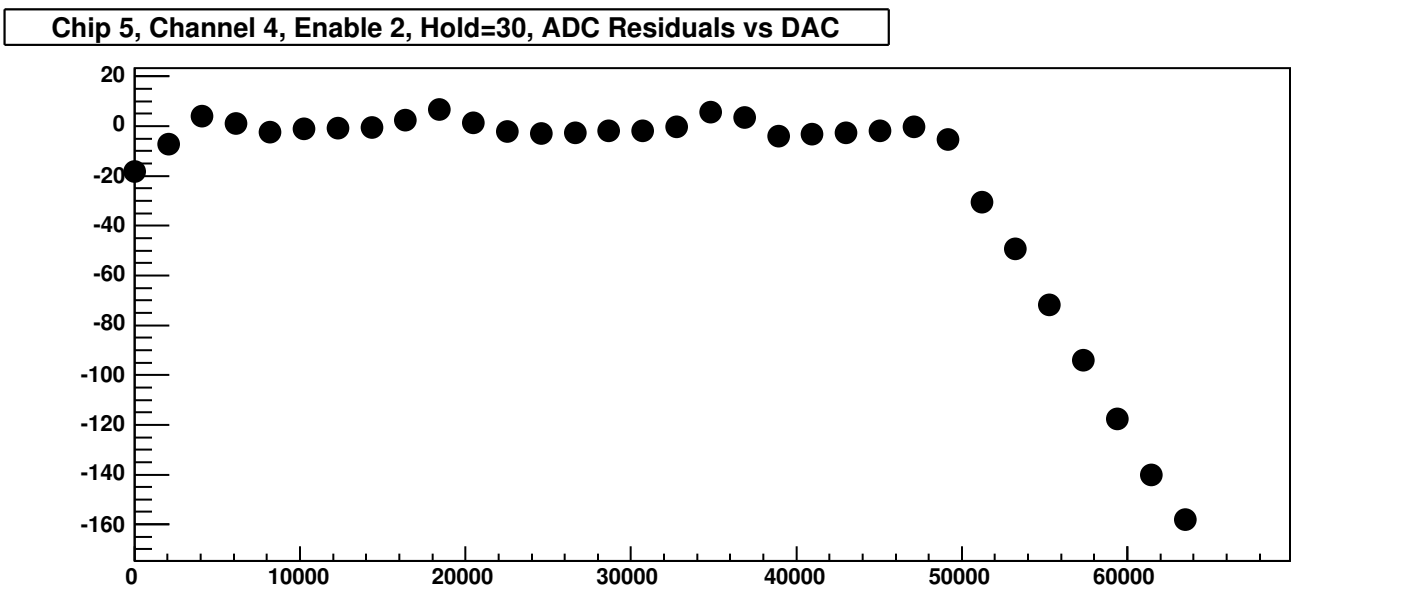

Chip 5, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.308e+04 / 15
p0 1314 ± 0.2557
p1 0.6304 ± 2.499e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

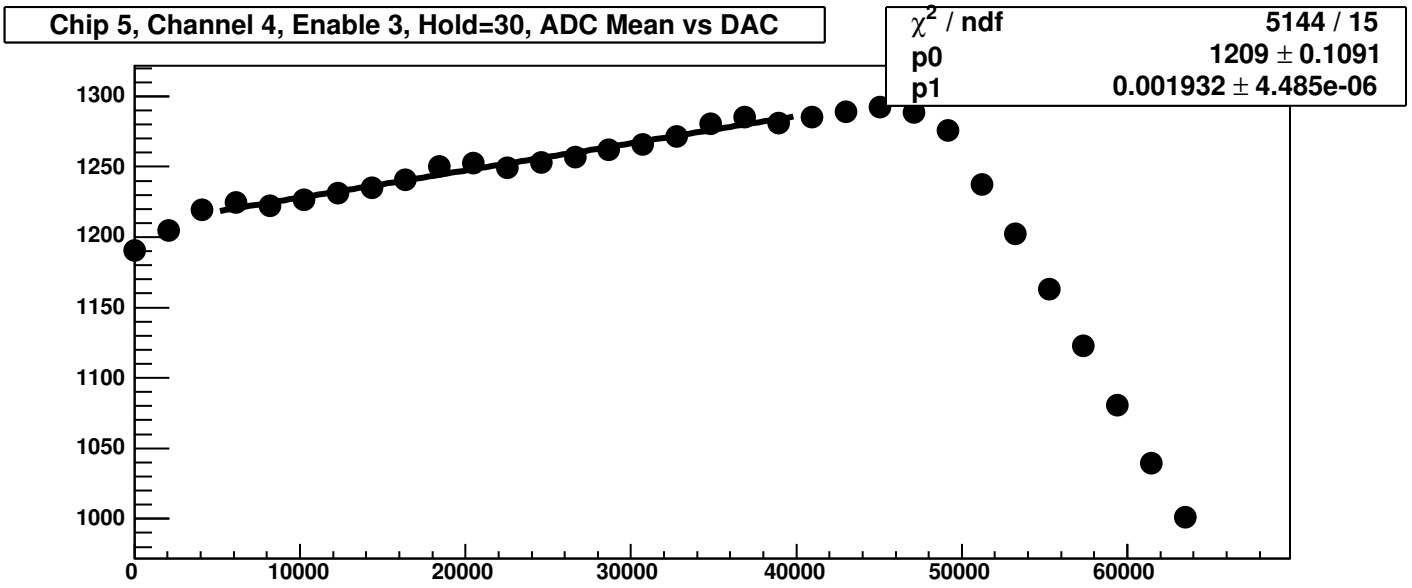

Chip 5, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

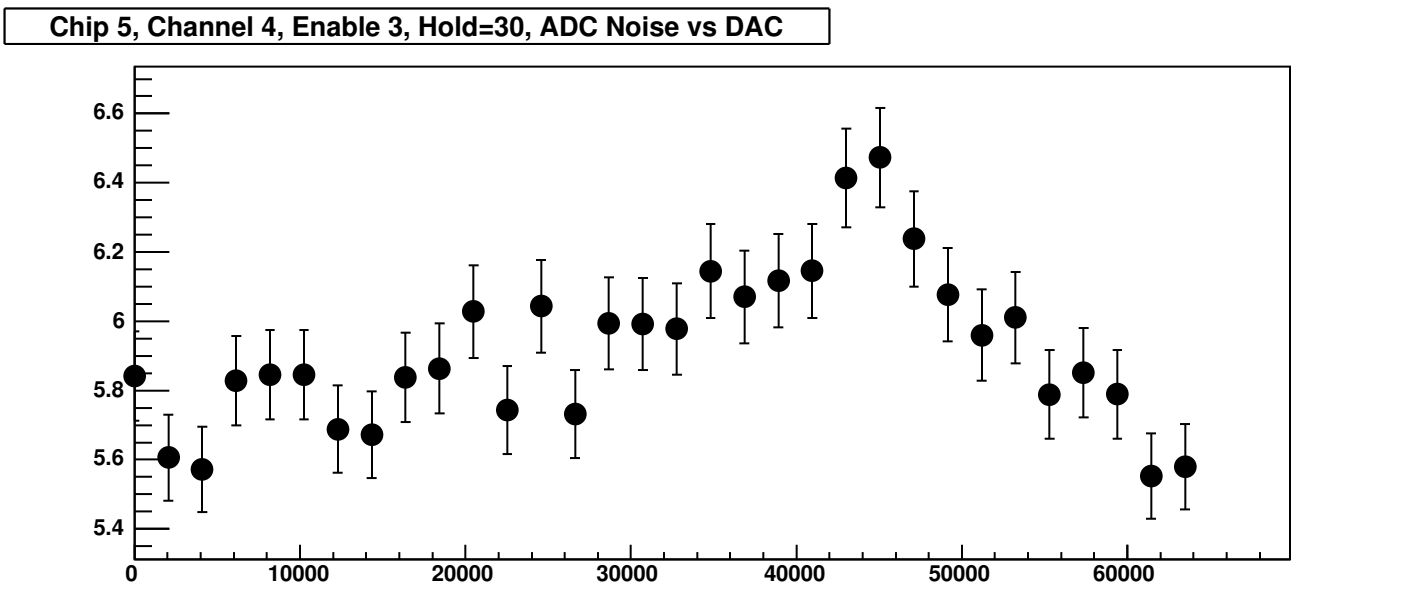

Chip 5, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

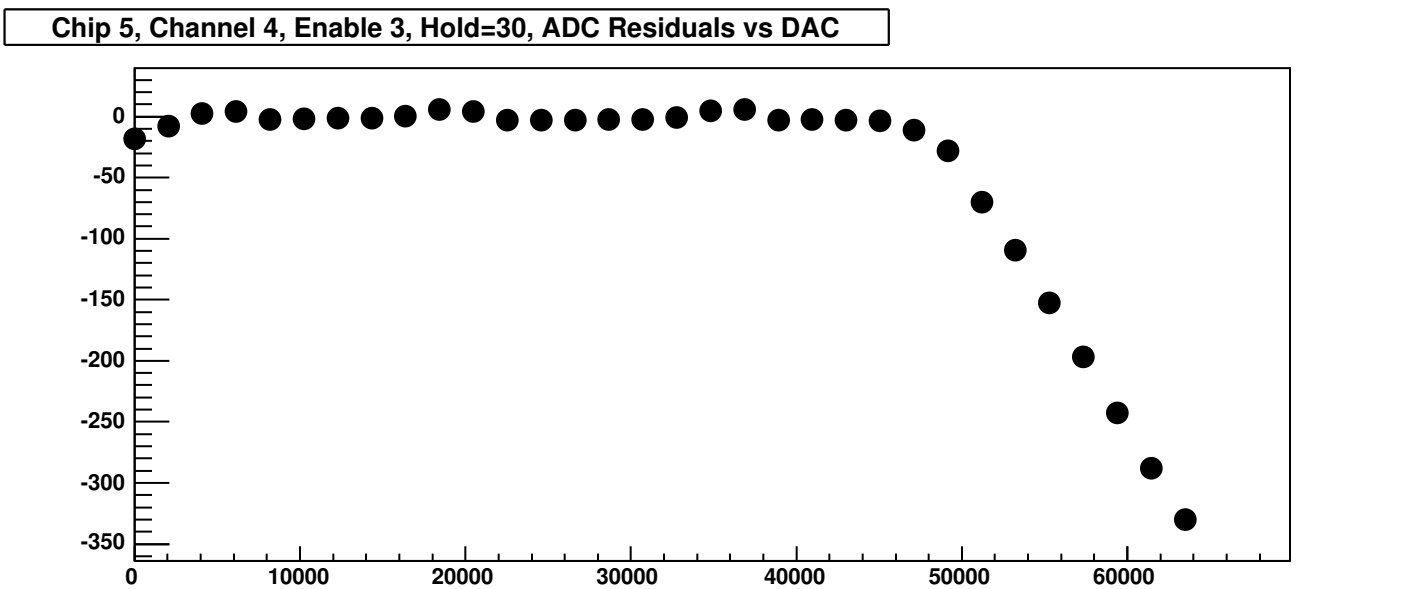

Chip 5, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4296 / 15
p0 1209 ± 0.1081
p1 0.001452 ± 4.44e-06
1280
1260
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

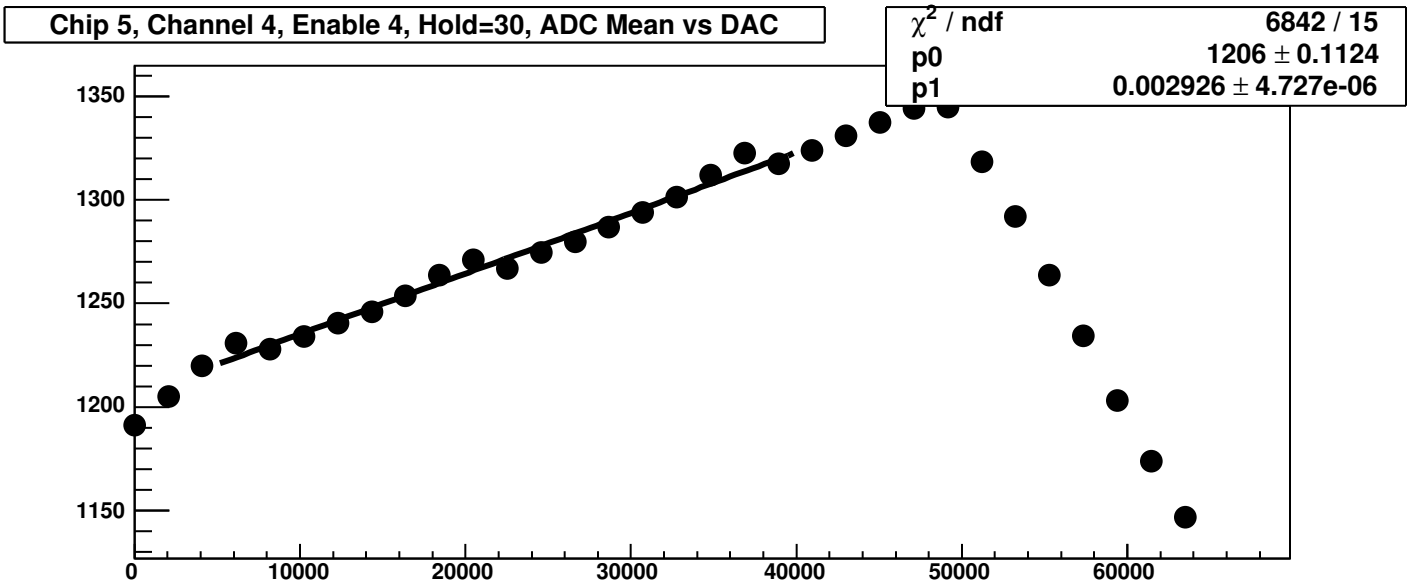

Chip 5, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

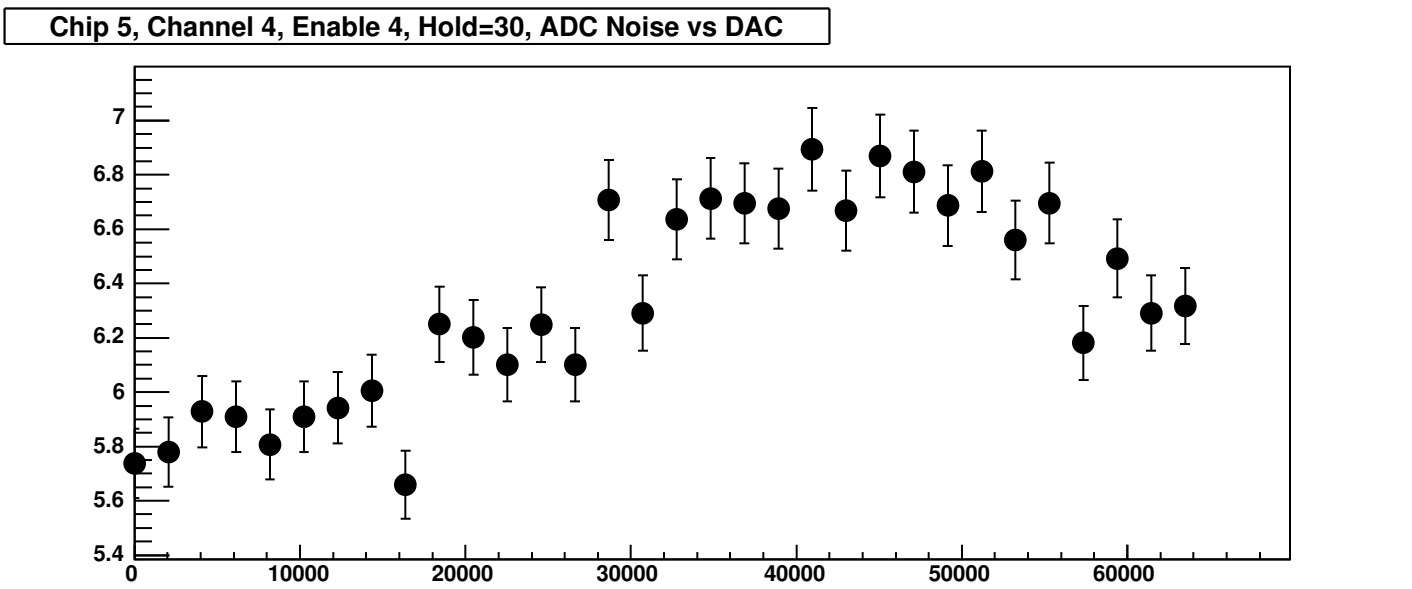

Chip 5, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

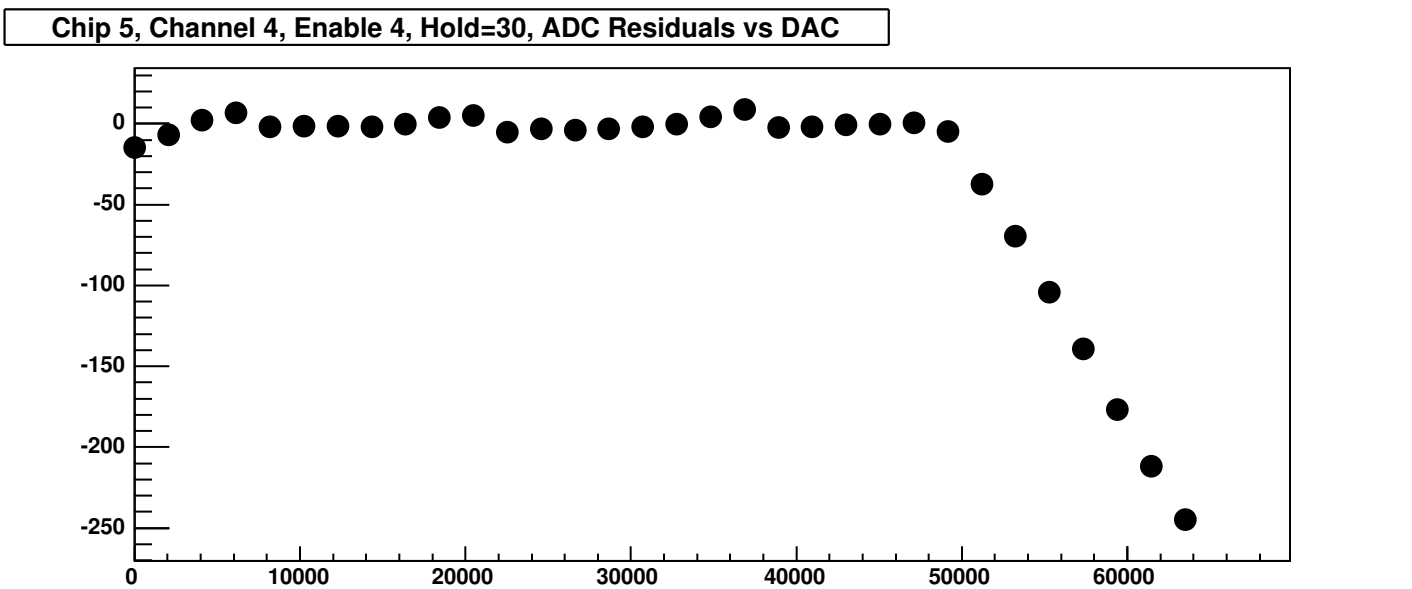

Chip 5, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5144 / 15
p0 1209 ± 0.1091
p1 0.001932 ± 4.485e-06
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

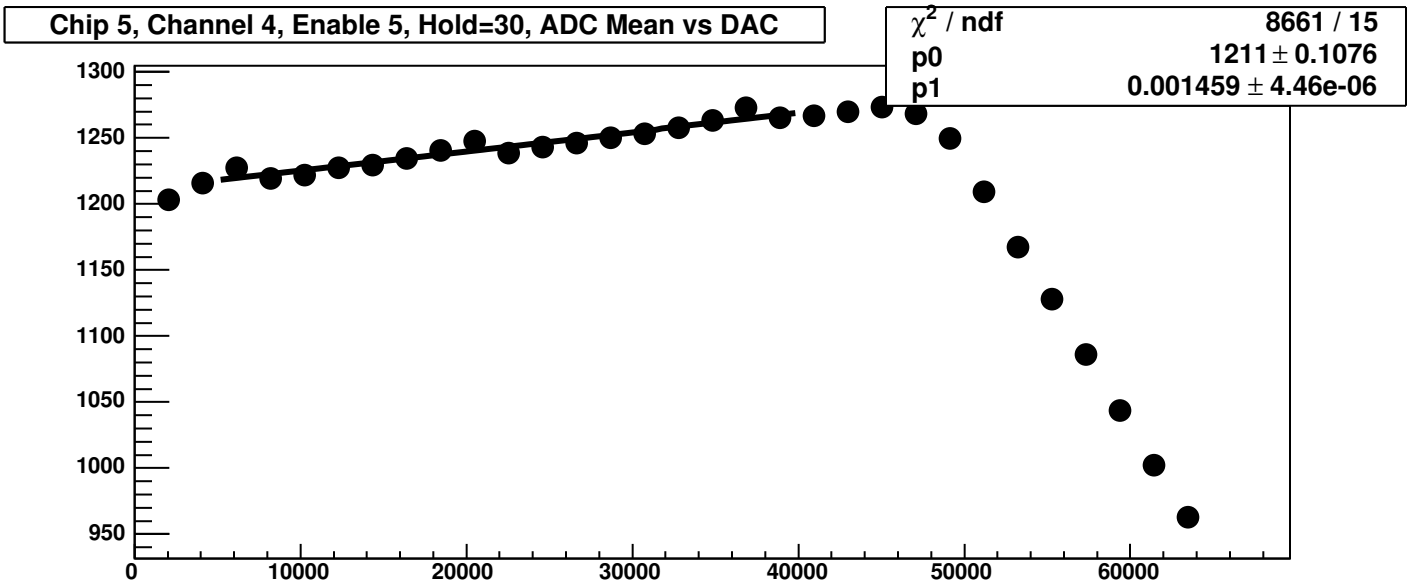

Chip 5, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

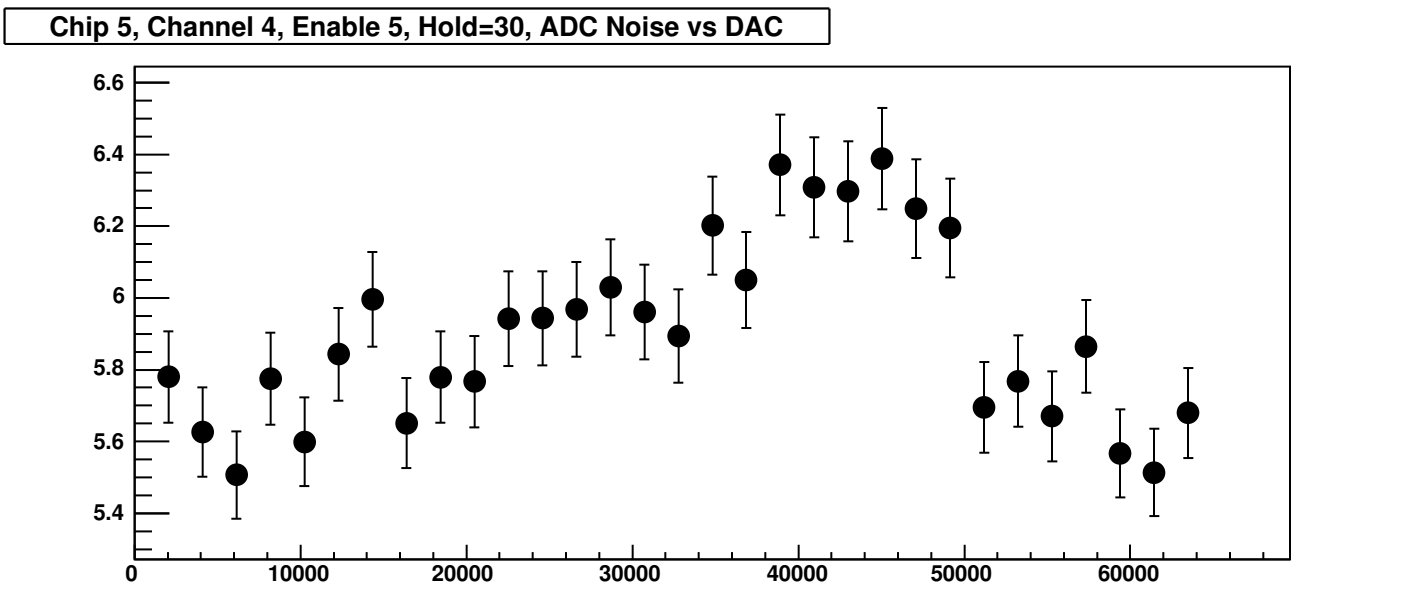

Chip 5, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

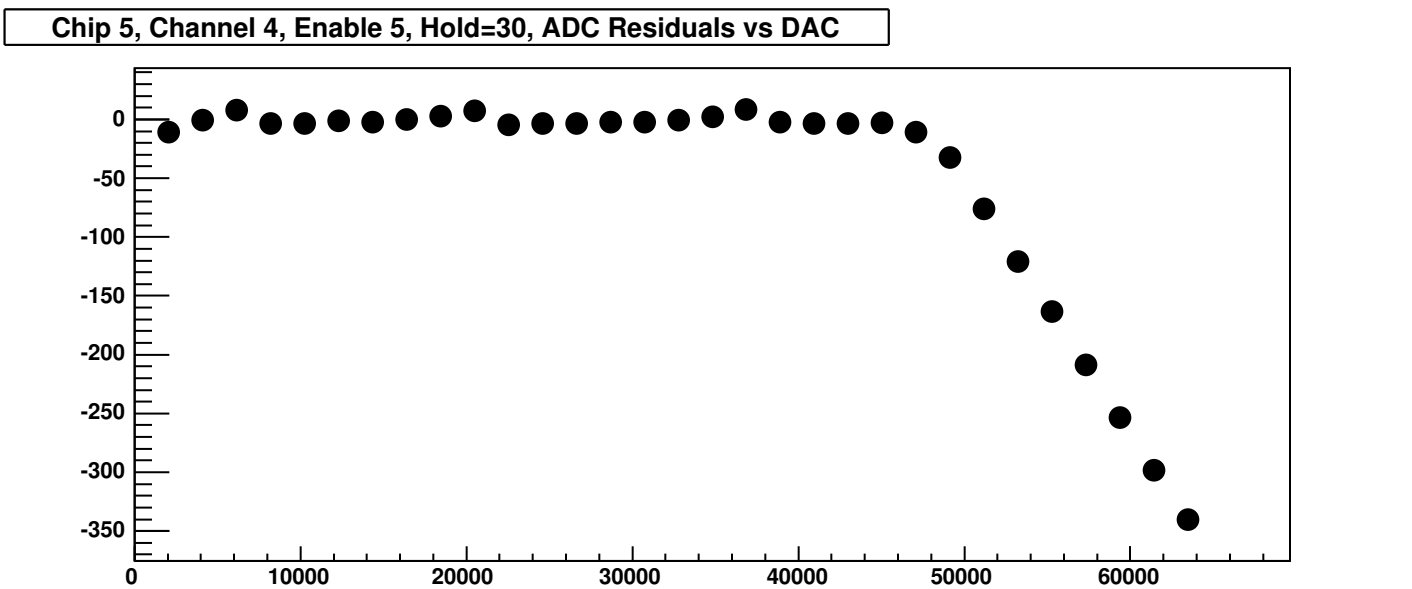

Chip 5, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6842 / 15
p0 1206 ± 0.1124
p1 0.002926 ± 4.727e-06
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

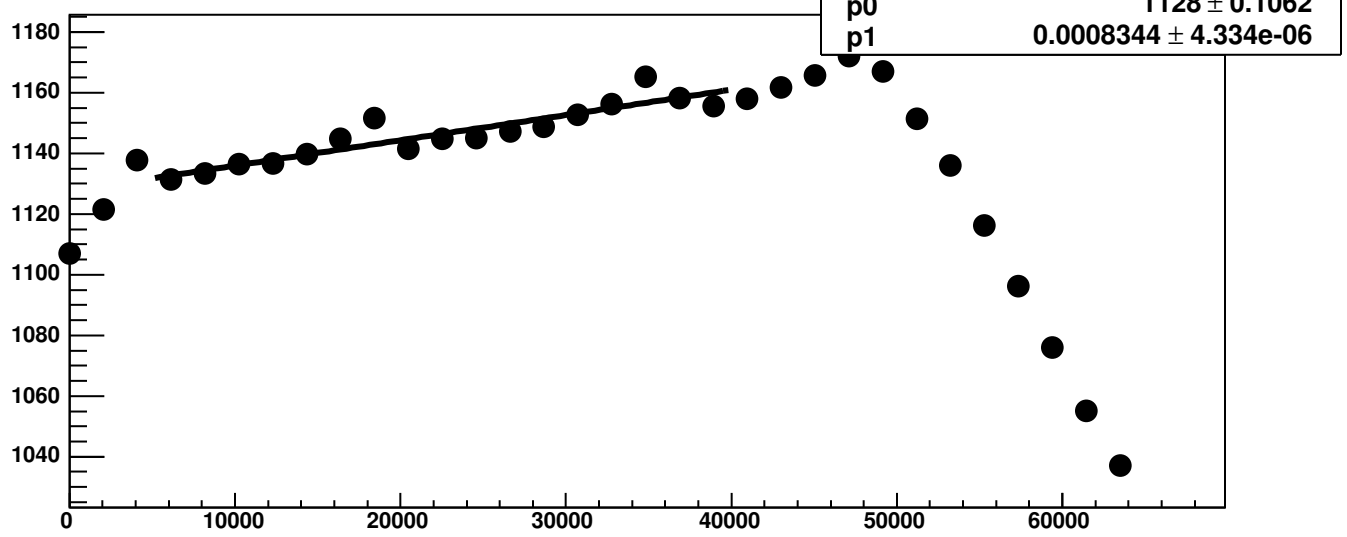

Chip 5, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8661 / 15
p0 1211 ± 0.1076
p1 0.001459 ± 4.46e-06
1300
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

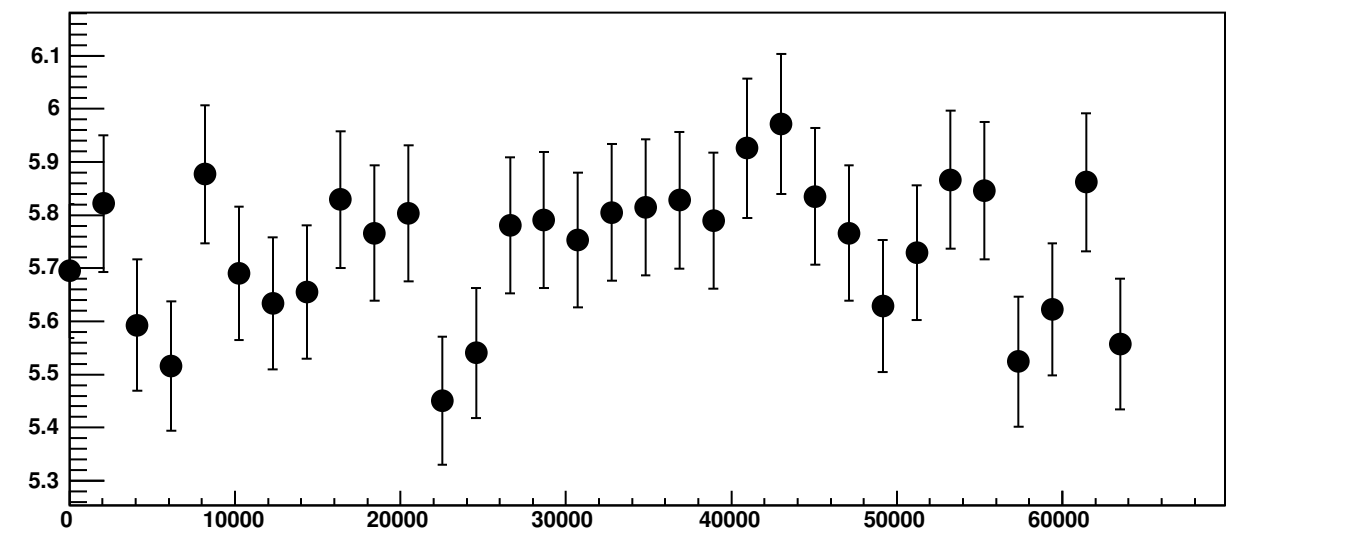

Chip 5, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

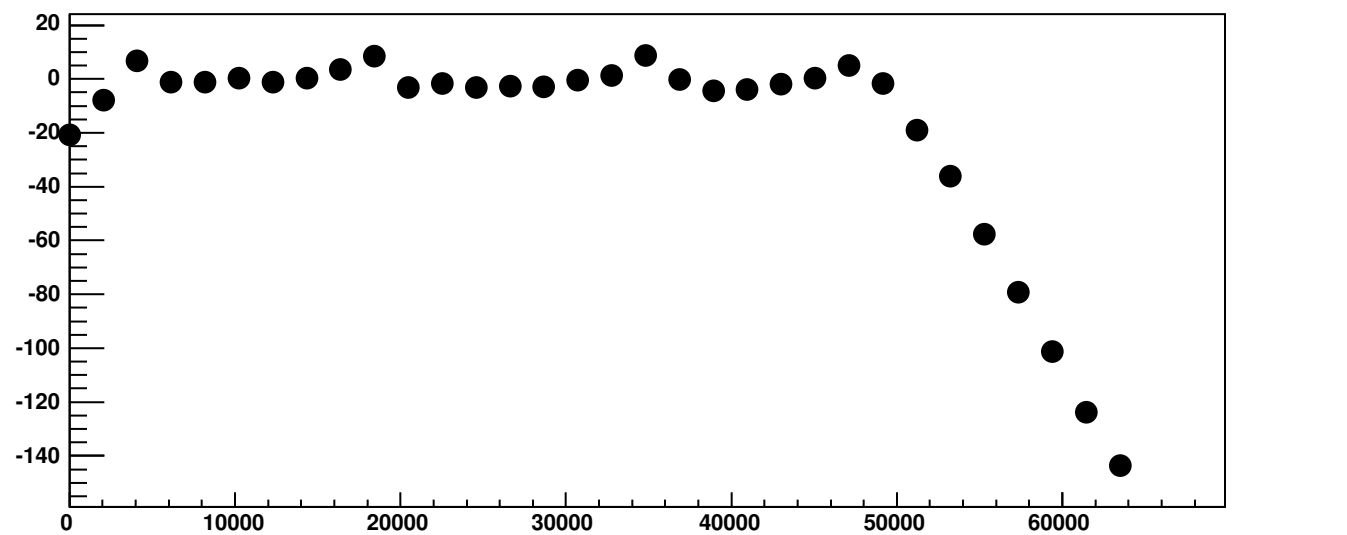
Chip 5, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC

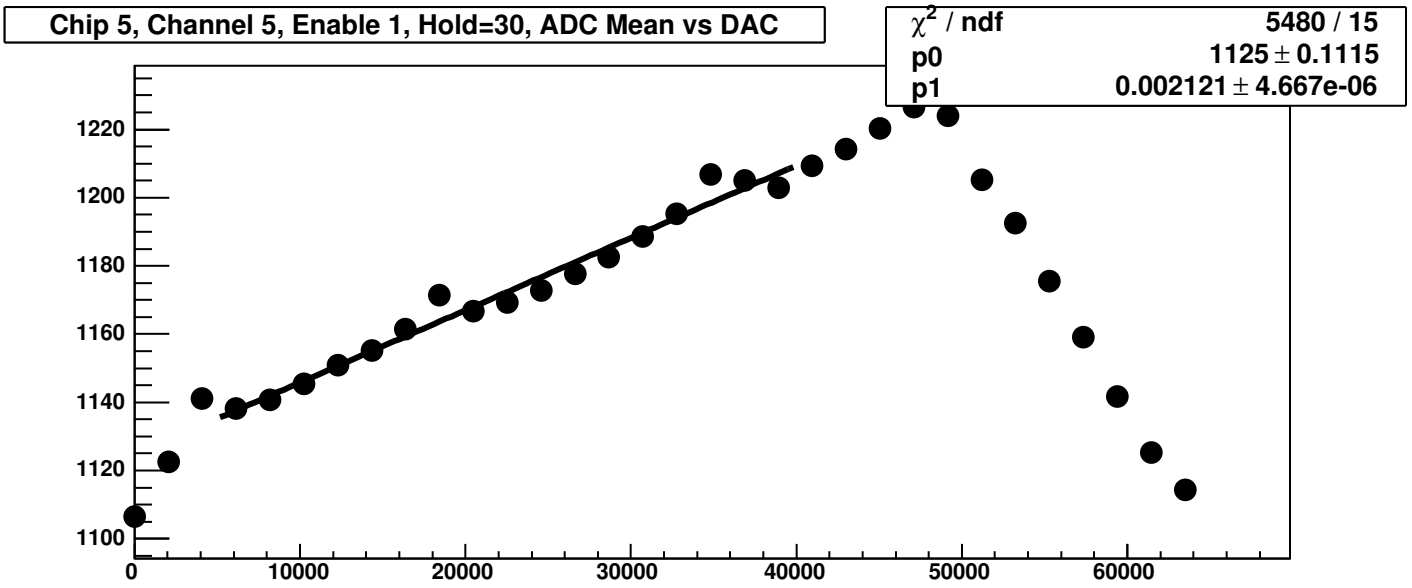


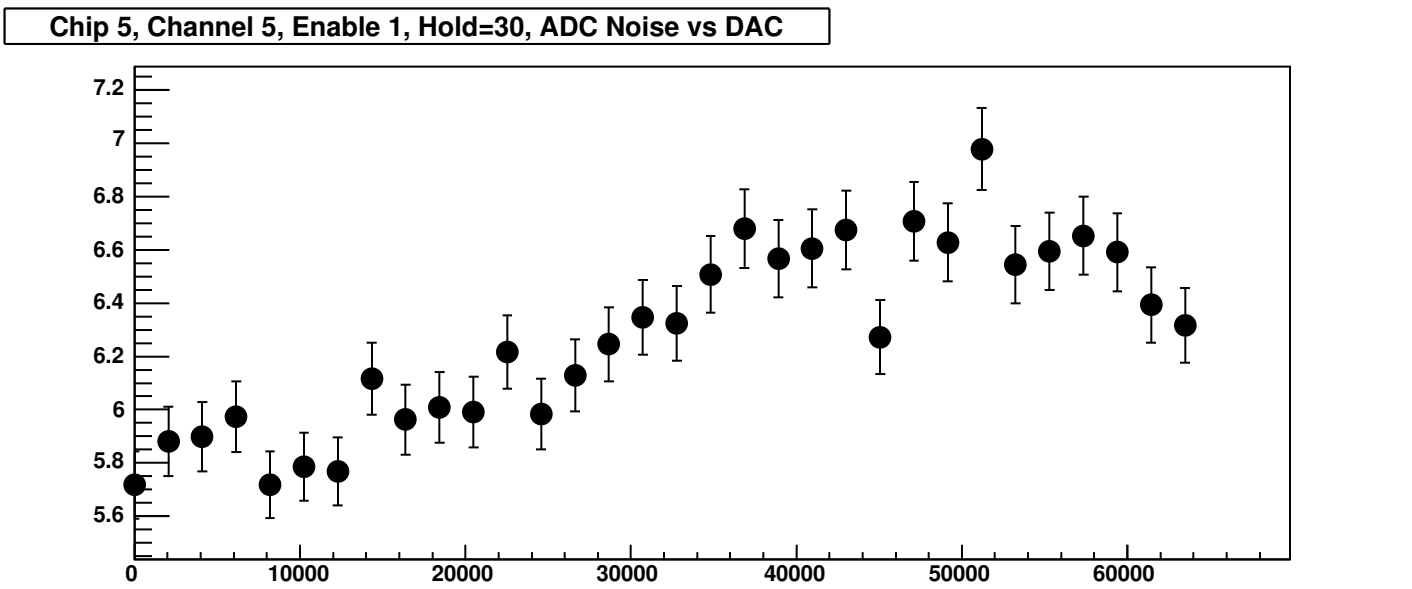
Chip 5, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC

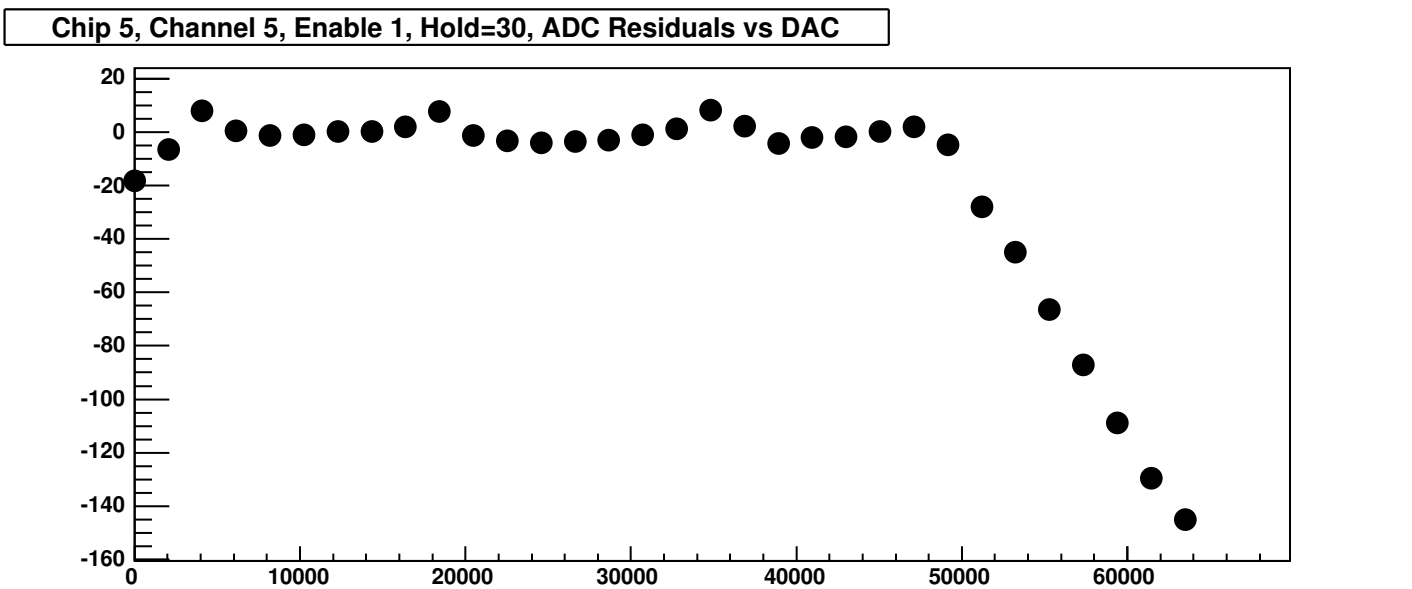


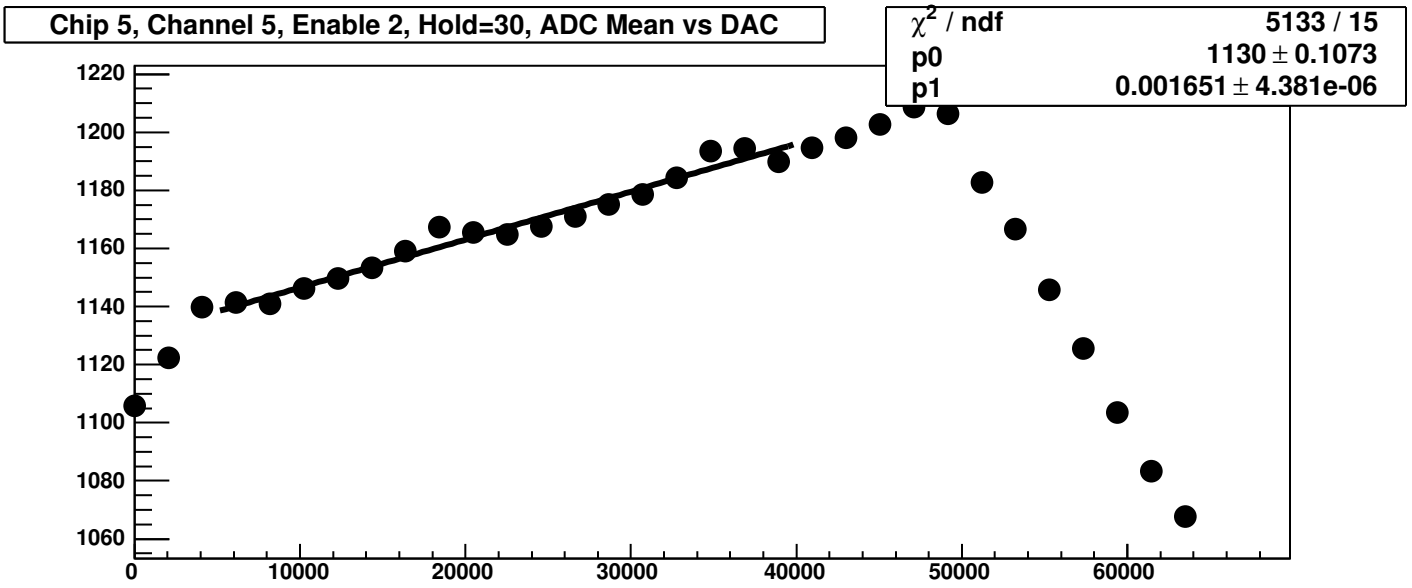
Chip 5, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

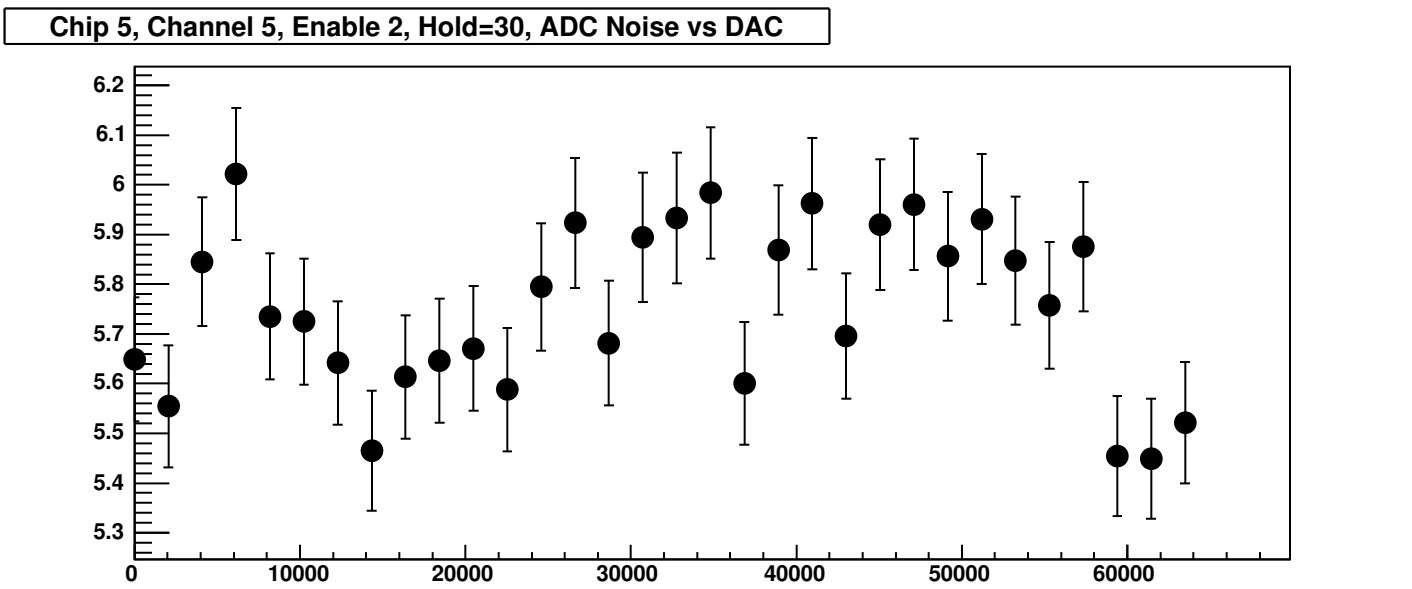


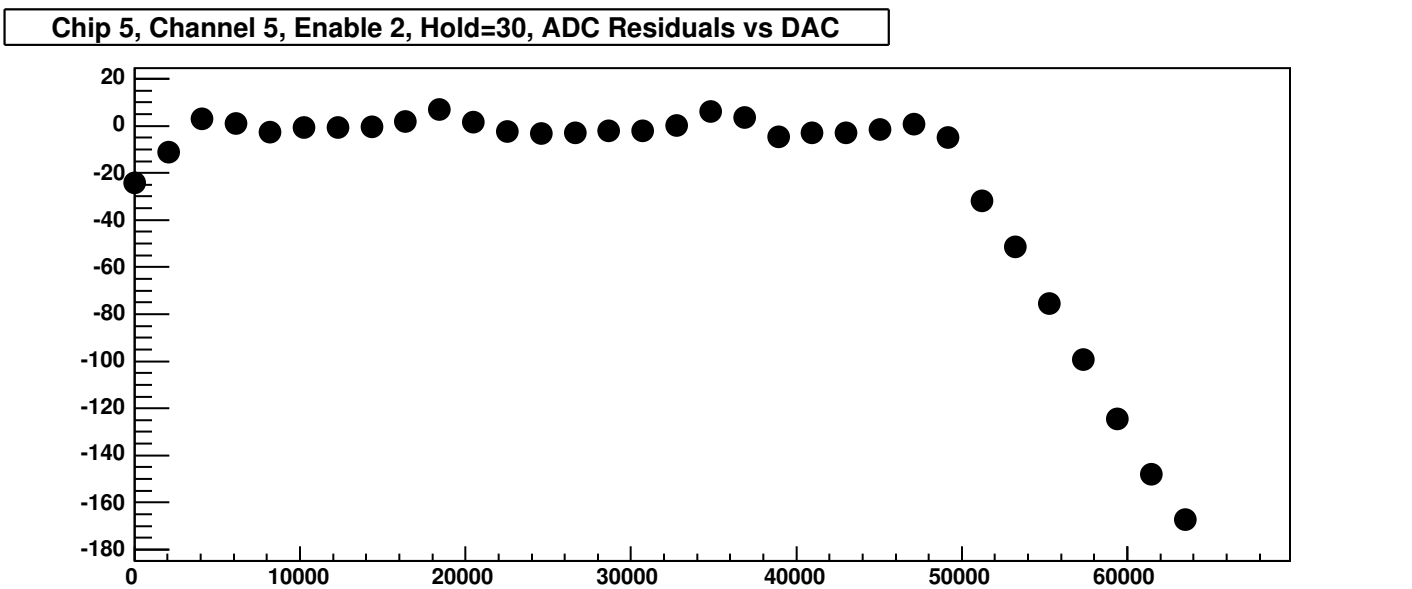

Chip 5, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5480 / 15
p0 1125 ± 0.1115
p1 0.002121 ± 4.667e-06
1220
1200
1180
1160
1140
1120
1100
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

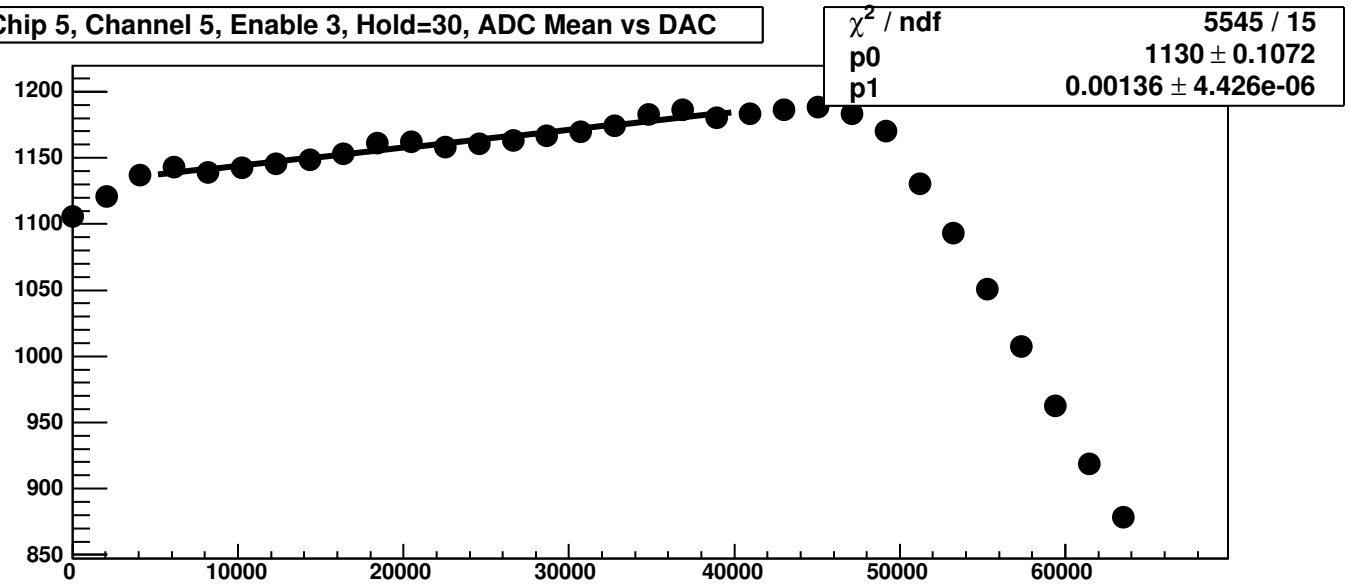

Chip 5, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5133 / 15
p0 1130 ± 0.1073
p1 0.001651 ± 4.381e-06
1220
1200
1180
1160
1140
1120
1100
1080
1060
0
10000
20000
30000
40000
50000
60000

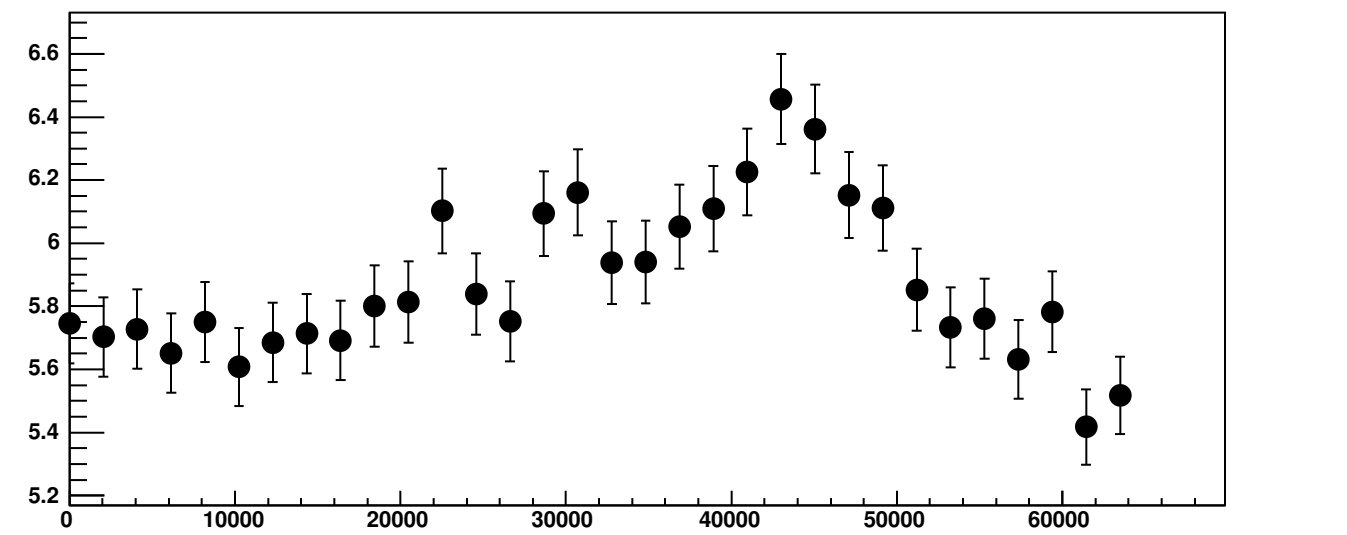

Chip 5, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

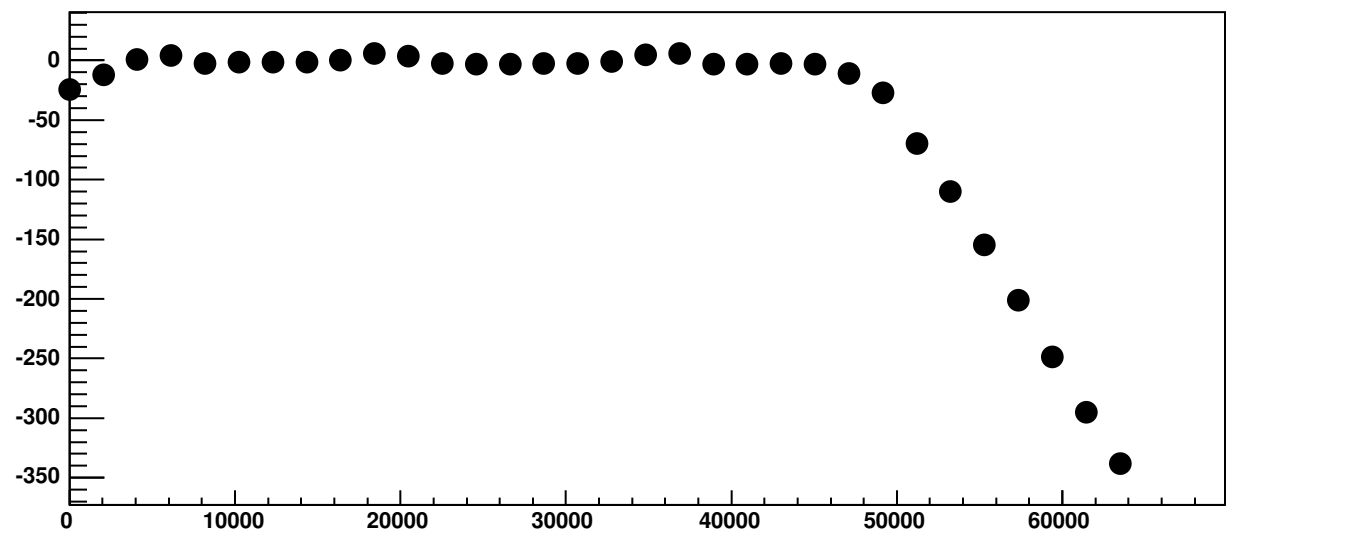
Chip 5, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

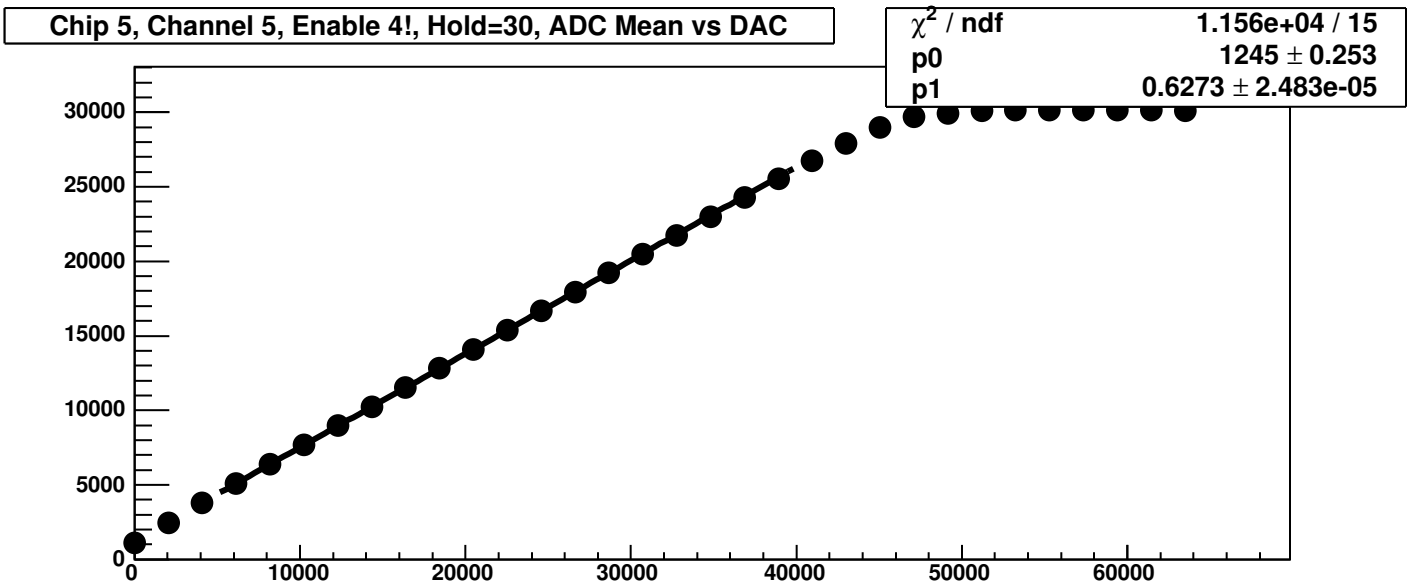


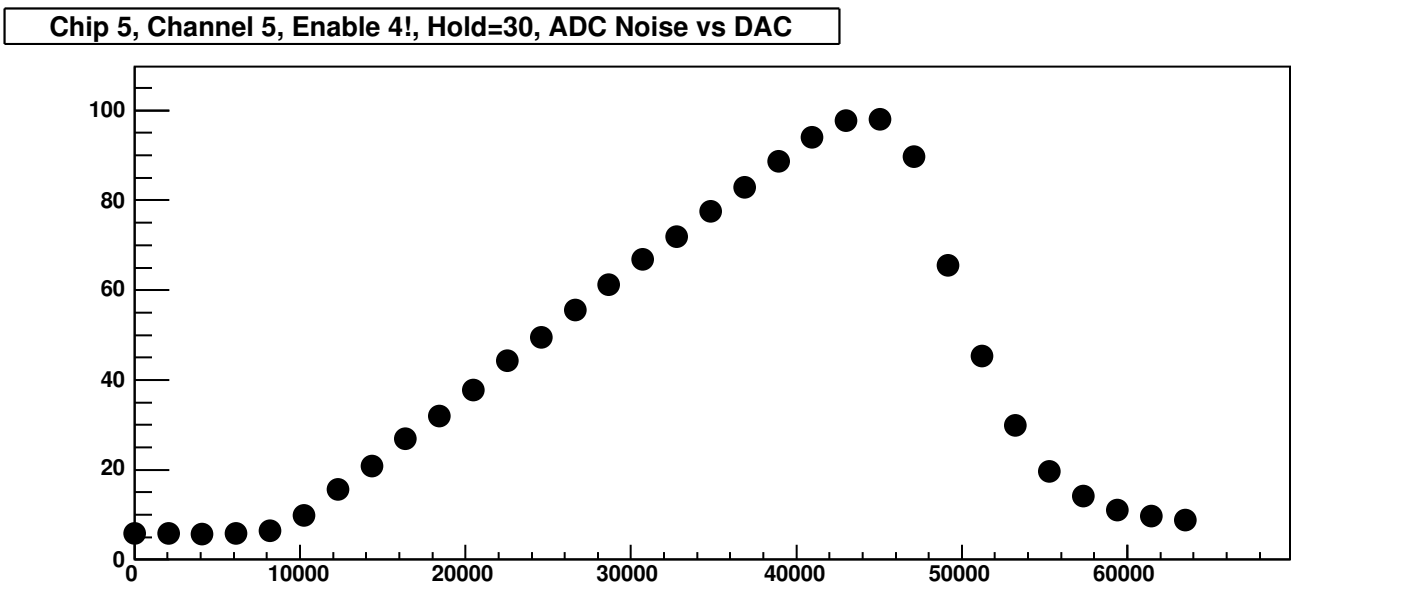
Chip 5, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

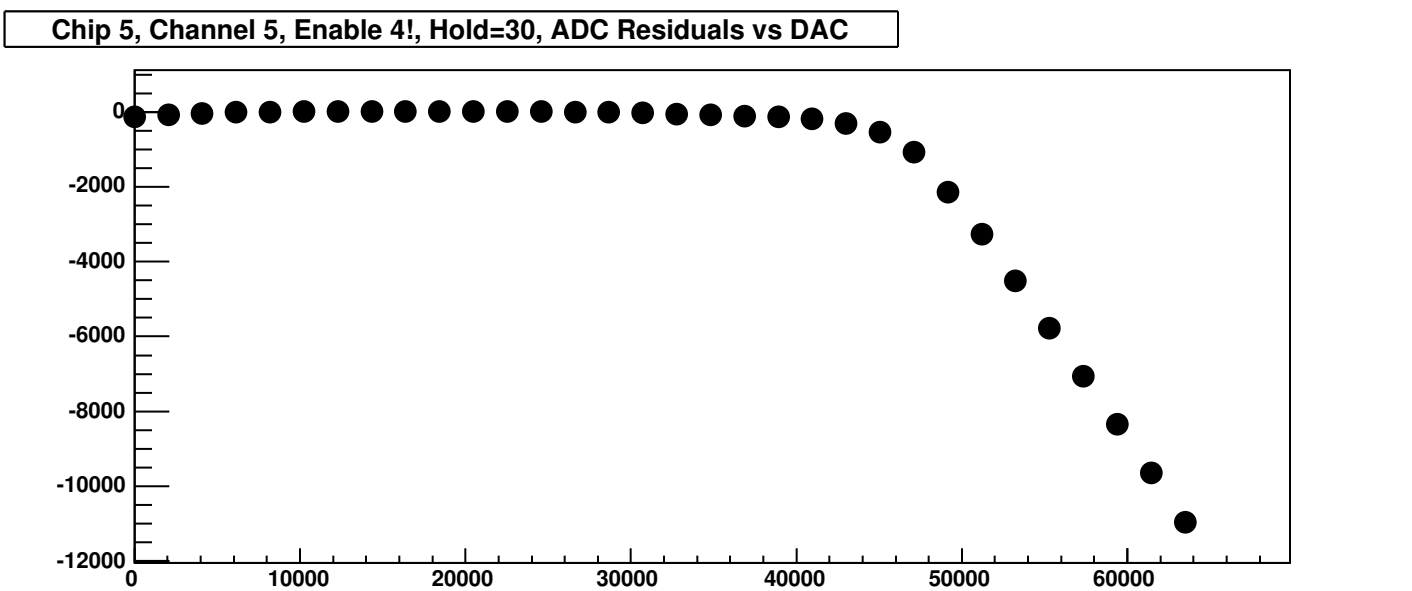


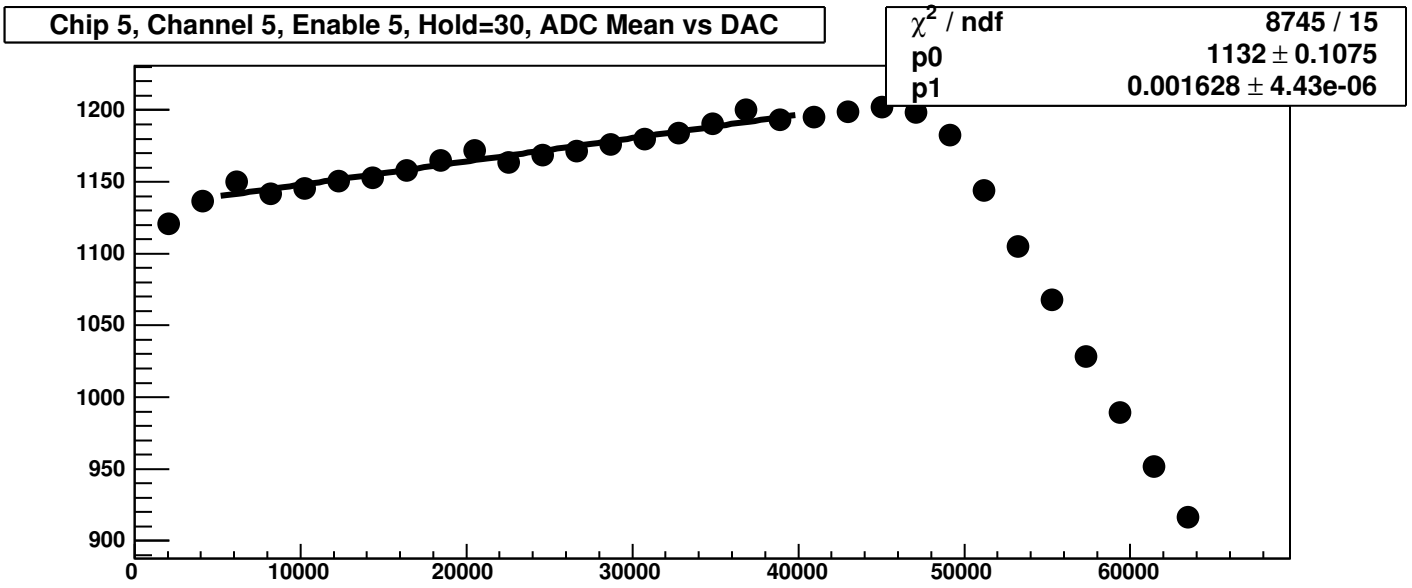
Chip 5, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

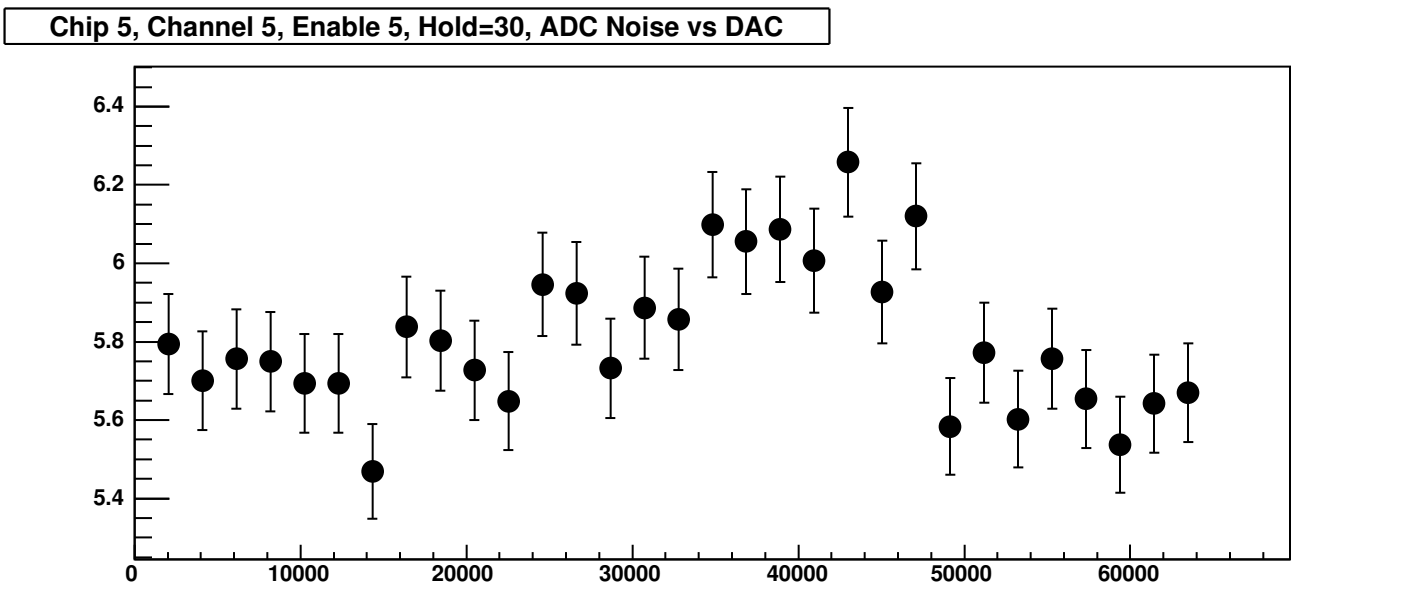


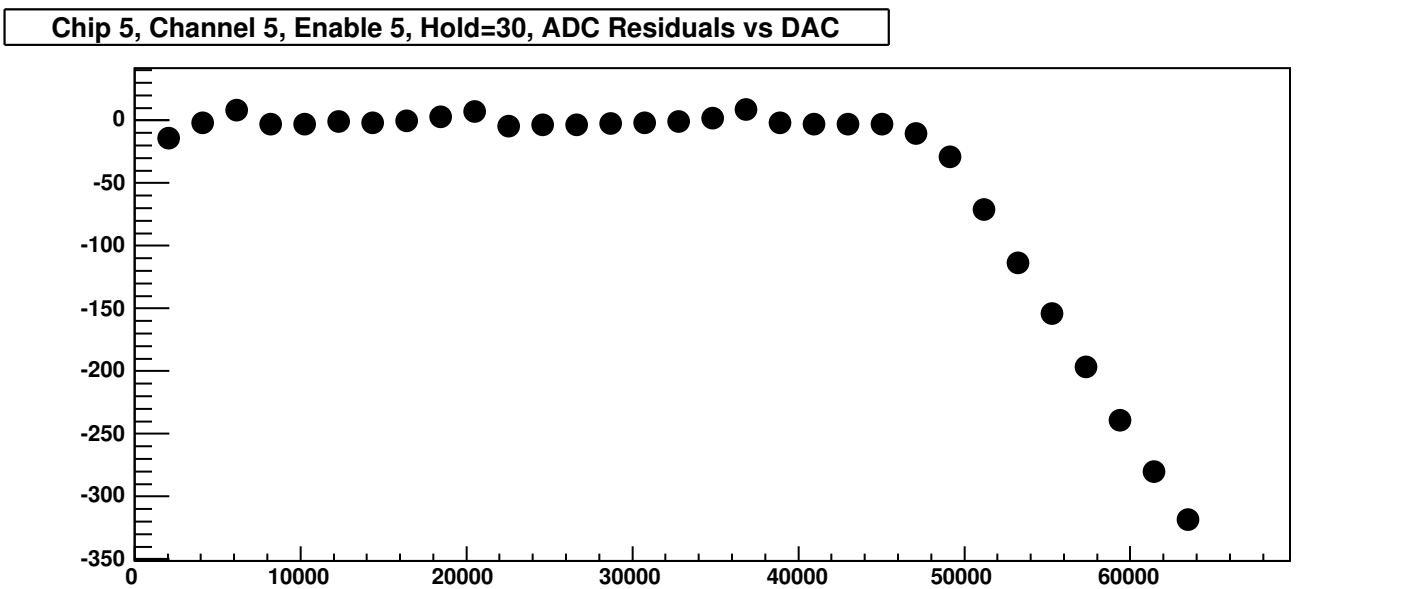

Chip 5, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.156e+04 / 15
p0 1245 ± 0.253
p1 0.6273 ± 2.483e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

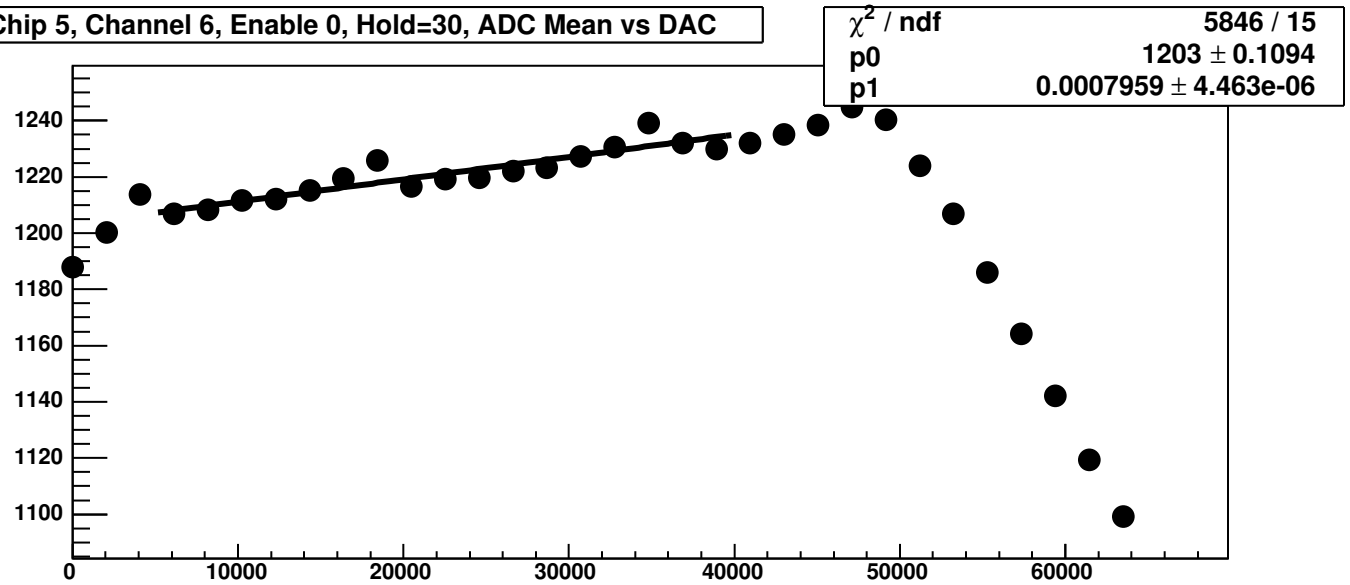

Chip 5, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8745 / 15
p0 1132 ± 0.1075
p1 0.001628 ± 4.43e-06
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

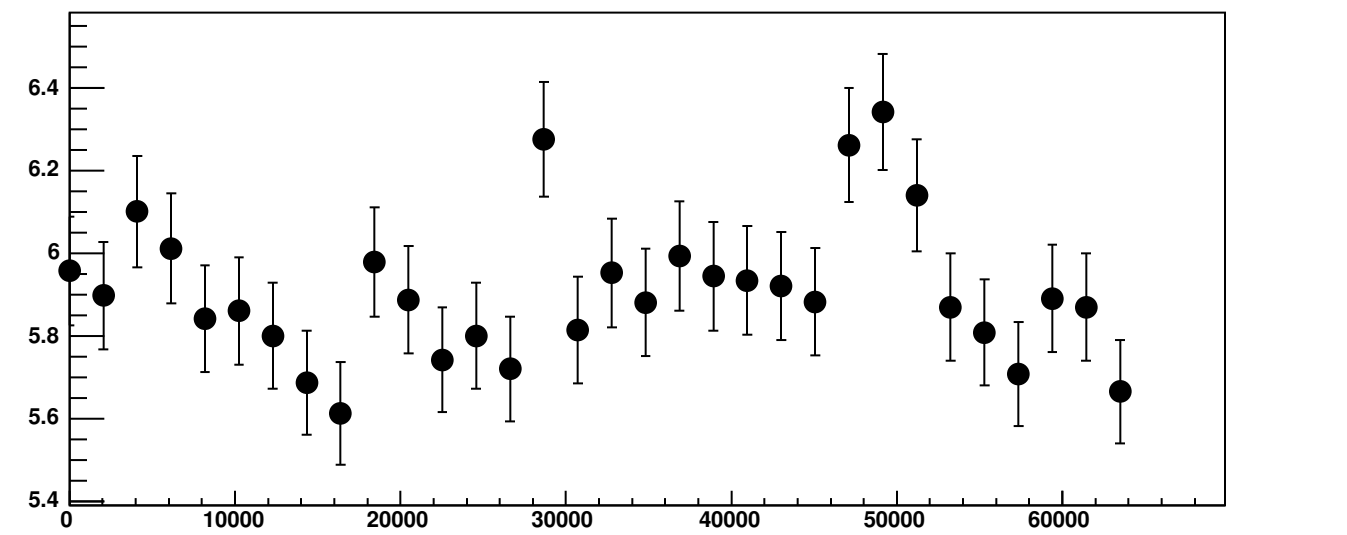

Chip 5, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

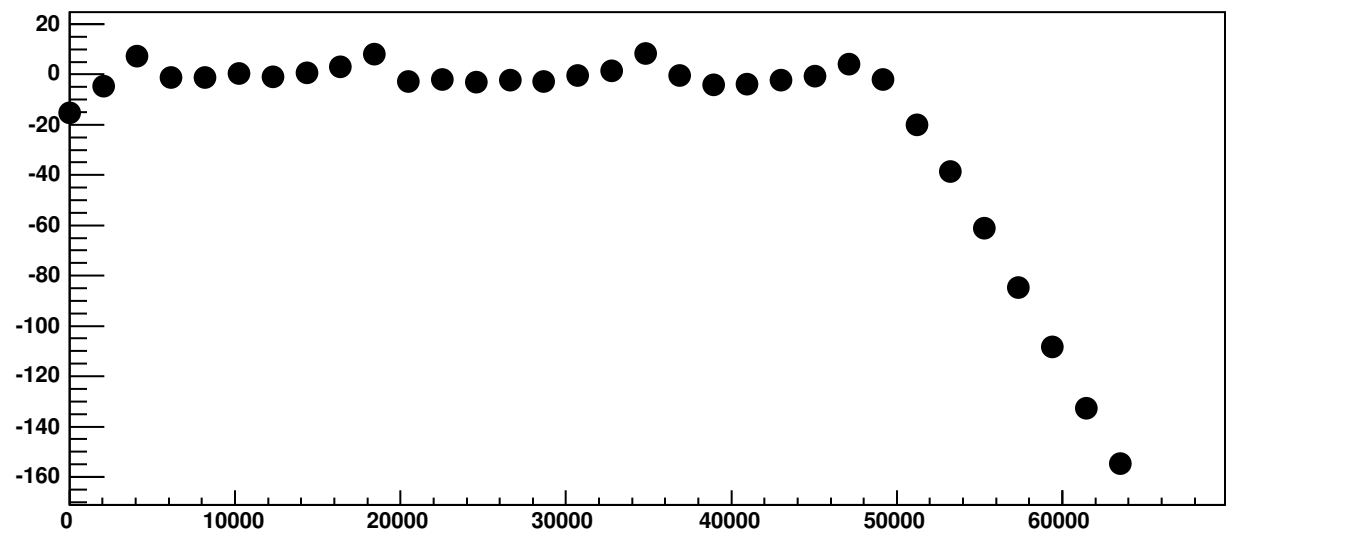
Chip 5, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC

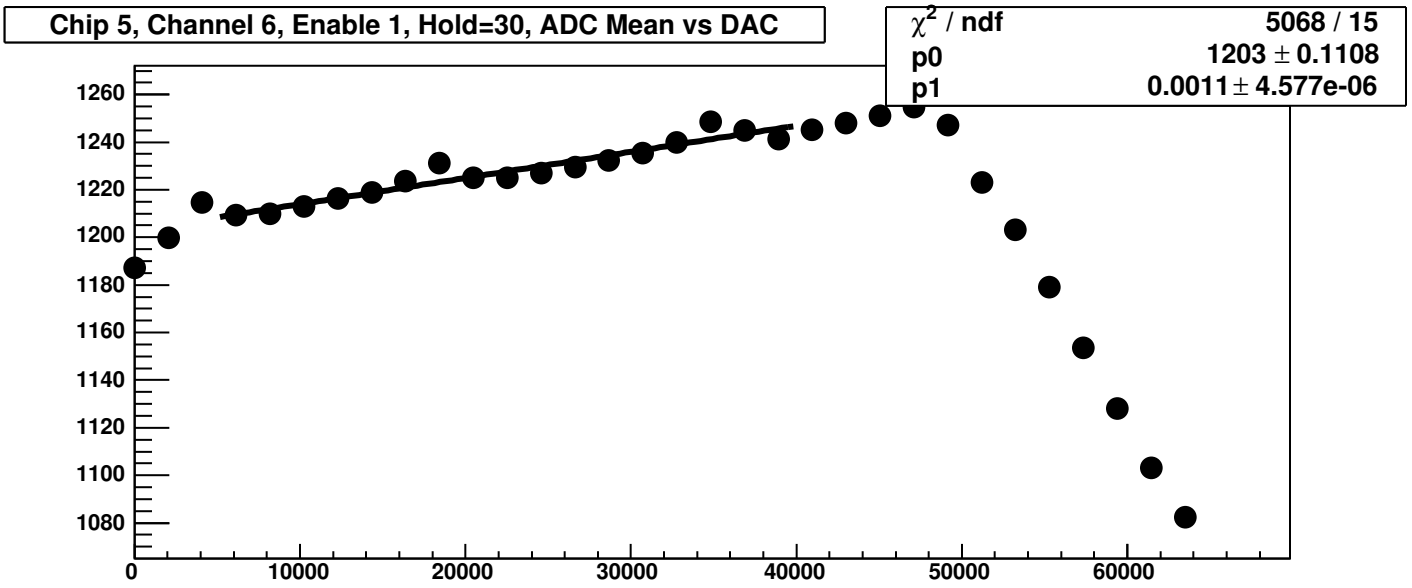


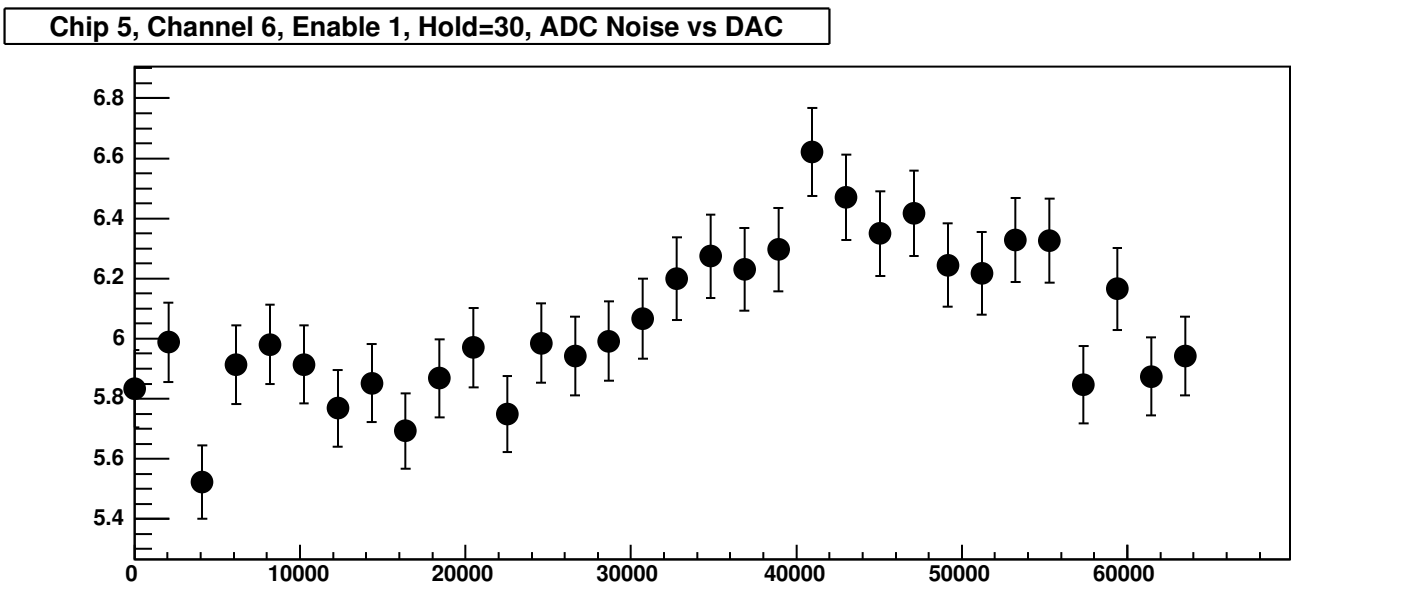
Chip 5, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC

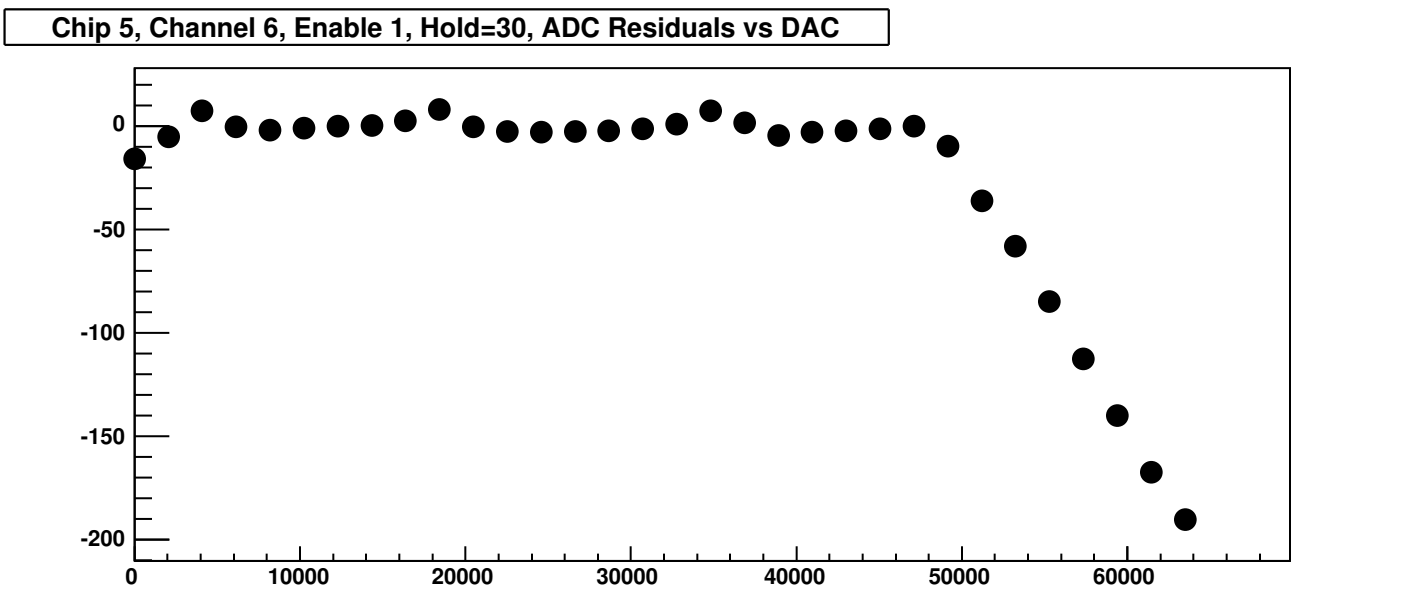


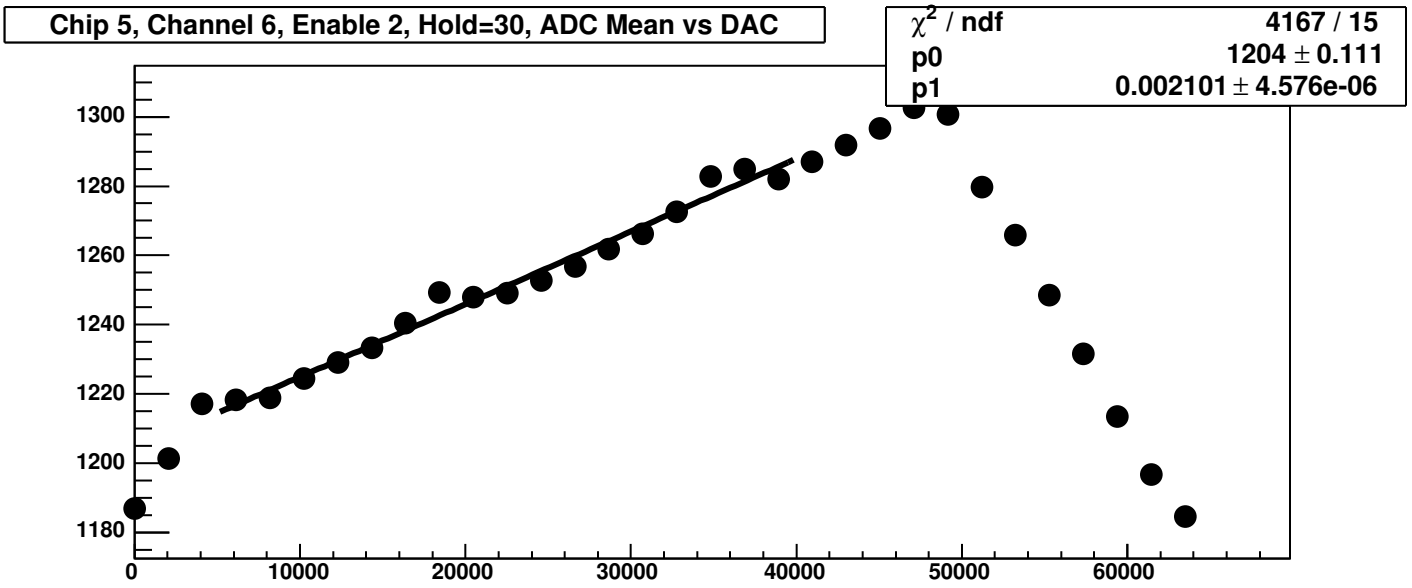
Chip 5, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

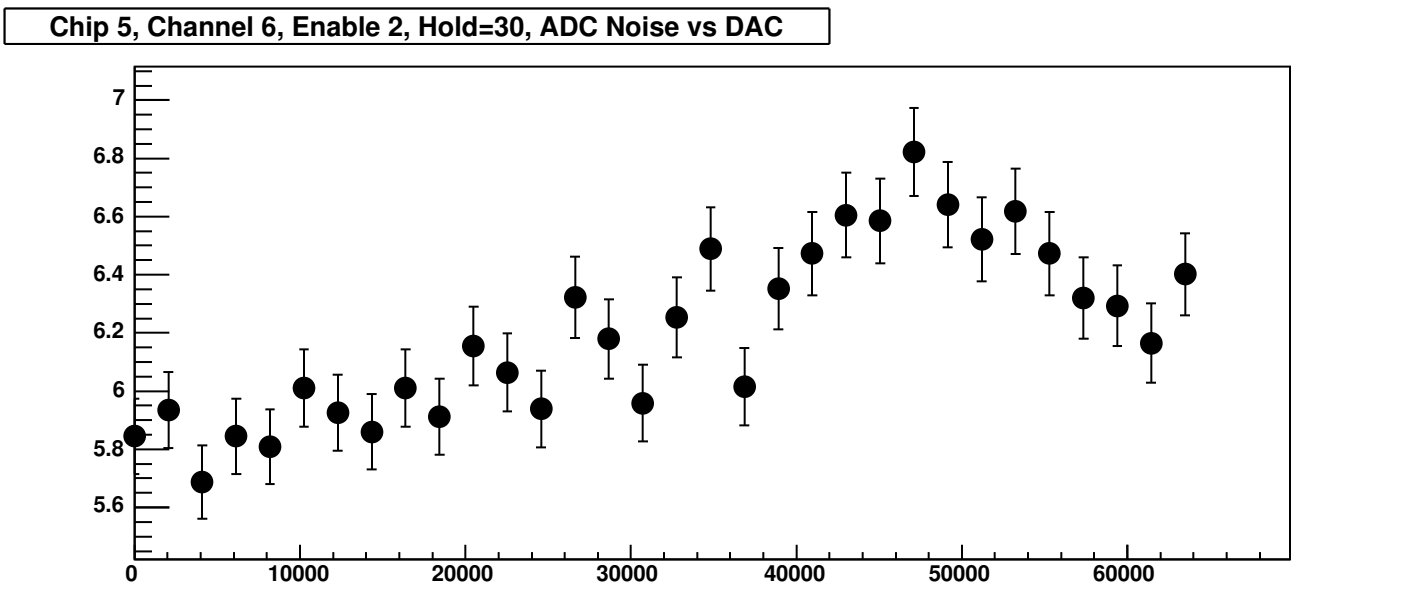


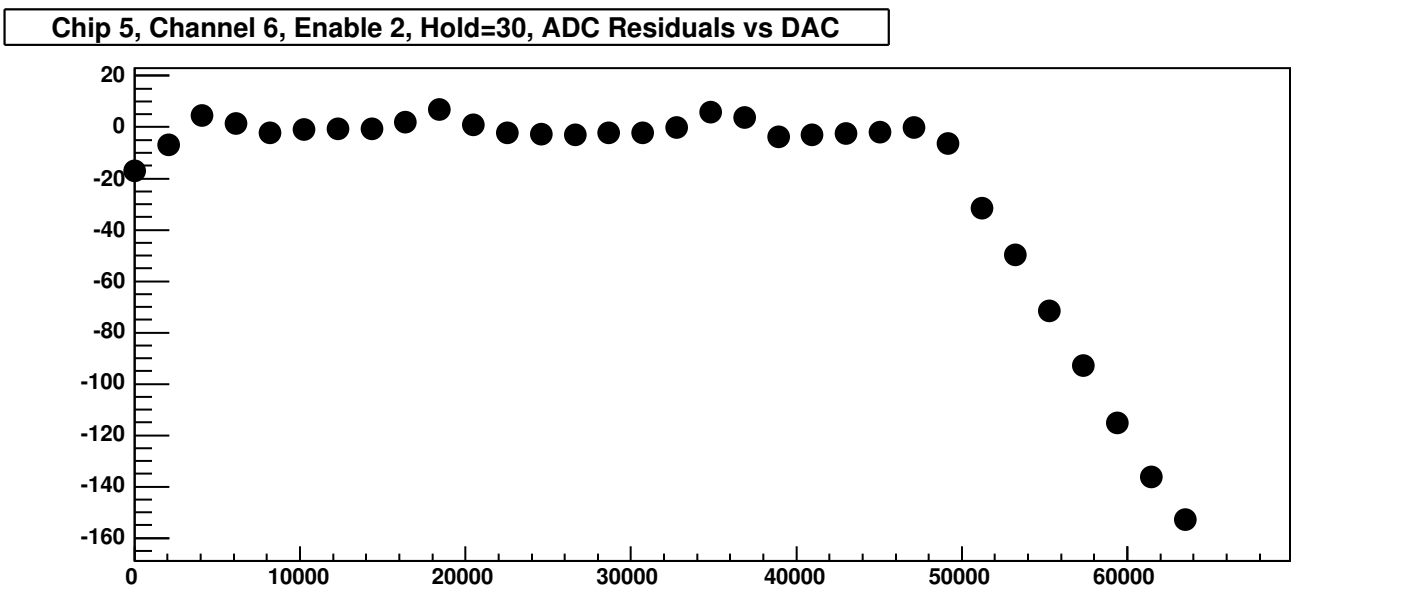

Chip 5, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5068 / 15
p0 1203 ± 0.1108
p1 0.0011 ± 4.577e-06
1260
1240
1220
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

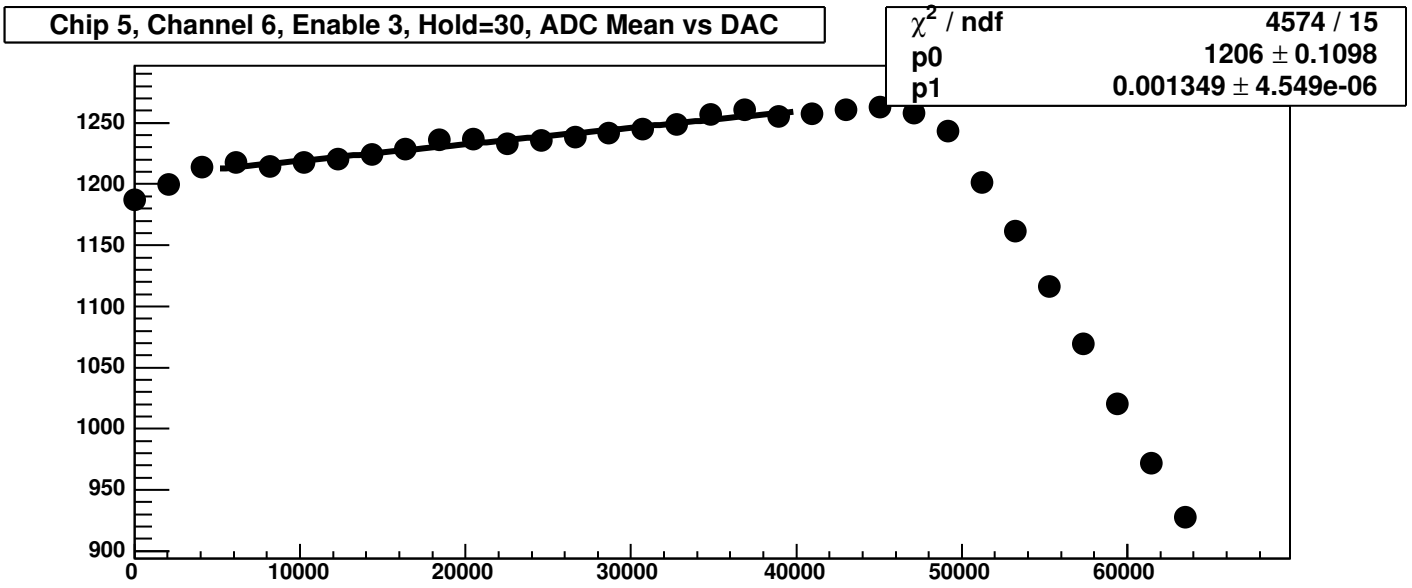

Chip 5, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

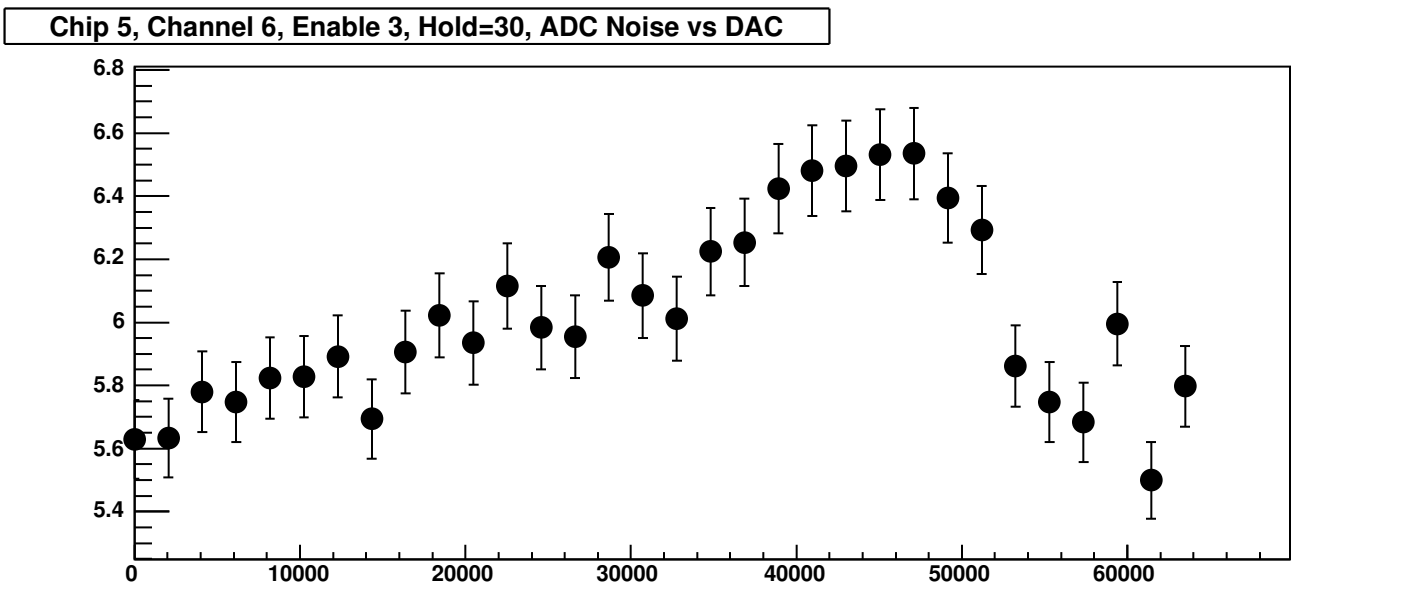

Chip 5, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

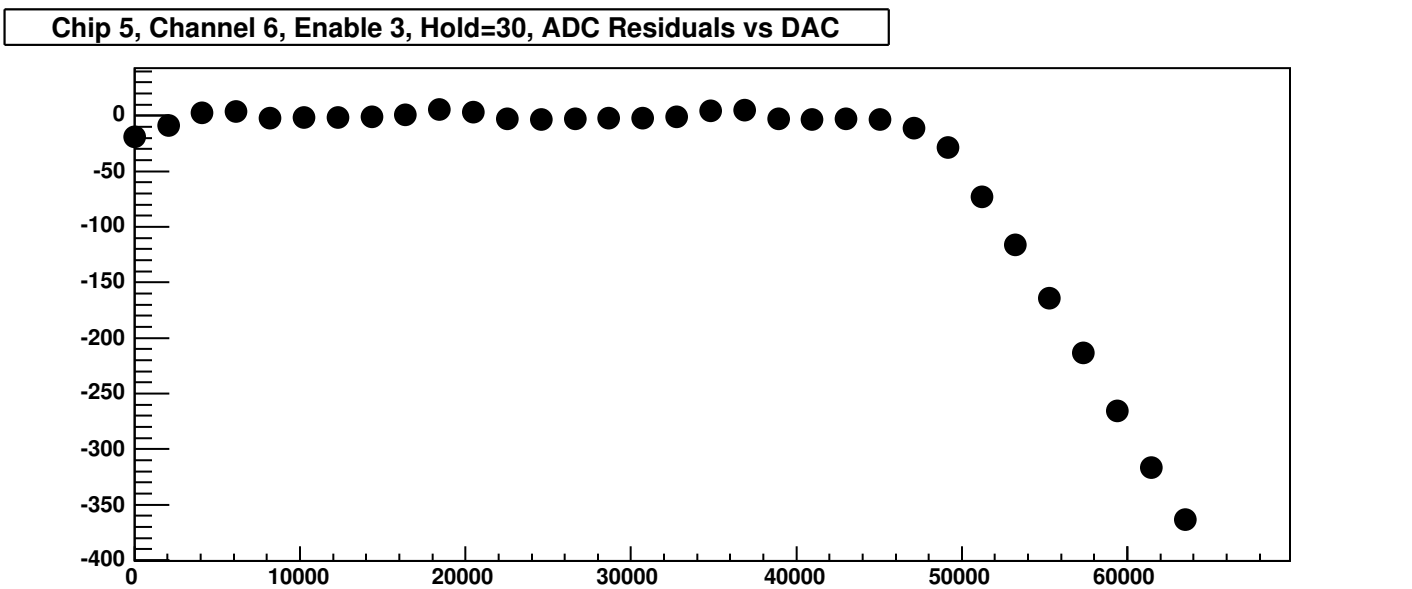

Chip 5, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4167 / 15
p0 1204 ± 0.111
p1 0.002101 ± 4.576e-06
1300
1280
1260
1240
1220
1200
1180
0
10000
20000
30000
40000
50000
60000

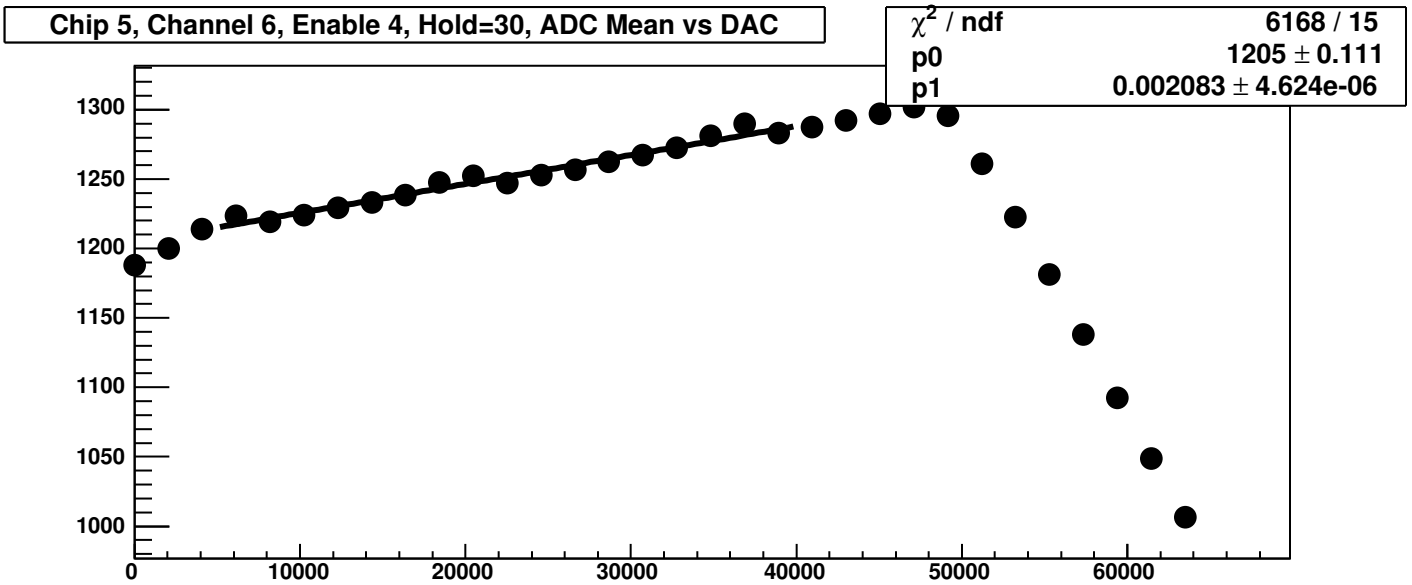

Chip 5, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

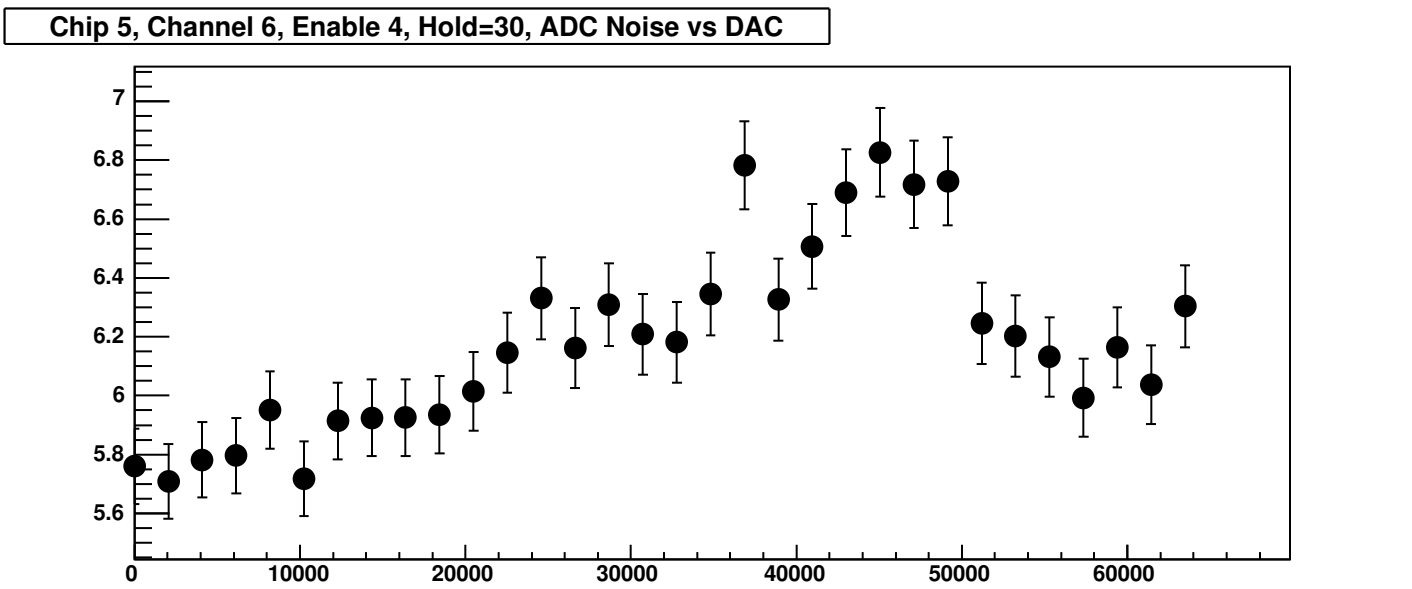

Chip 5, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

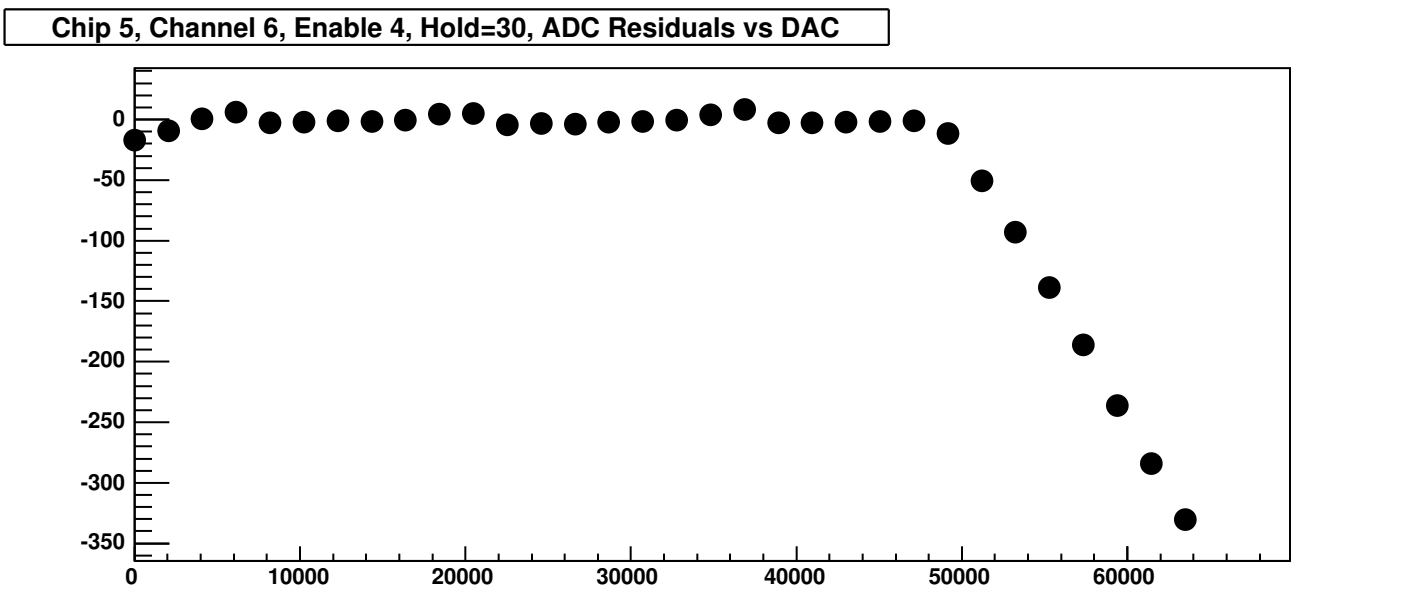

Chip 5, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4574 / 15
p0 1206 ± 0.1098
p1 0.001349 ± 4.549e-06
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

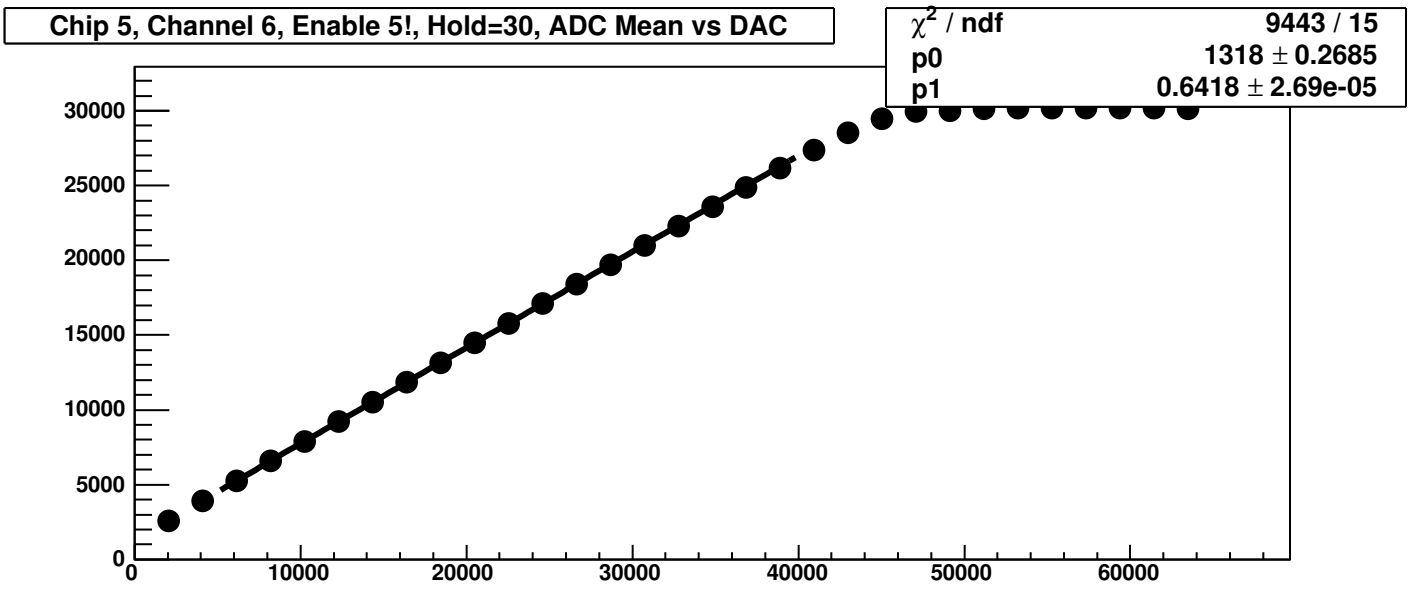

Chip 5, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

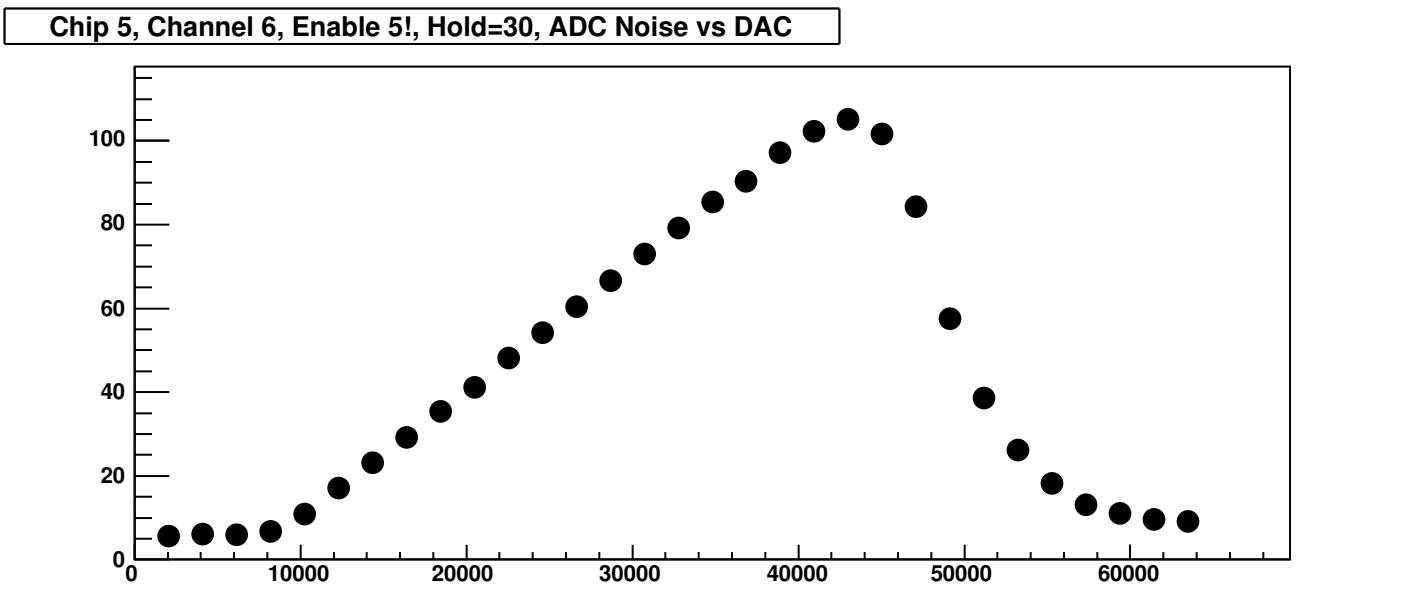

Chip 5, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

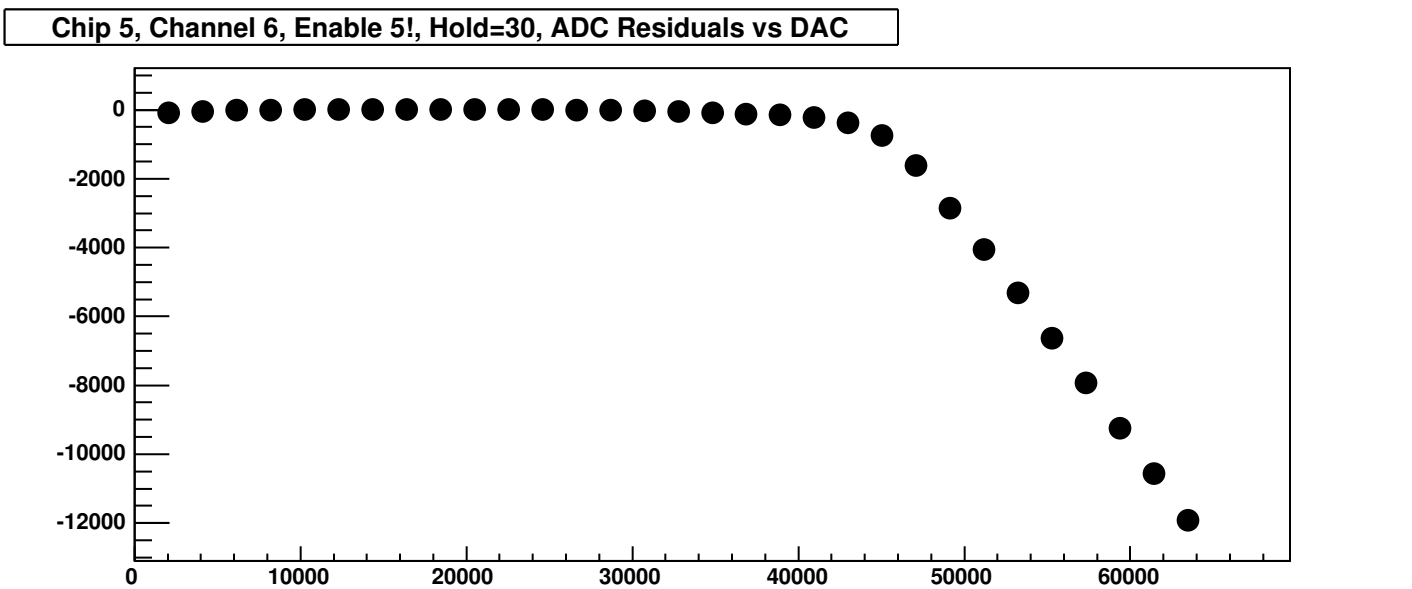

Chip 5, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6168 / 15
p0 1205 ± 0.111
p1 0.002083 ± 4.624e-06
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

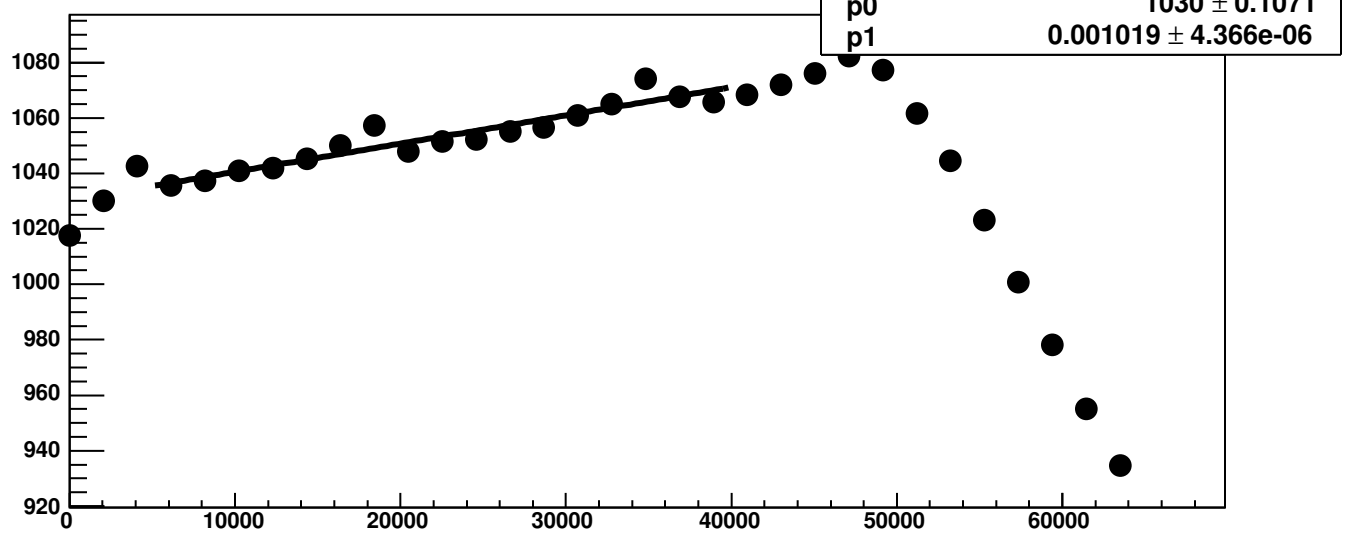

Chip 5, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 9443 / 15
p0 1318 ± 0.2685
p1 0.6418 ± 2.69e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

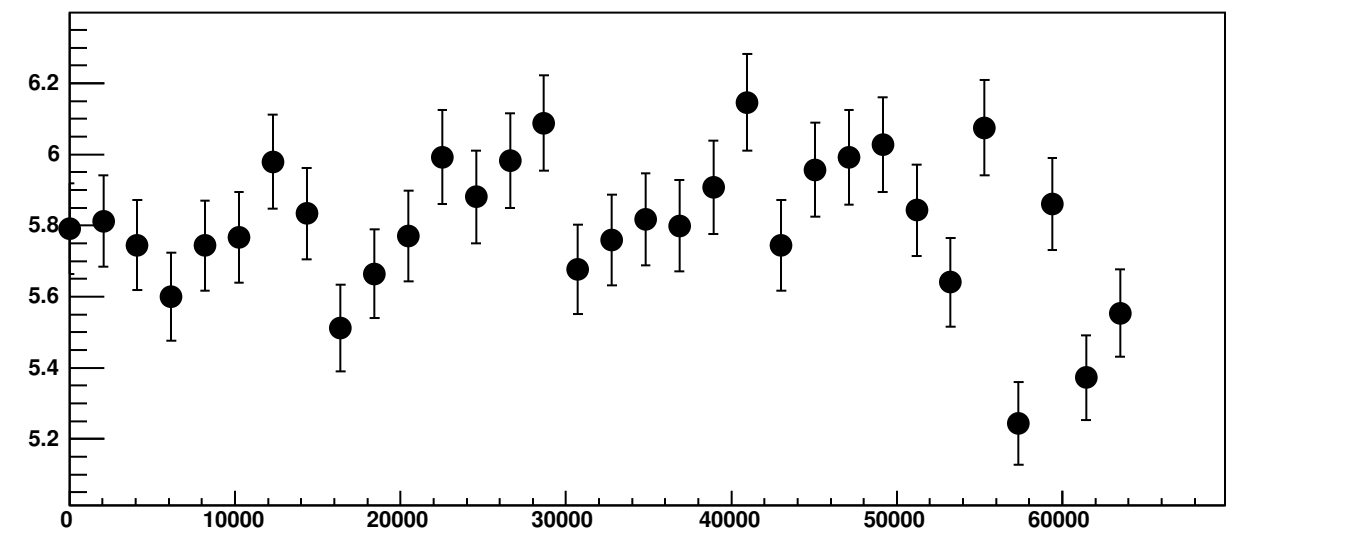

Chip 5, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

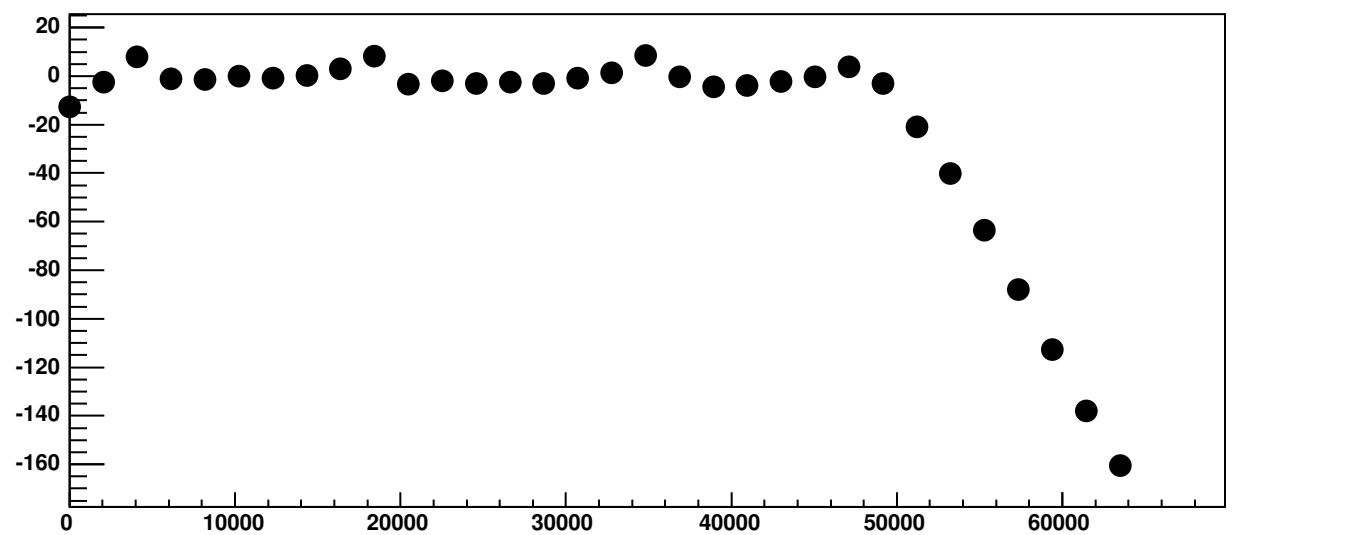
Chip 5, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC

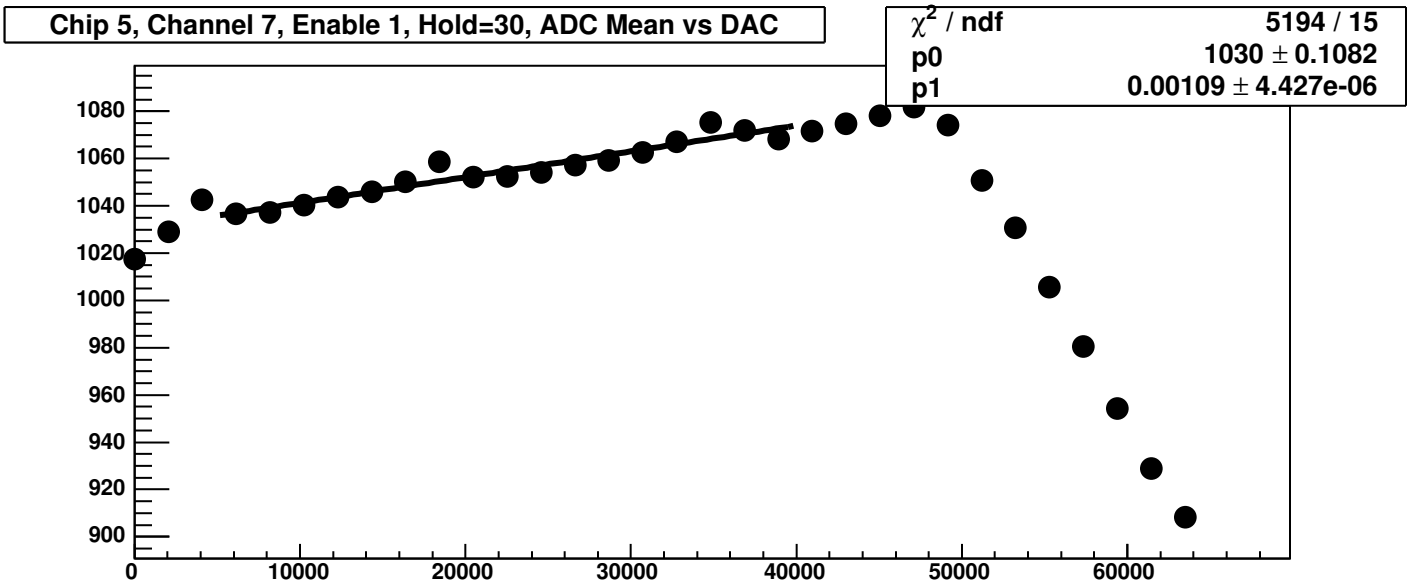


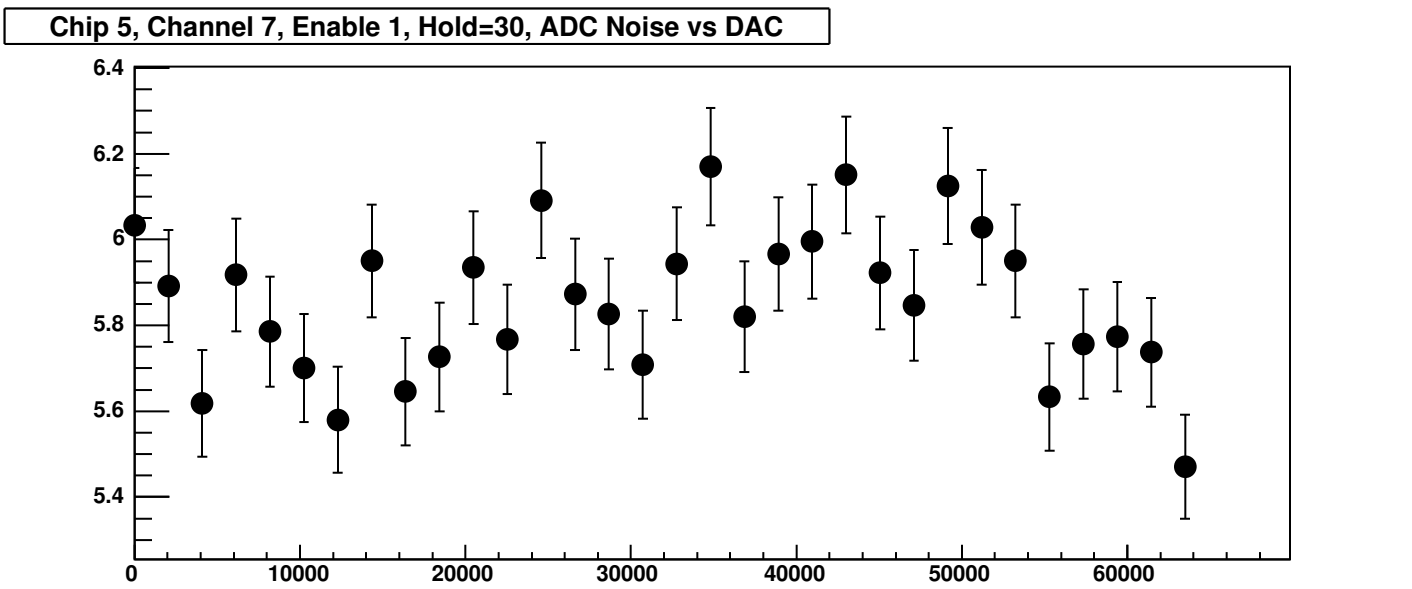
Chip 5, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC

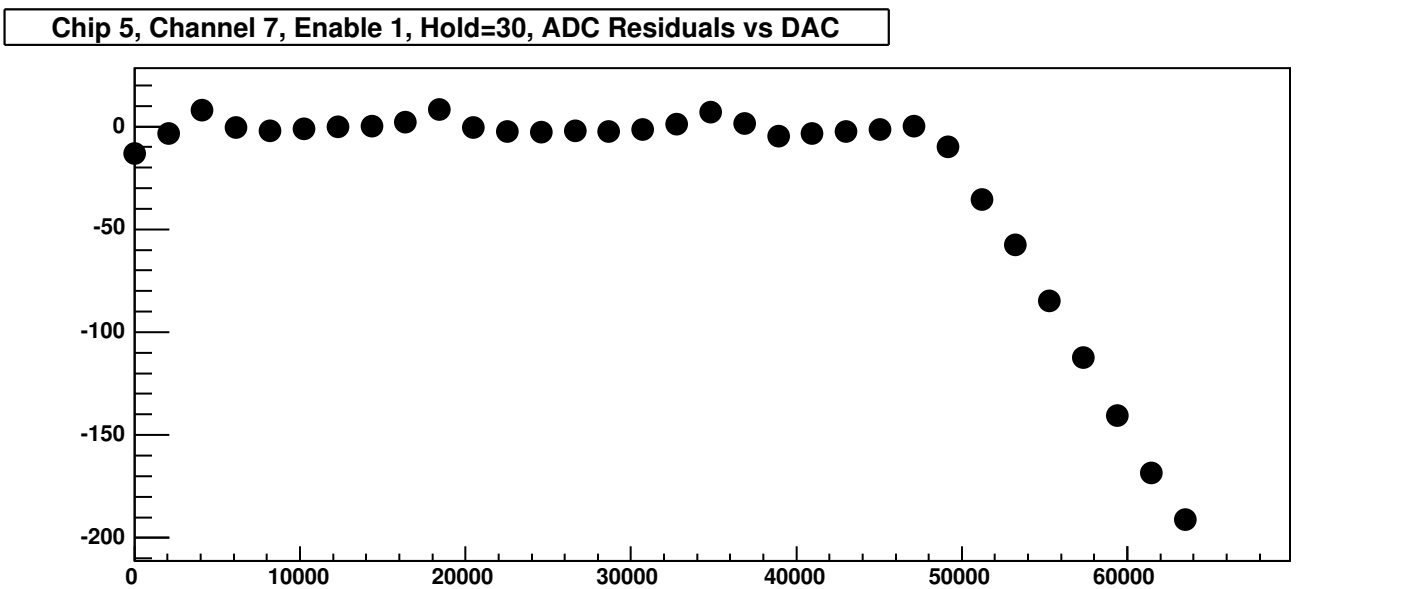


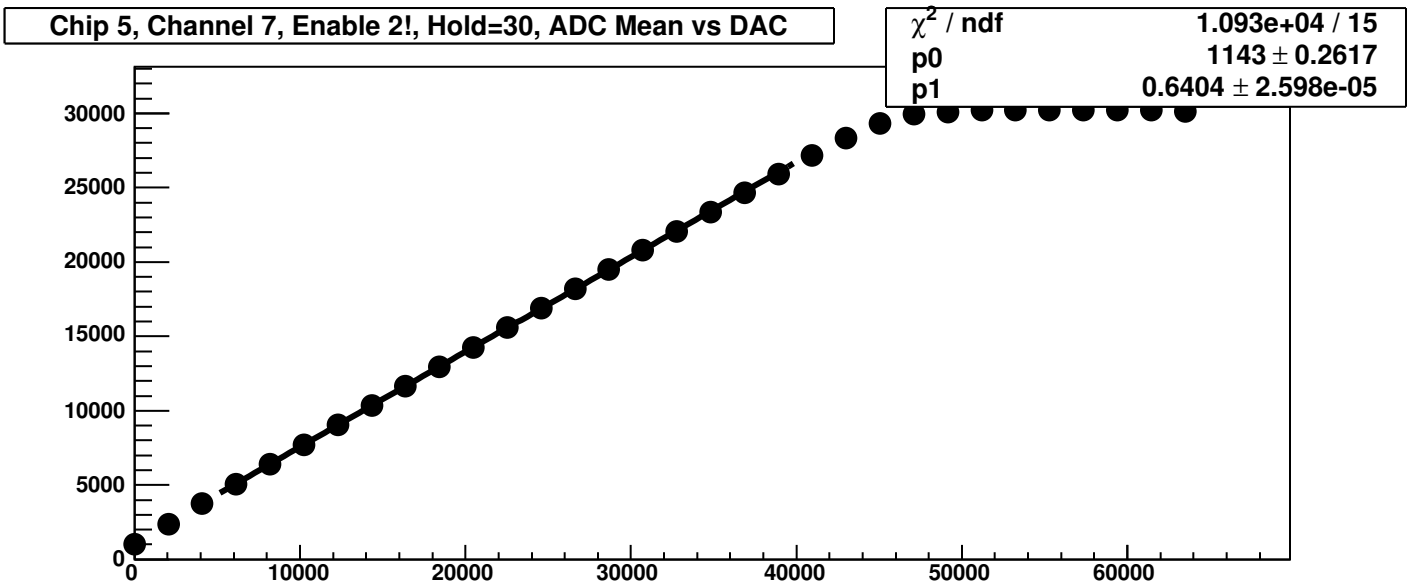
Chip 5, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

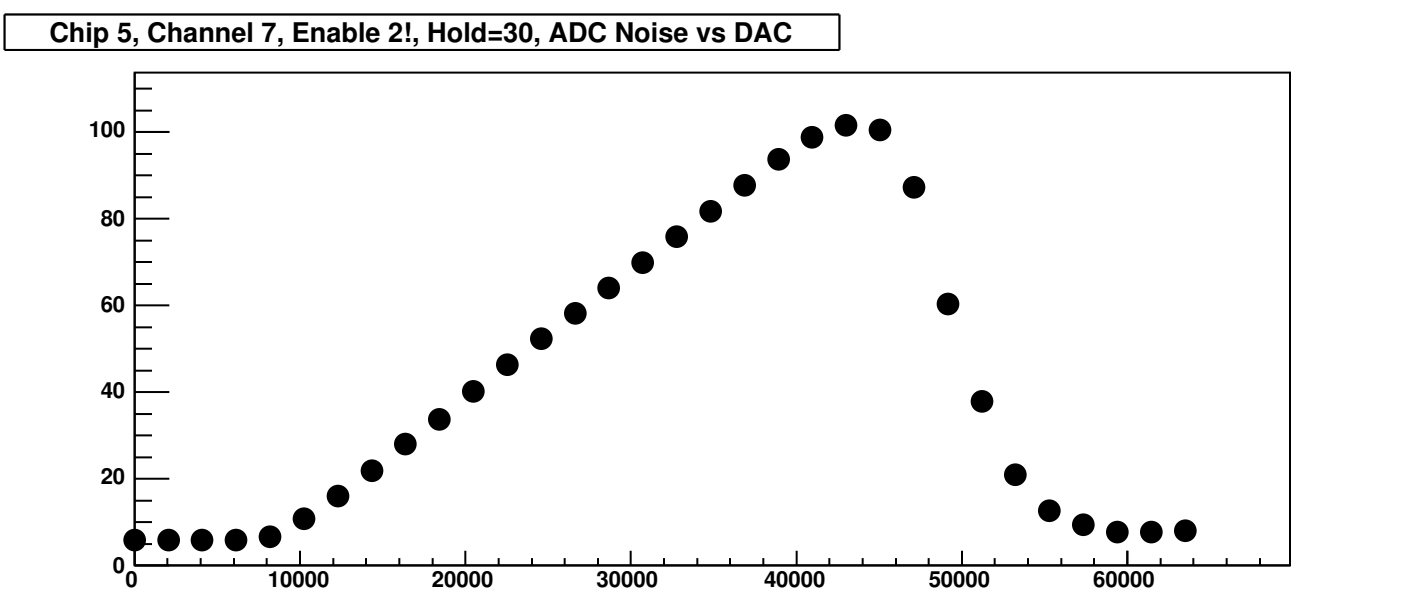


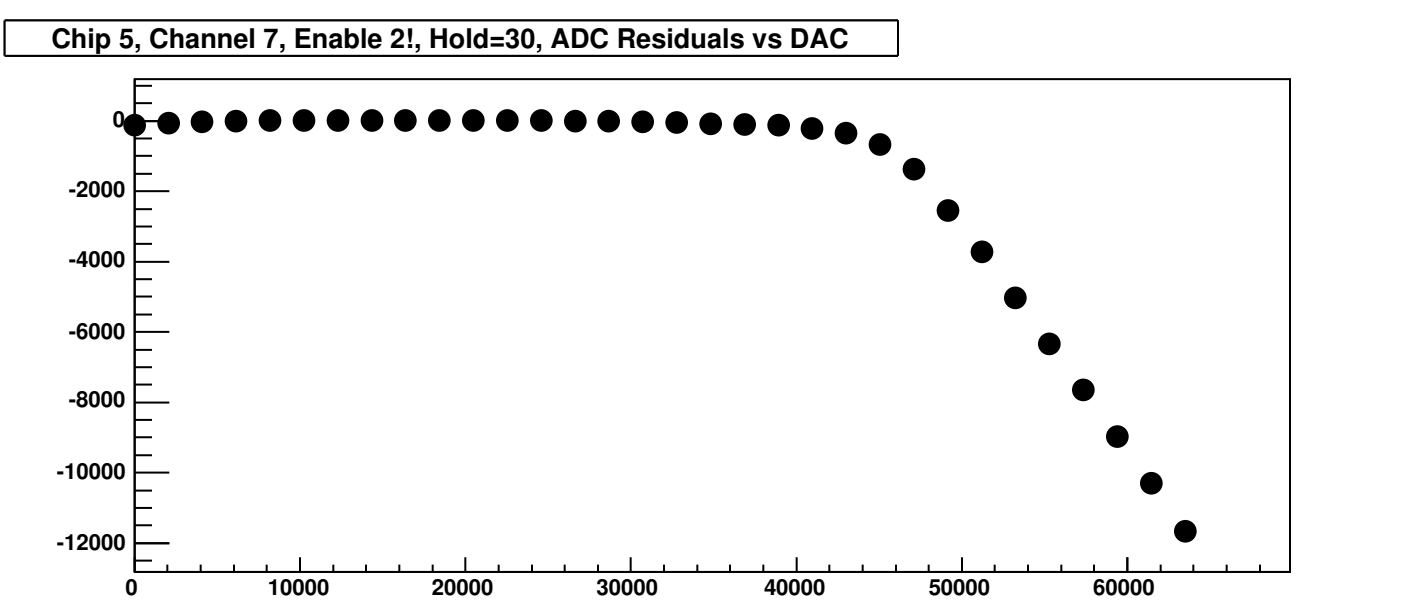

Chip 5, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5194 / 15
p0 1030 ± 0.1082
p1 0.00109 ± 4.427e-06
1080
1060
1040
1020
1000
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000

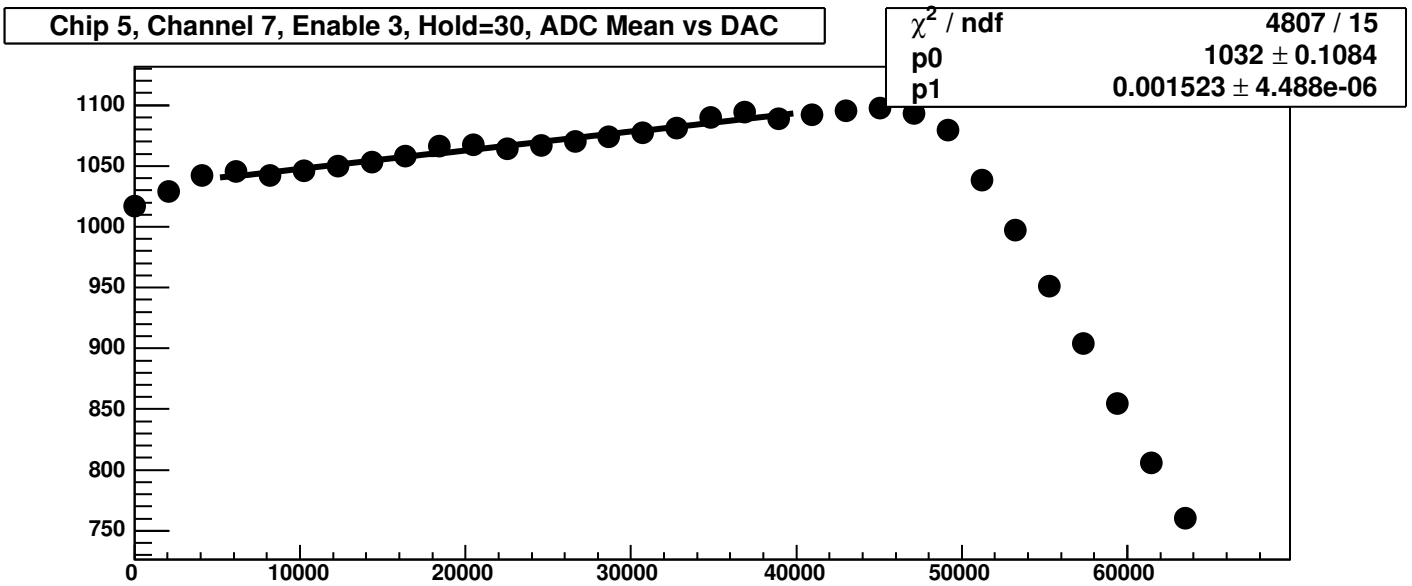

Chip 5, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

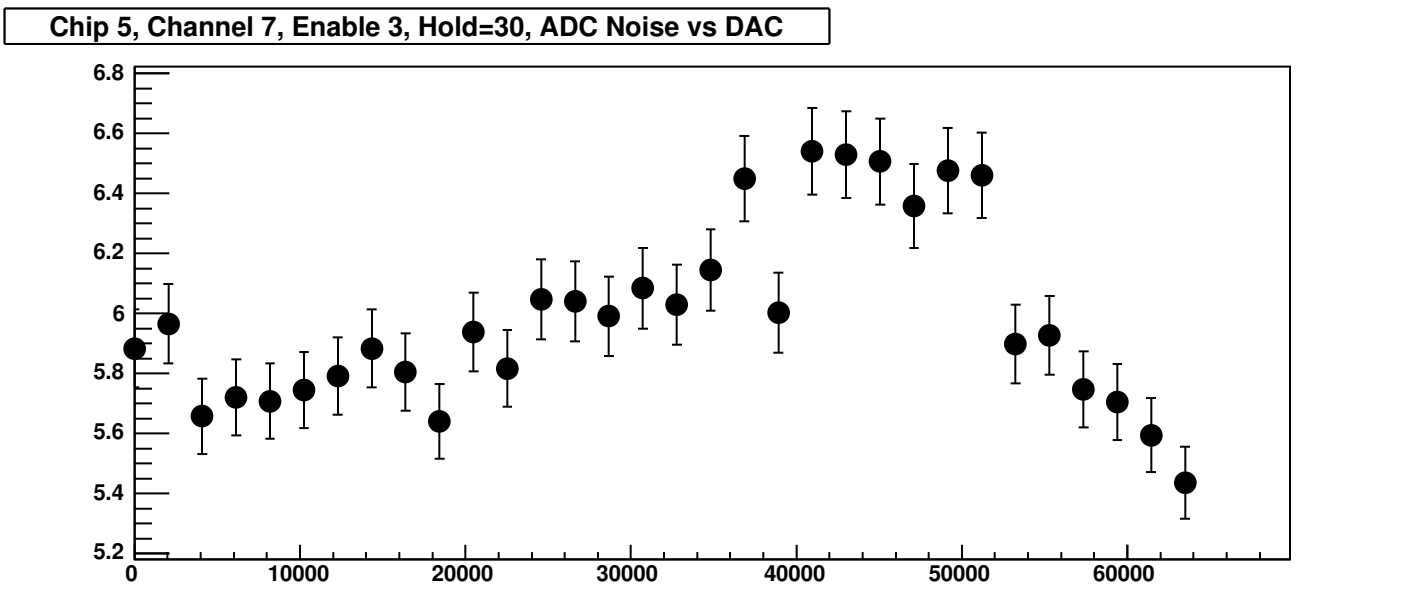

Chip 5, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

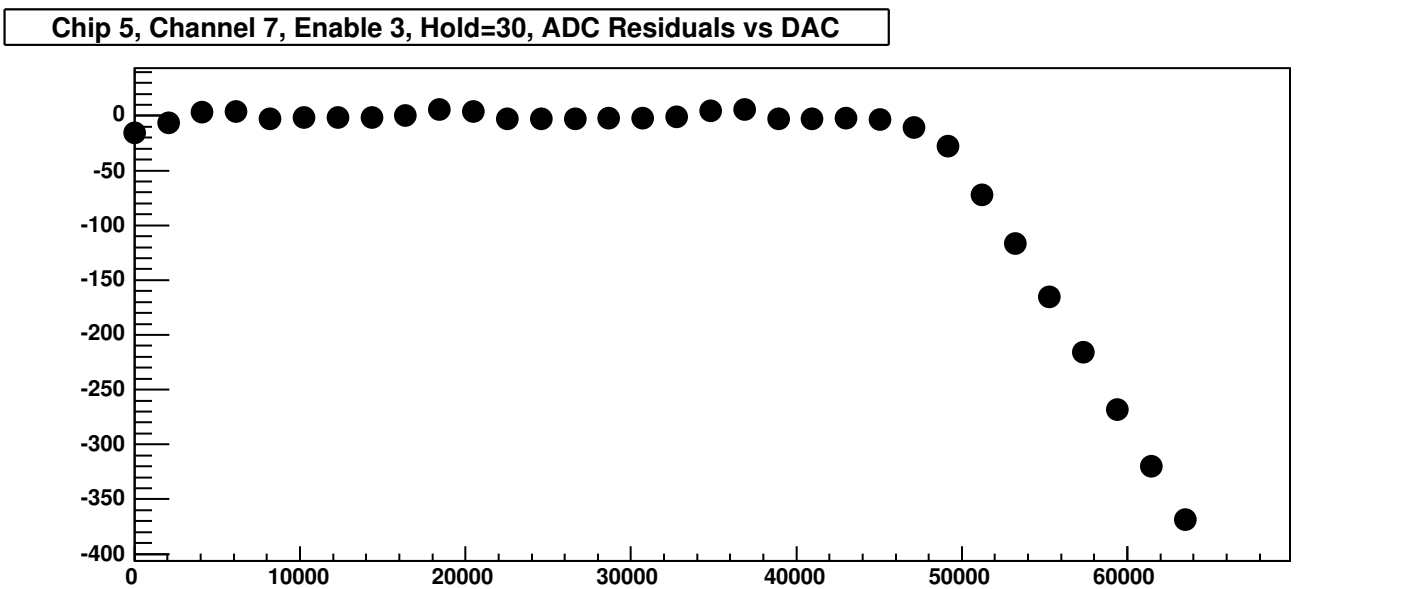

Chip 5, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.093e+04 / 15
p0 1143 ± 0.2617
p1 0.6404 ± 2.598e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

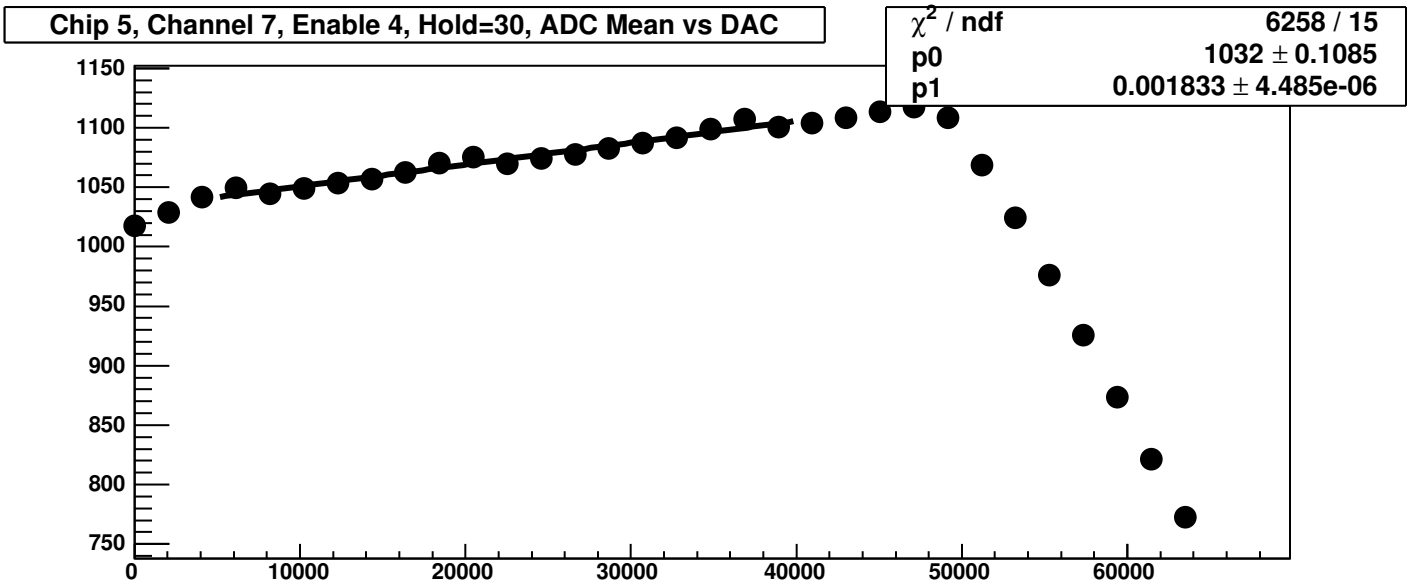

Chip 5, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

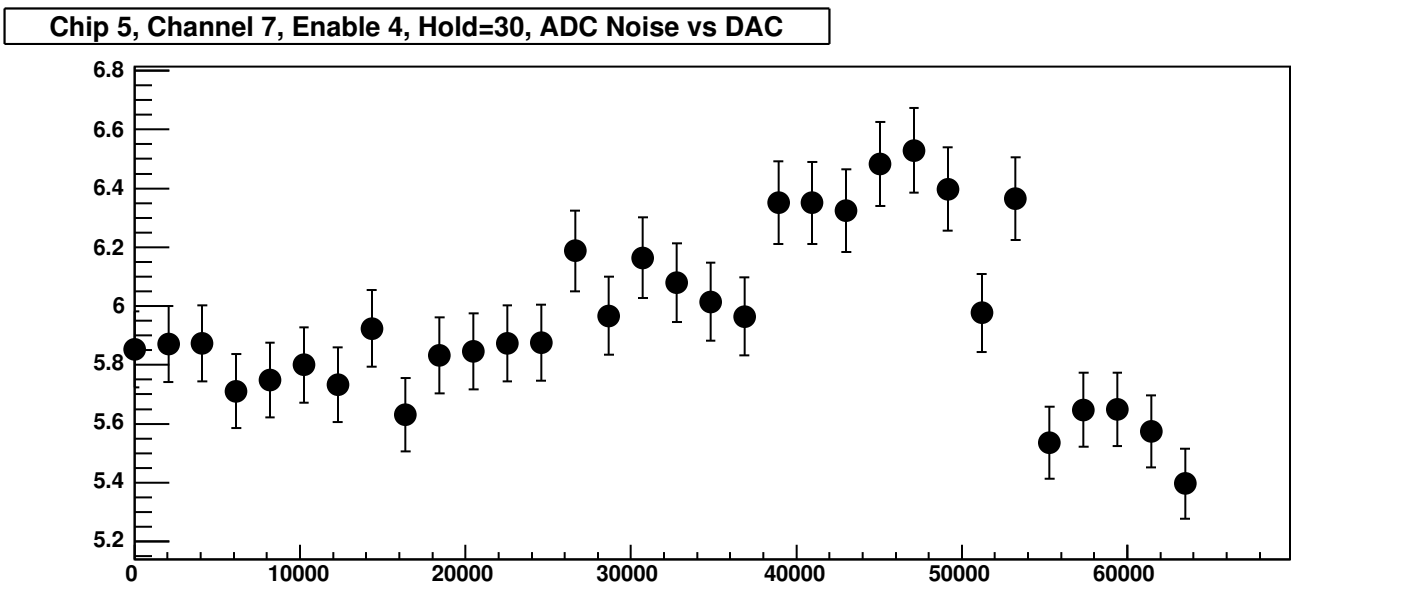

Chip 5, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

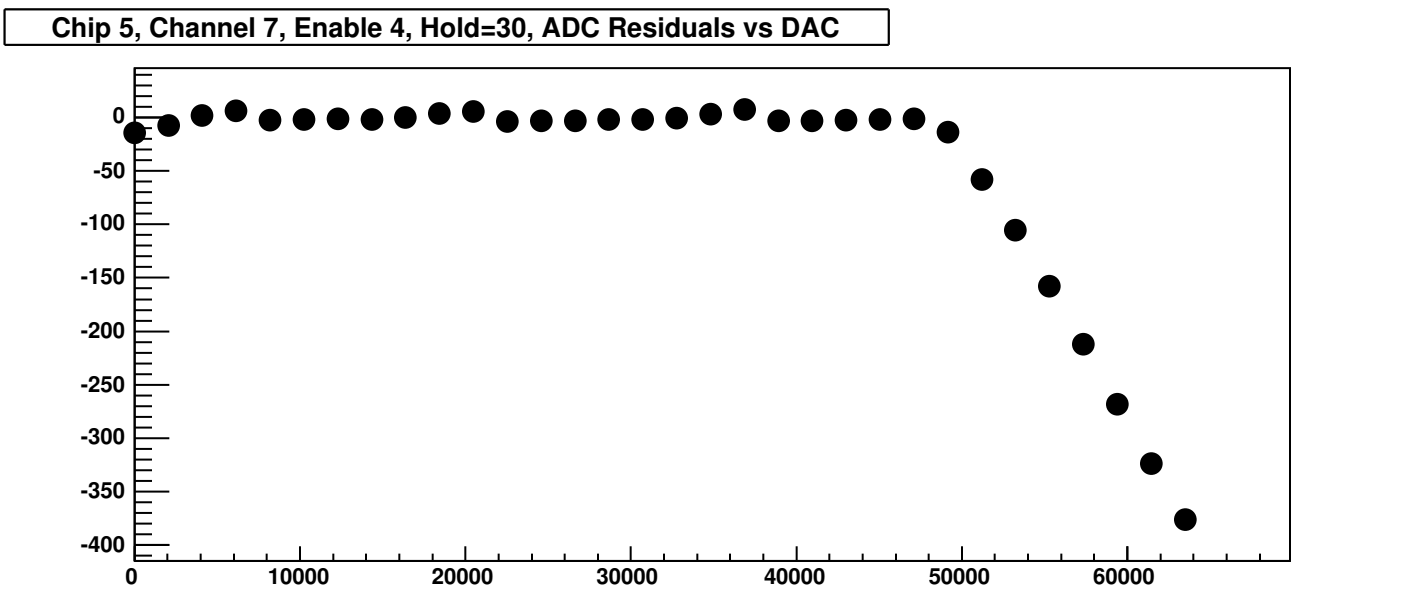

Chip 5, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4807 / 15
p0 1032 ± 0.1084
p1 0.001523 ± 4.488e-06
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

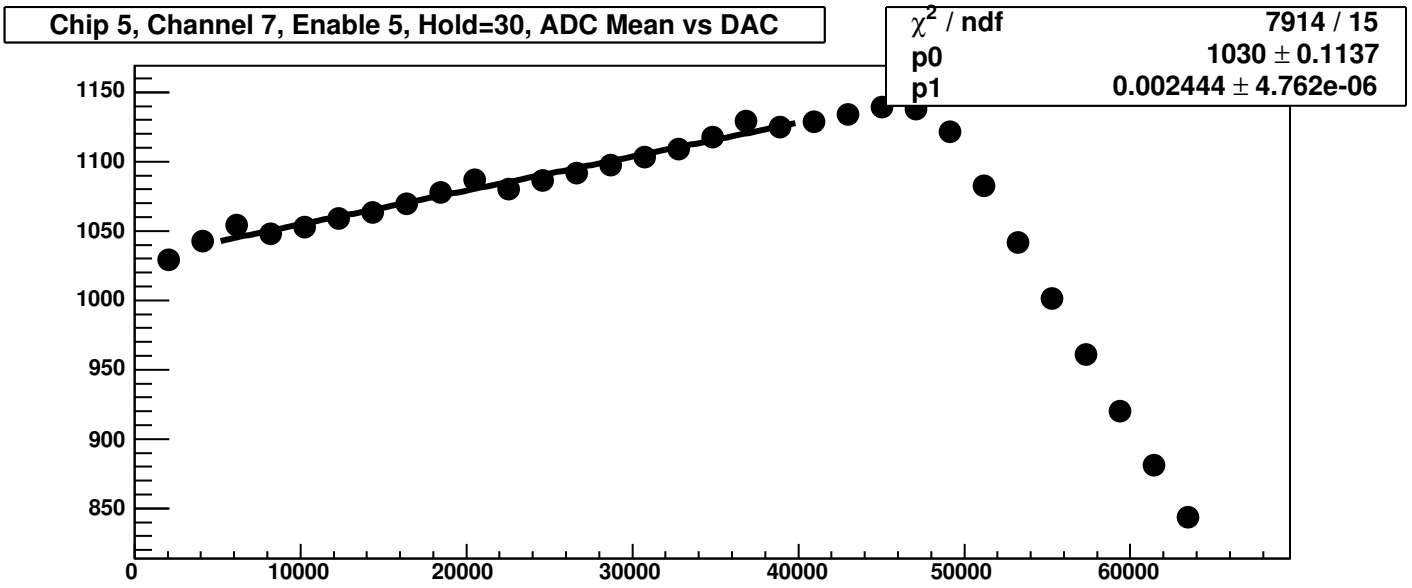

Chip 5, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

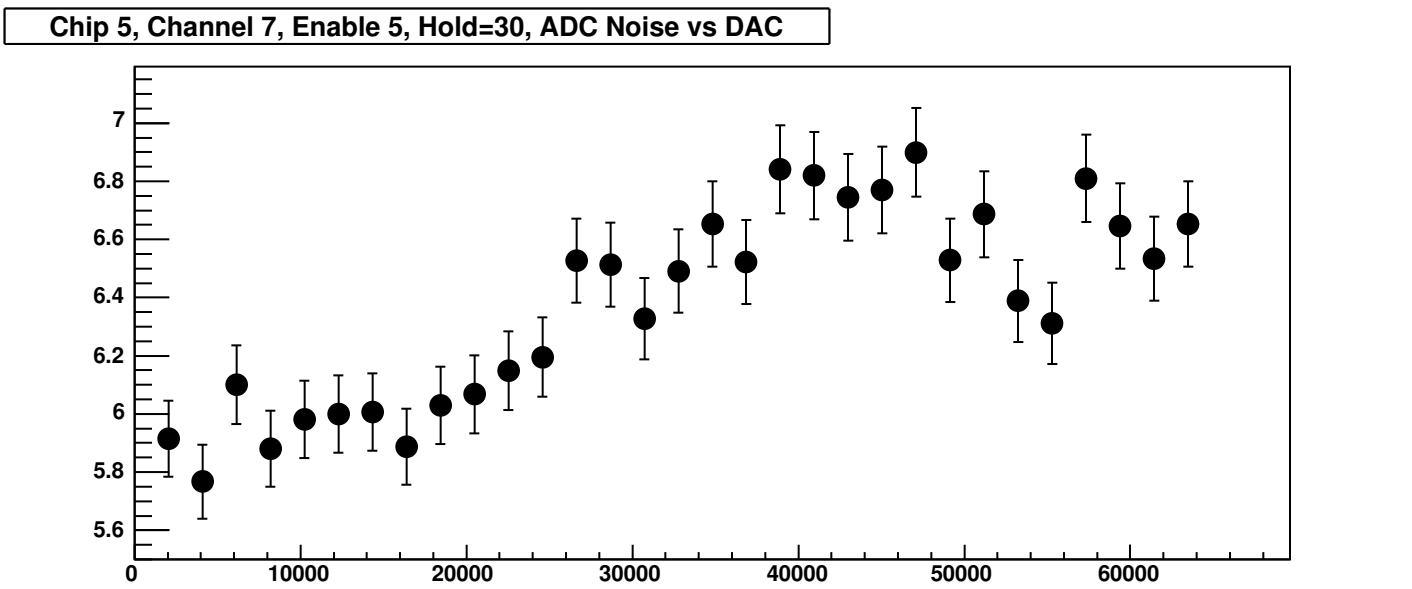

Chip 5, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

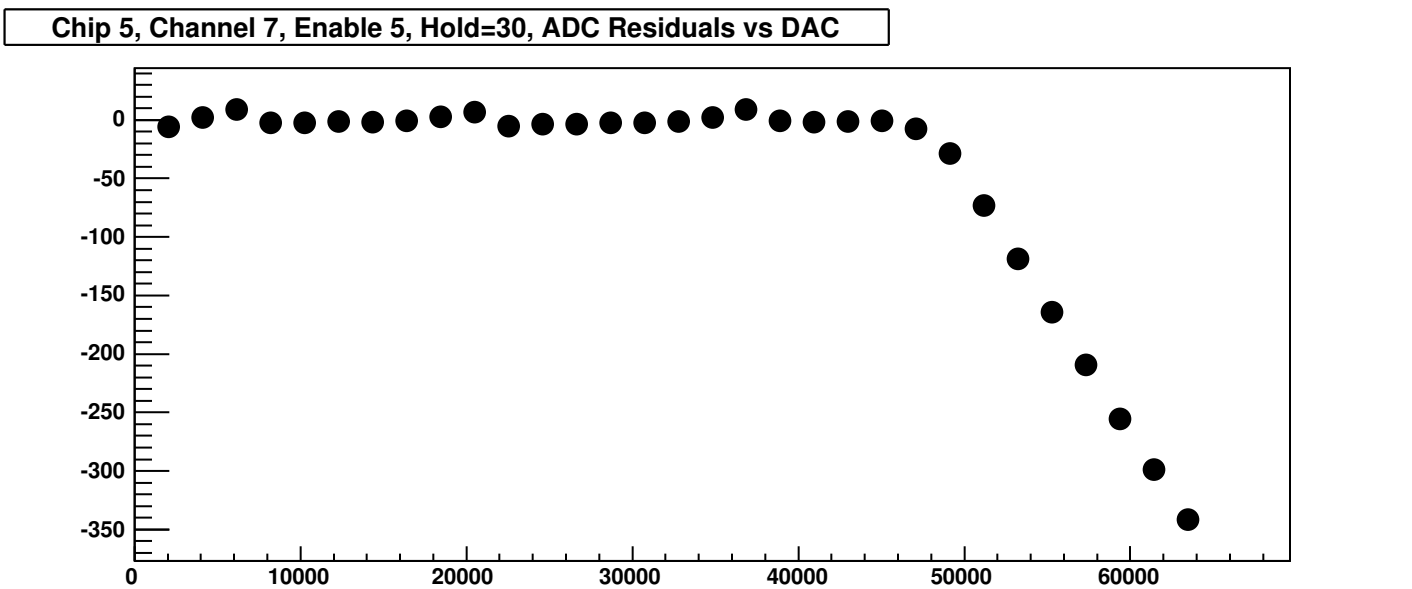

Chip 5, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6258 / 15
p0 1032 ± 0.1085
p1 0.001833 ± 4.485e-06
1150
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

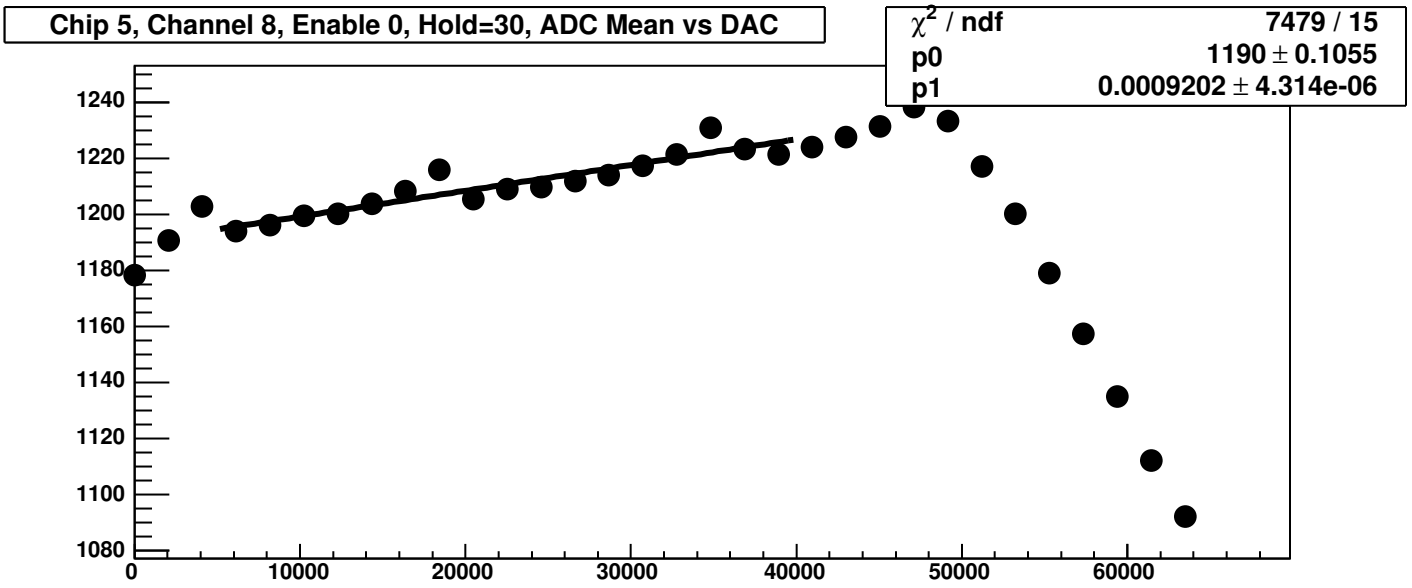

Chip 5, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

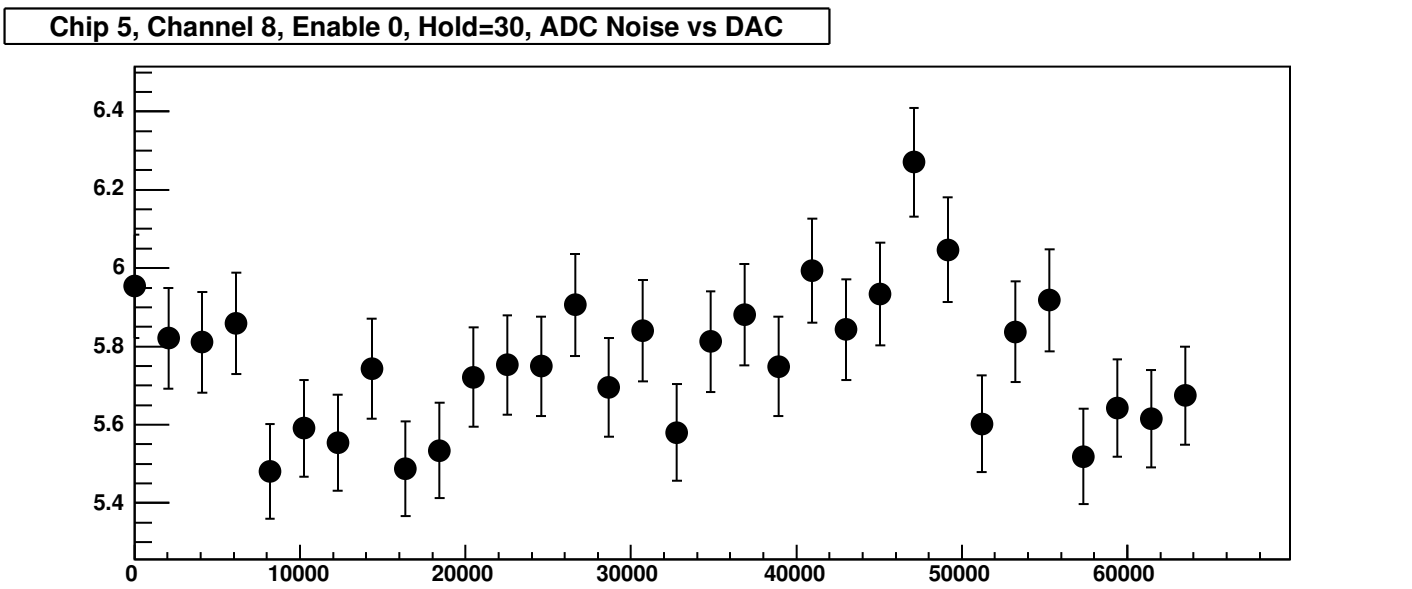

Chip 5, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

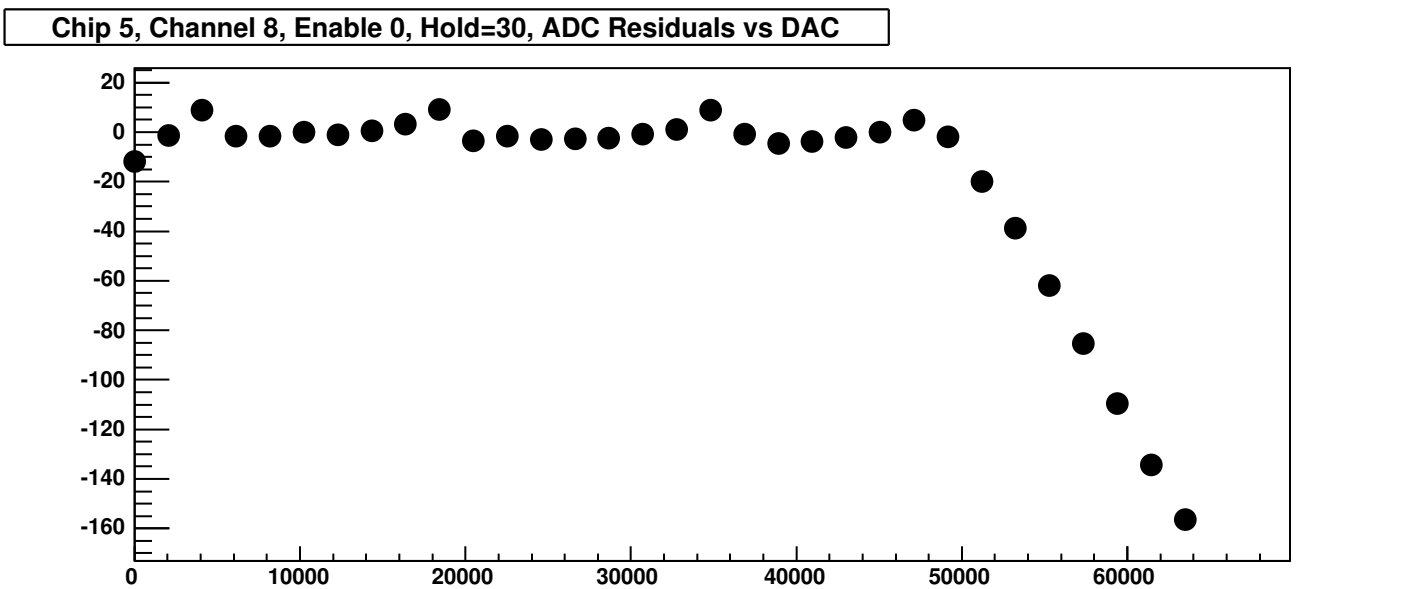

Chip 5, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7914 / 15
p0 1030 ± 0.1137
p1 0.002444 ± 4.762e-06
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

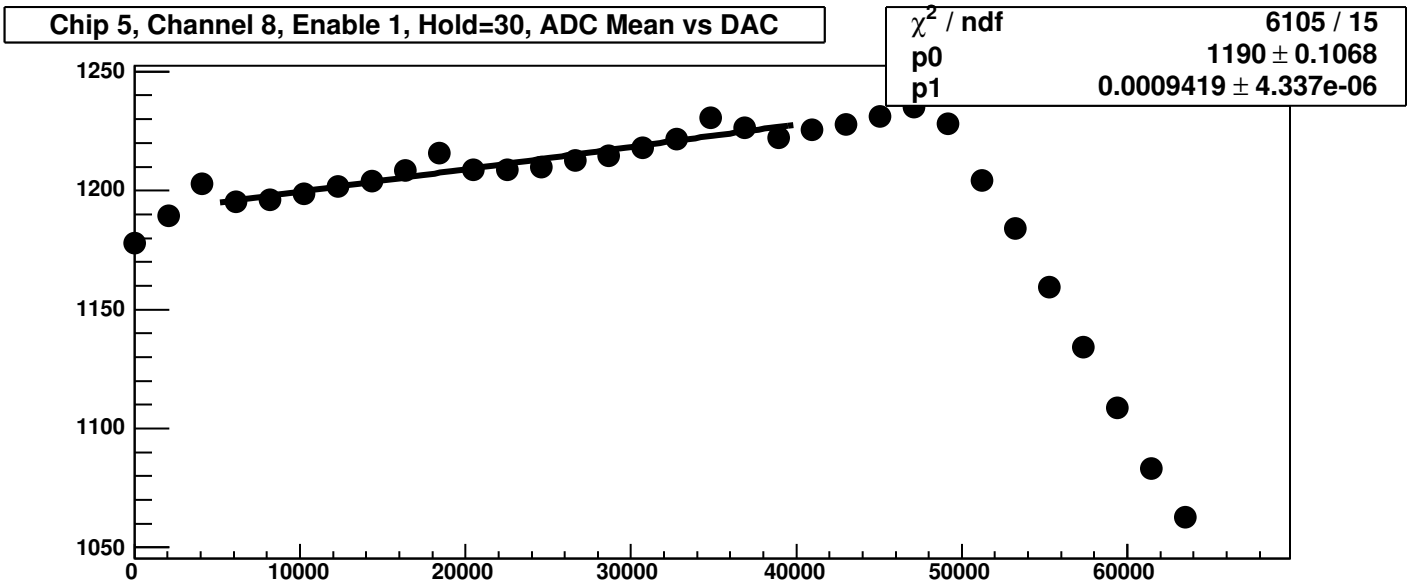

Chip 5, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

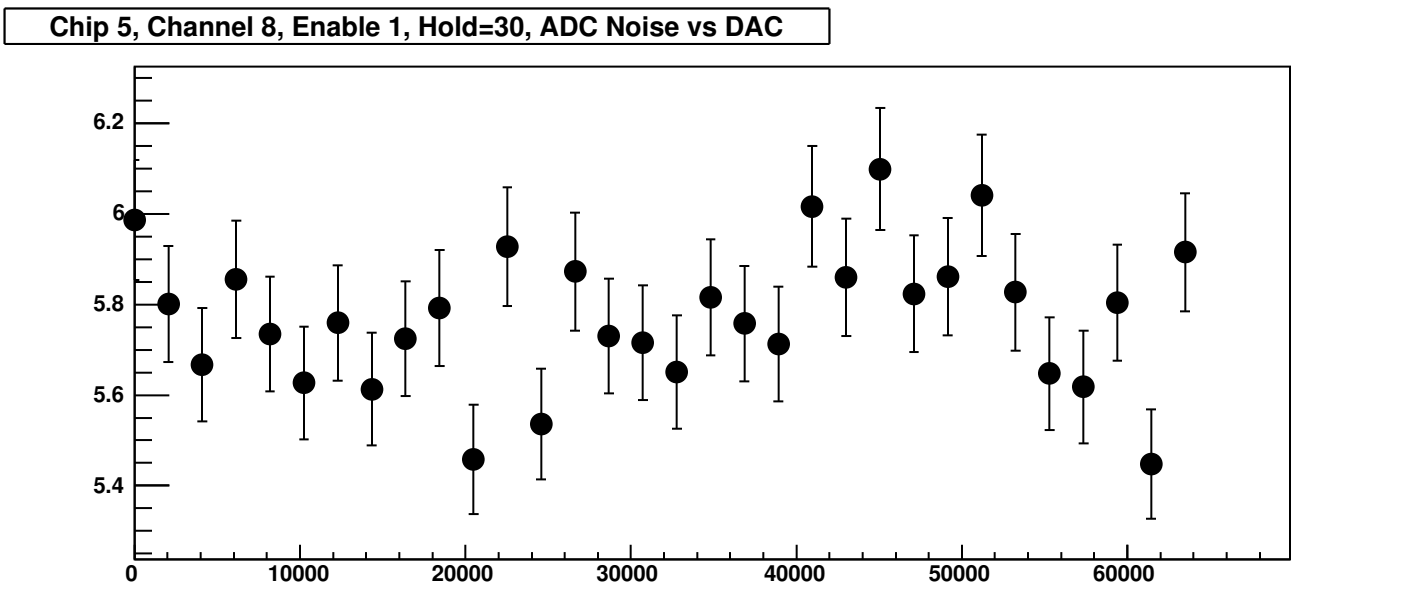

Chip 5, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

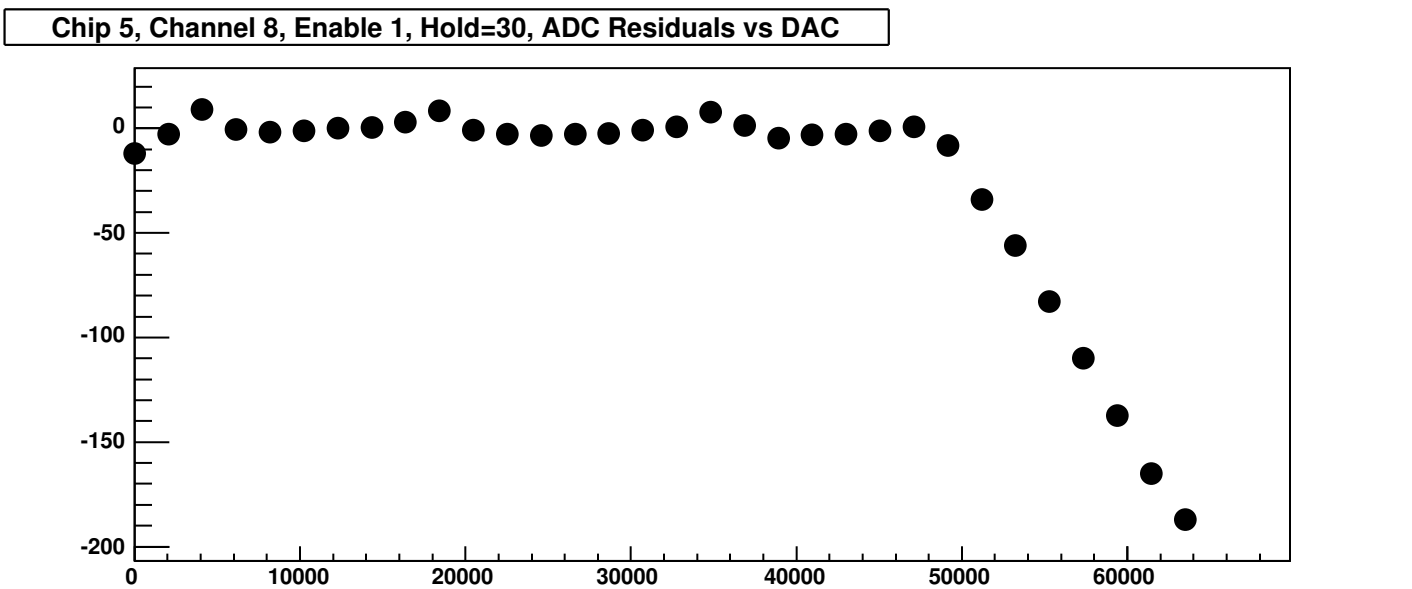

Chip 5, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7479 / 15
p0 1190 ± 0.1055
p1 0.0009202 ± 4.314e-06
1240
1220
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

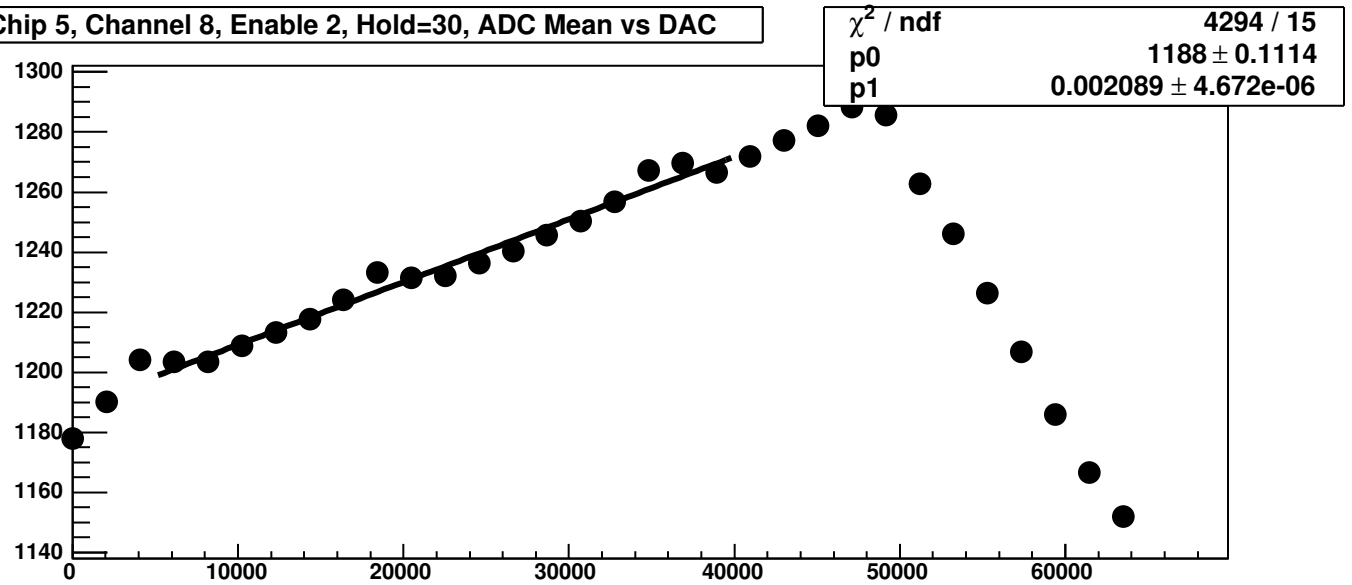

Chip 5, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6105 / 15
p0 1190 ± 0.1068
p1 0.0009419 ± 4.337e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

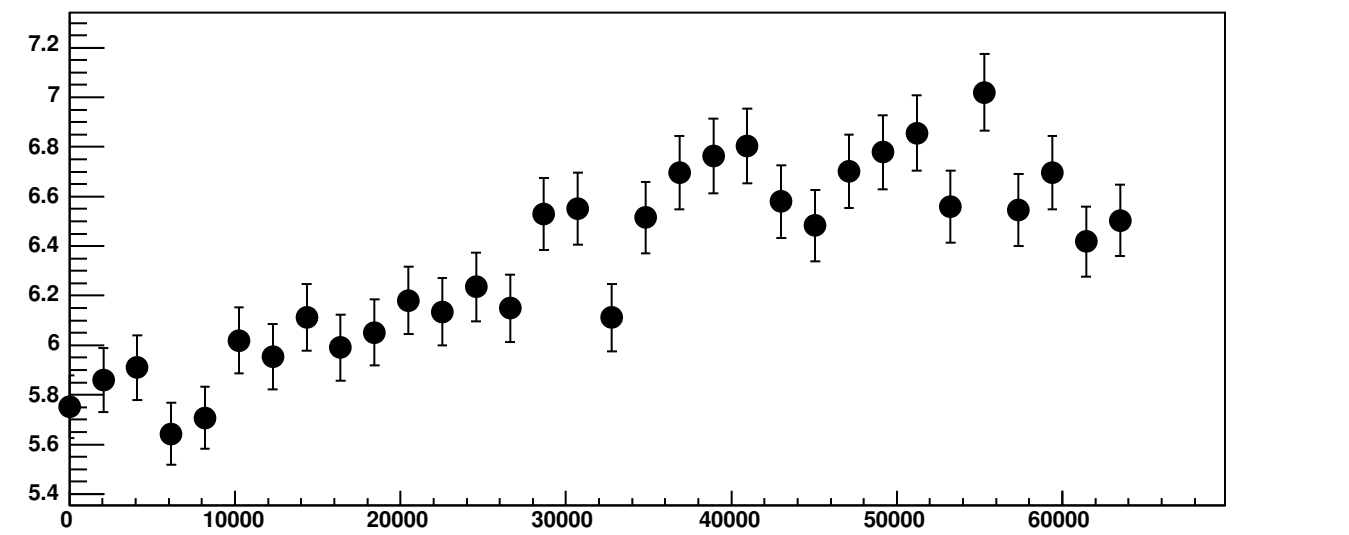

Chip 5, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

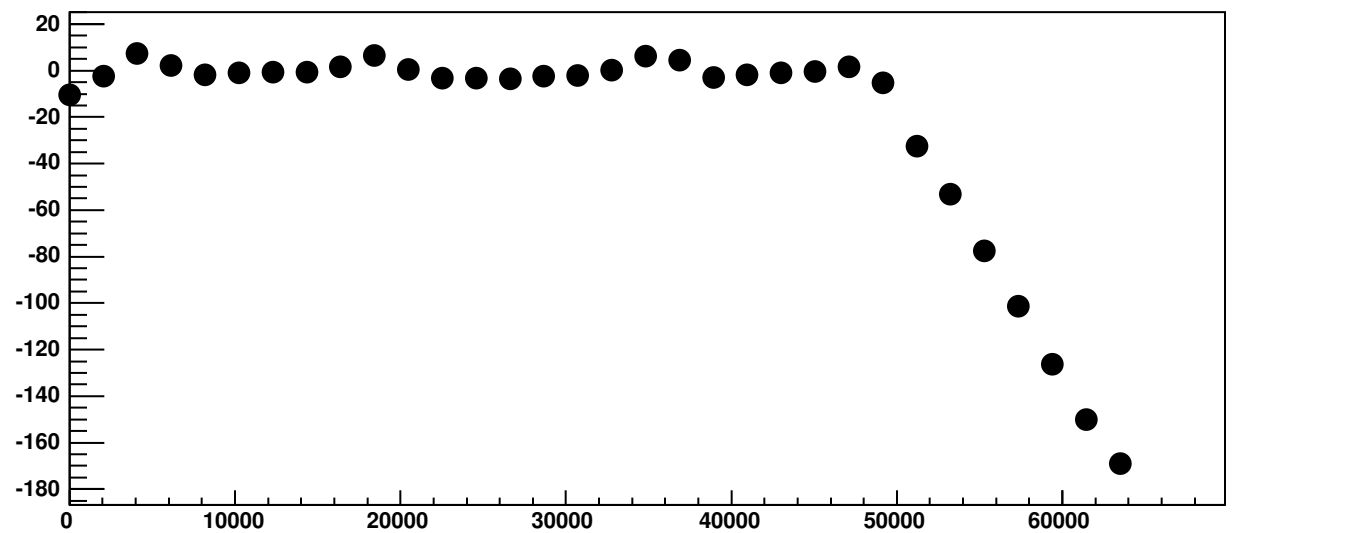
Chip 5, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC

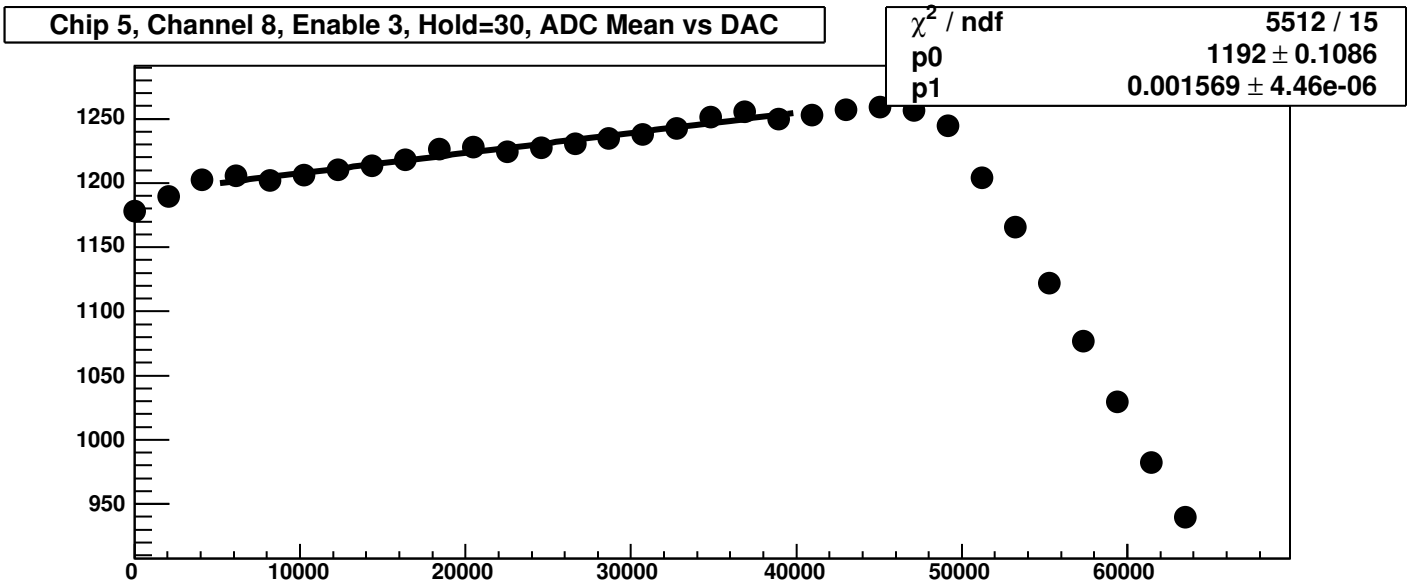


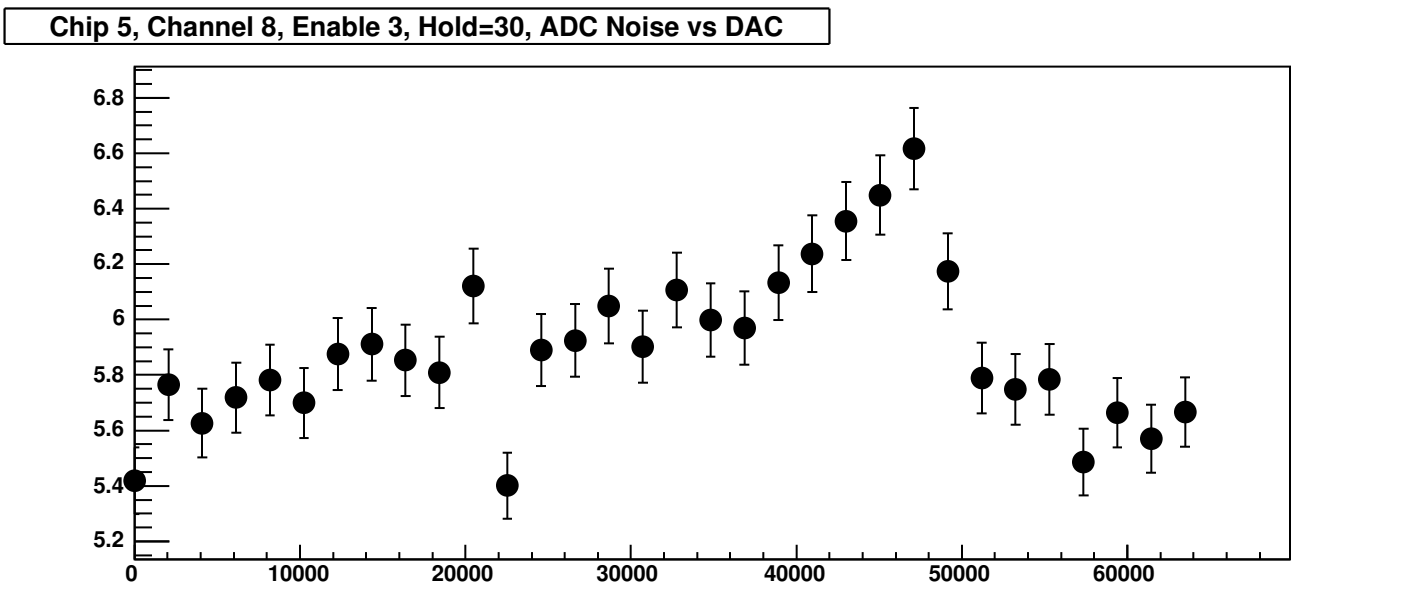
Chip 5, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC

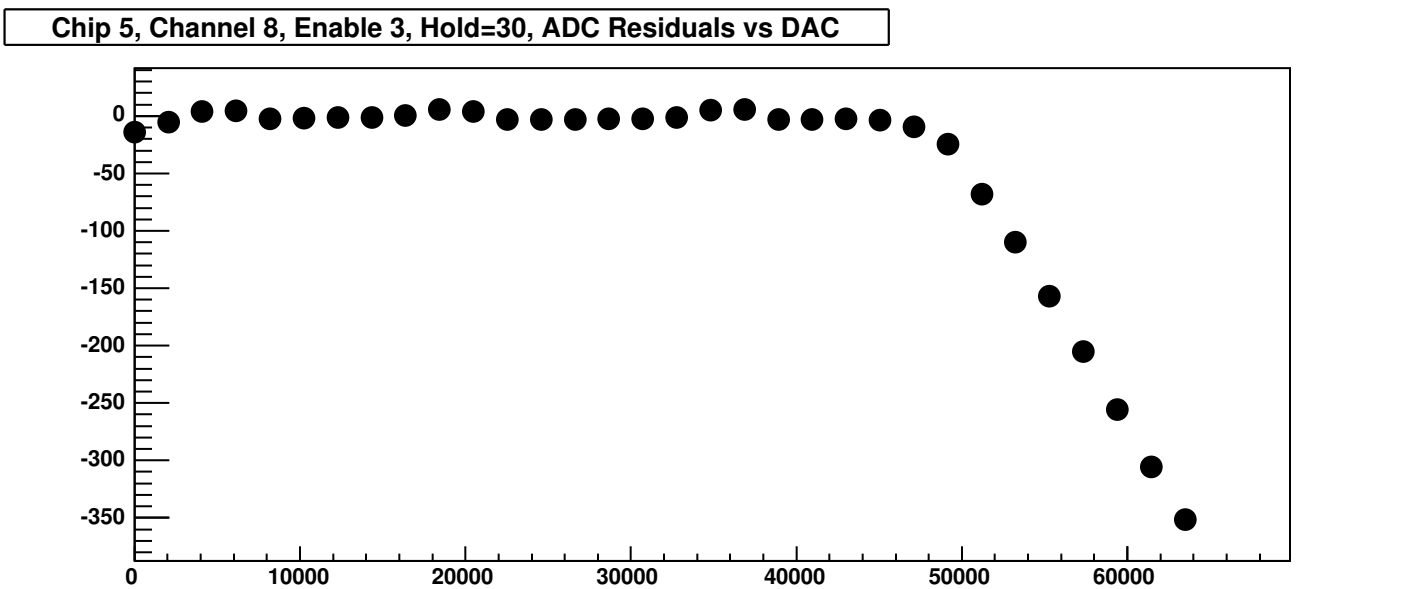


Chip 5, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC

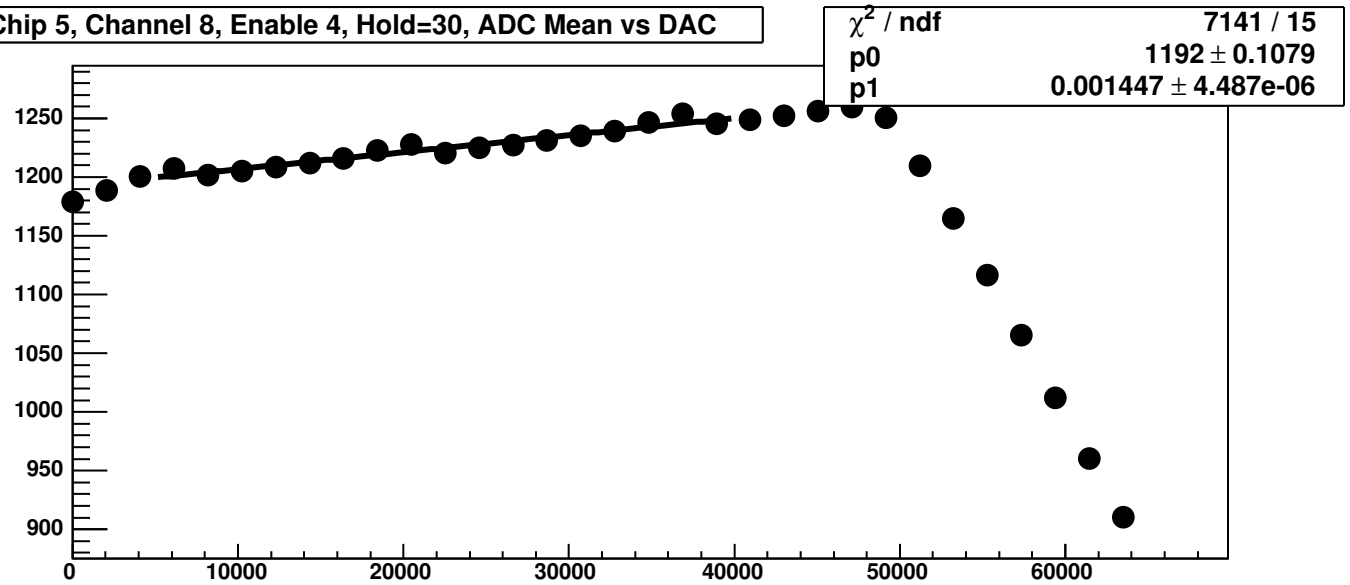



Chip 5, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5512 / 15
p0 1192 ± 0.1086
p1 0.001569 ± 4.46e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

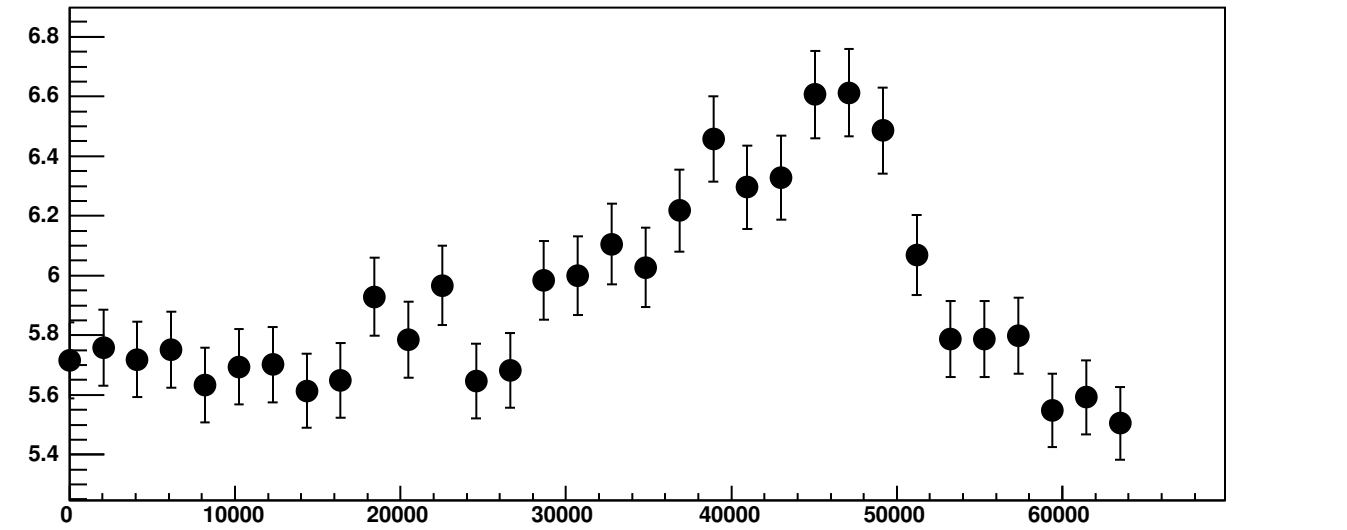

Chip 5, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

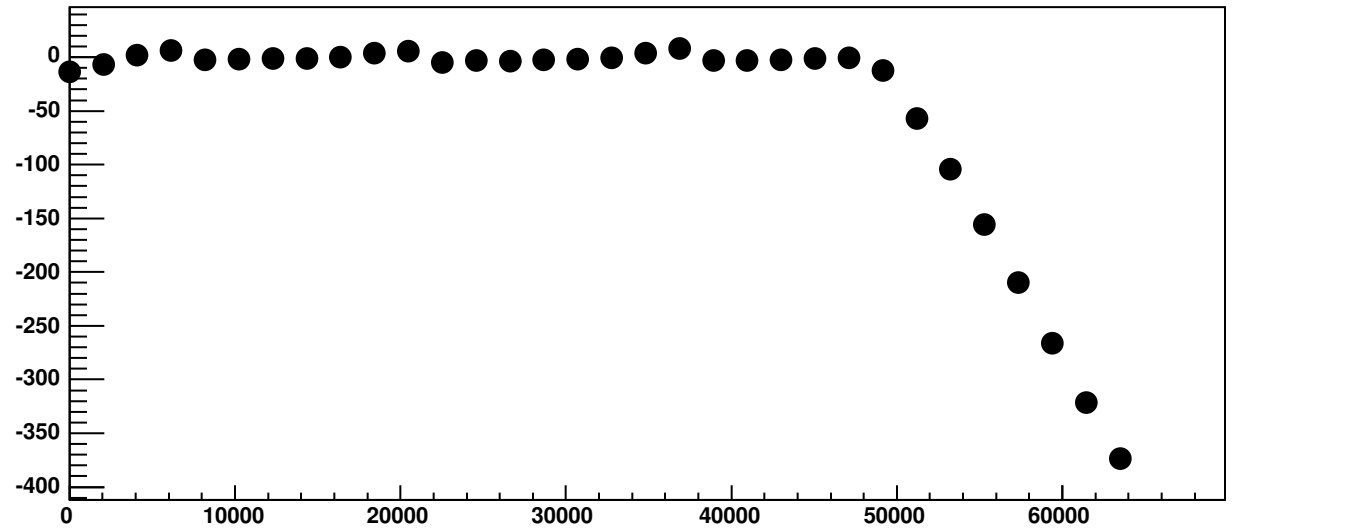
Chip 5, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

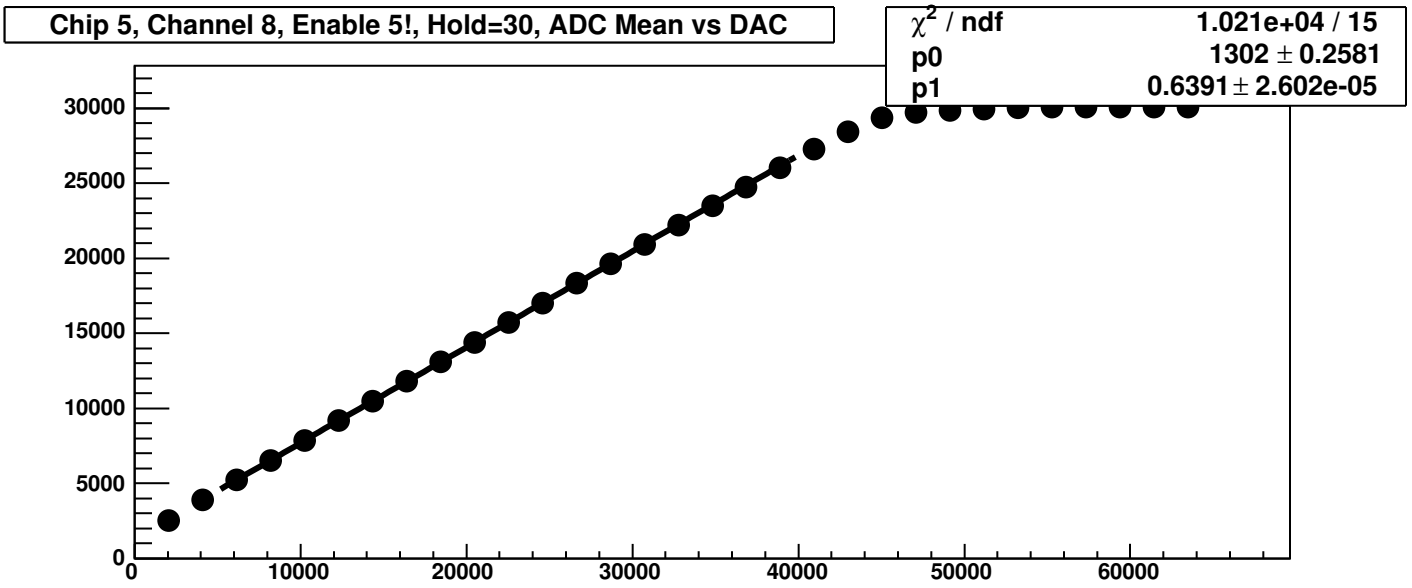


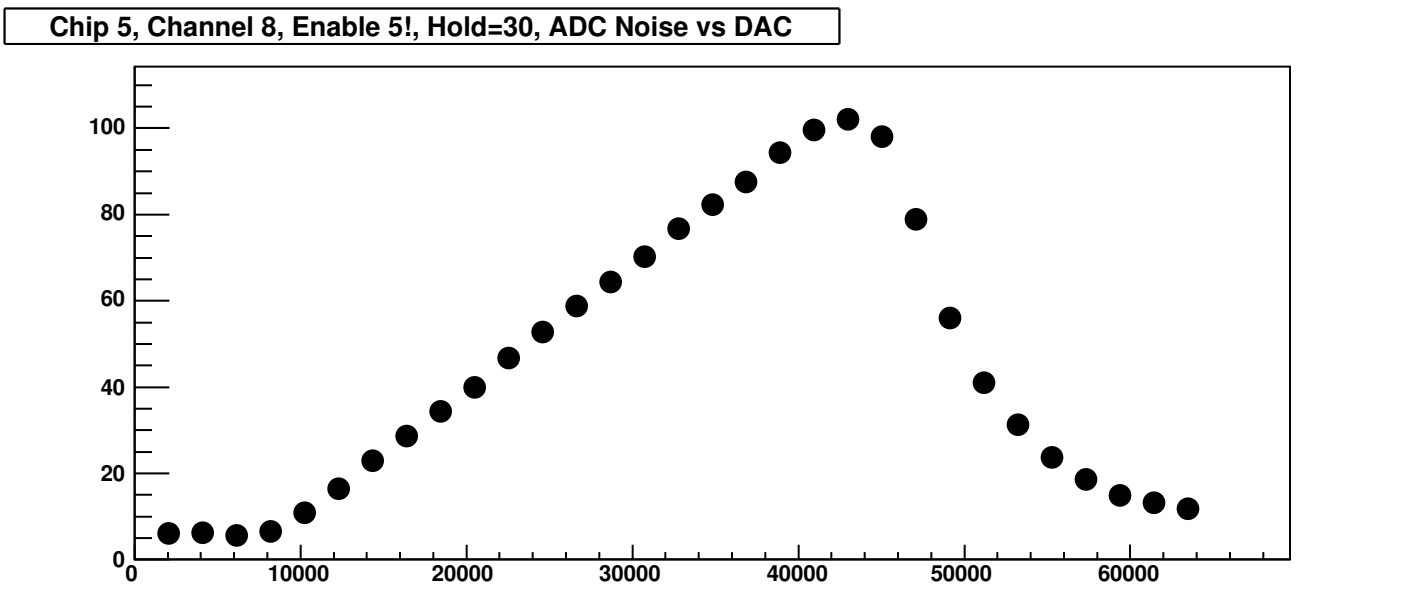
Chip 5, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC

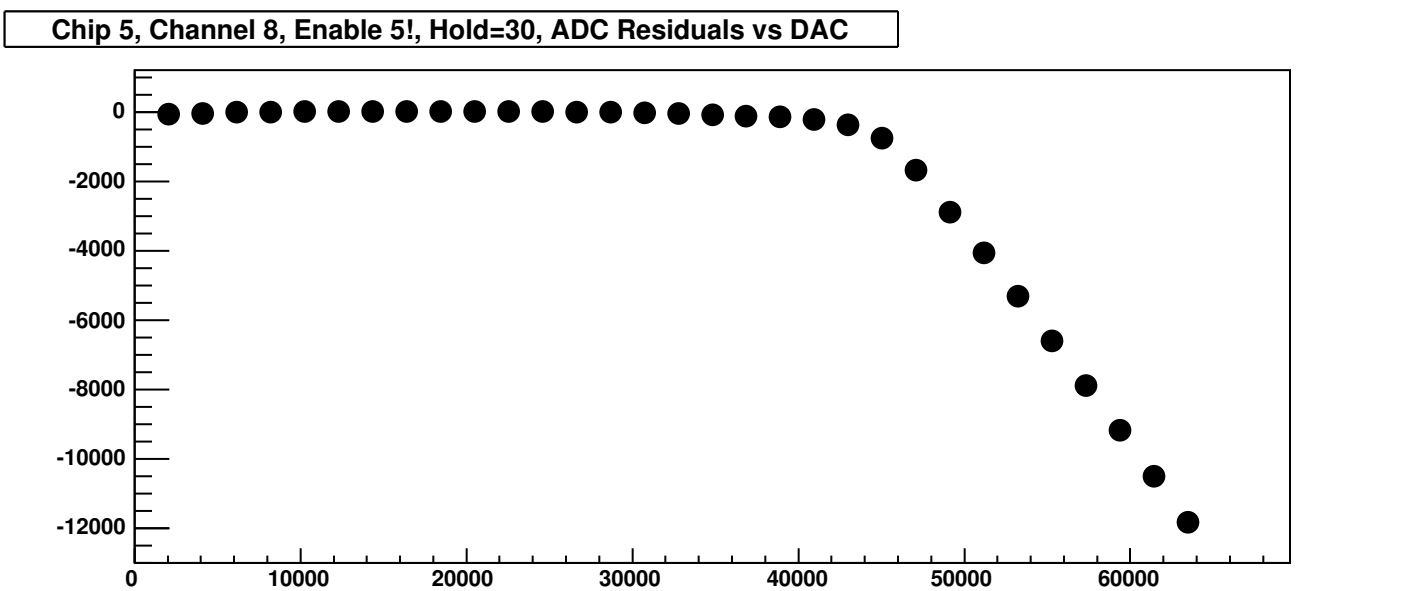


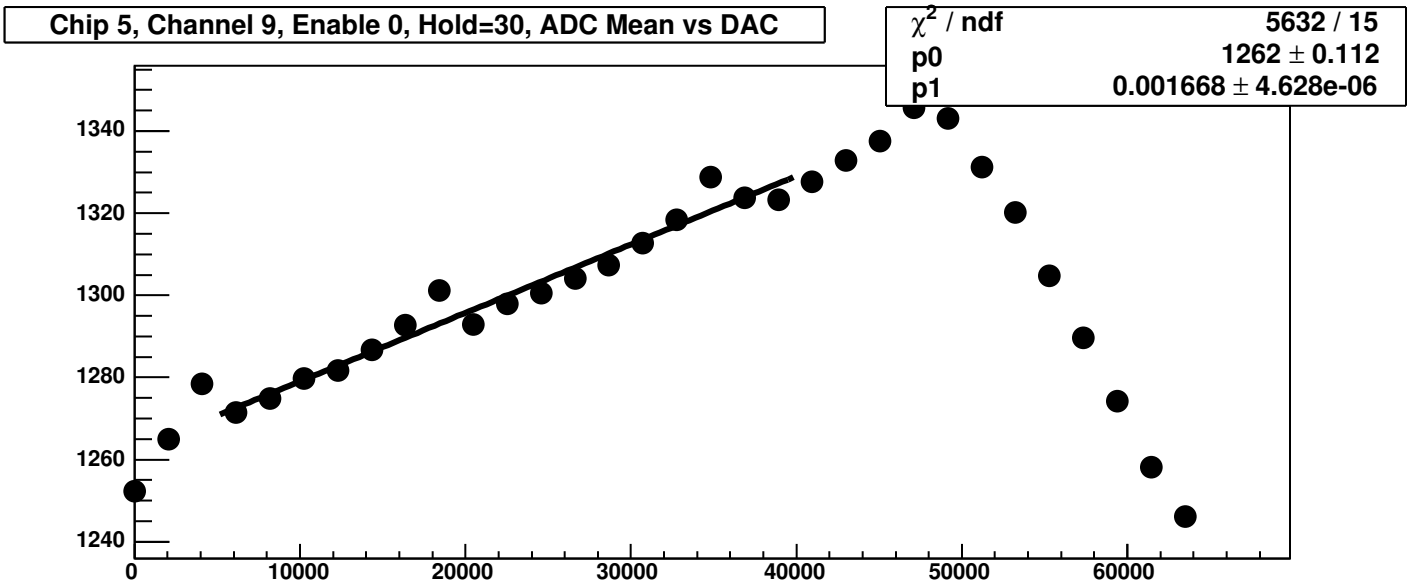
Chip 5, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC

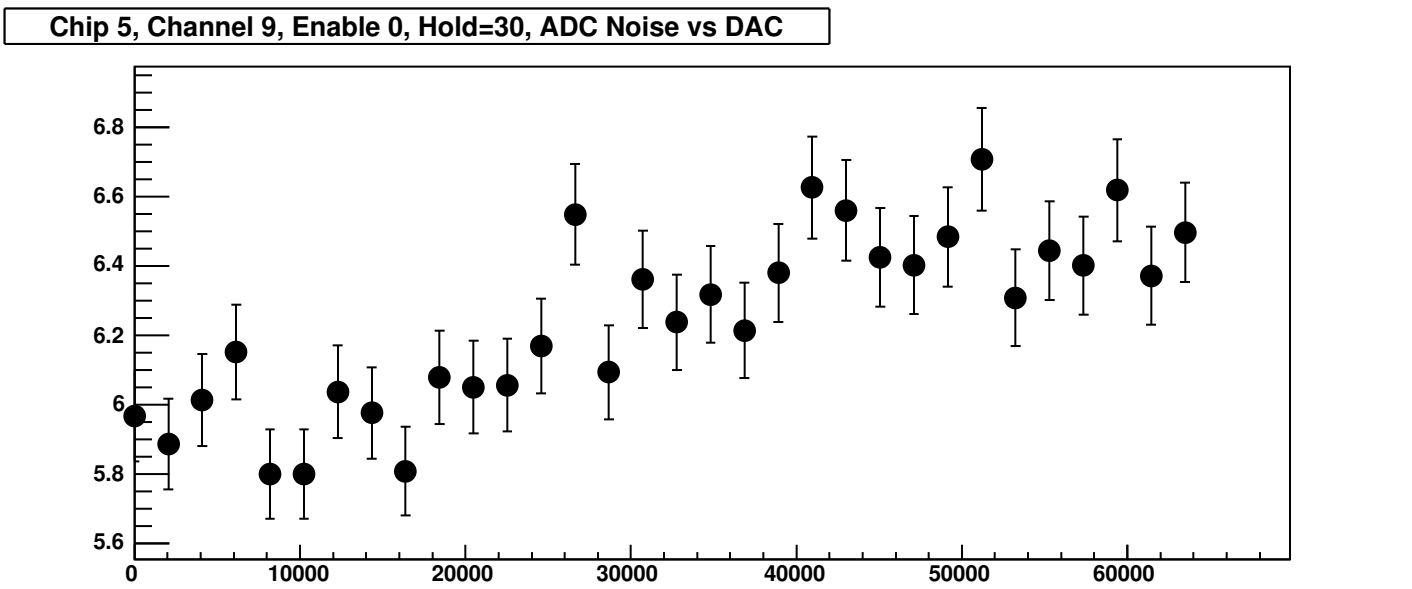


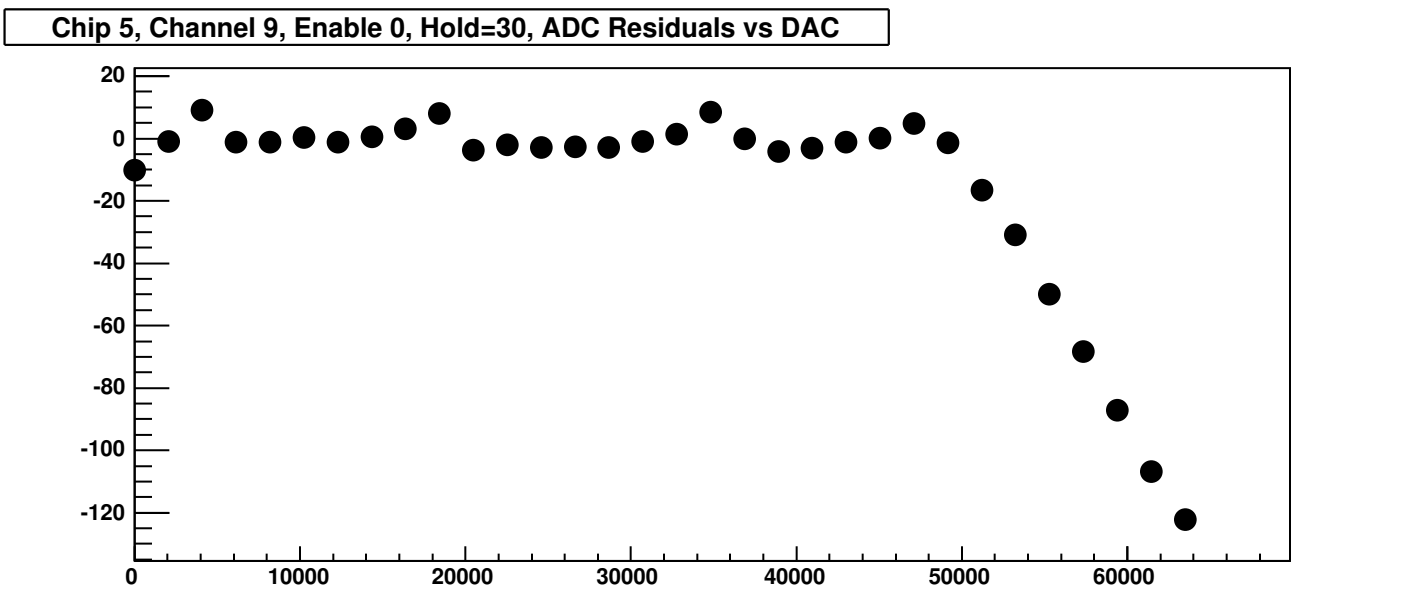

Chip 5, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.021e+04 / 15
p0 1302 ± 0.2581
p1 0.6391 ± 2.602e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

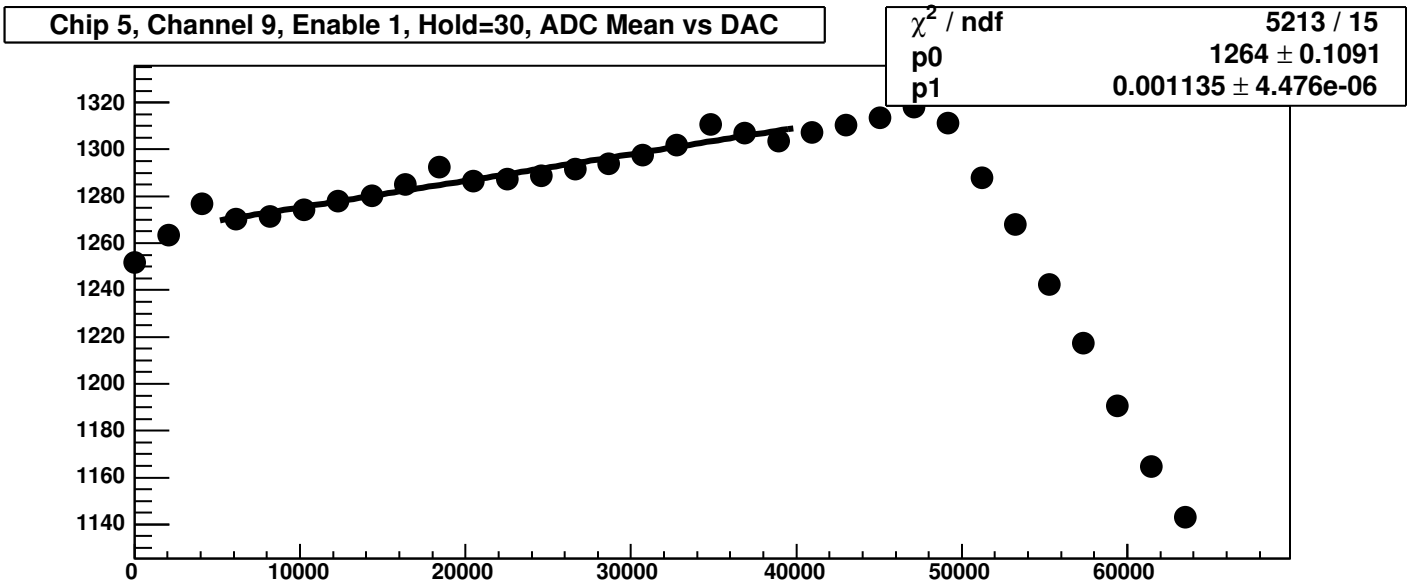

Chip 5, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

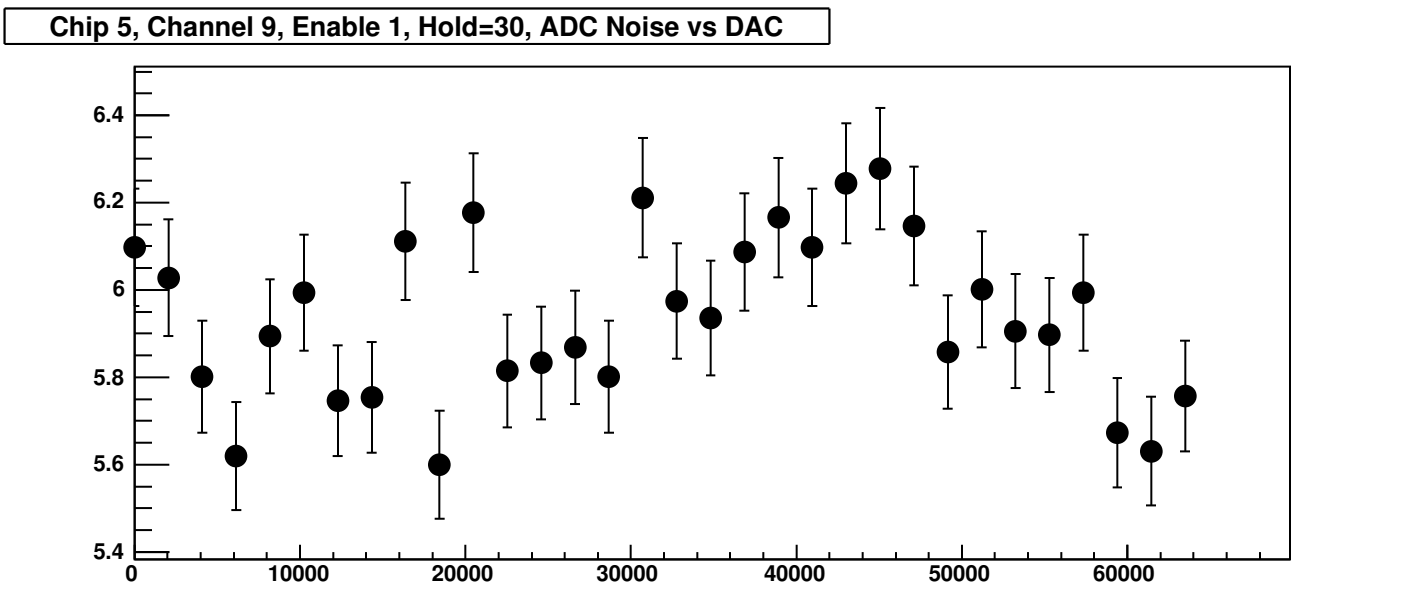

Chip 5, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

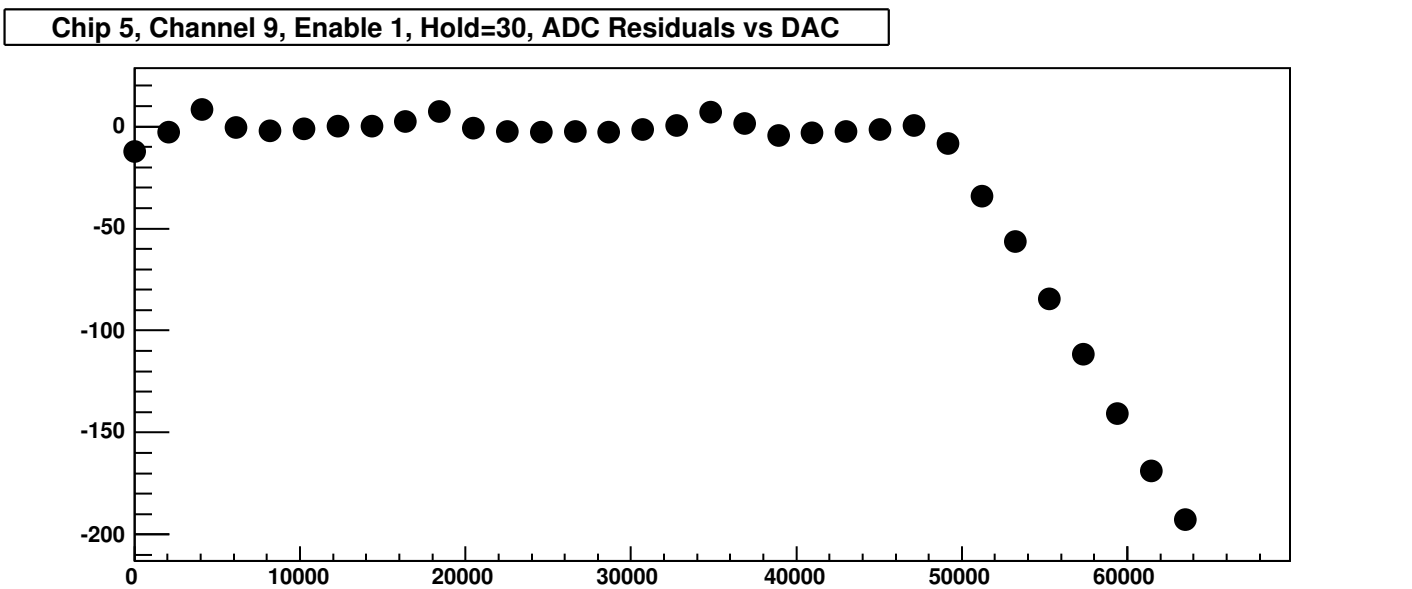

Chip 5, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5632 / 15
p0 1262 ± 0.112
p1 0.001668 ± 4.628e-06
1340
1320
1300
1280
1260
1240
0
10000
20000
30000
40000
50000
60000

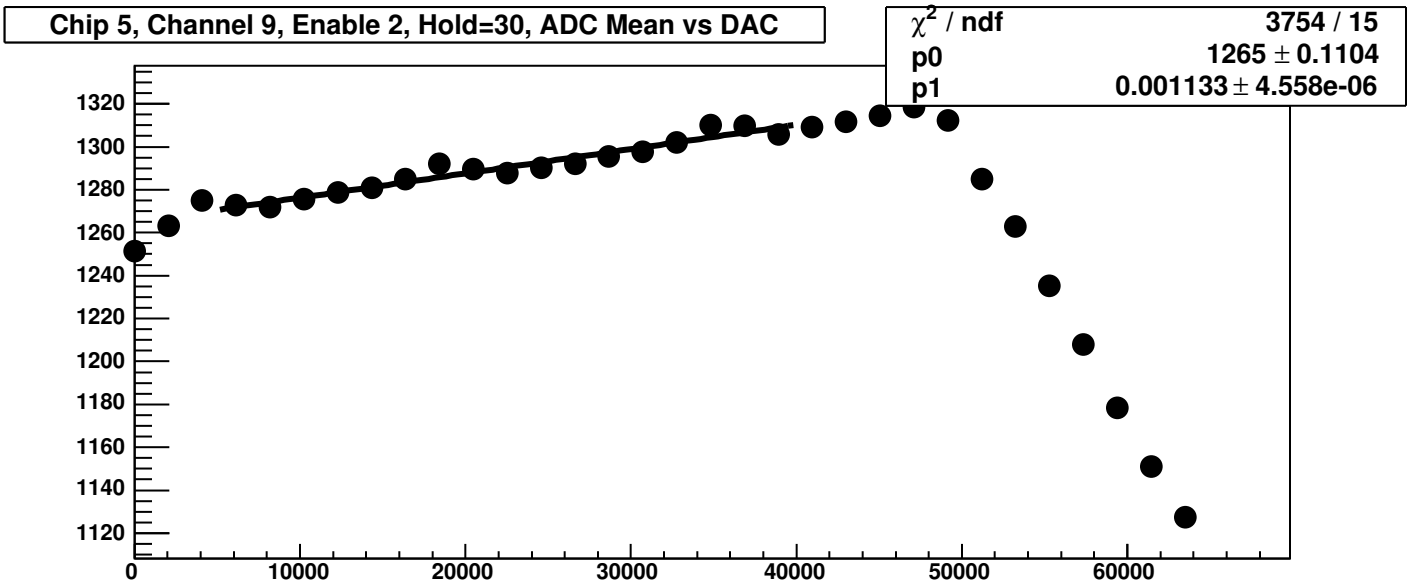

Chip 5, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

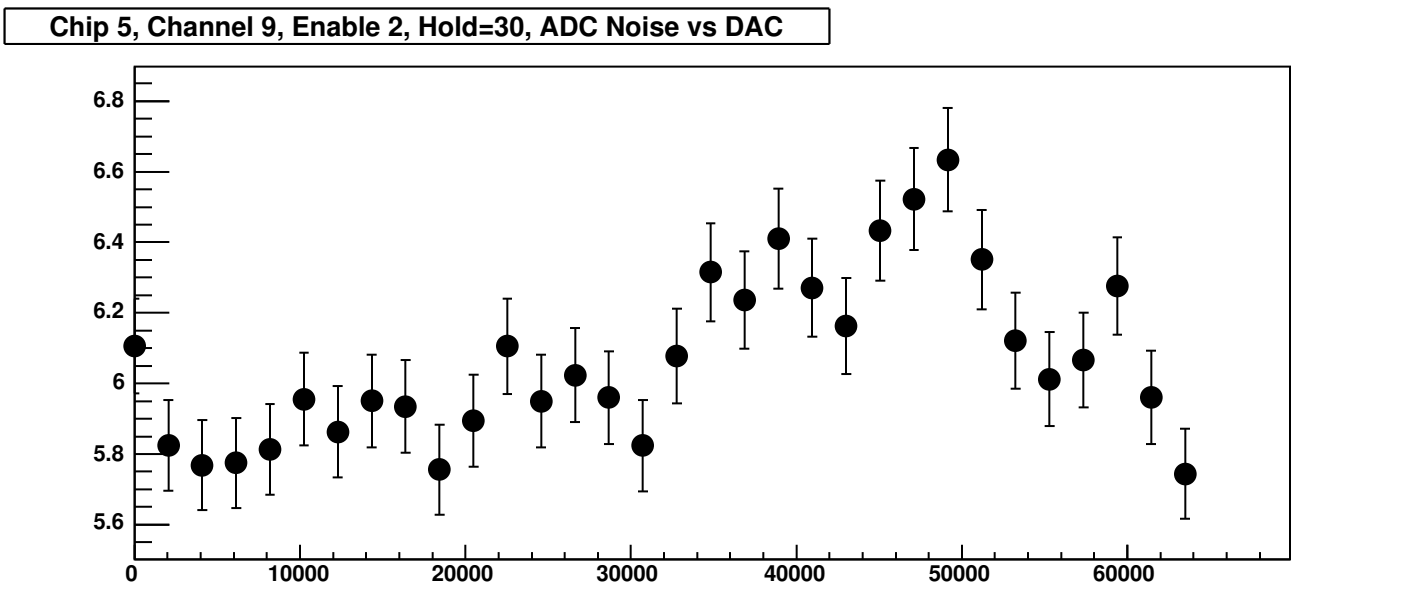

Chip 5, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

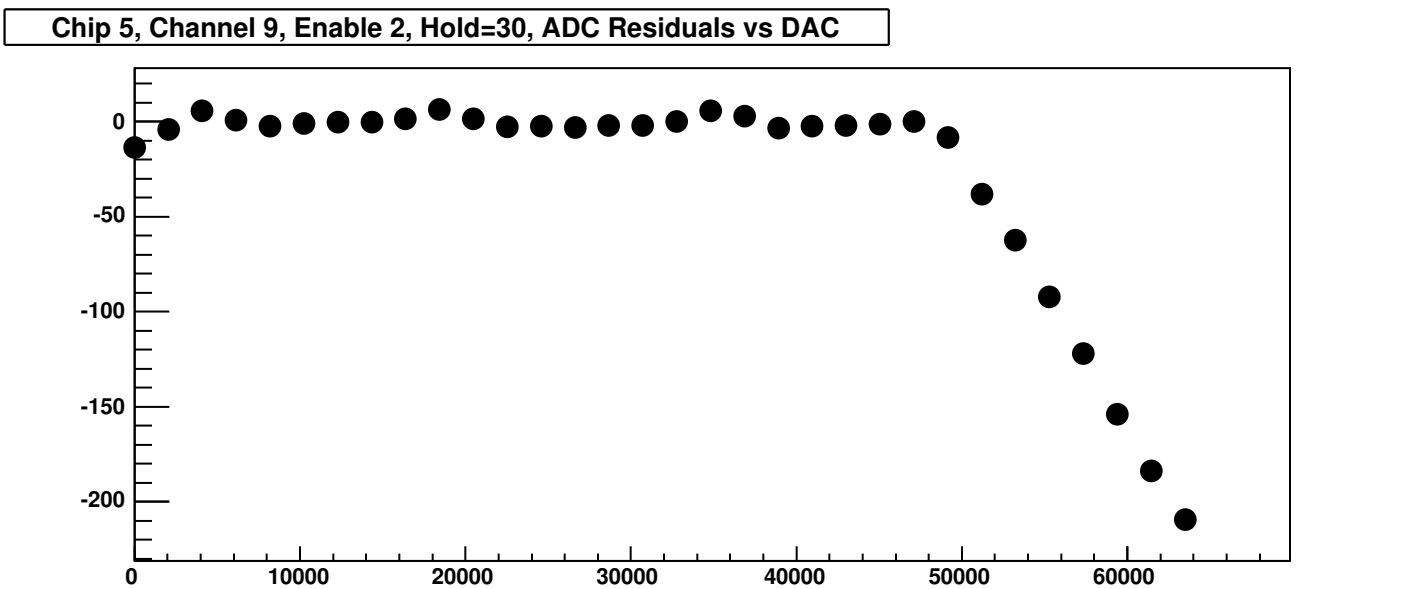

Chip 5, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5213 / 15
p0 1264 ± 0.1091
p1 0.001135 ± 4.476e-06
1320
1300
1280
1260
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

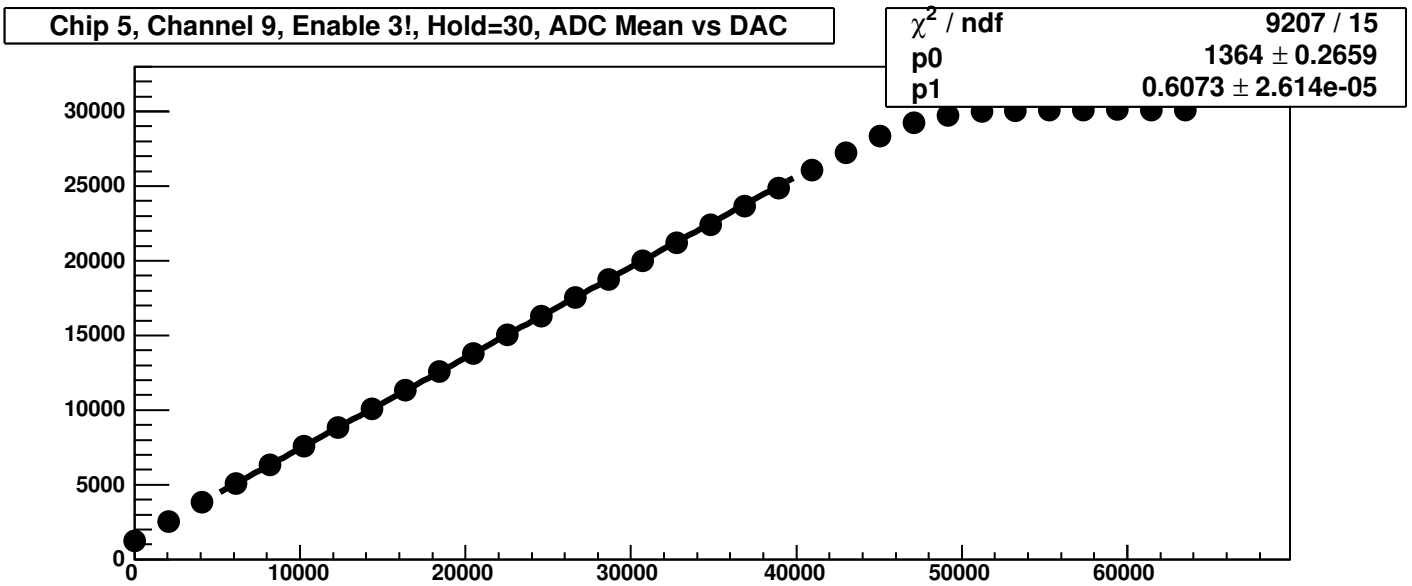

Chip 5, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

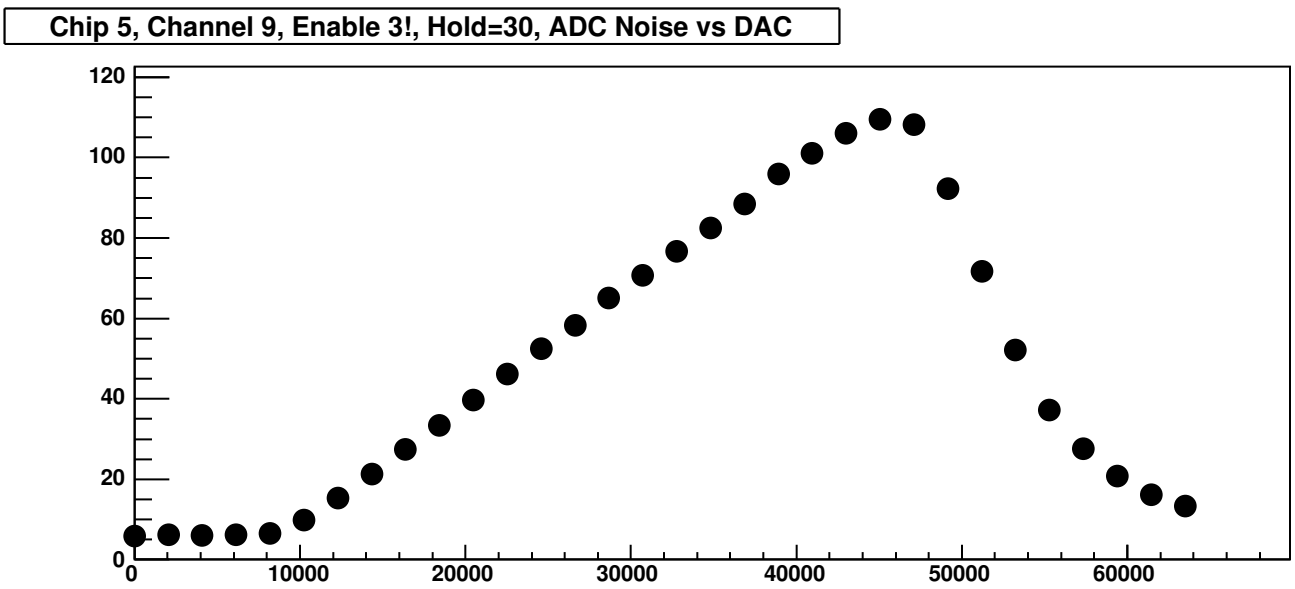

Chip 5, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

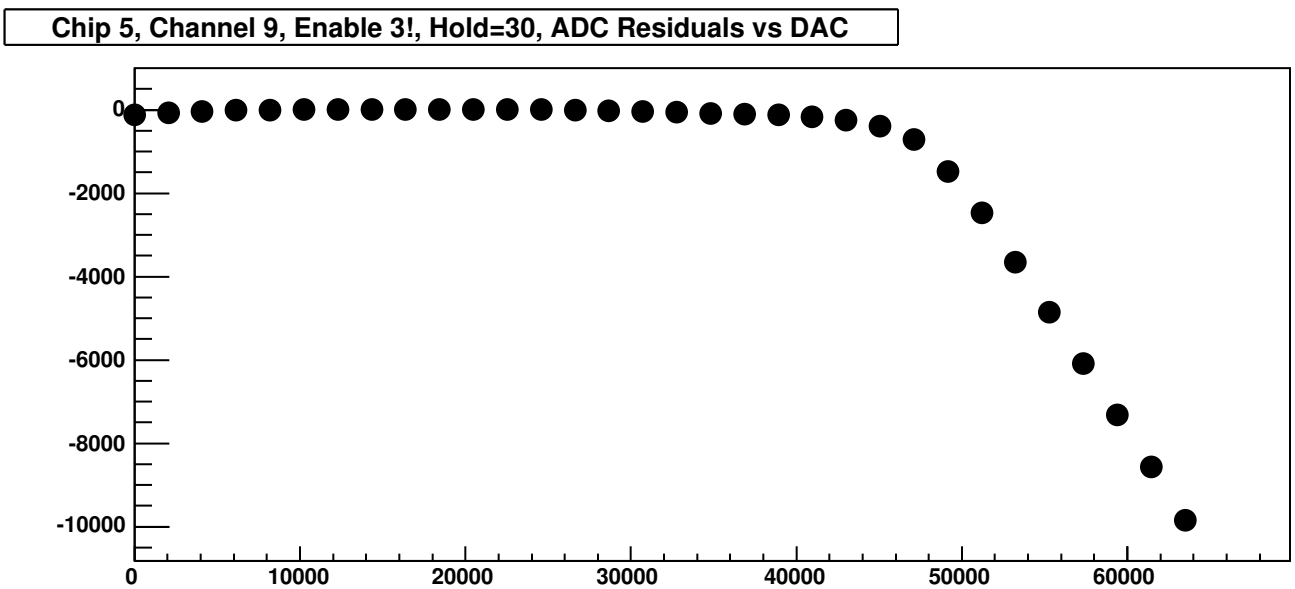

Chip 5, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3754 / 15
p0 1265 ± 0.1104
p1 0.001133 ± 4.558e-06
1320
1300
1280
1260
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000

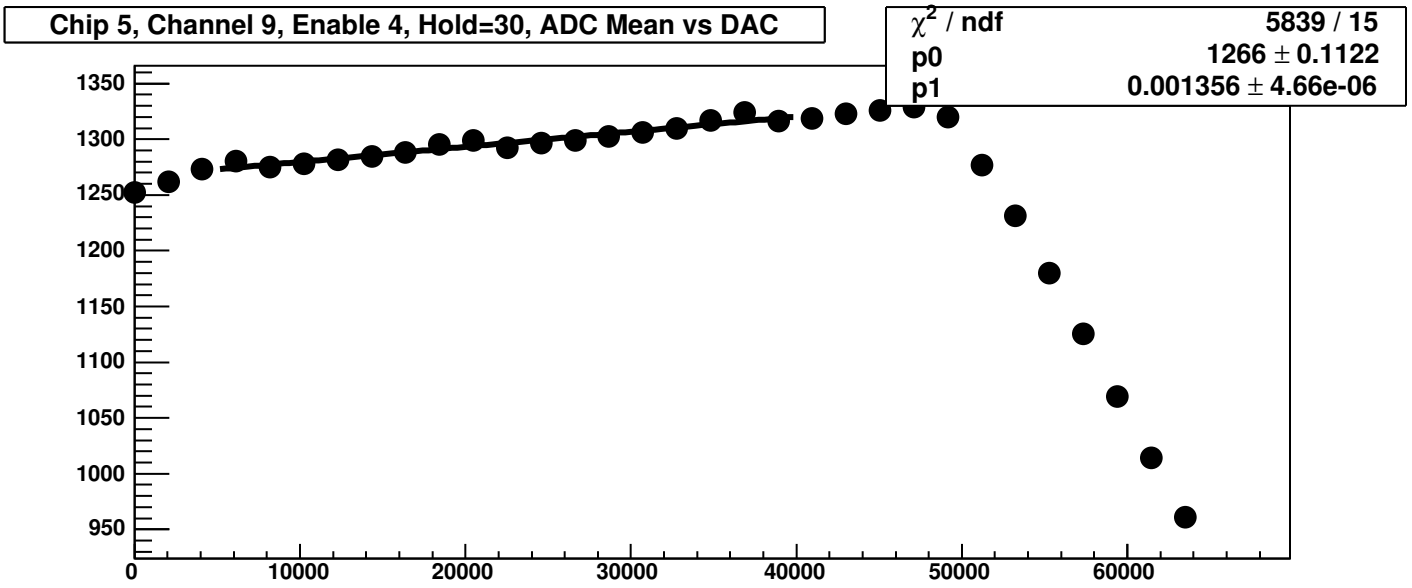

Chip 5, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

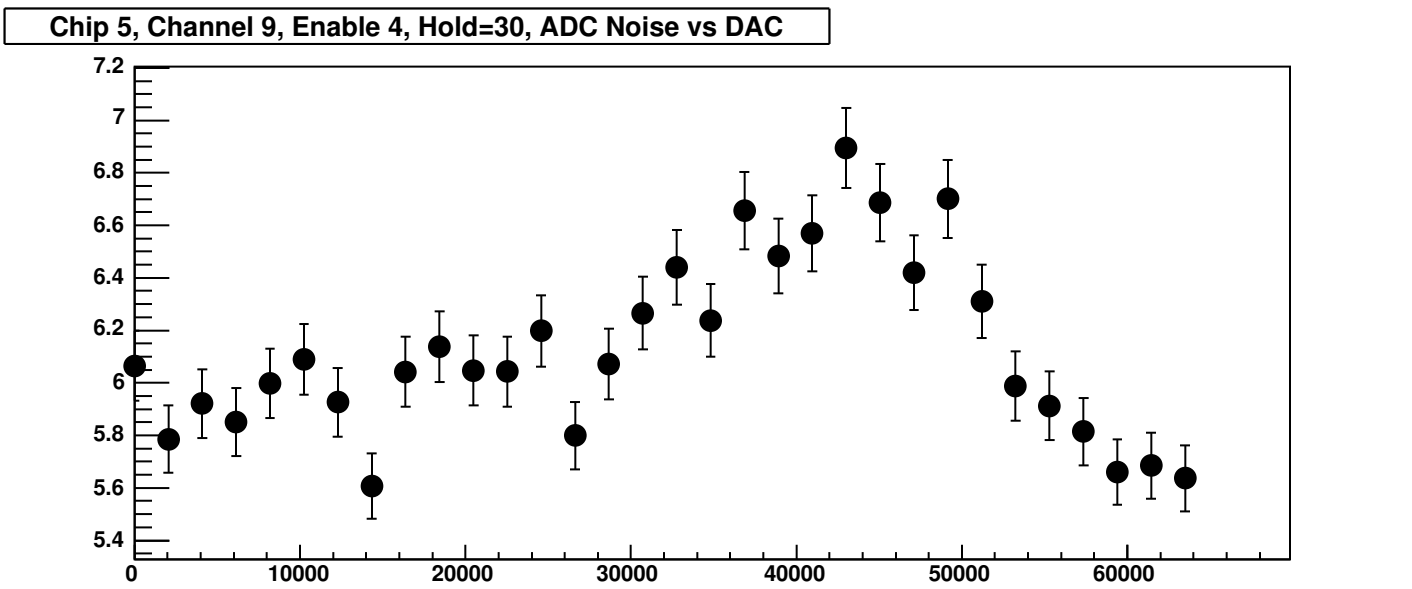

Chip 5, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

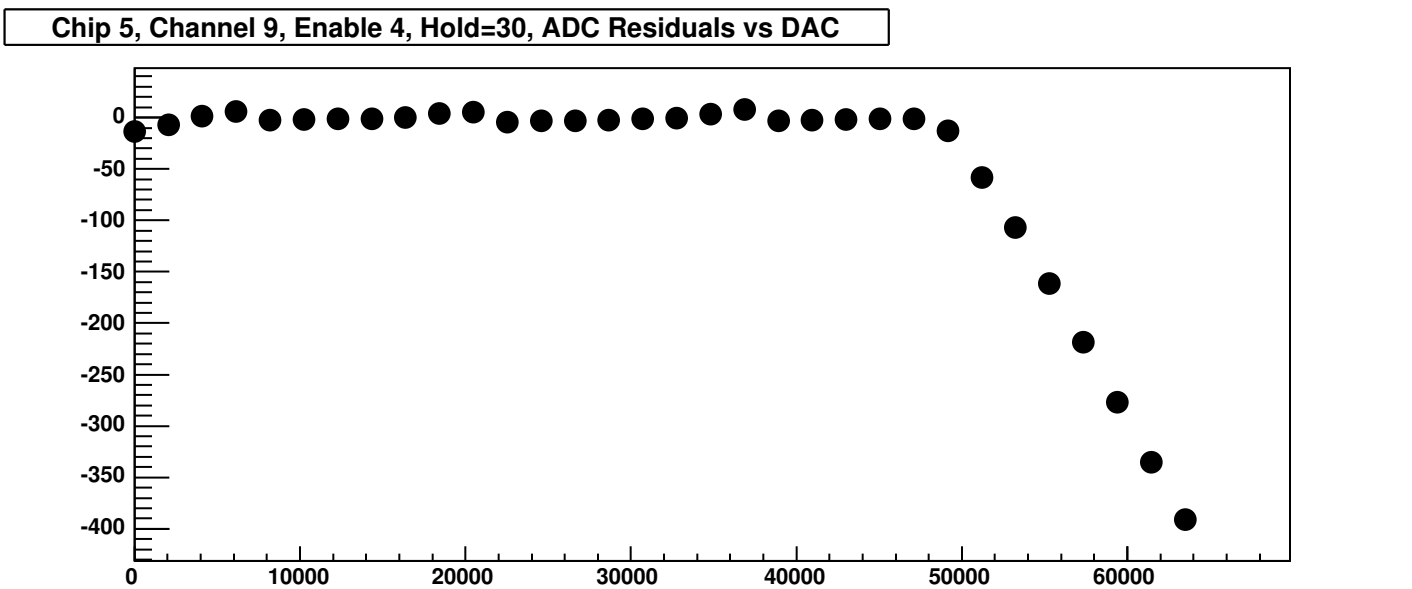

Chip 5, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 9207 / 15
p0 1364 ± 0.2659
p1 0.6073 ± 2.614e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

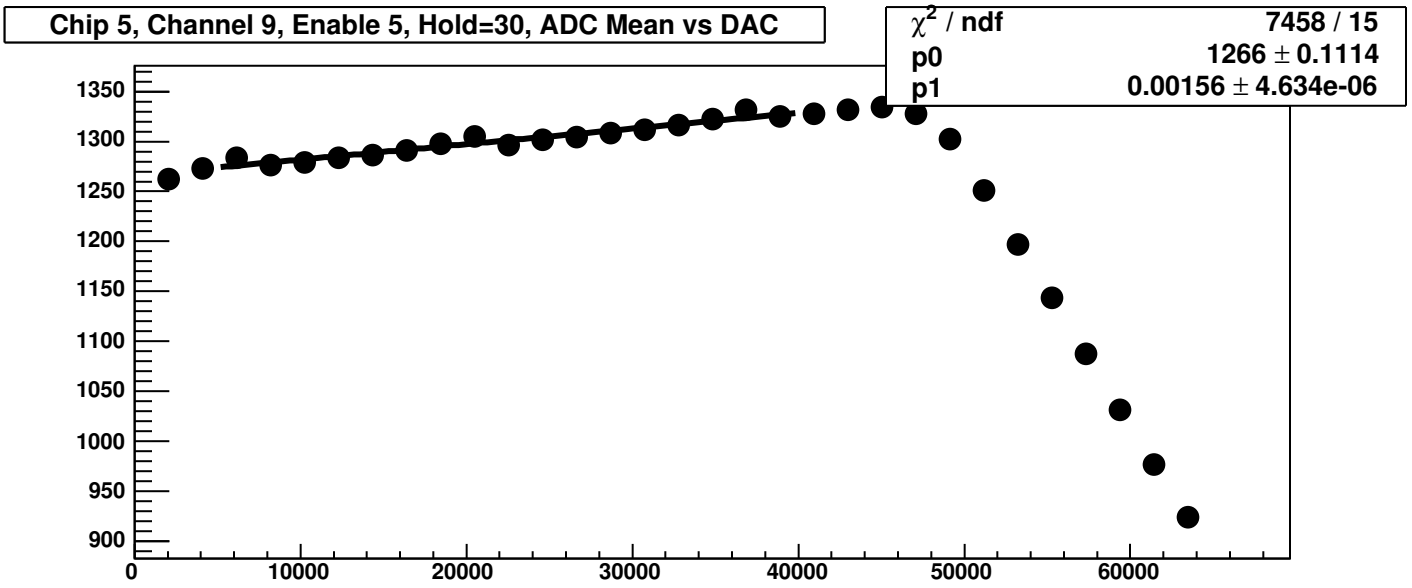

Chip 5, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

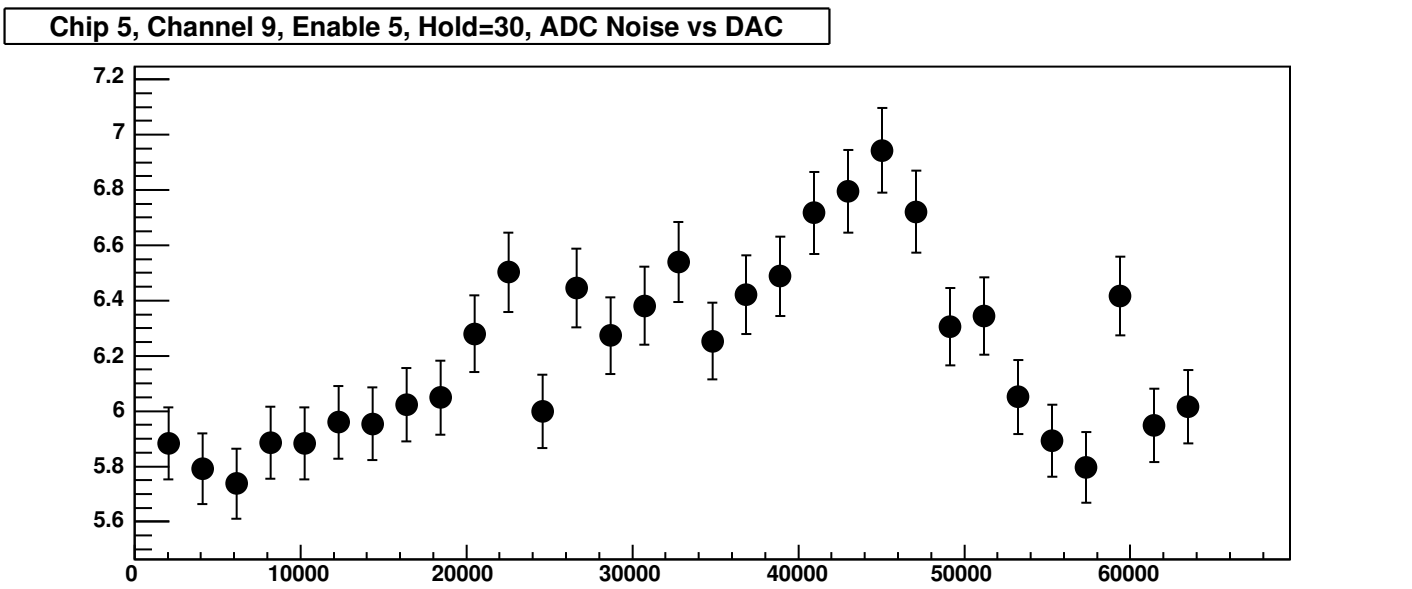

Chip 5, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

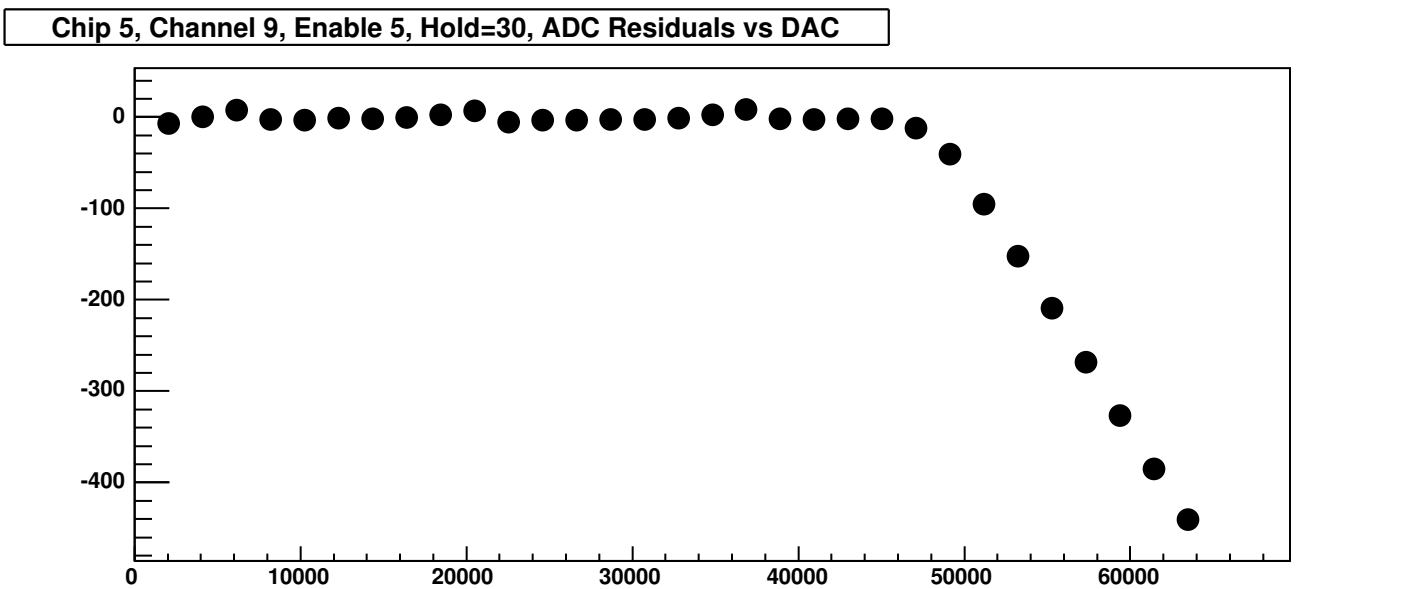

Chip 5, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5839 / 15
p0 1266 ± 0.1122
p1 0.001356 ± 4.66e-06
1350
1300
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

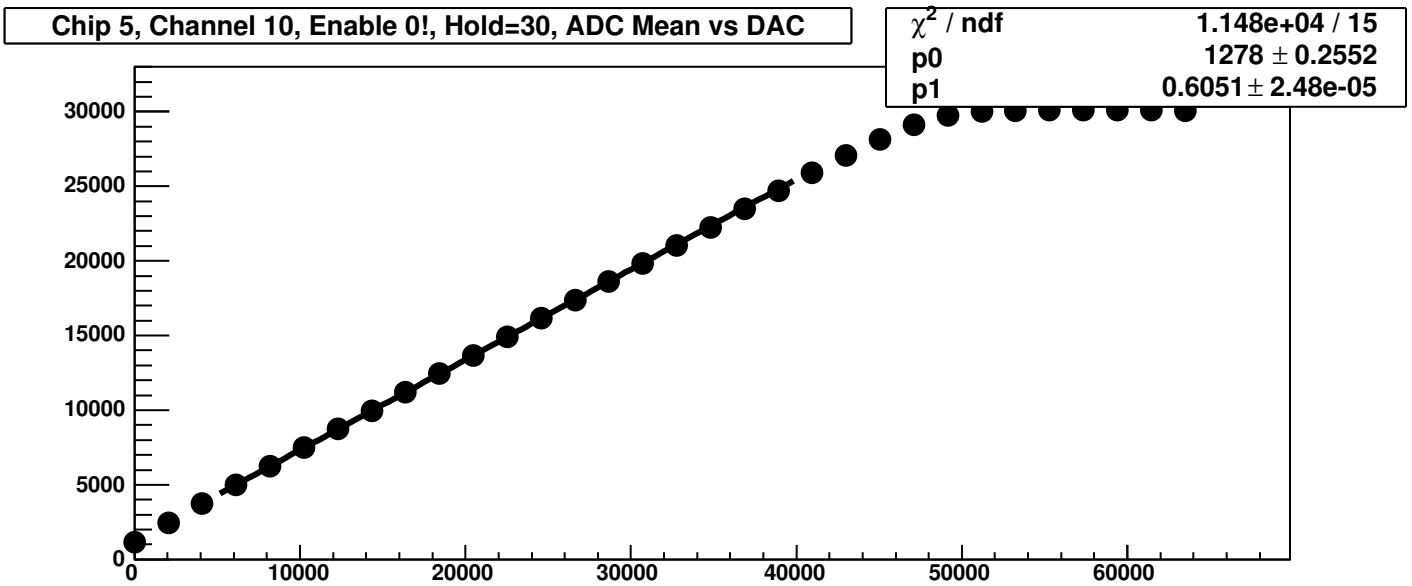

Chip 5, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

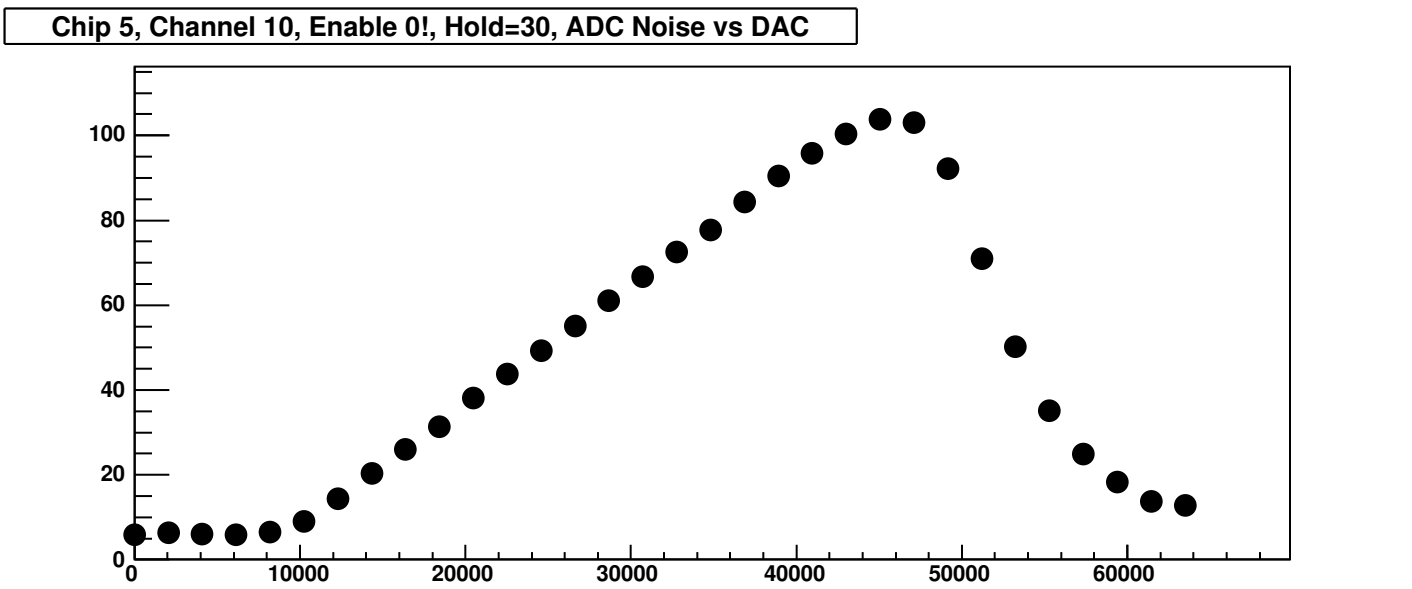

Chip 5, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

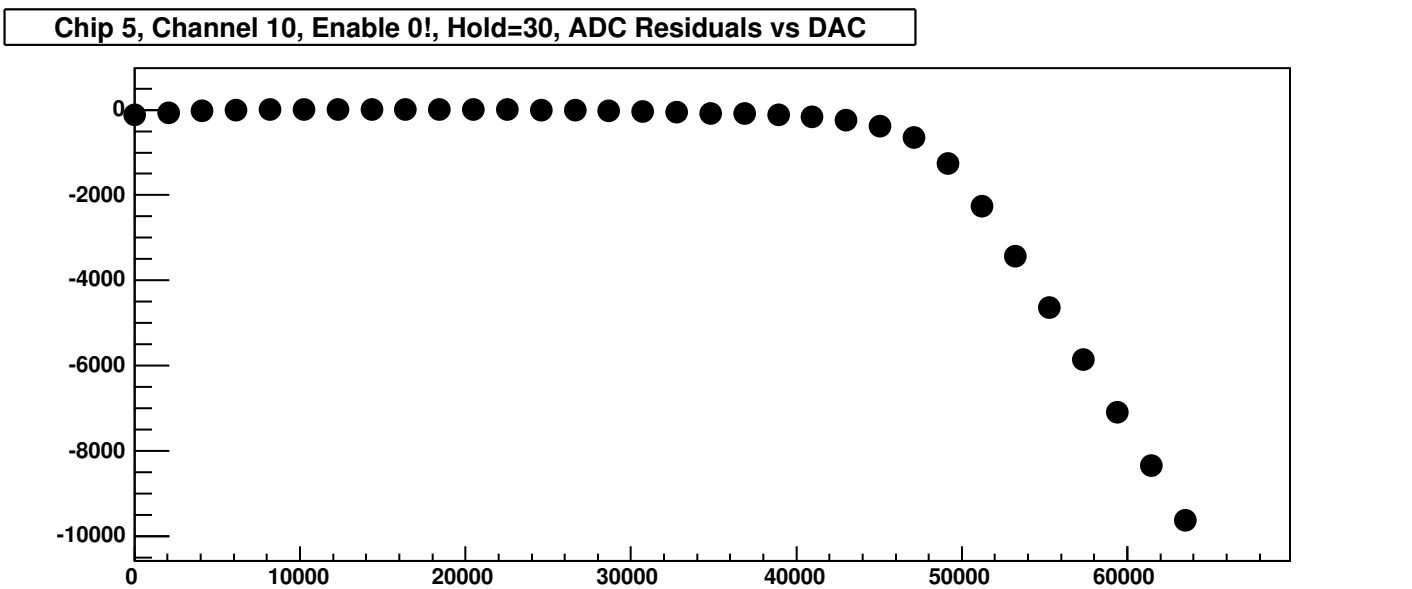

Chip 5, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7458 / 15
p0 1266 ± 0.1114
p1 0.00156 ± 4.634e-06
1350
1300
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

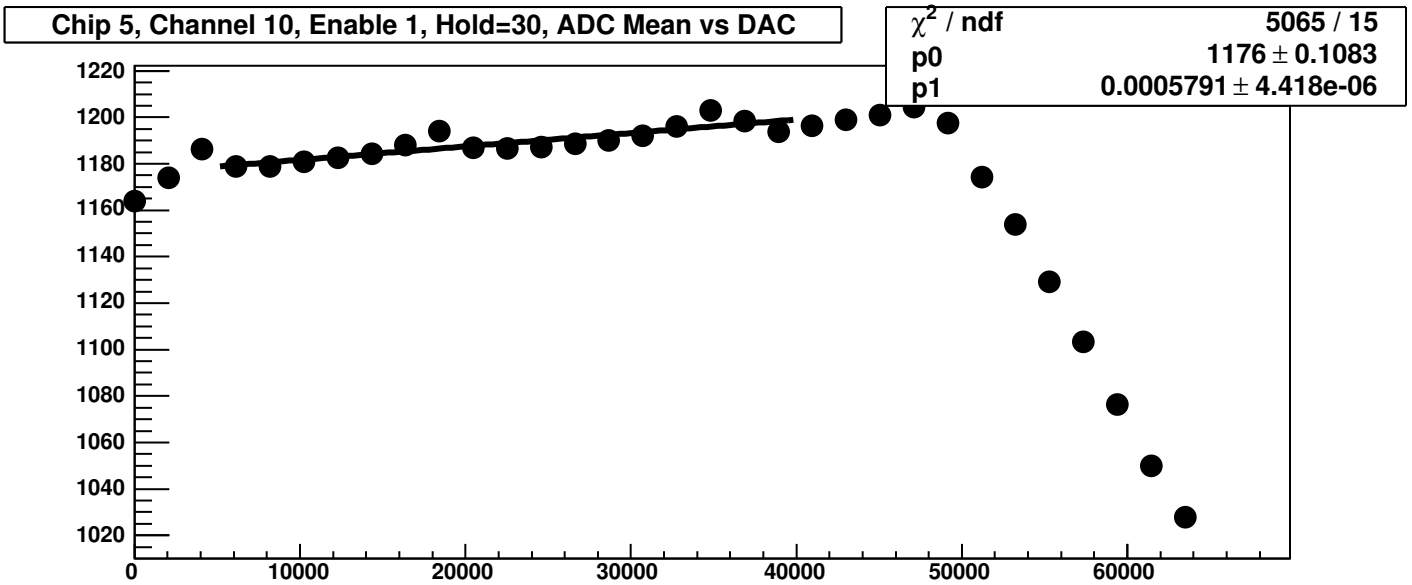

Chip 5, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

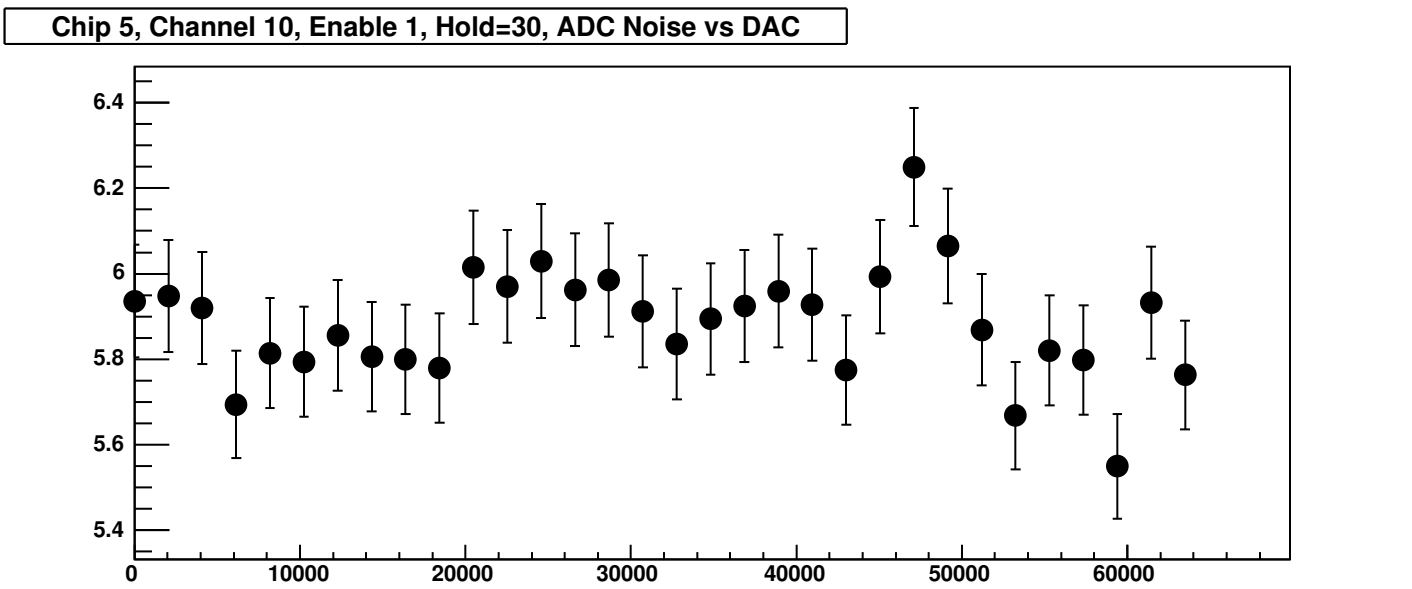

Chip 5, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

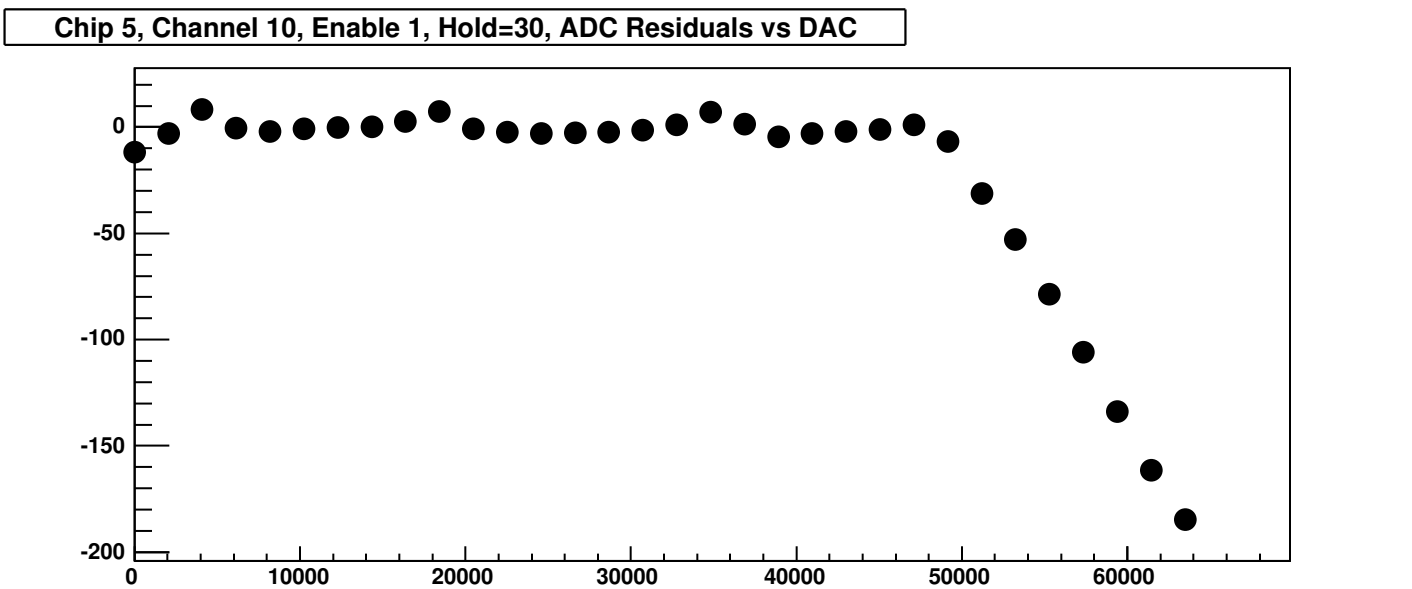

Chip 5, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.148e+04 / 15
p0 1278 ± 0.2552
p1 0.6051 ± 2.48e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

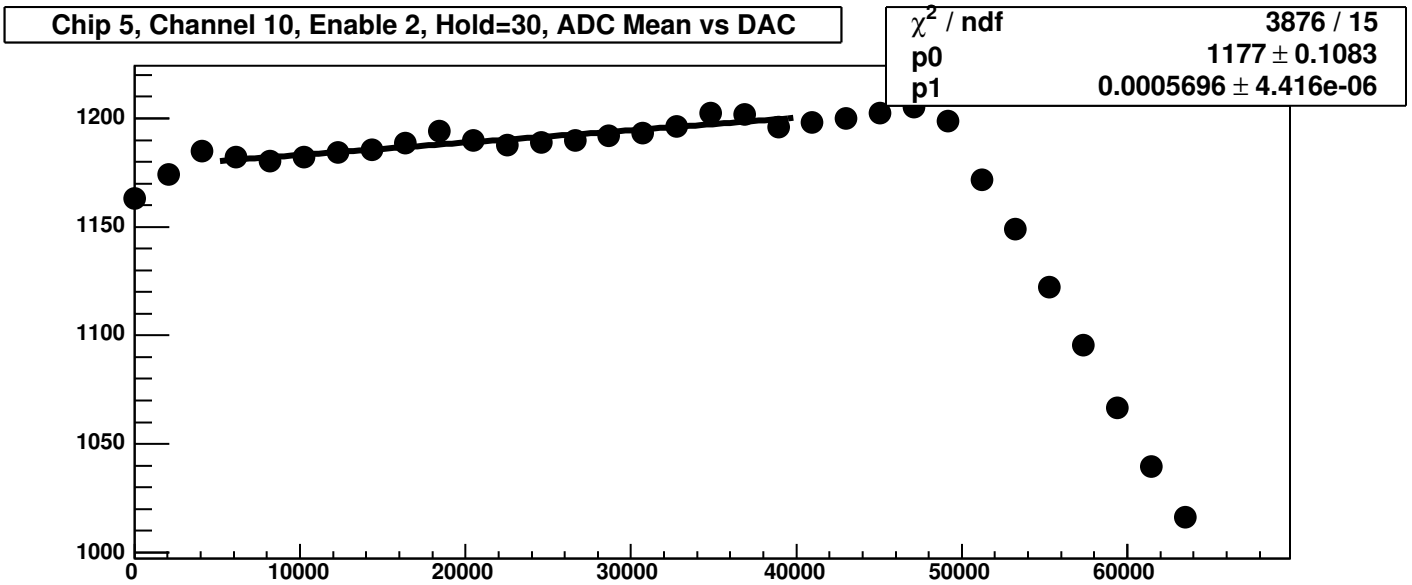

Chip 5, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

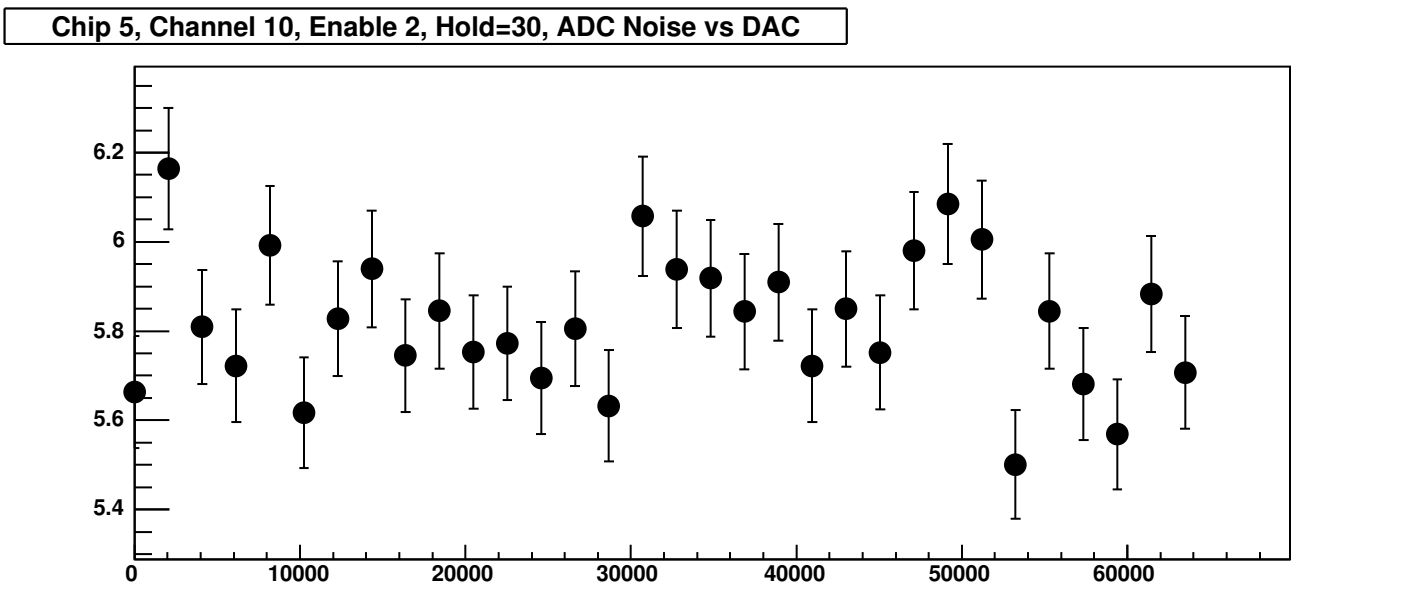

Chip 5, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

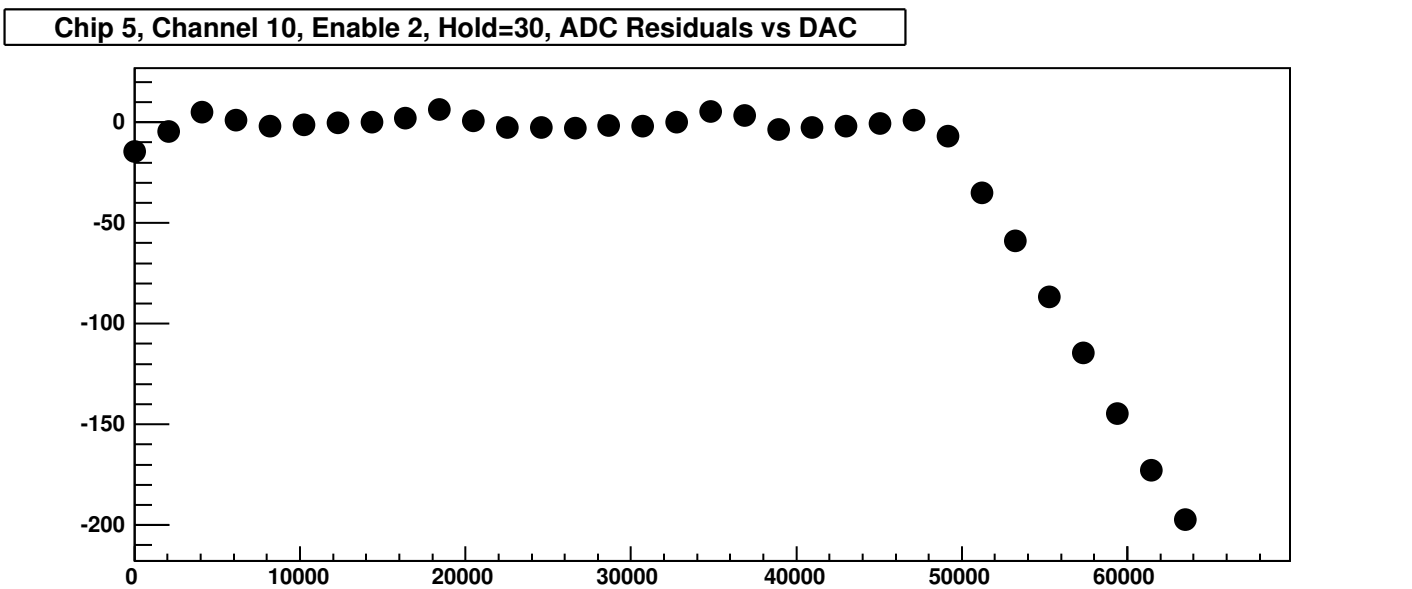

Chip 5, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5065 / 15
p0 1176 ± 0.1083
p1 0.0005791 ± 4.418e-06
1220
1200
1180
1160
1140
1120
1100
1080
1060
1040
1020
0
10000
20000
30000
40000
50000
60000

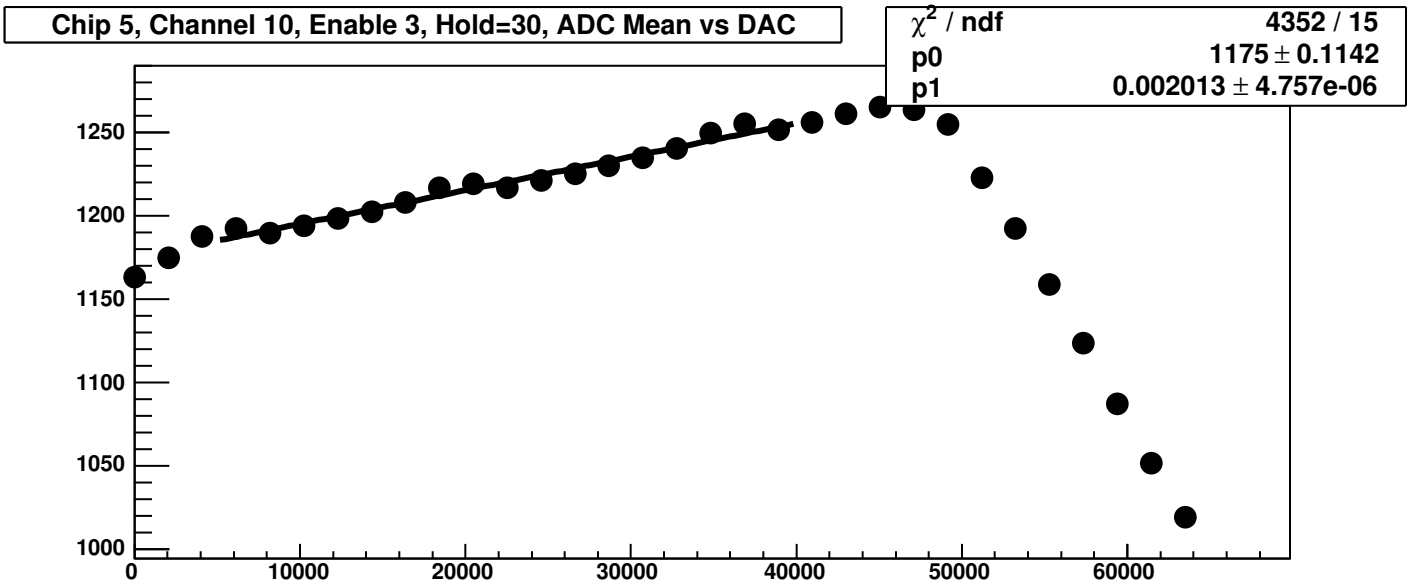

Chip 5, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

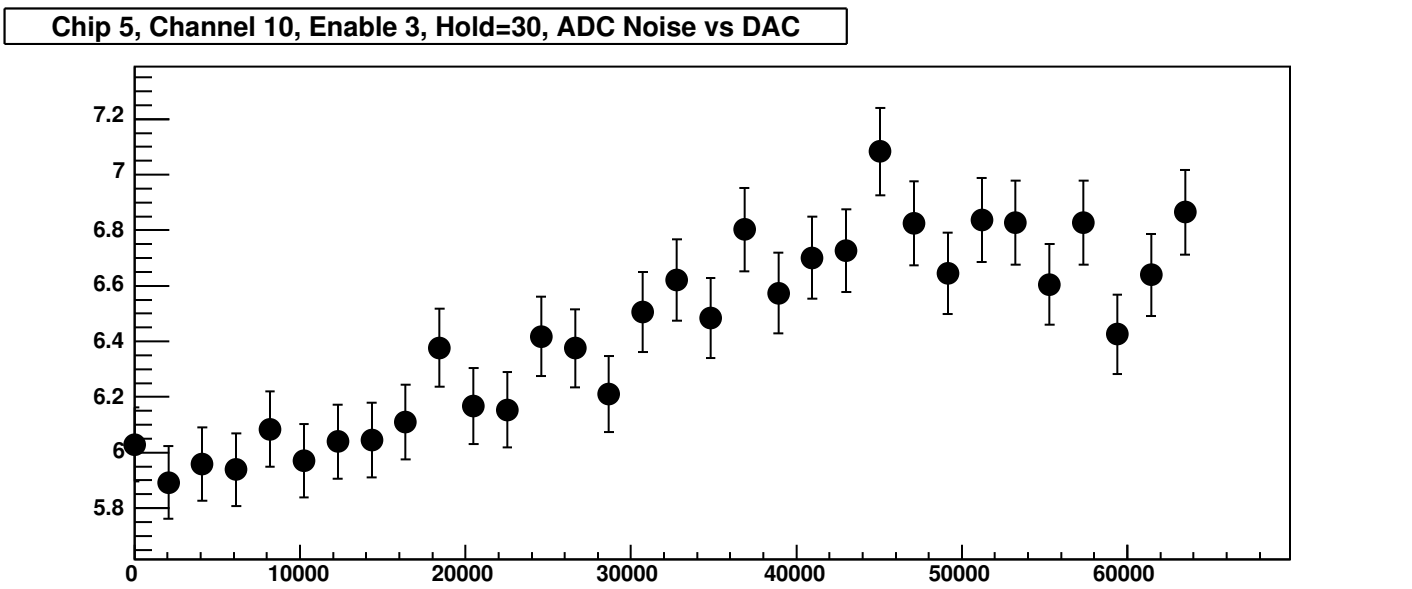

Chip 5, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

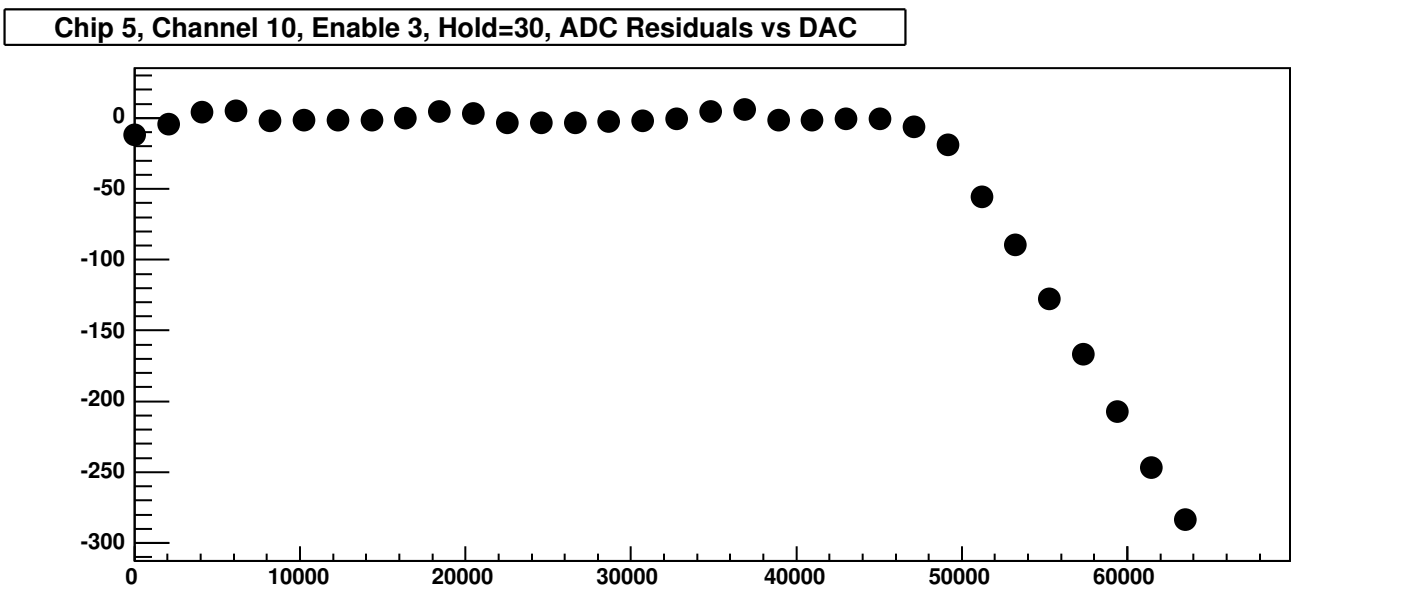

Chip 5, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3876 / 15
p0 1177 ± 0.1083
p1 0.0005696 ± 4.416e-06
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

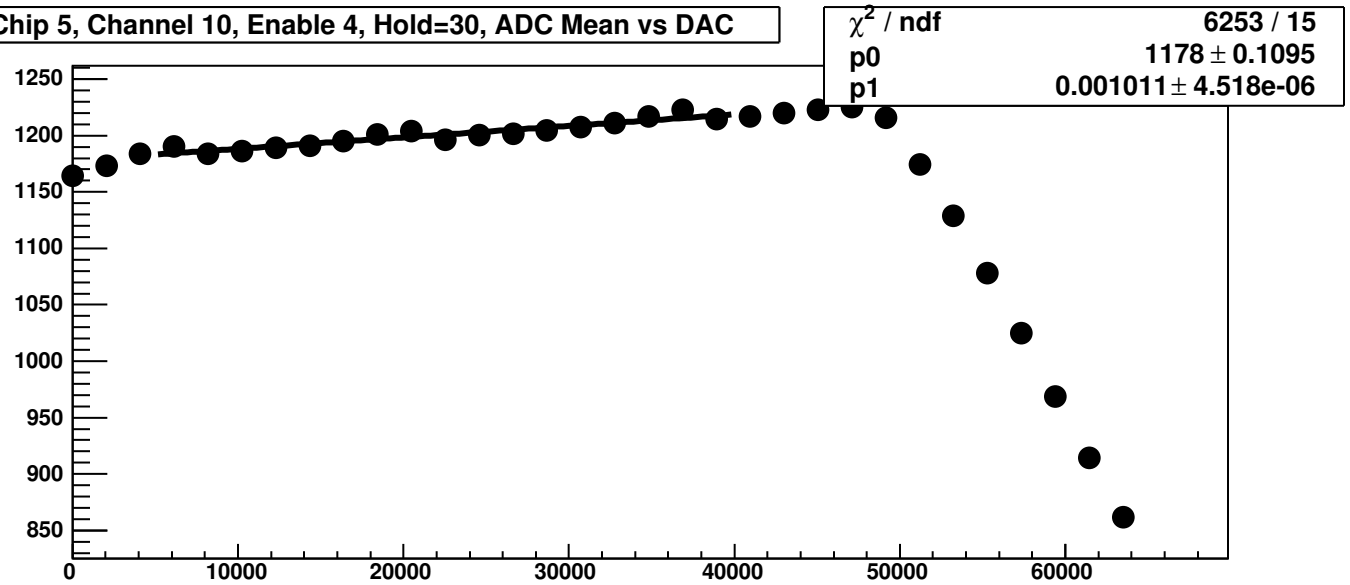

Chip 5, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4352 / 15
p0 1175 ± 0.1142
p1 0.002013 ± 4.757e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

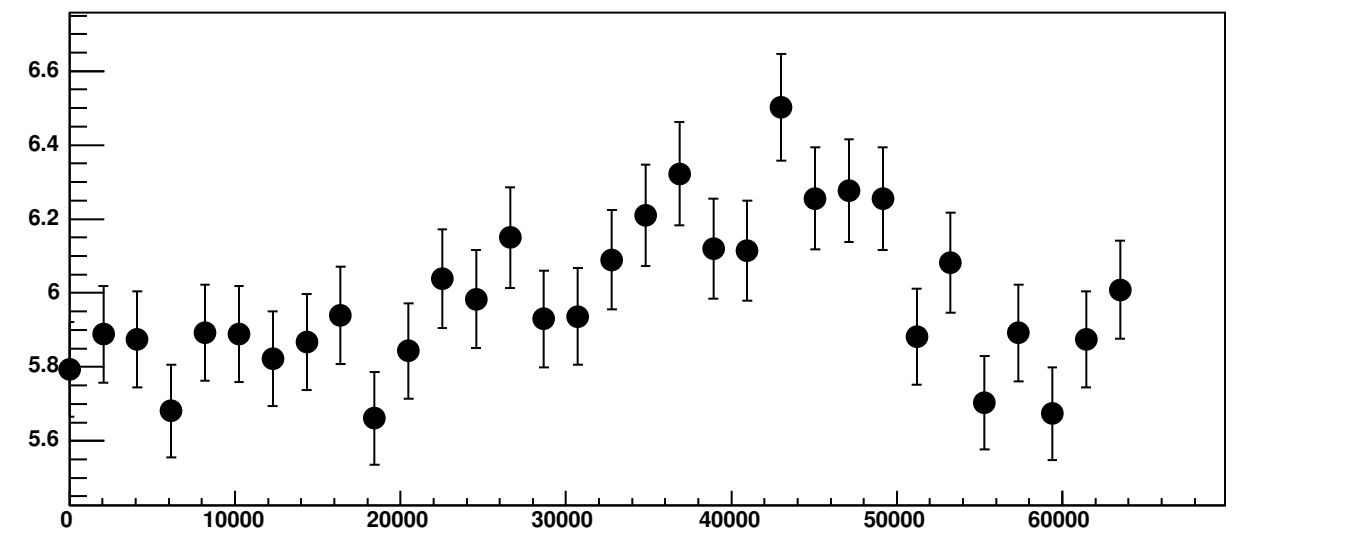

Chip 5, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

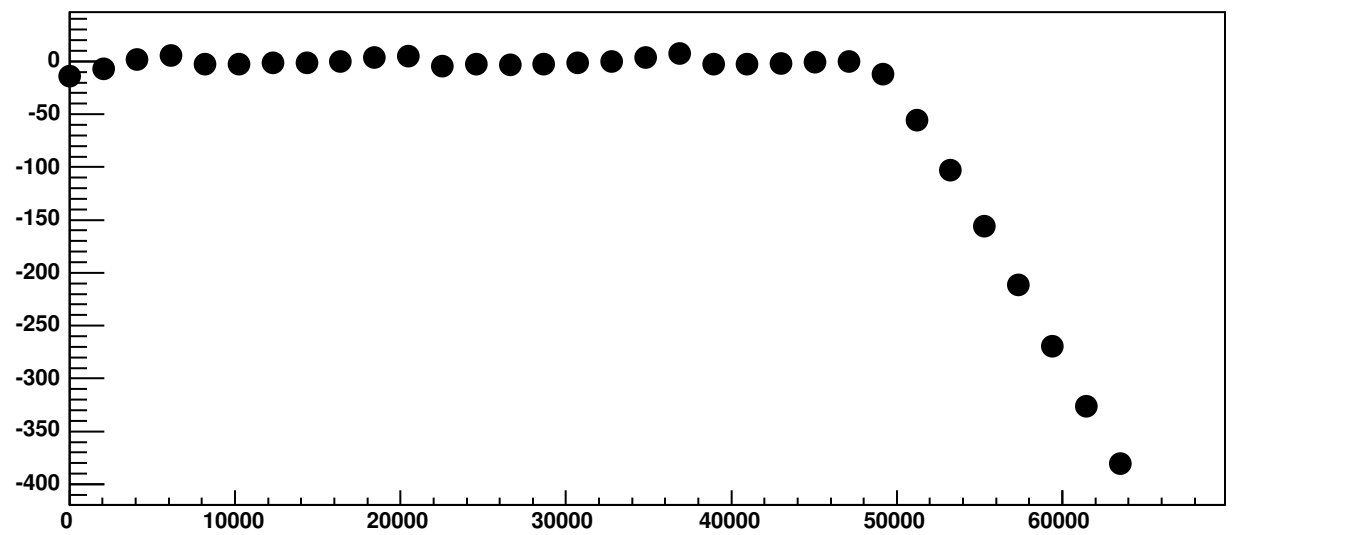
Chip 5, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC



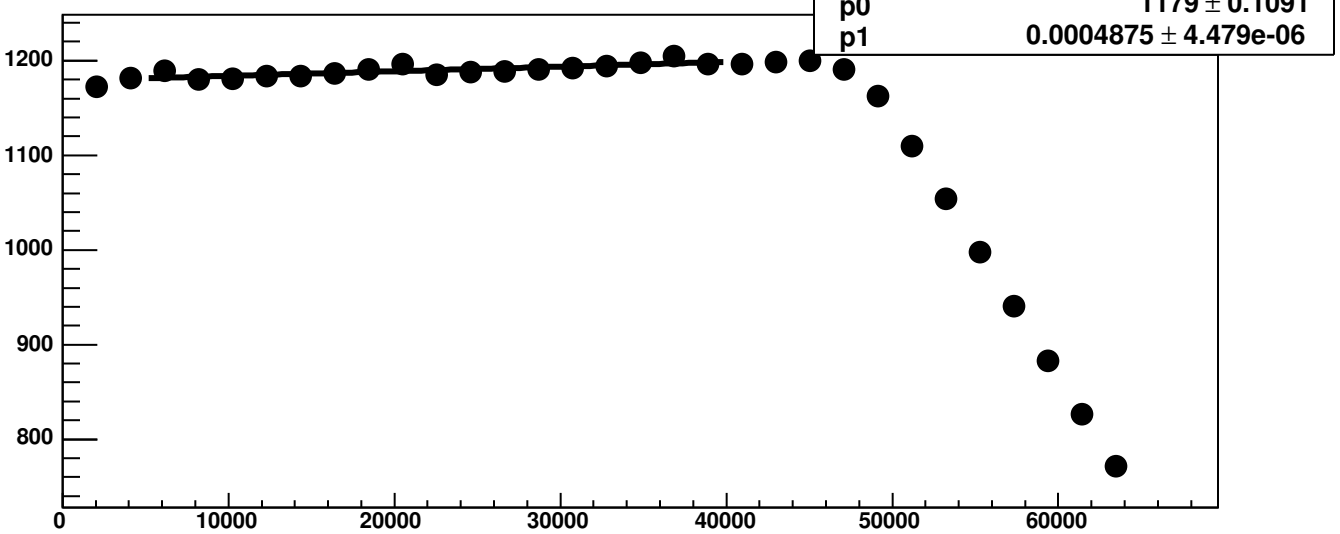
Chip 5, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



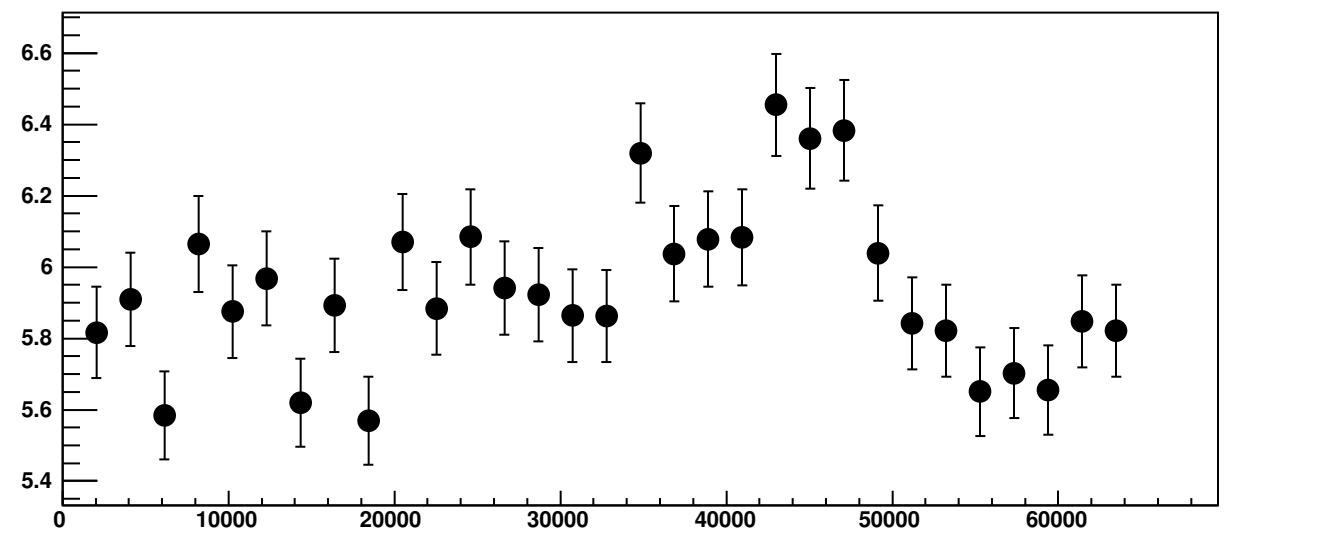
Chip 5, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



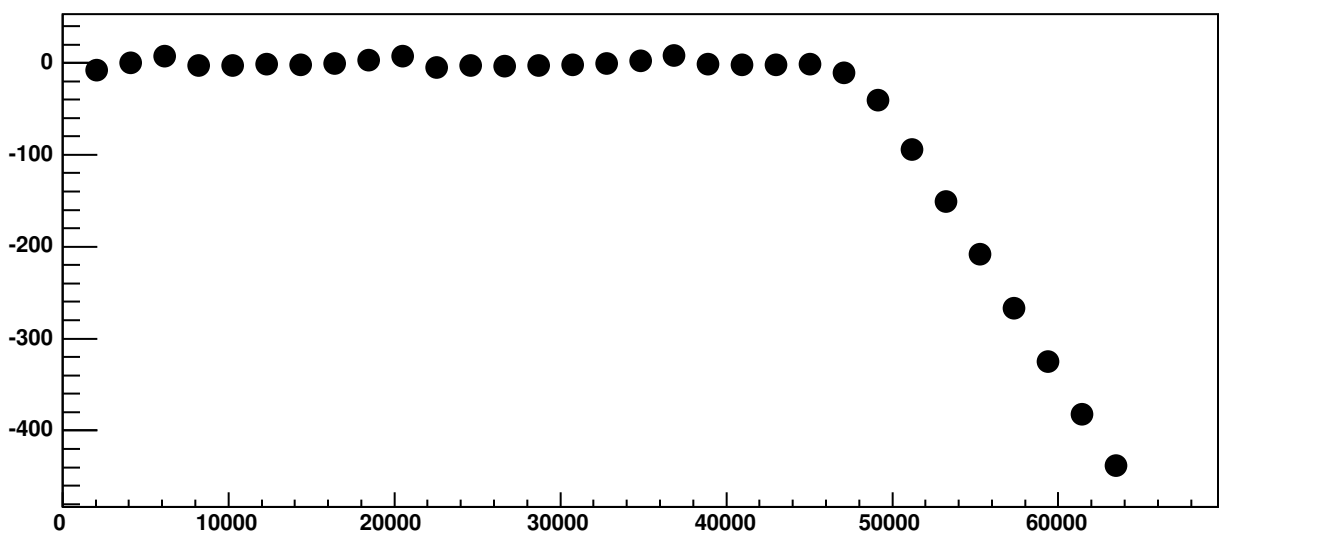
Chip 5, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC

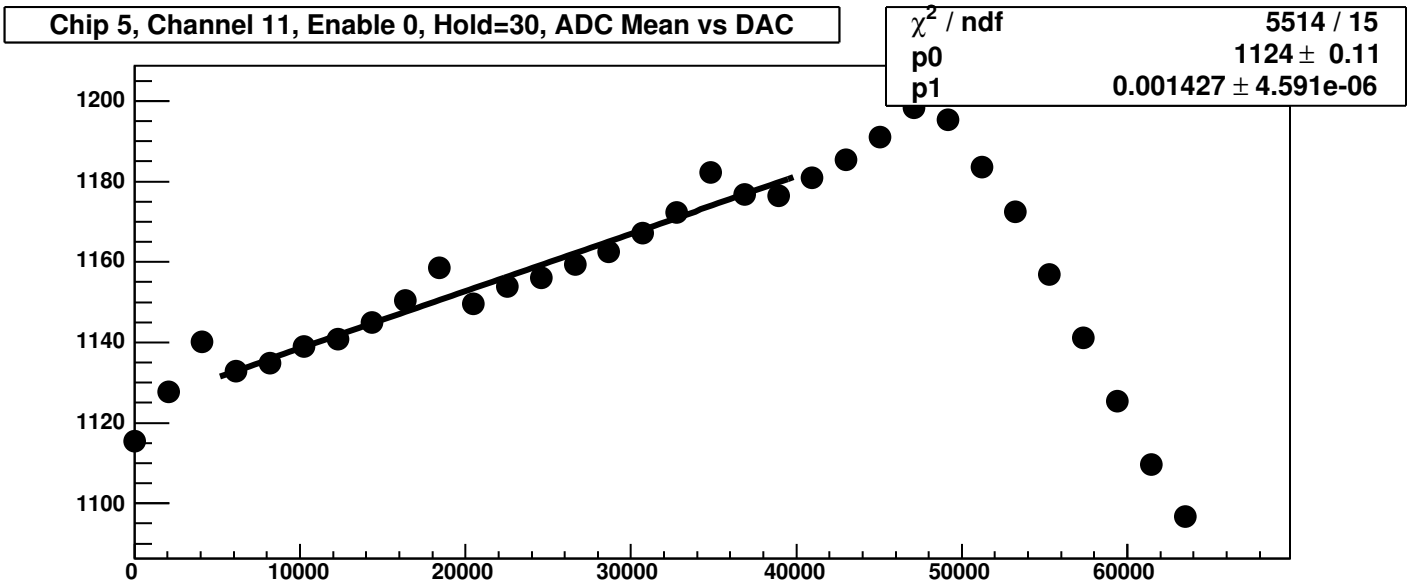


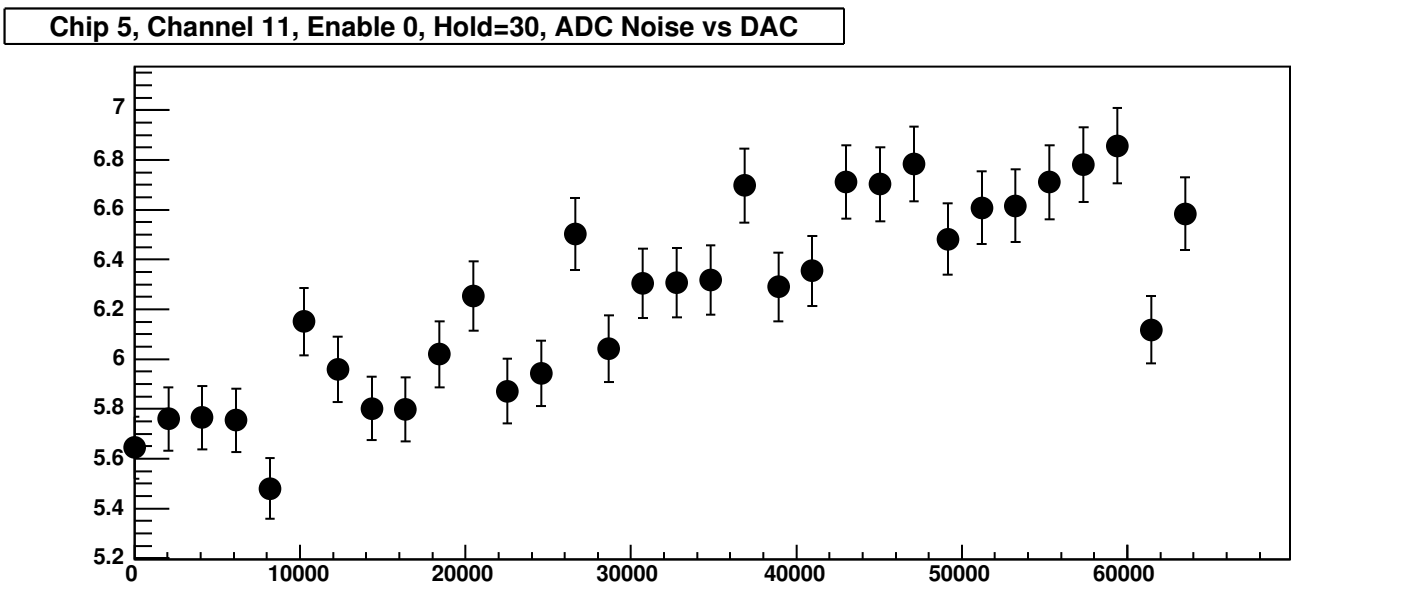
Chip 5, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC

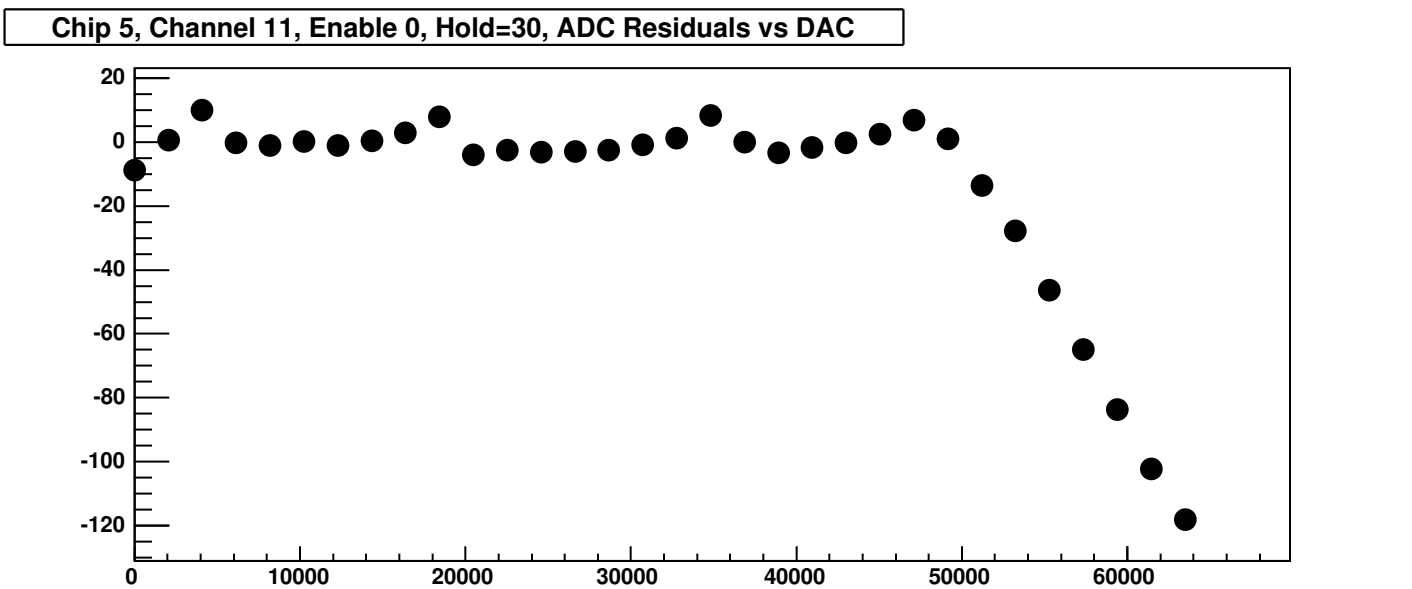


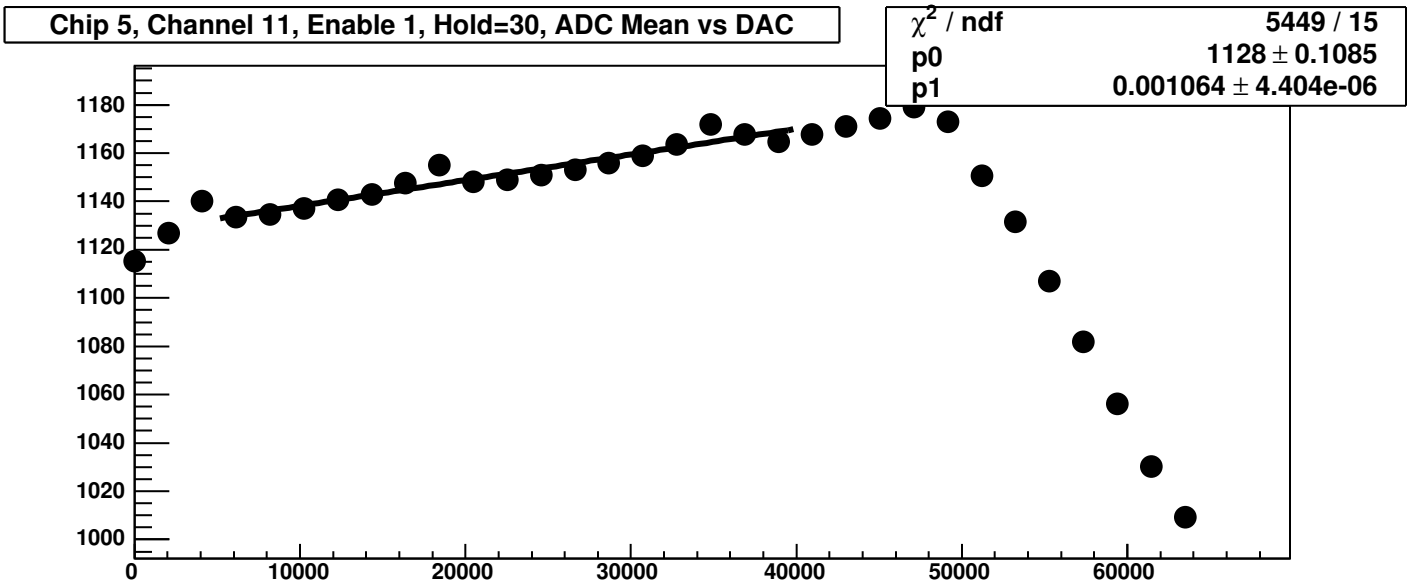
Chip 5, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC

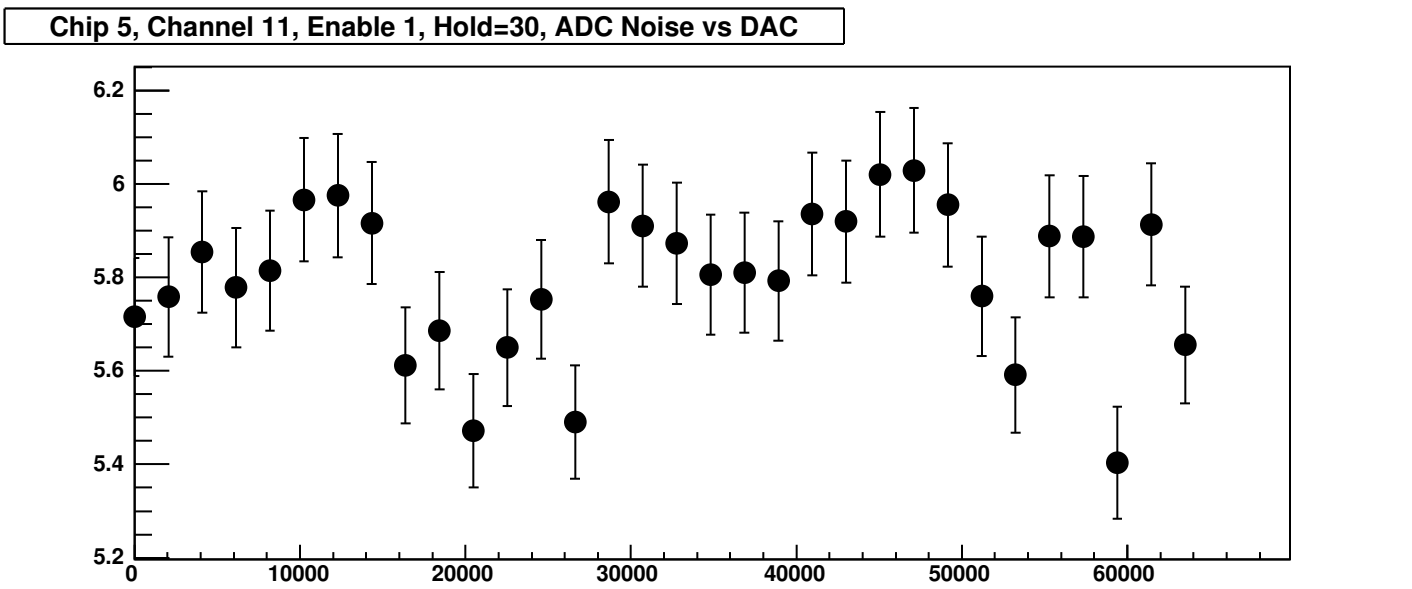


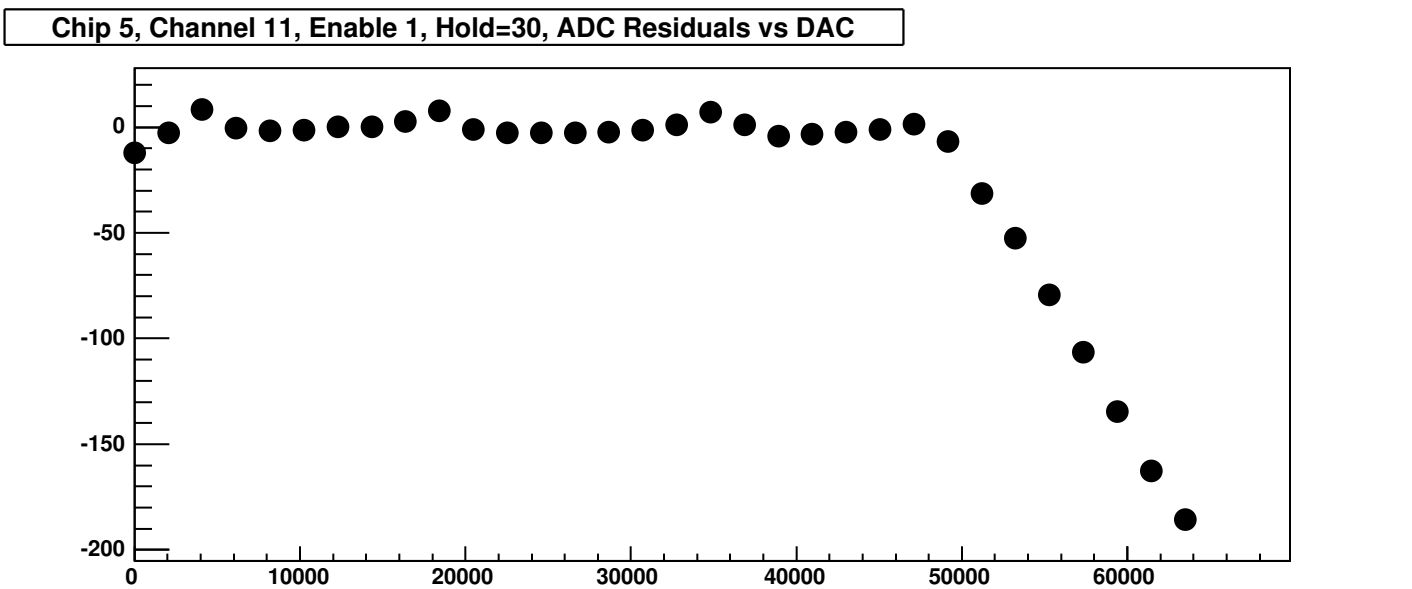

Chip 5, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5514 / 15
p0 1124 ± 0.11
p1 0.001427 ± 4.591e-06
1200
1180
1160
1140
1120
1100
0
10000
20000
30000
40000
50000
60000

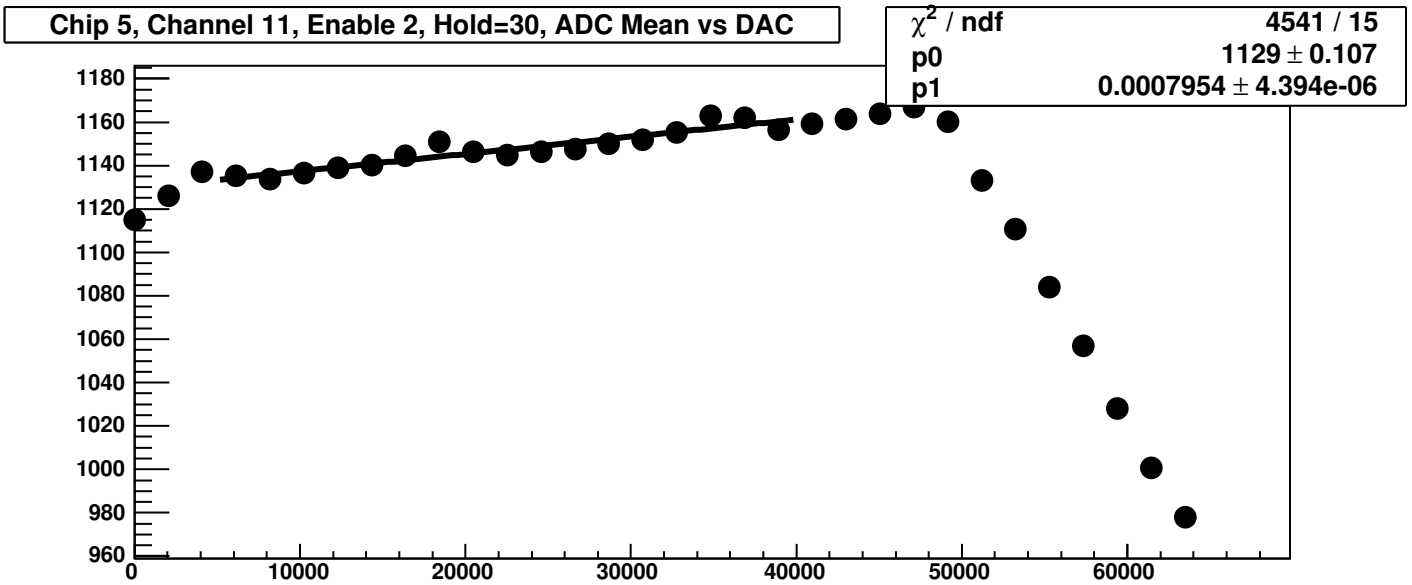

Chip 5, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

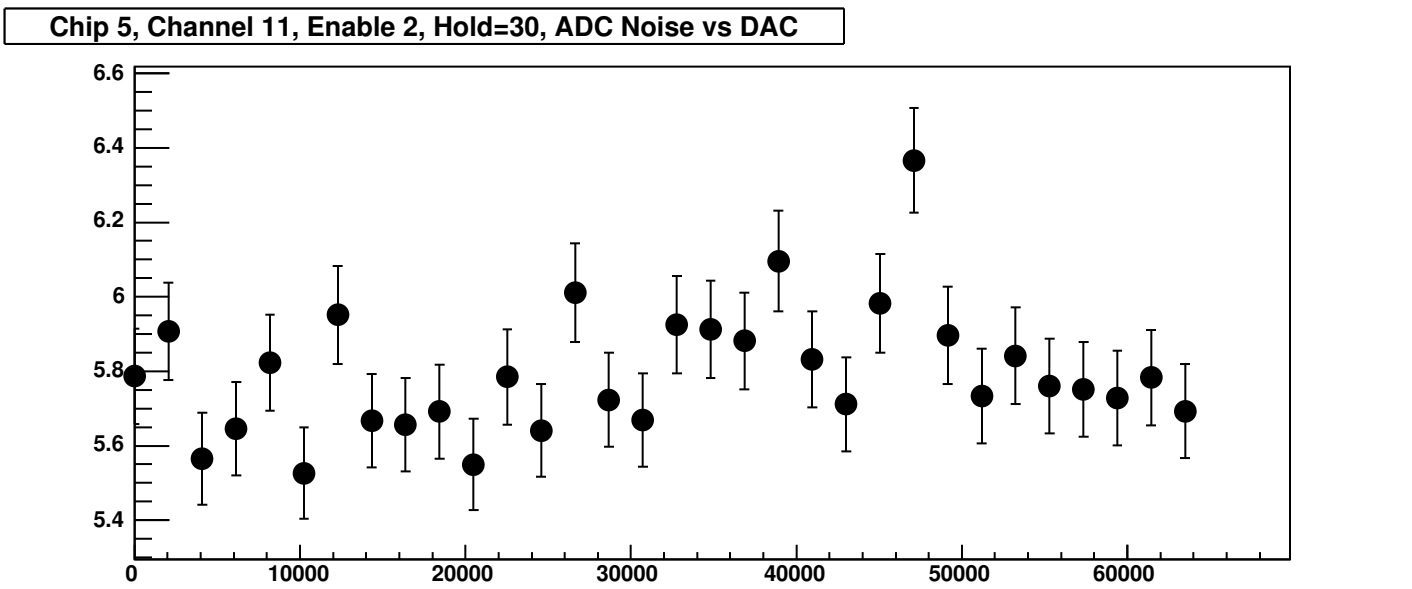

Chip 5, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

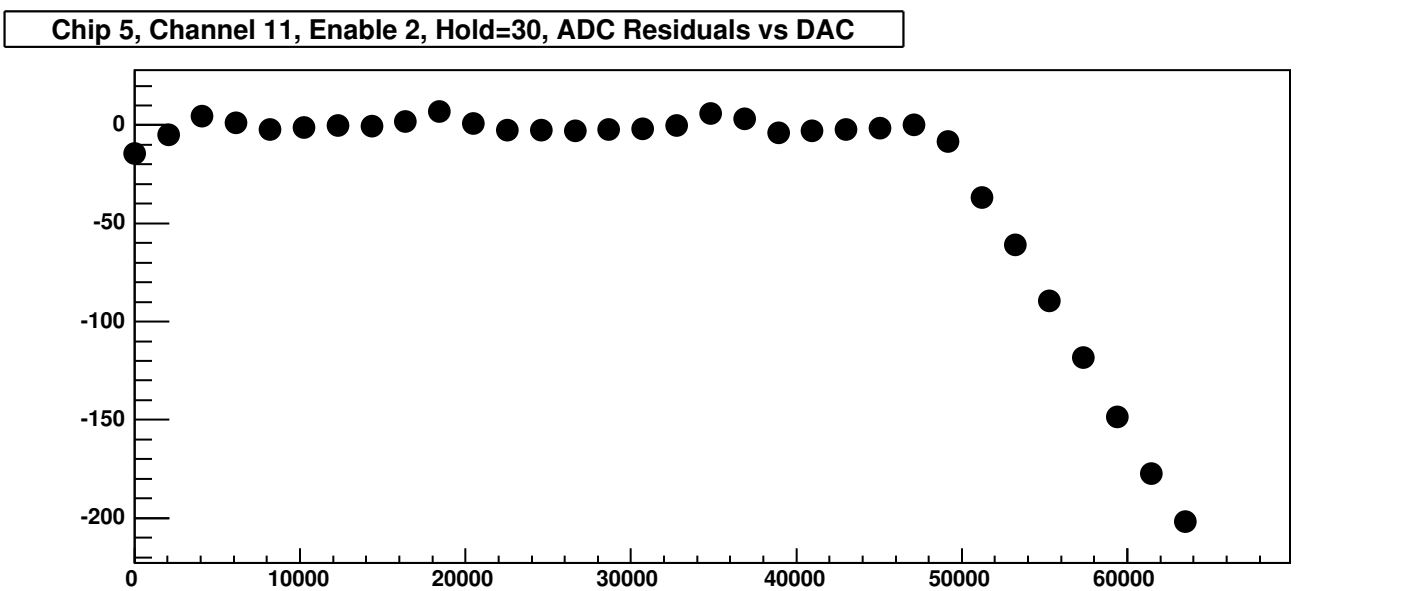

Chip 5, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5449 / 15
p0 1128 ± 0.1085
p1 0.001064 ± 4.404e-06
1180
1160
1140
1120
1100
1080
1060
1040
1020
1000
0
10000
20000
30000
40000
50000
60000

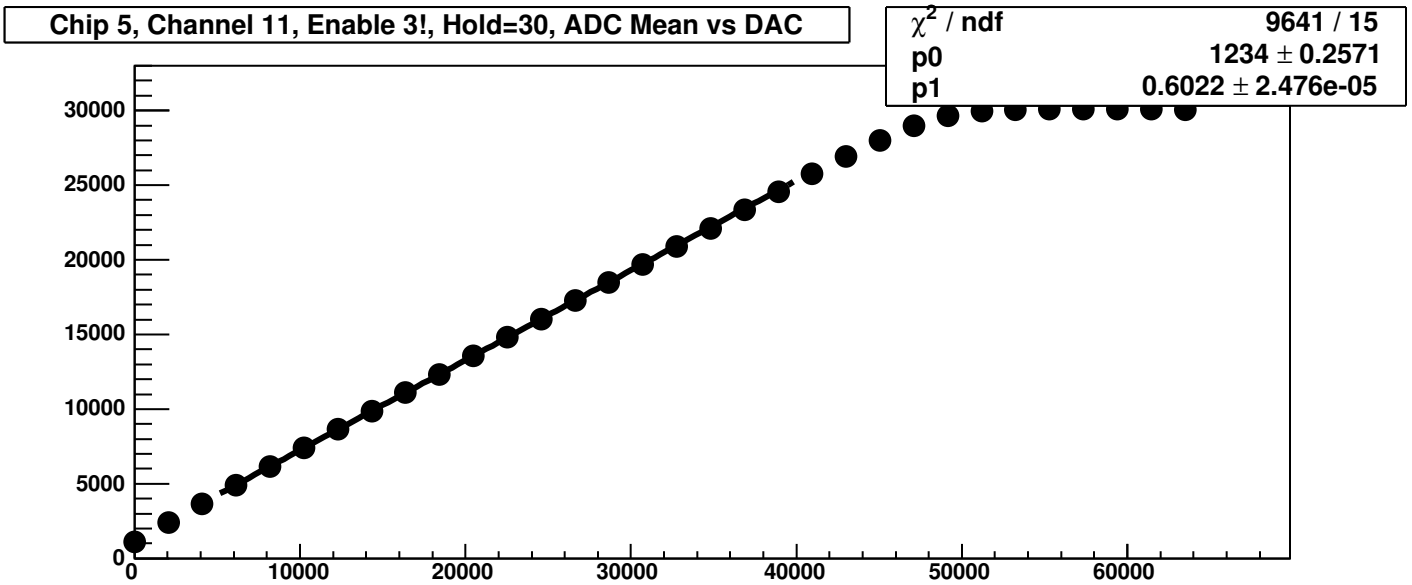

Chip 5, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

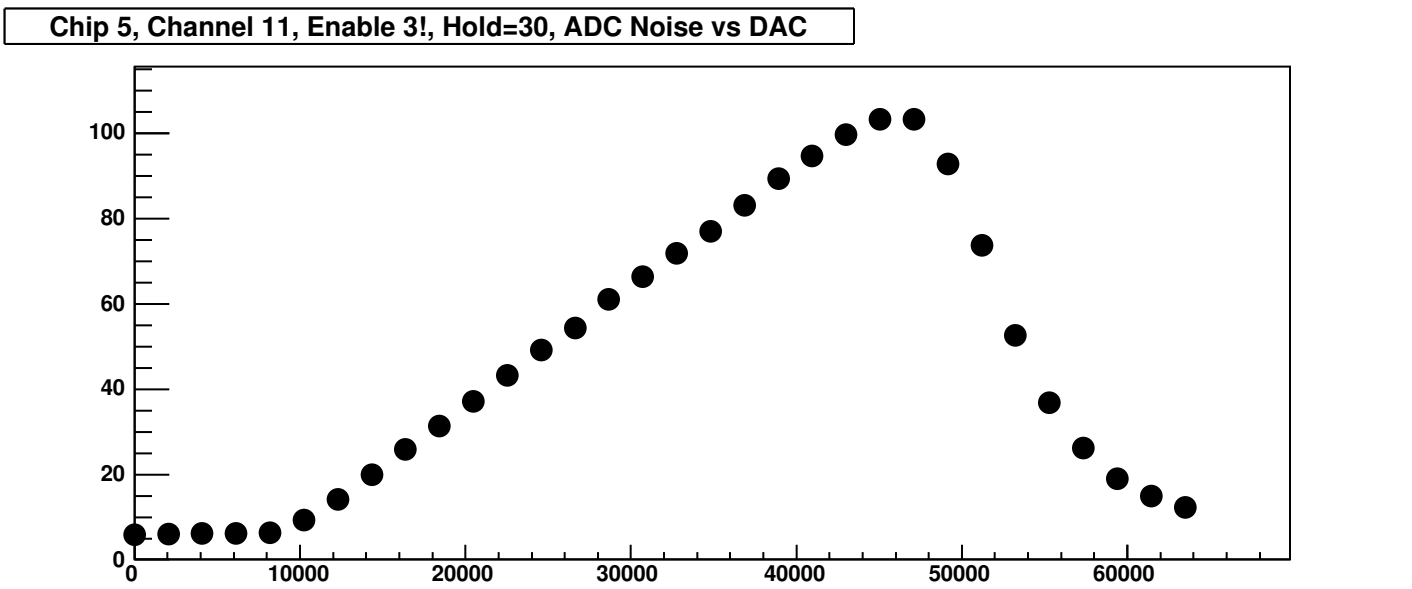

Chip 5, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

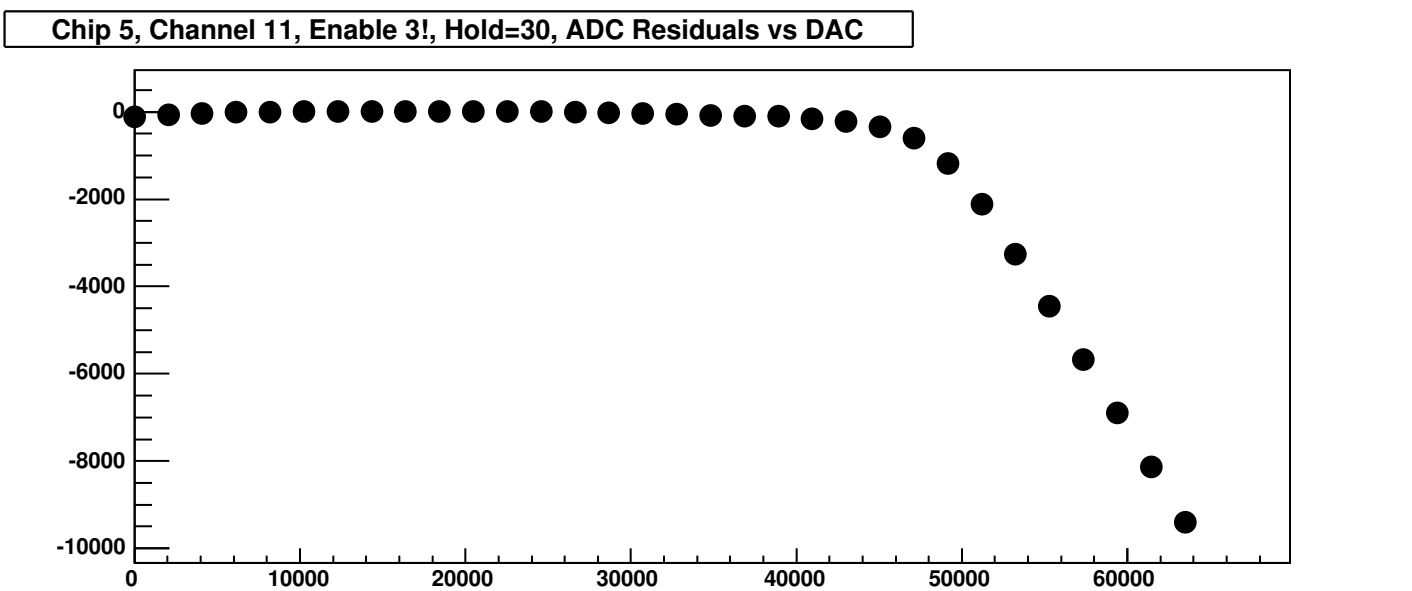

Chip 5, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4541 / 15
p0 1129 ± 0.107
p1 0.0007954 ± 4.394e-06
1180
1160
1140
1120
1100
1080
1060
1040
1020
1000
980
960
0
10000
20000
30000
40000
50000
60000

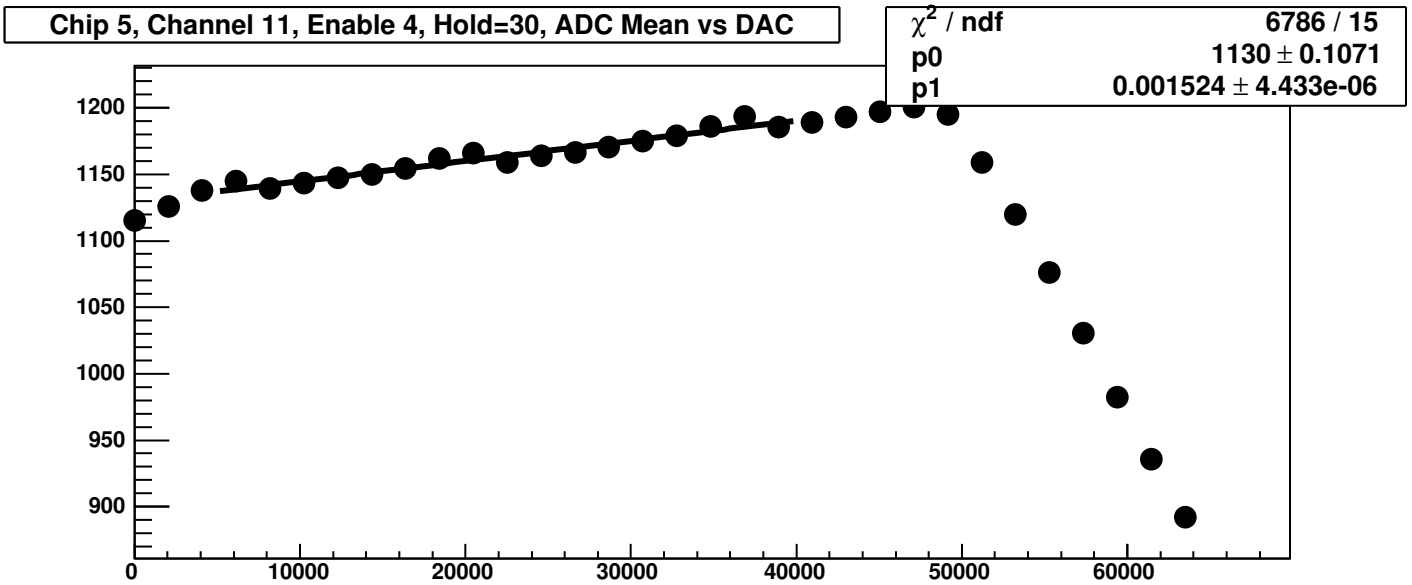

Chip 5, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

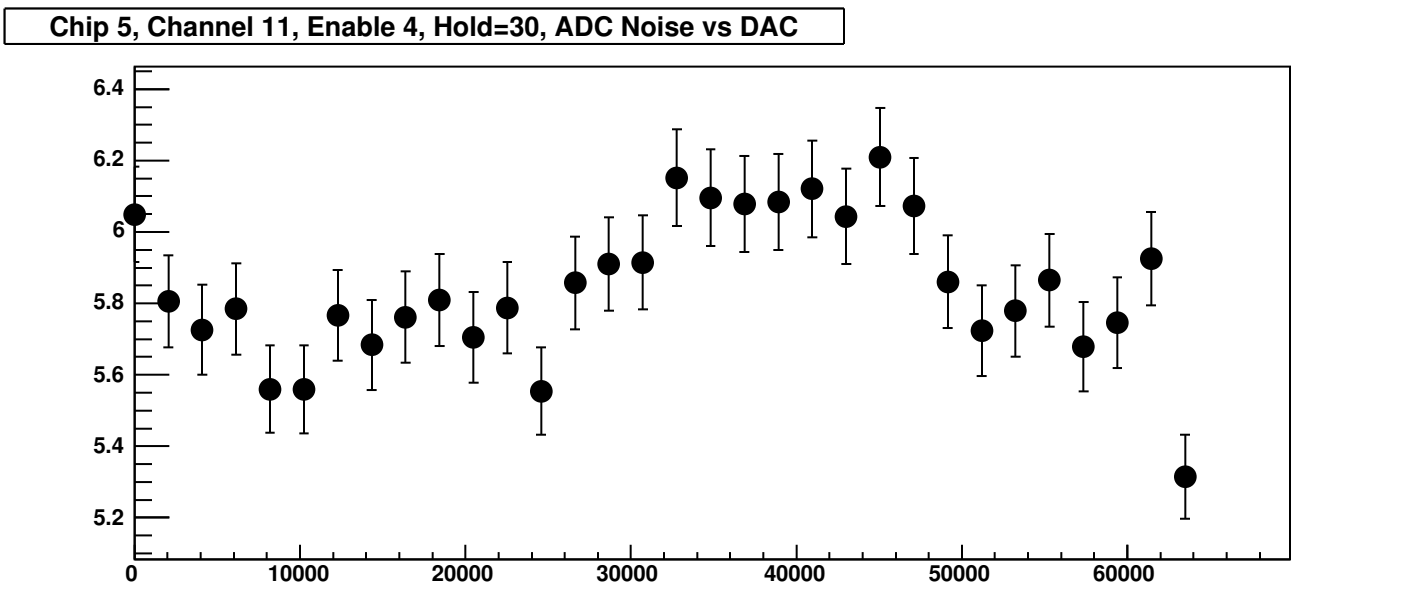

Chip 5, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

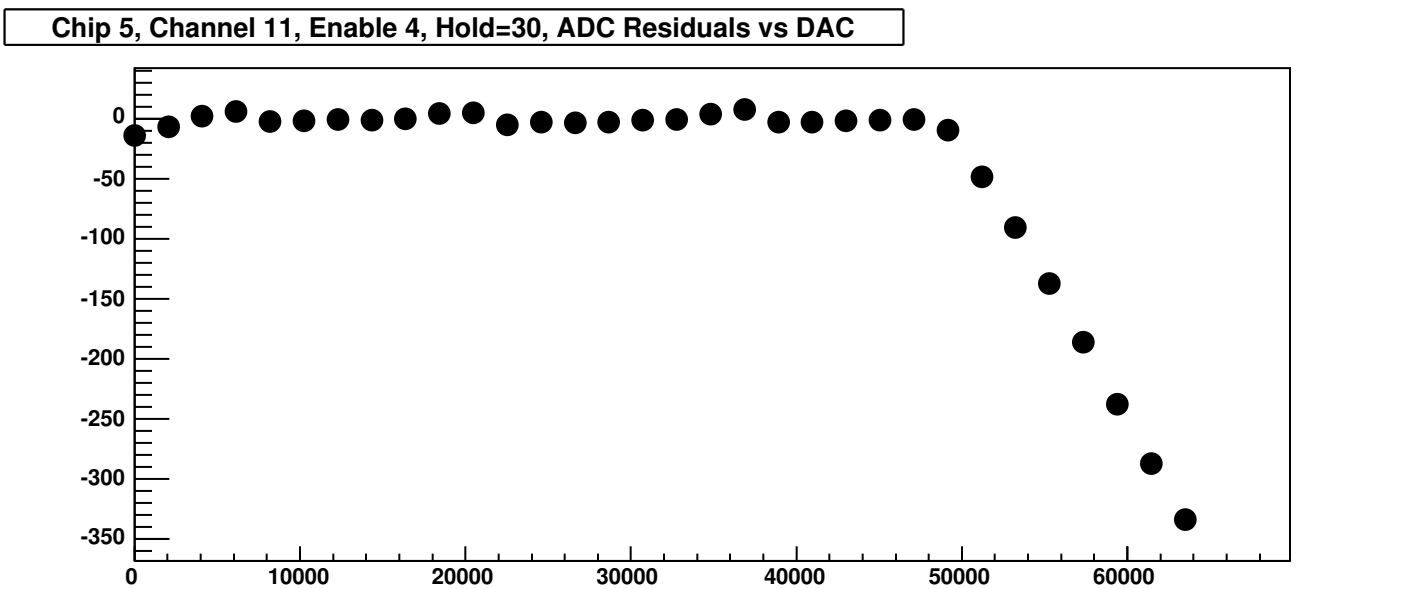

Chip 5, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 9641 / 15
p0 1234 ± 0.2571
p1 0.6022 ± 2.476e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

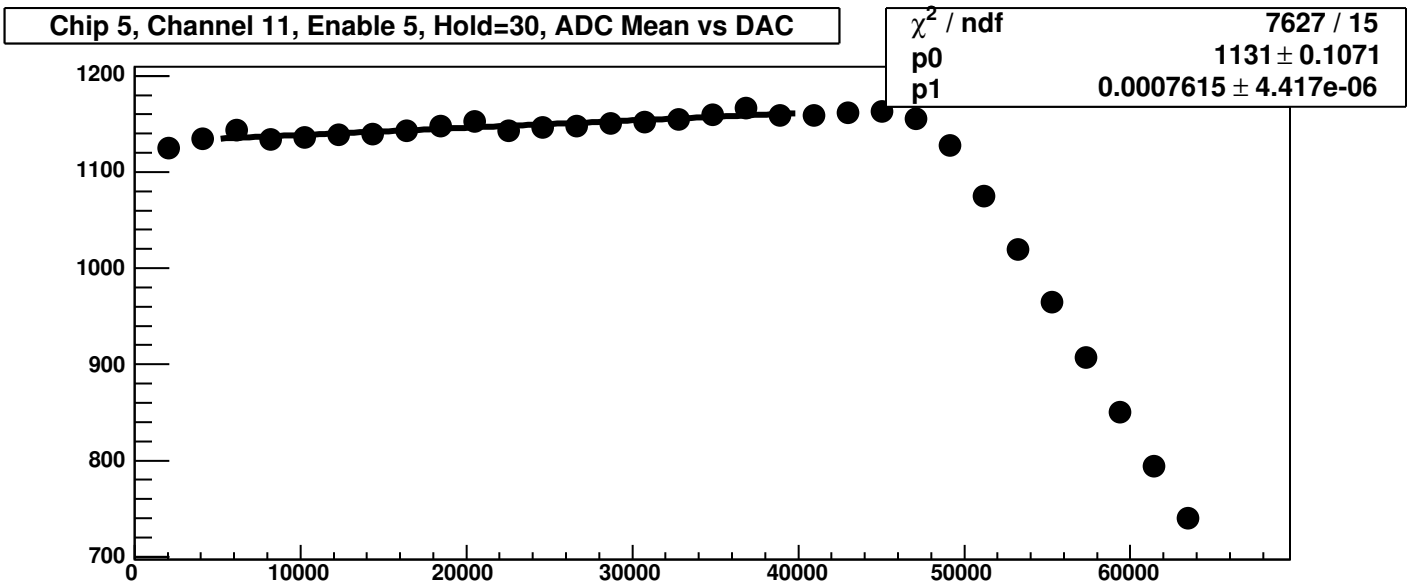

Chip 5, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

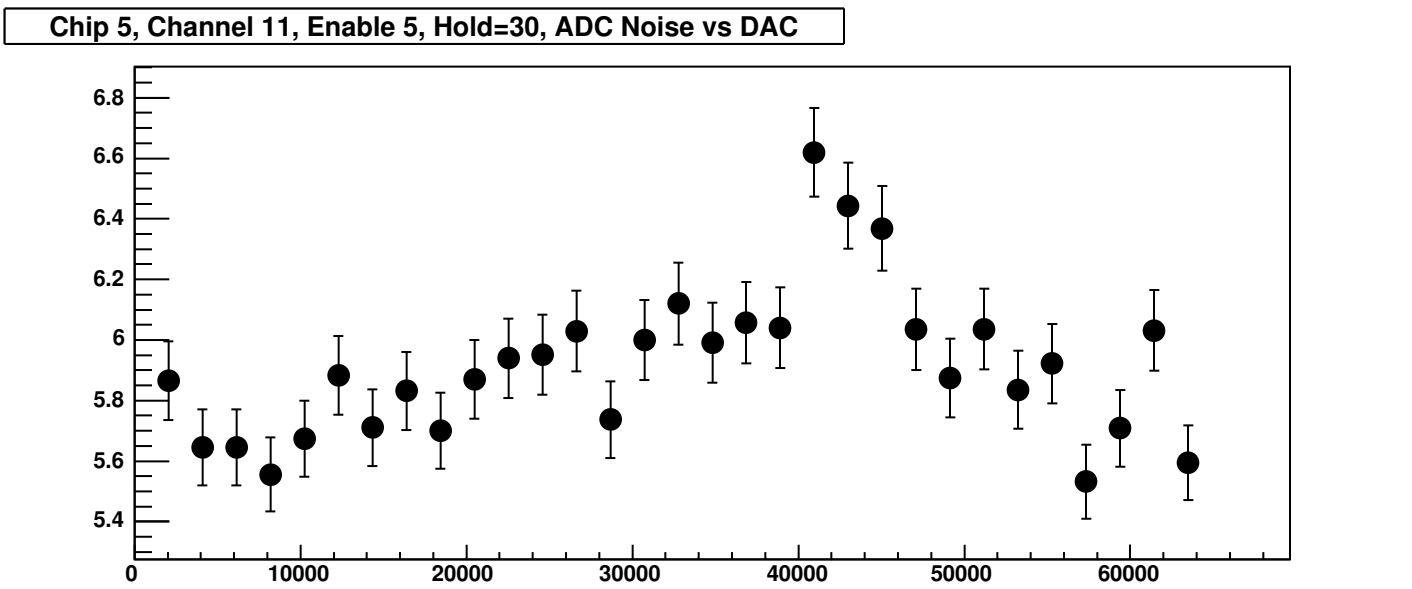

Chip 5, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

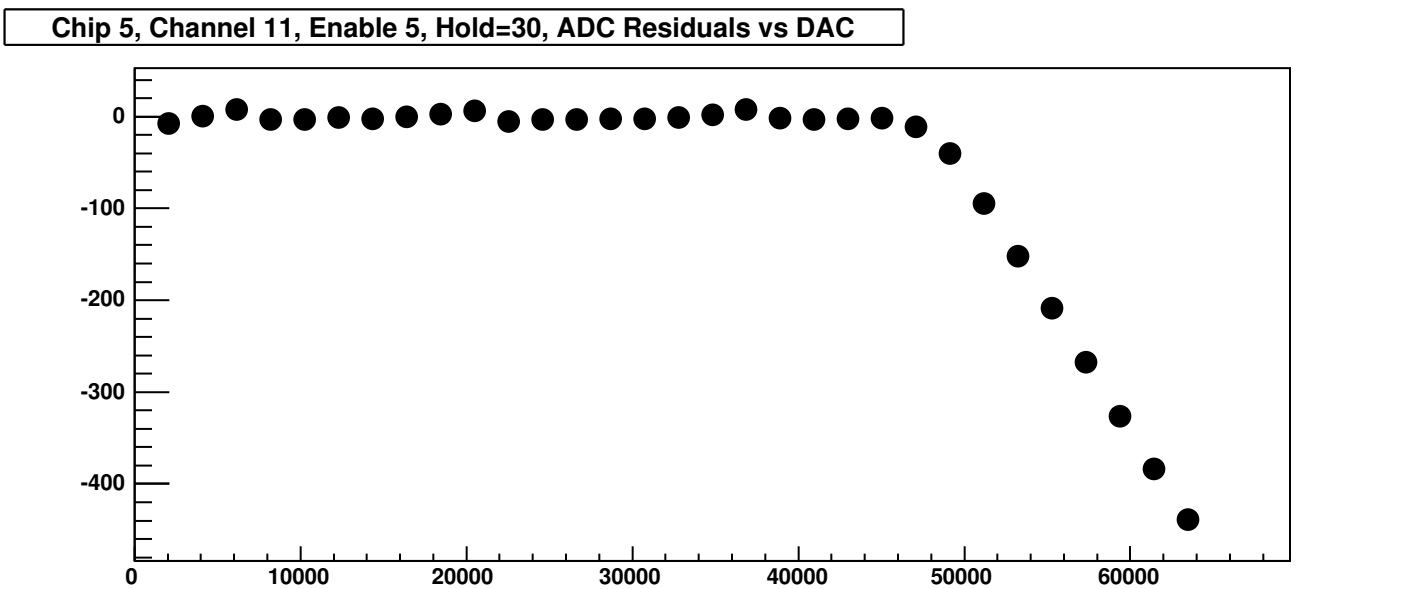

Chip 5, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6786 / 15
p0 1130 ± 0.1071
p1 0.001524 ± 4.433e-06
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

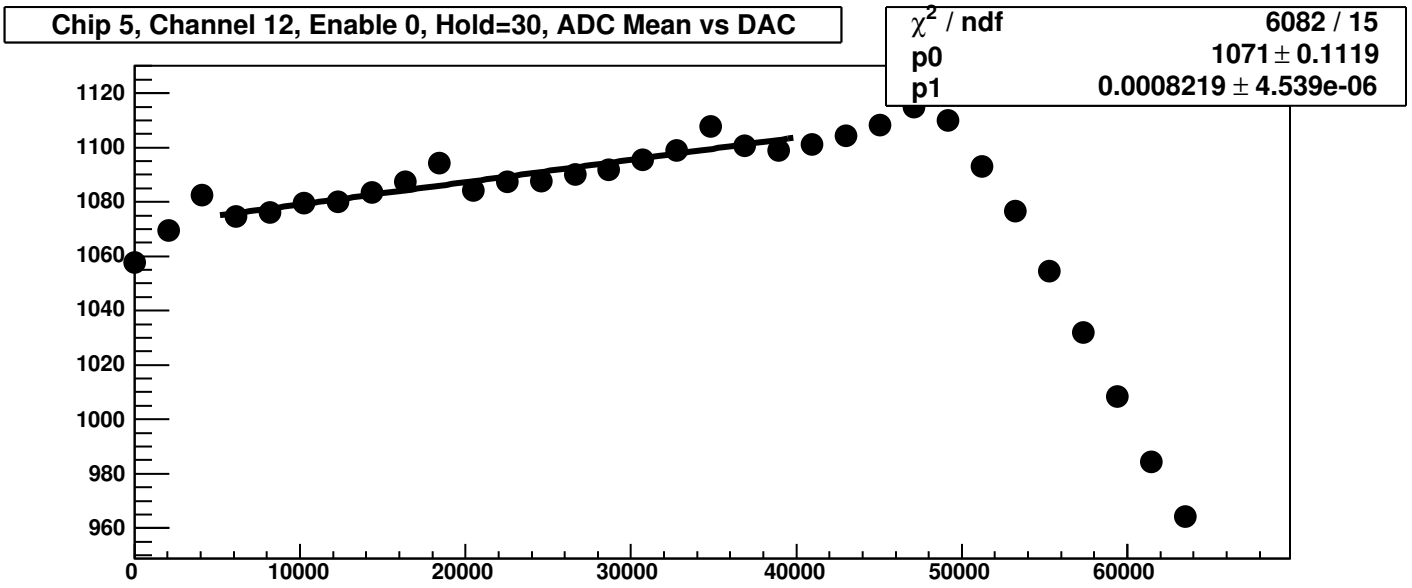

Chip 5, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

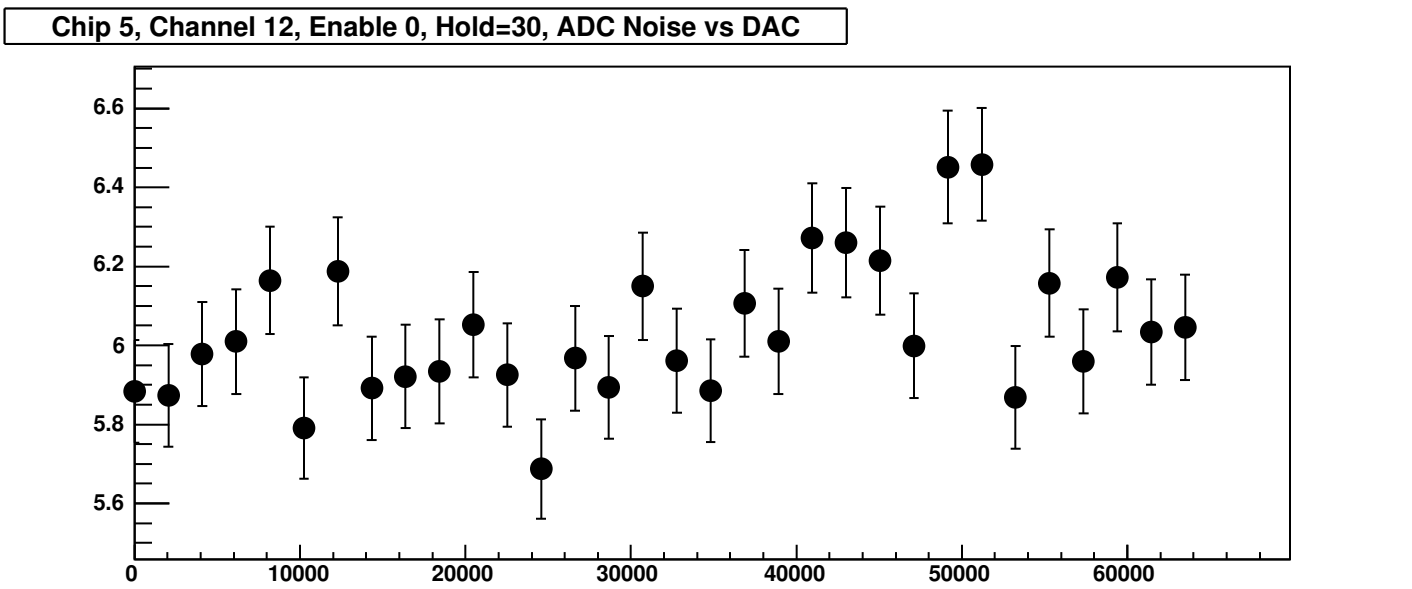

Chip 5, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

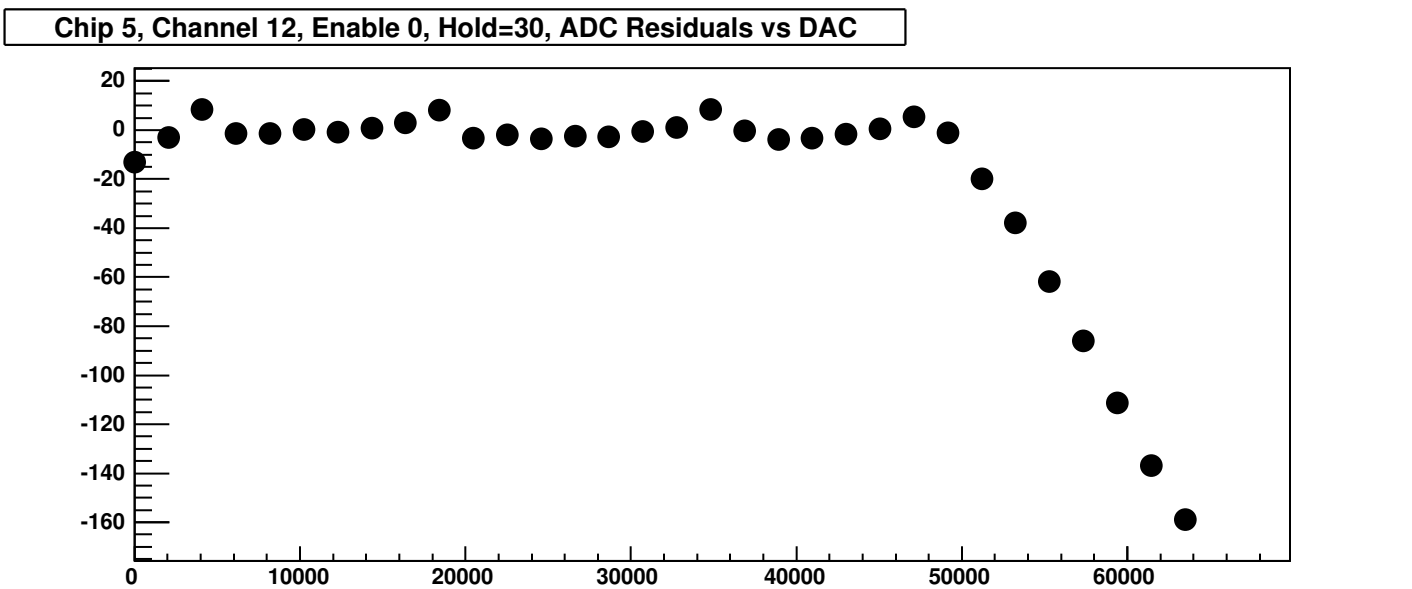

Chip 5, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7627 / 15
p0 1131 ± 0.1071
p1 0.0007615 ± 4.417e-06
1200
1100
1000
900
800
700
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

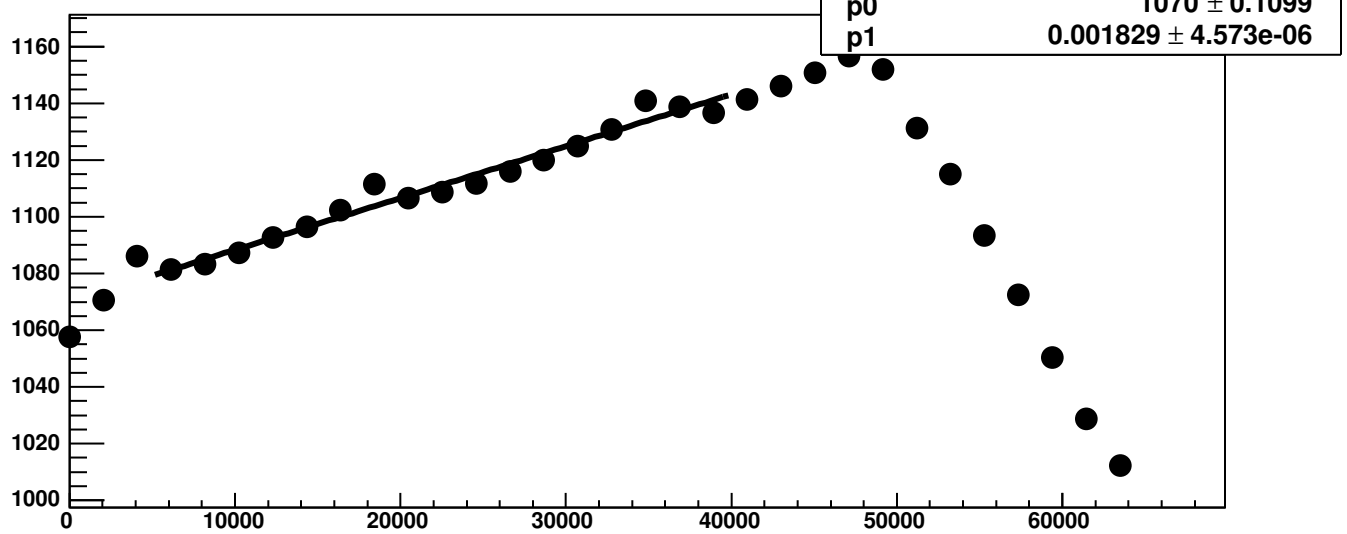

Chip 5, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6082 / 15
p0 1071 ± 0.1119
p1 0.0008219 ± 4.539e-06
1120
1100
1080
1060
1040
1020
1000
980
960
0
10000
20000
30000
40000
50000
60000

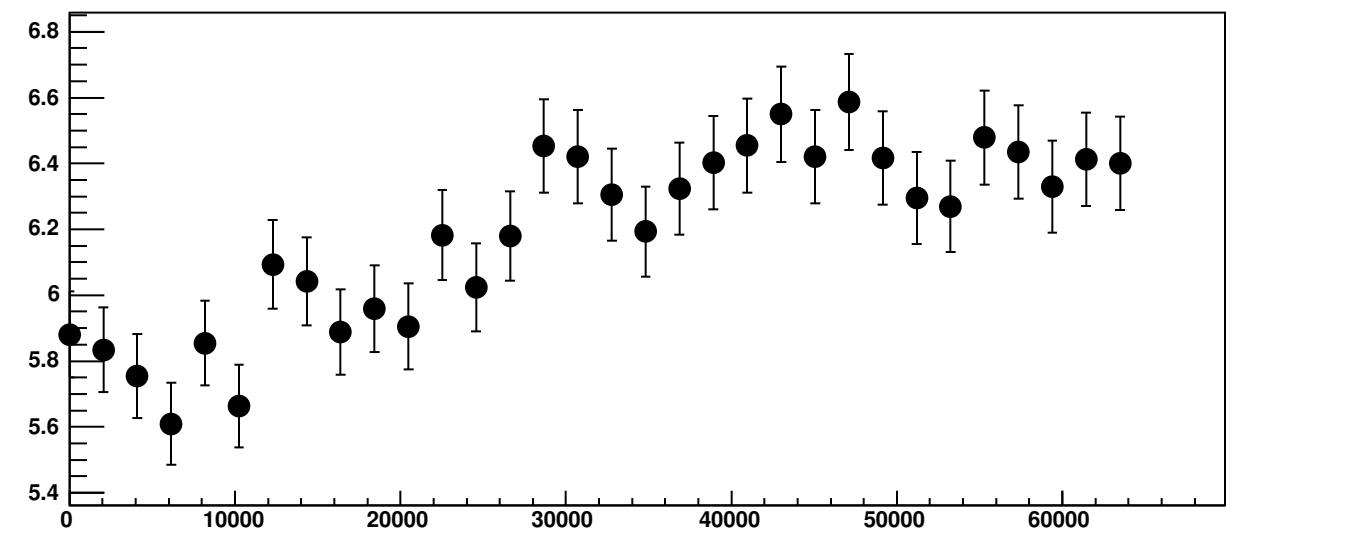

Chip 5, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

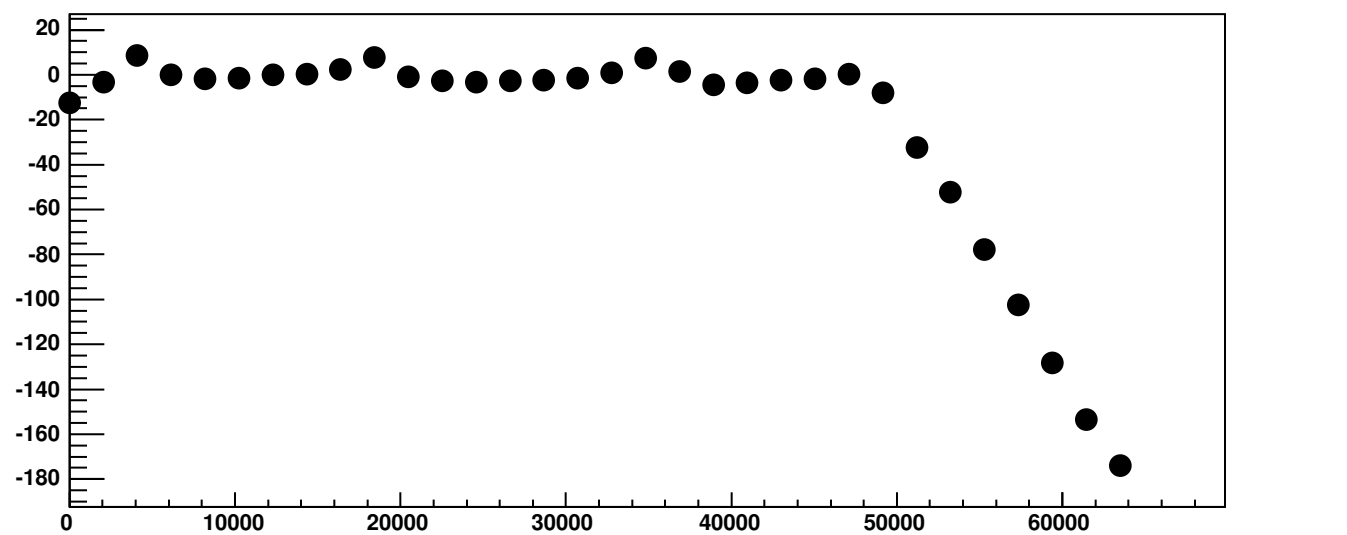
Chip 5, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC

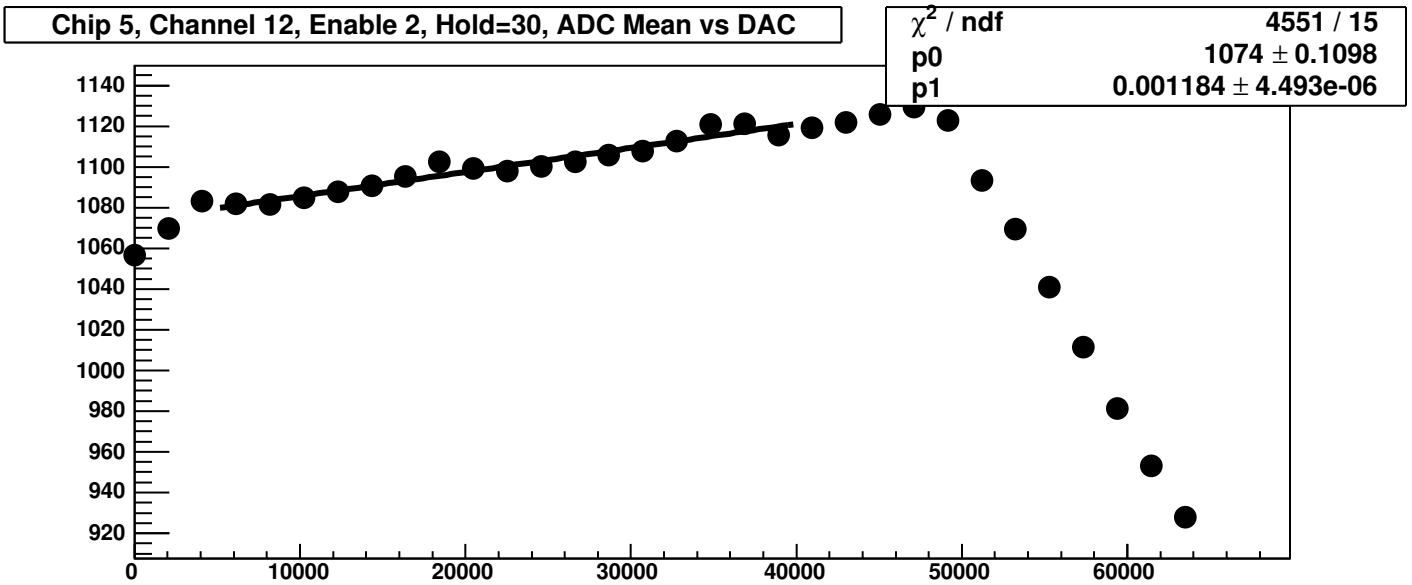


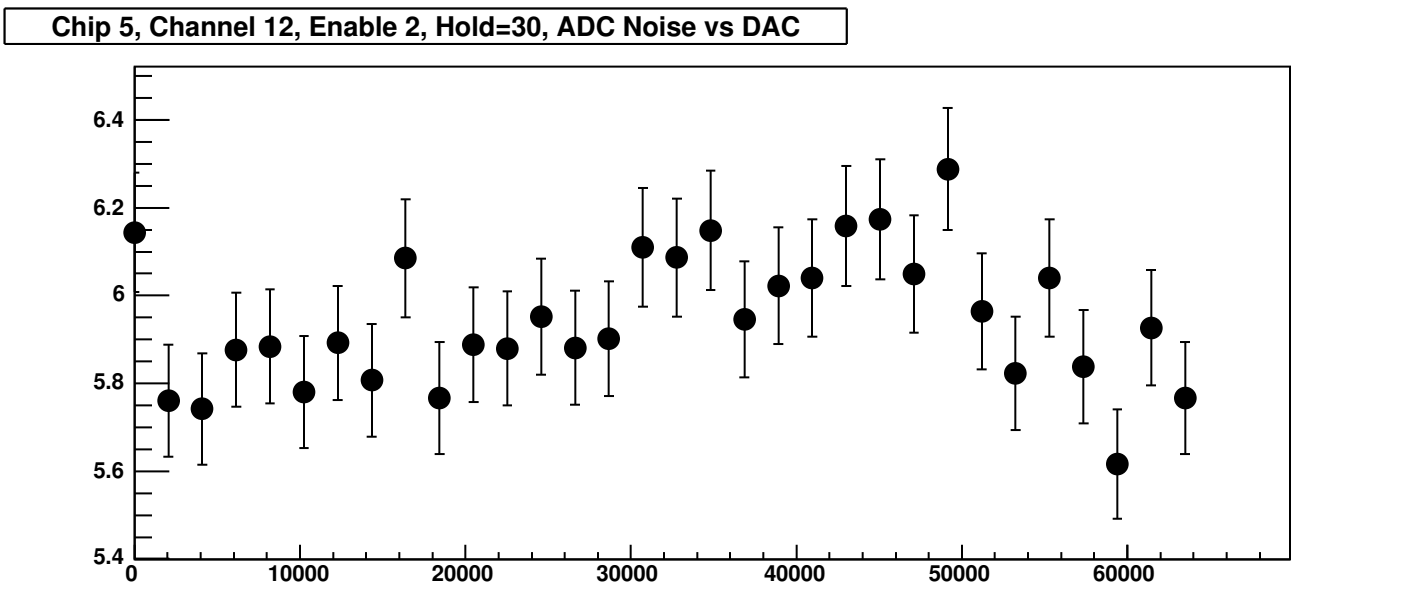
Chip 5, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC

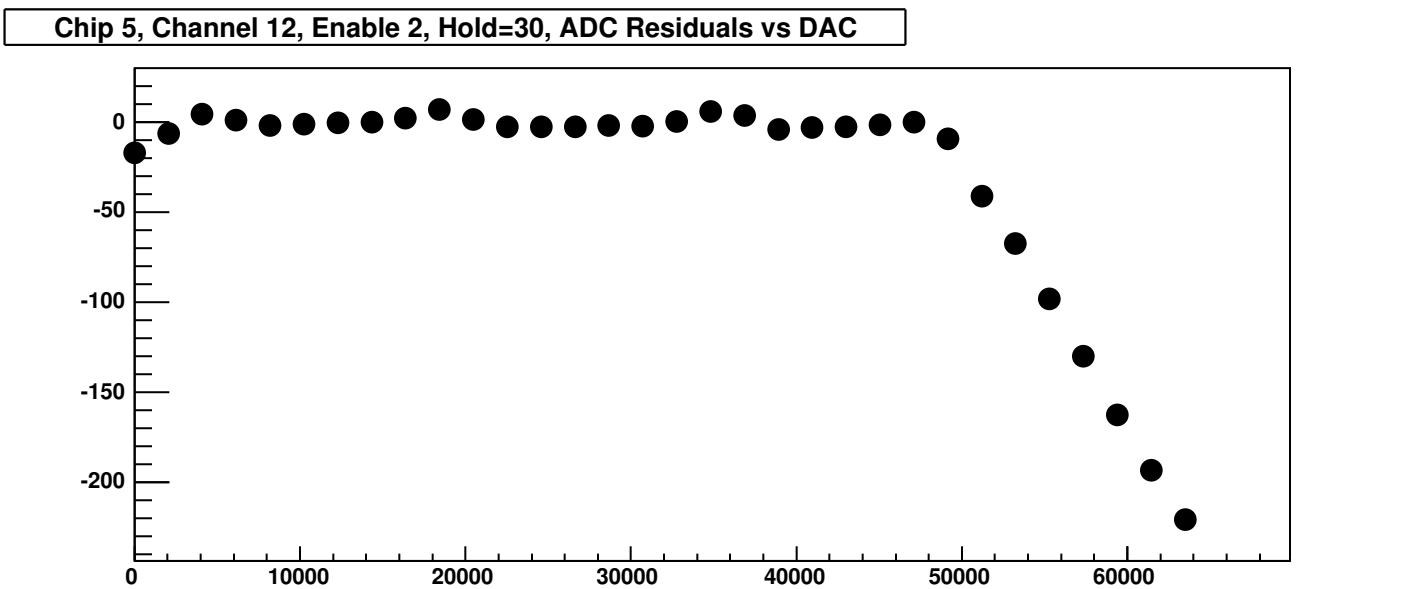


Chip 5, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC

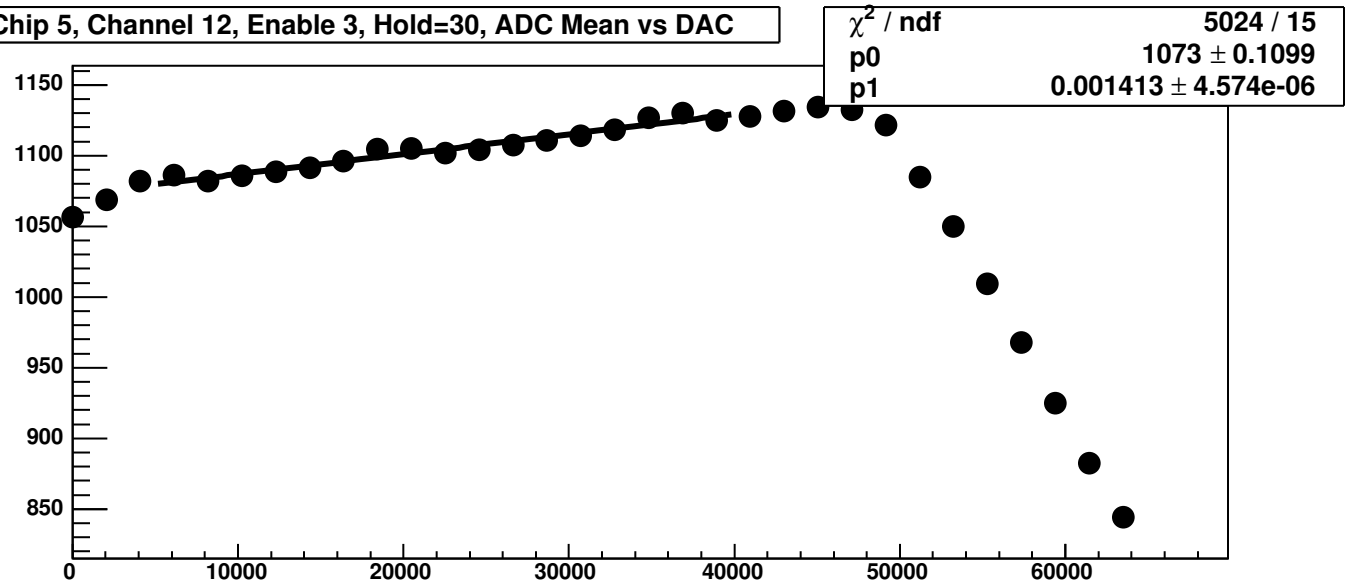



Chip 5, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4551 / 15
p0 1074 ± 0.1098
p1 0.001184 ± 4.493e-06
1140
1120
1100
1080
1060
1040
1020
1000
980
960
940
920
0
10000
20000
30000
40000
50000
60000

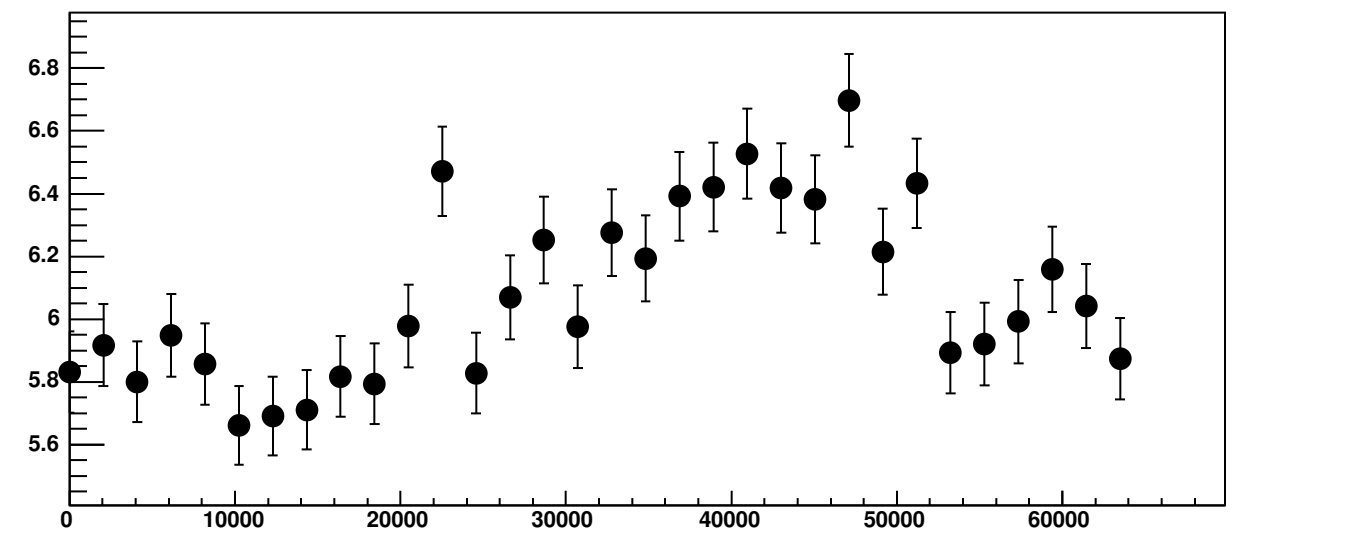

Chip 5, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

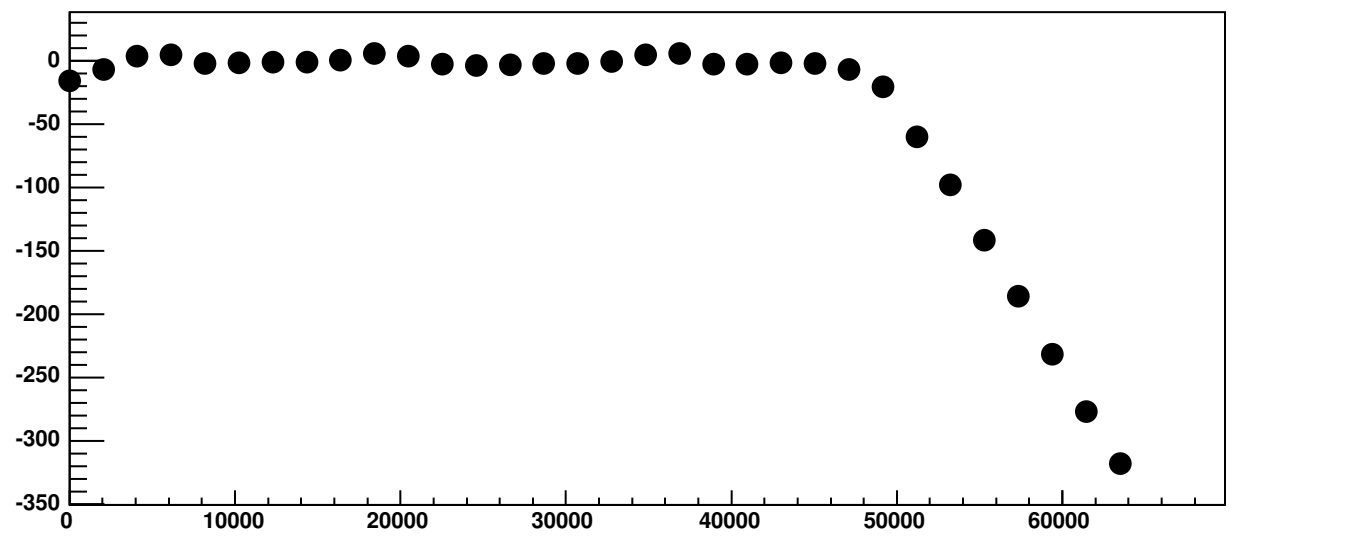
Chip 5, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

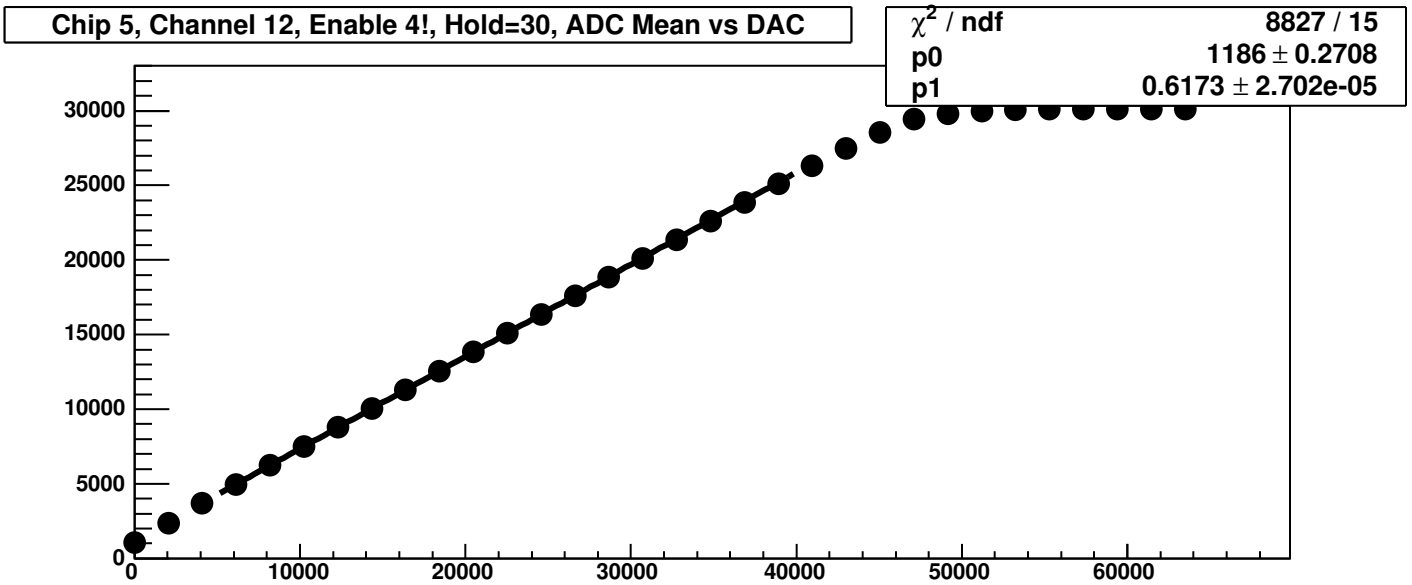


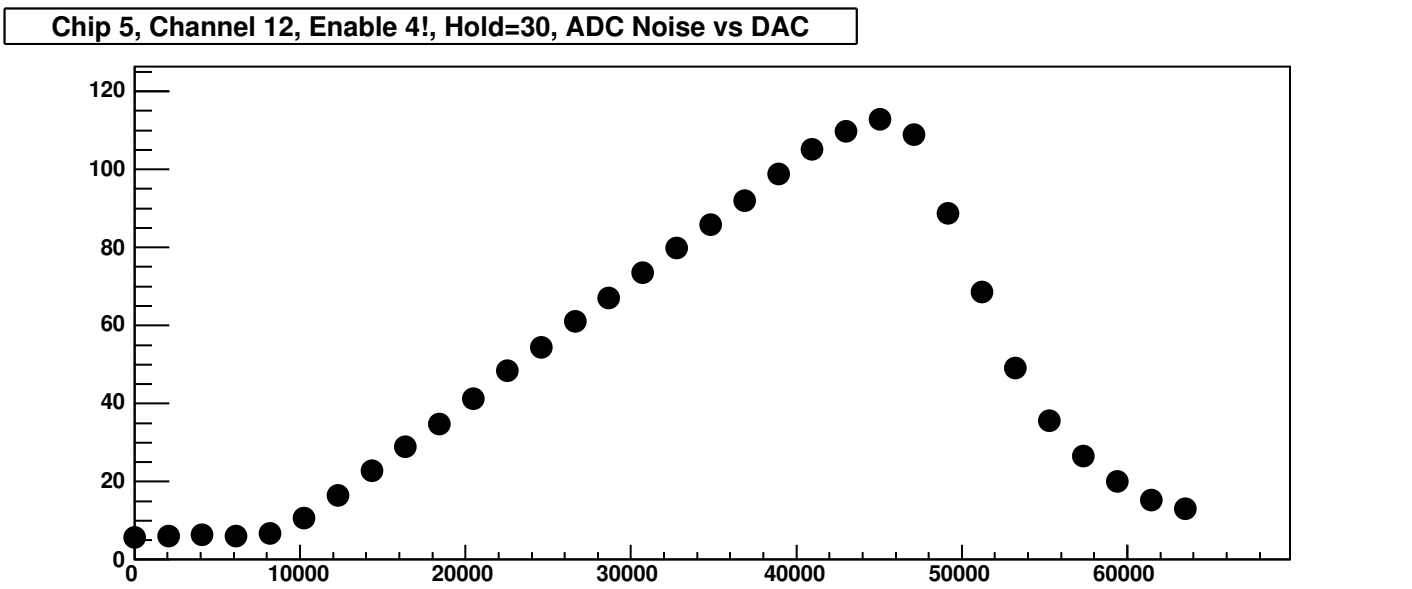
Chip 5, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC

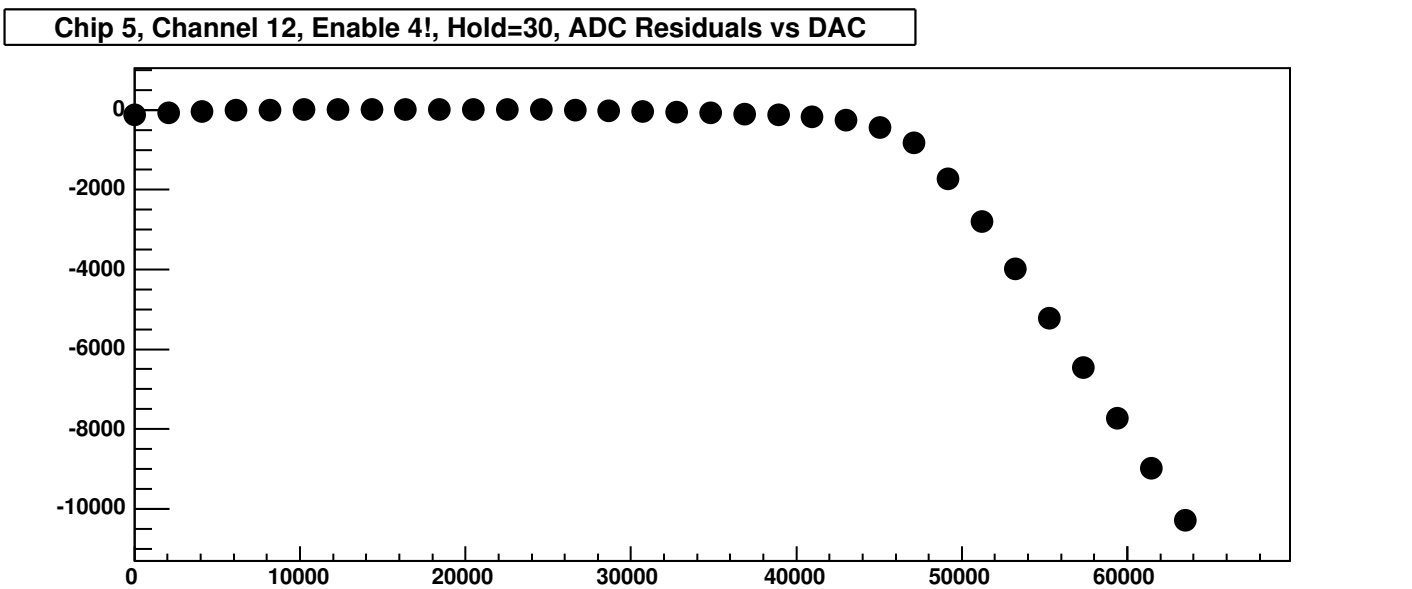


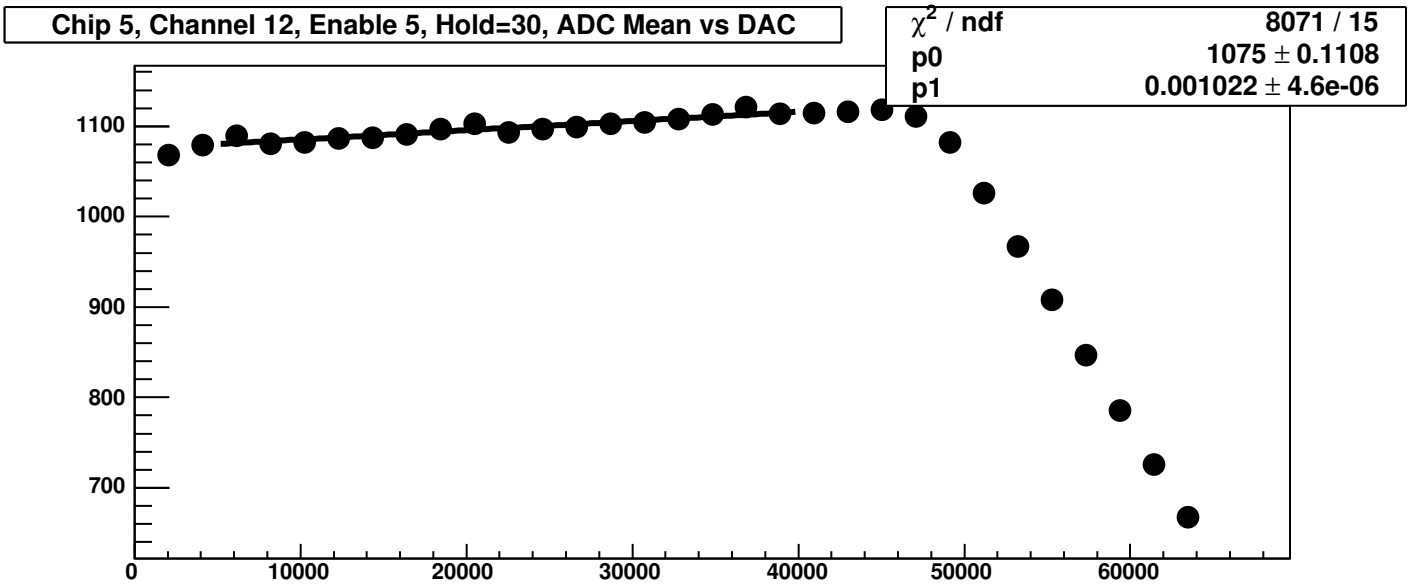
Chip 5, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

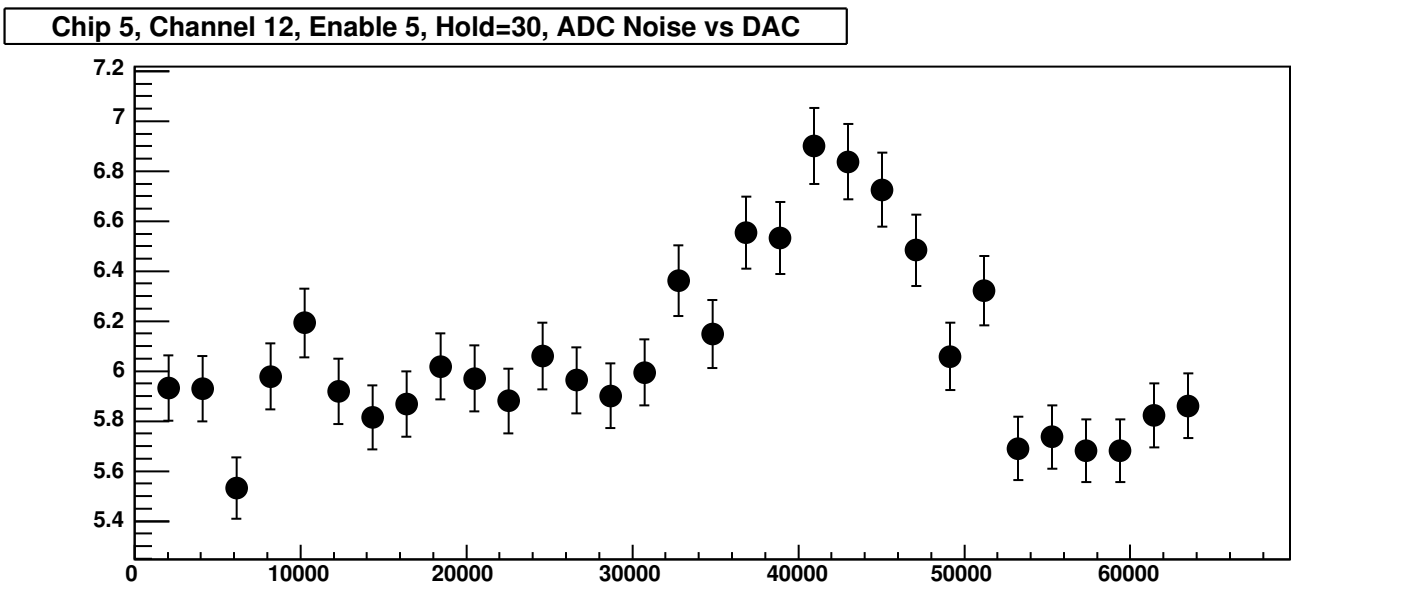


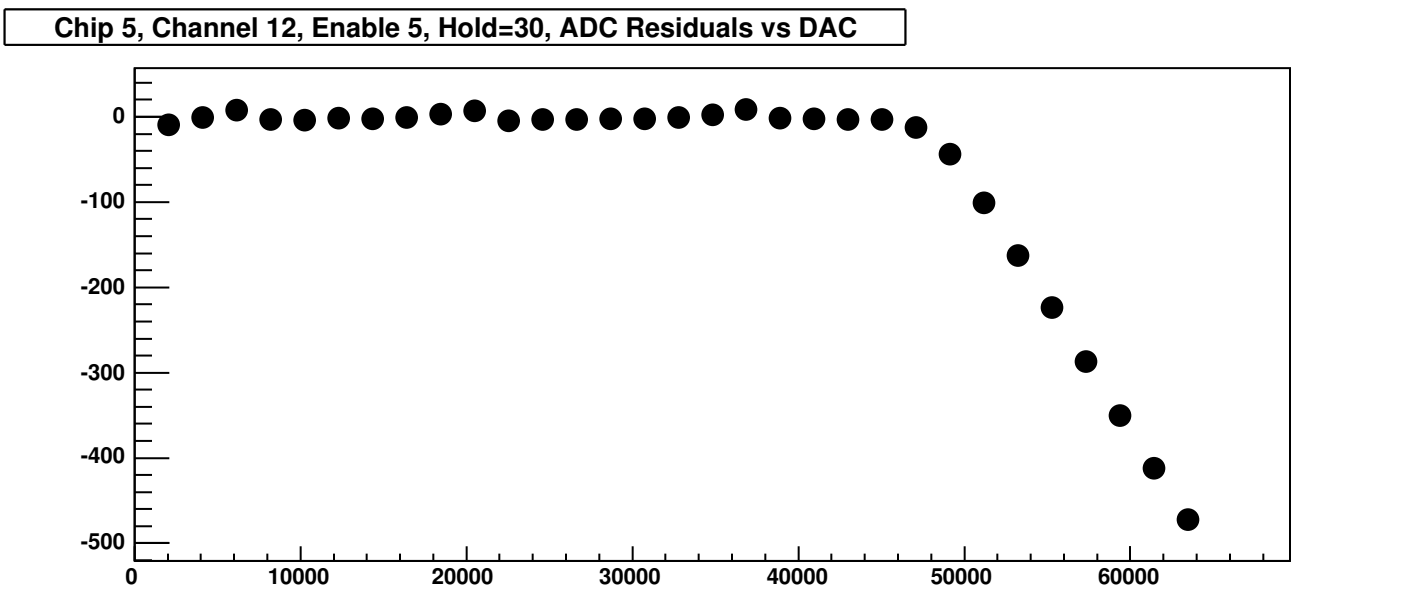

Chip 5, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 8827 / 15
p0 1186 ± 0.2708
p1 0.6173 ± 2.702e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

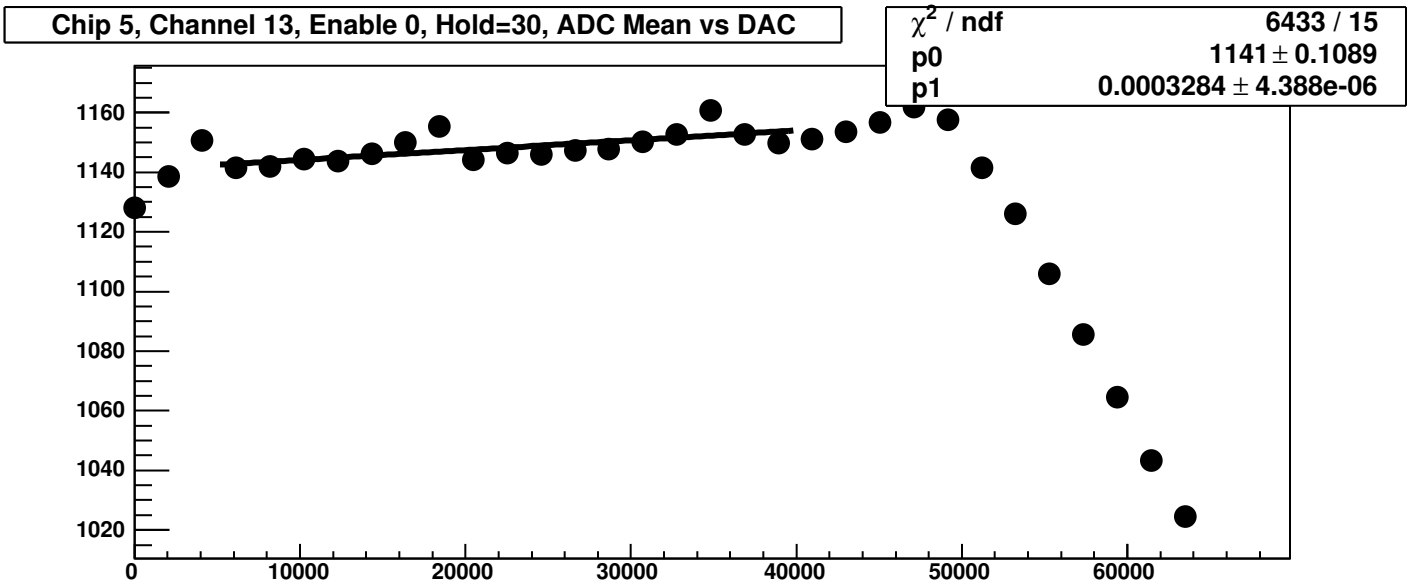

Chip 5, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

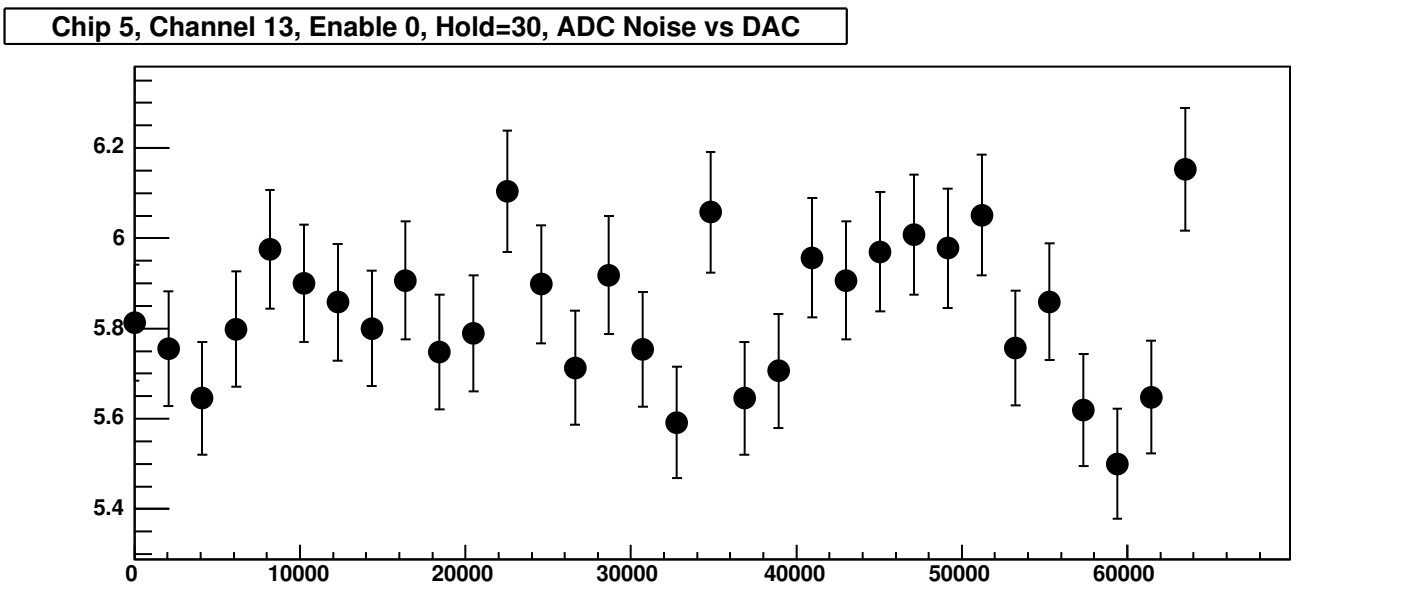

Chip 5, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

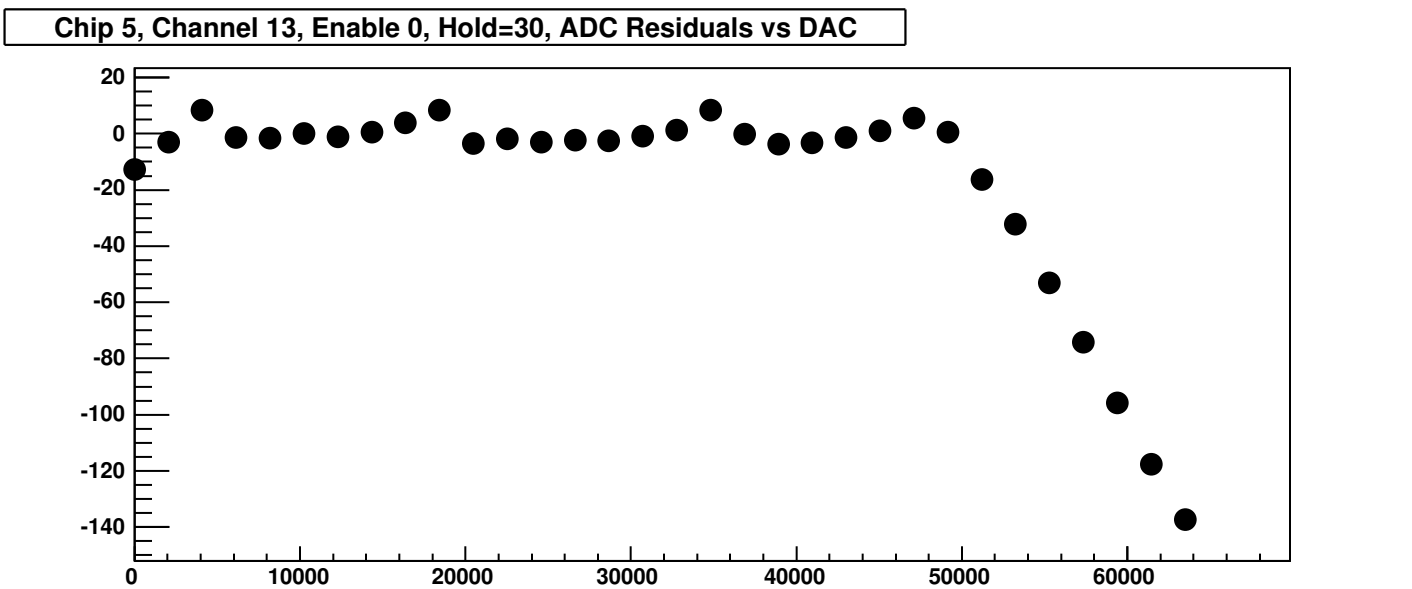

Chip 5, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8071 / 15
p0 1075 ± 0.1108
p1 0.001022 ± 4.6e-06
1100
1000
900
800
700
0
10000
20000
30000
40000
50000
60000

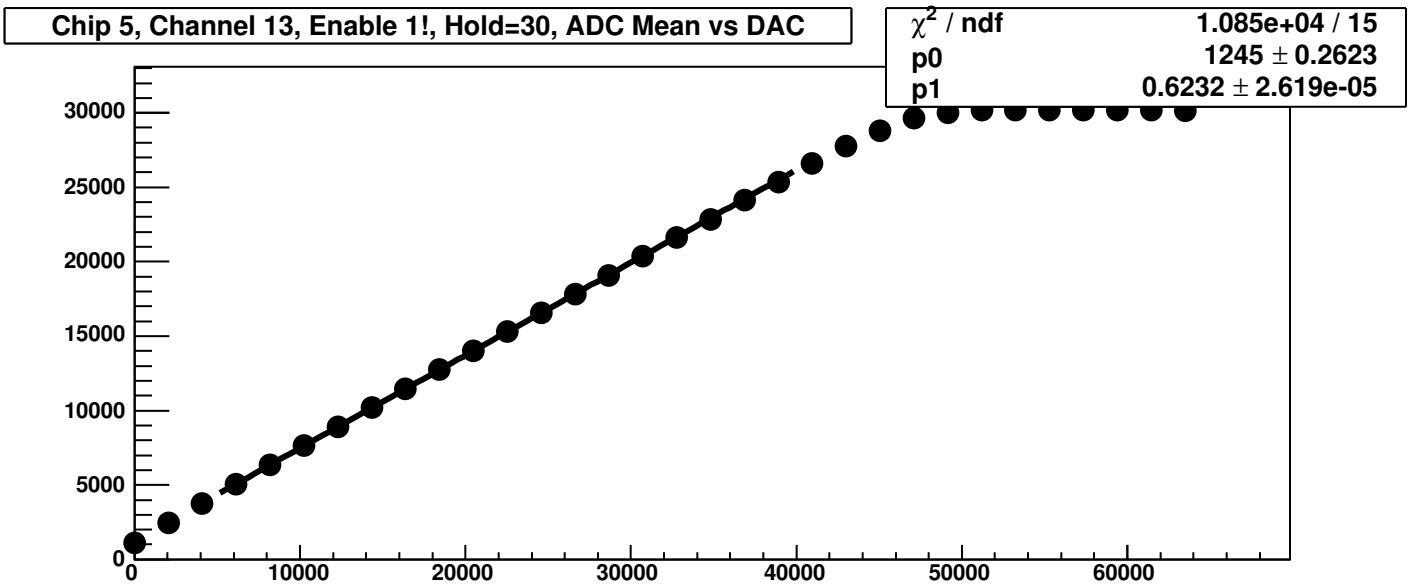

Chip 5, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

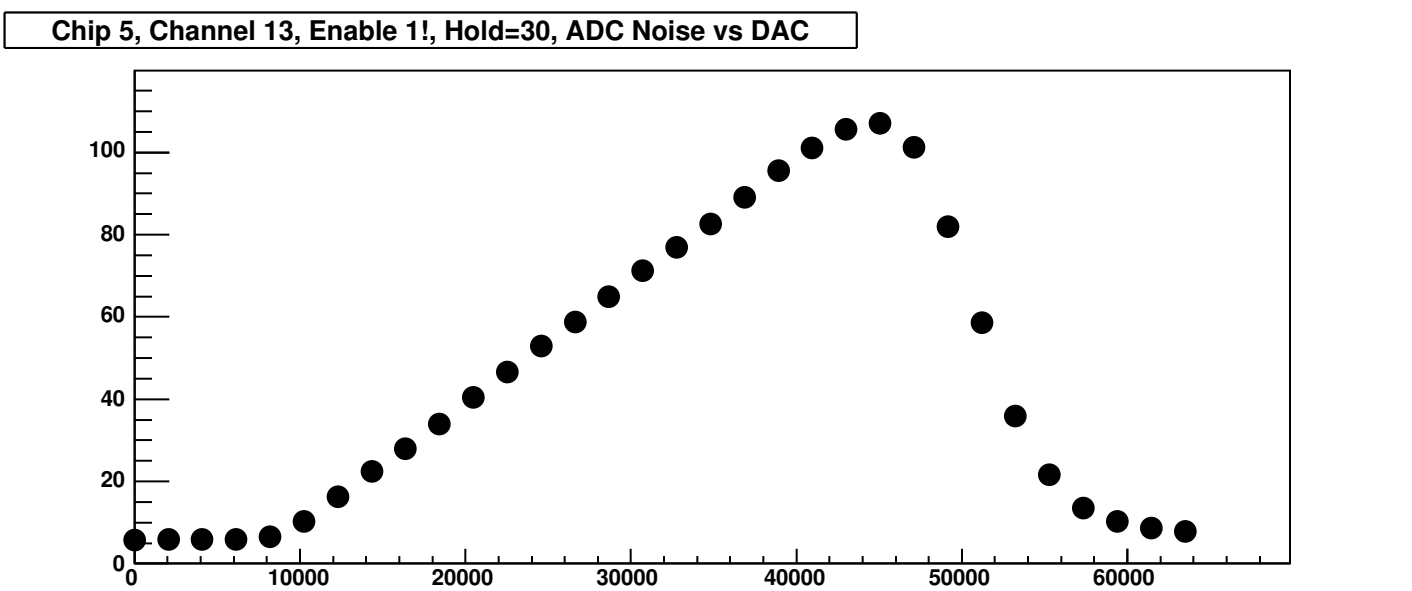

Chip 5, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

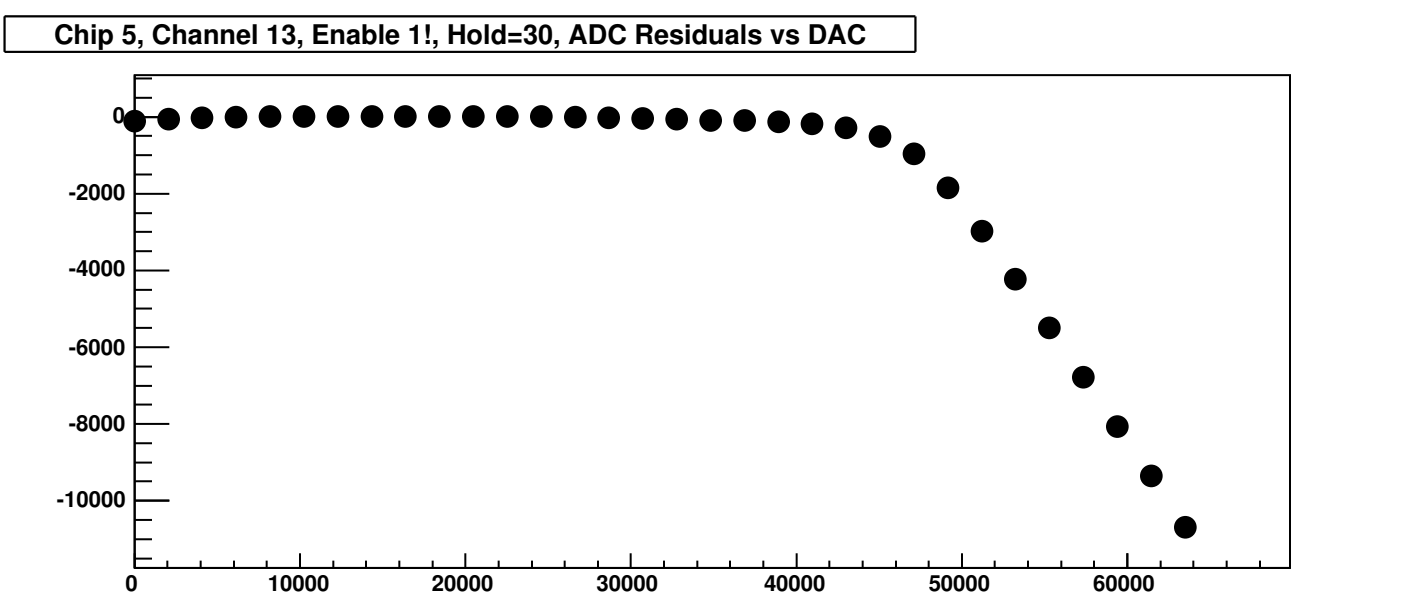

Chip 5, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6433 / 15
p0 1141 ± 0.1089
p1 0.0003284 ± 4.388e-06
1160
1140
1120
1100
1080
1060
1040
1020
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

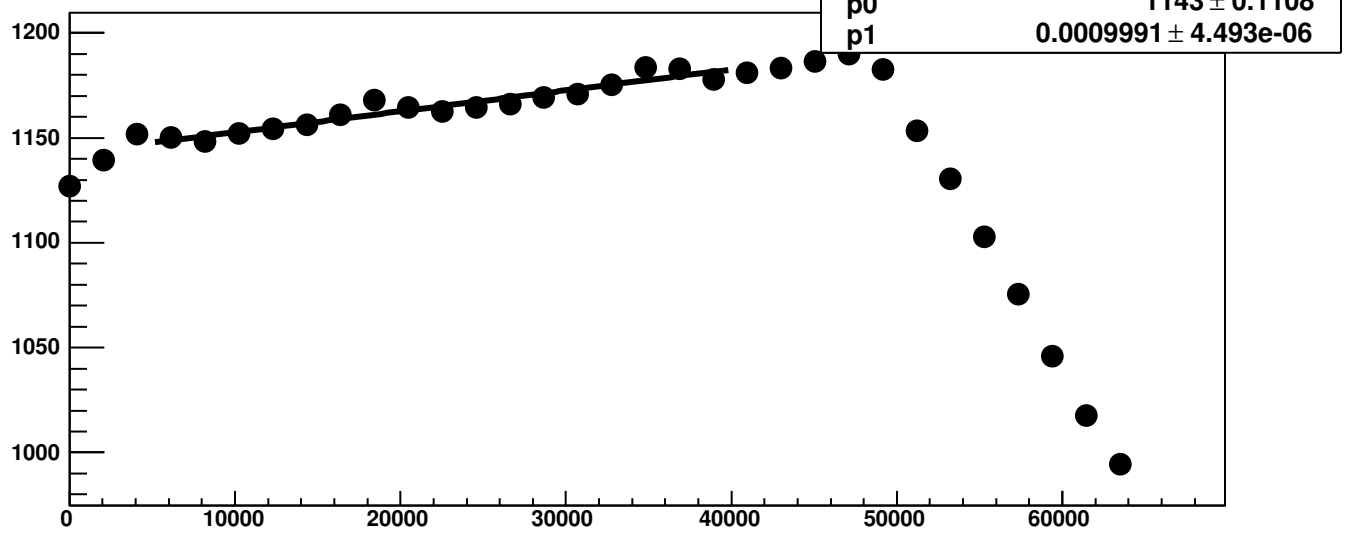

Chip 5, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.085e+04 / 15
p0 1245 ± 0.2623
p1 0.6232 ± 2.619e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

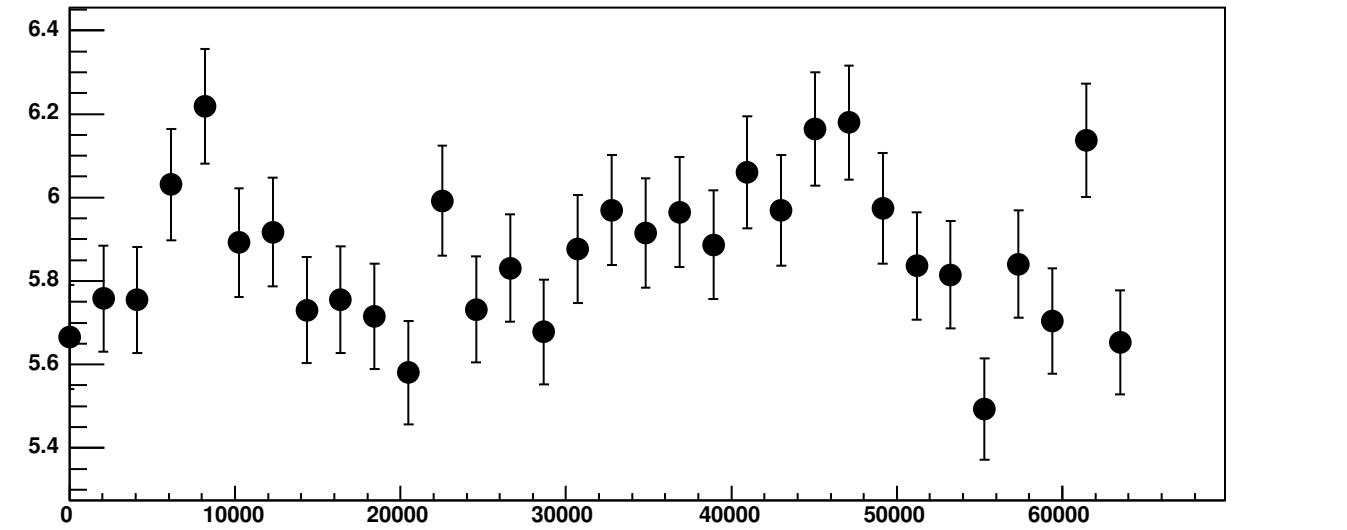

Chip 5, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

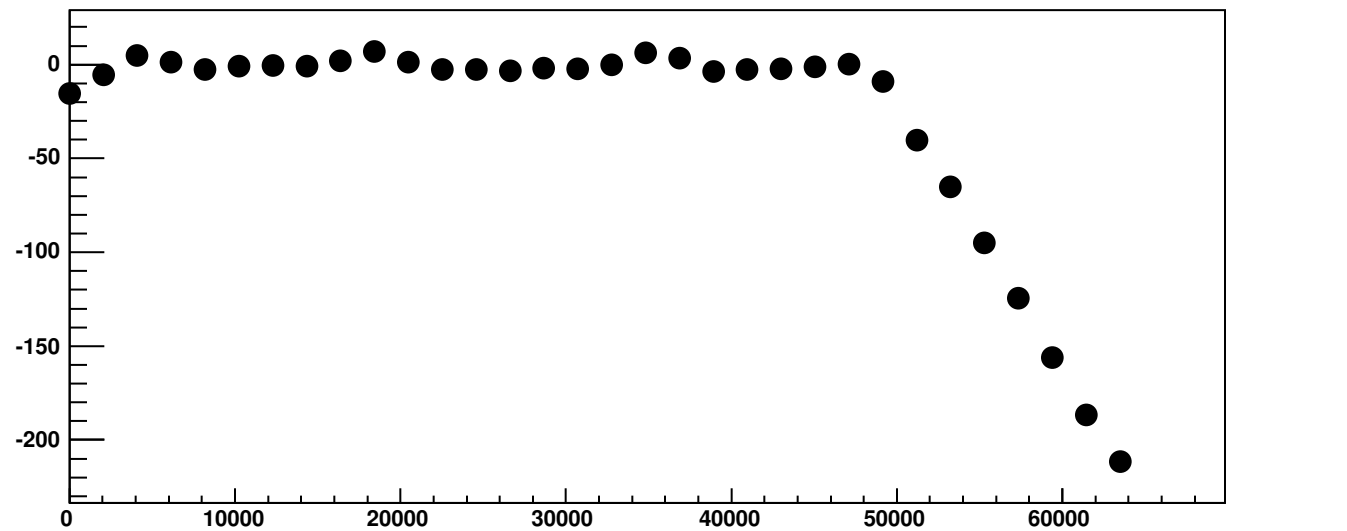
Chip 5, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC

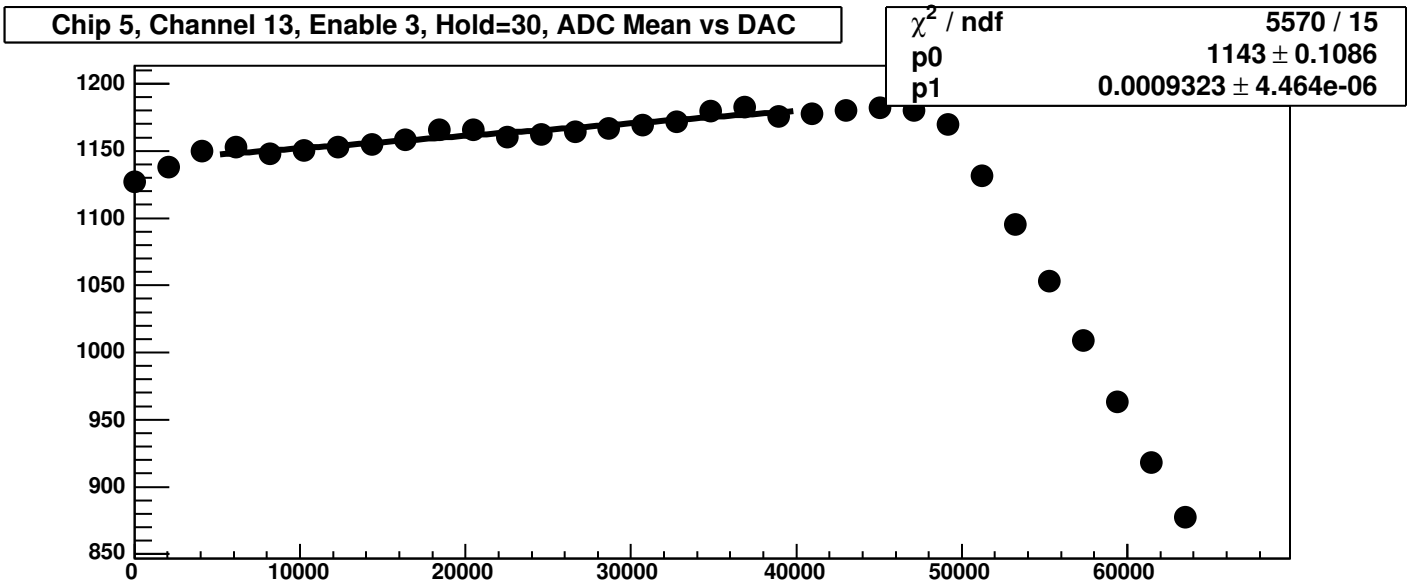


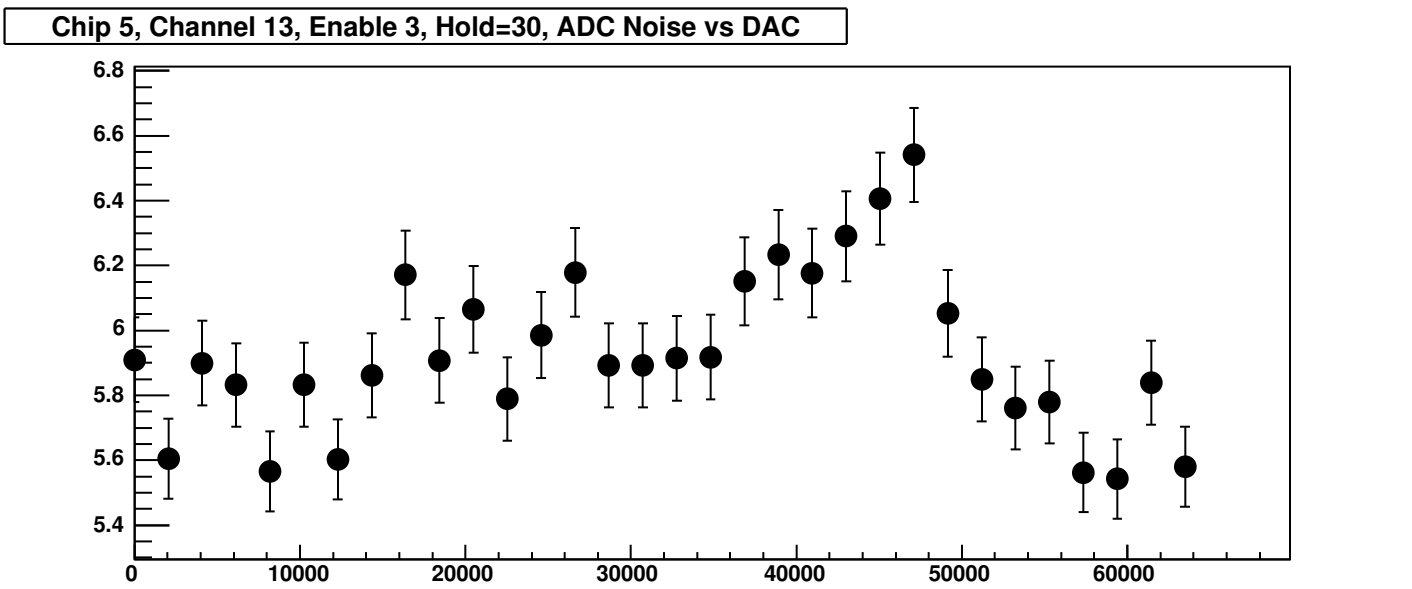
Chip 5, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC

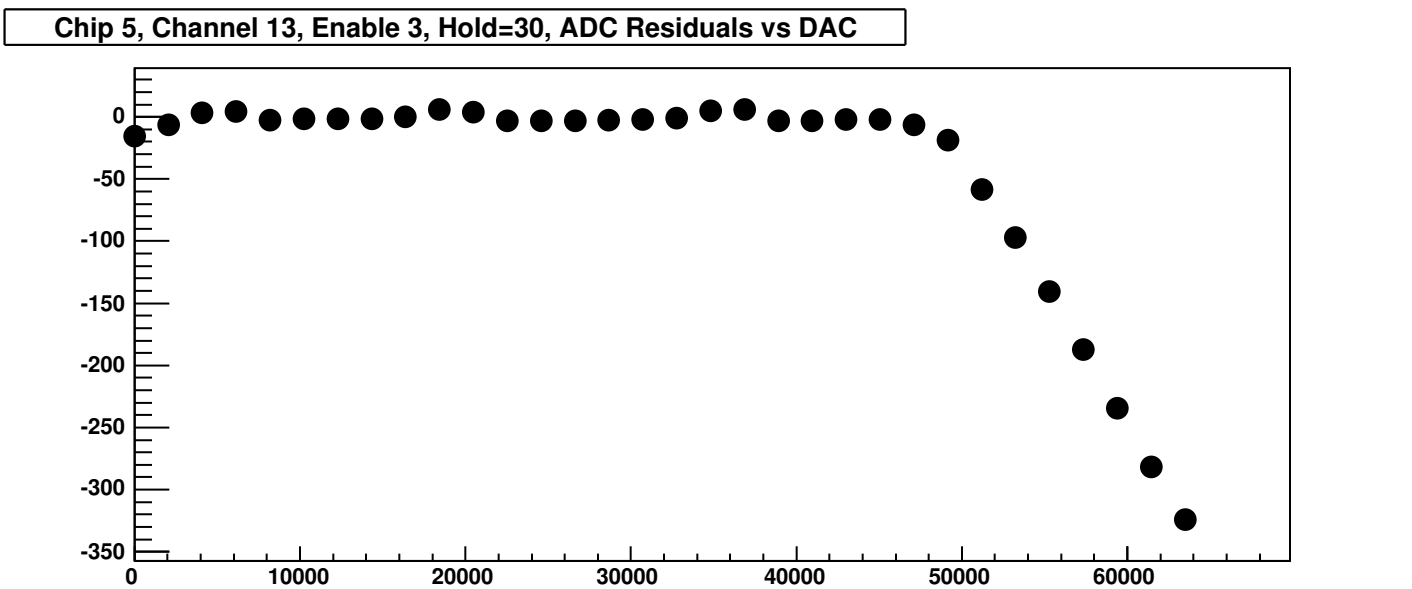


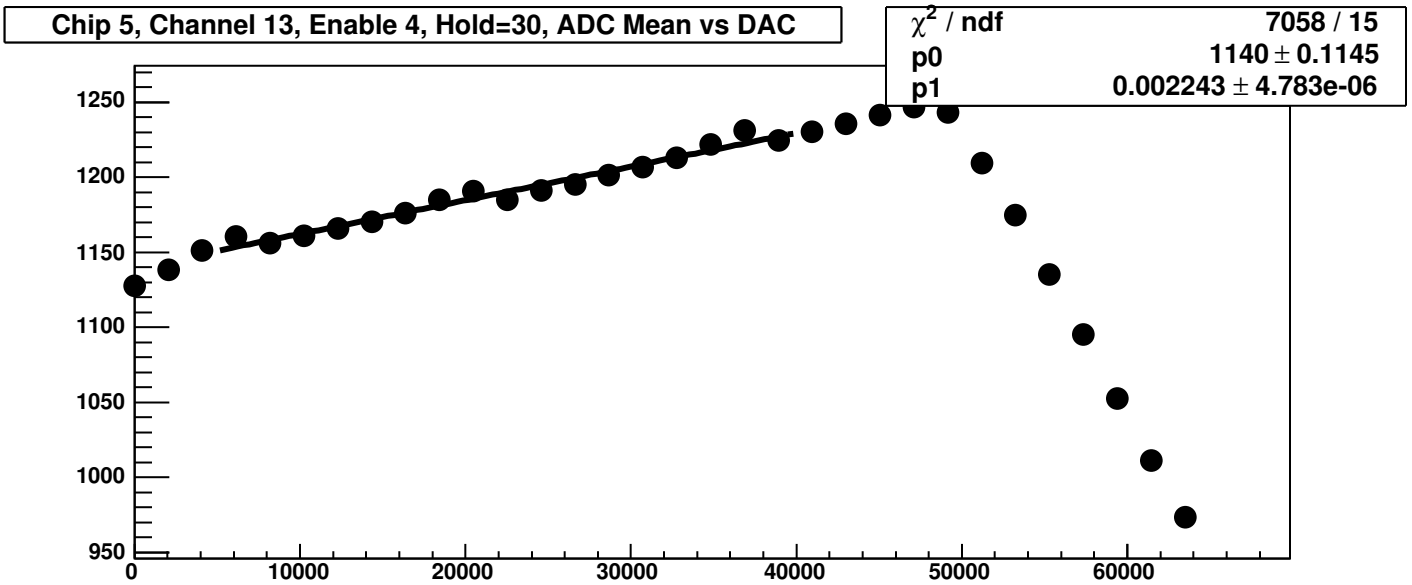
Chip 5, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC

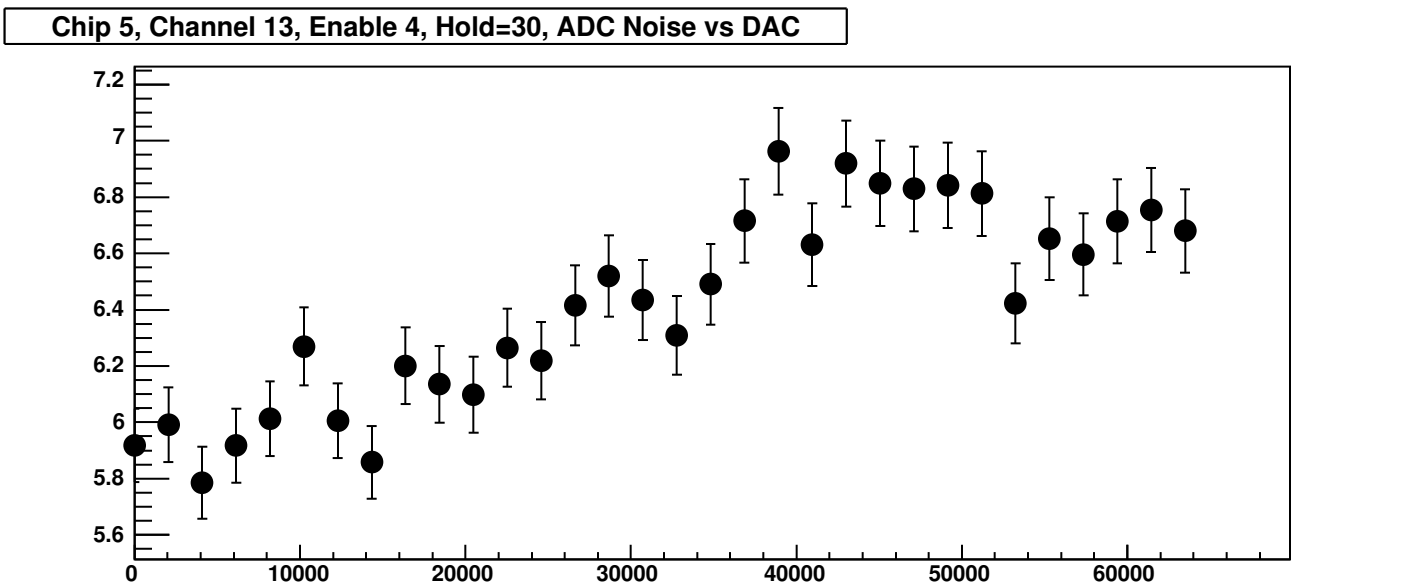


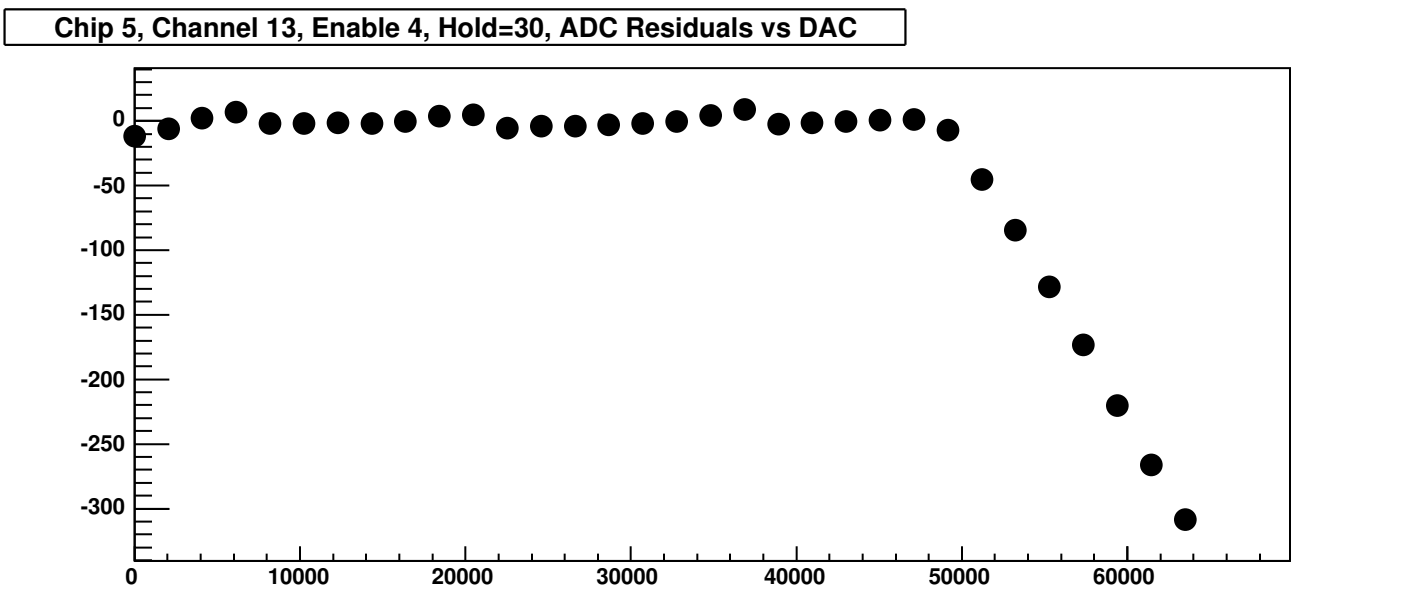

Chip 5, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5570 / 15
p0 1143 ± 0.1086
p1 0.0009323 ± 4.464e-06
1200
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

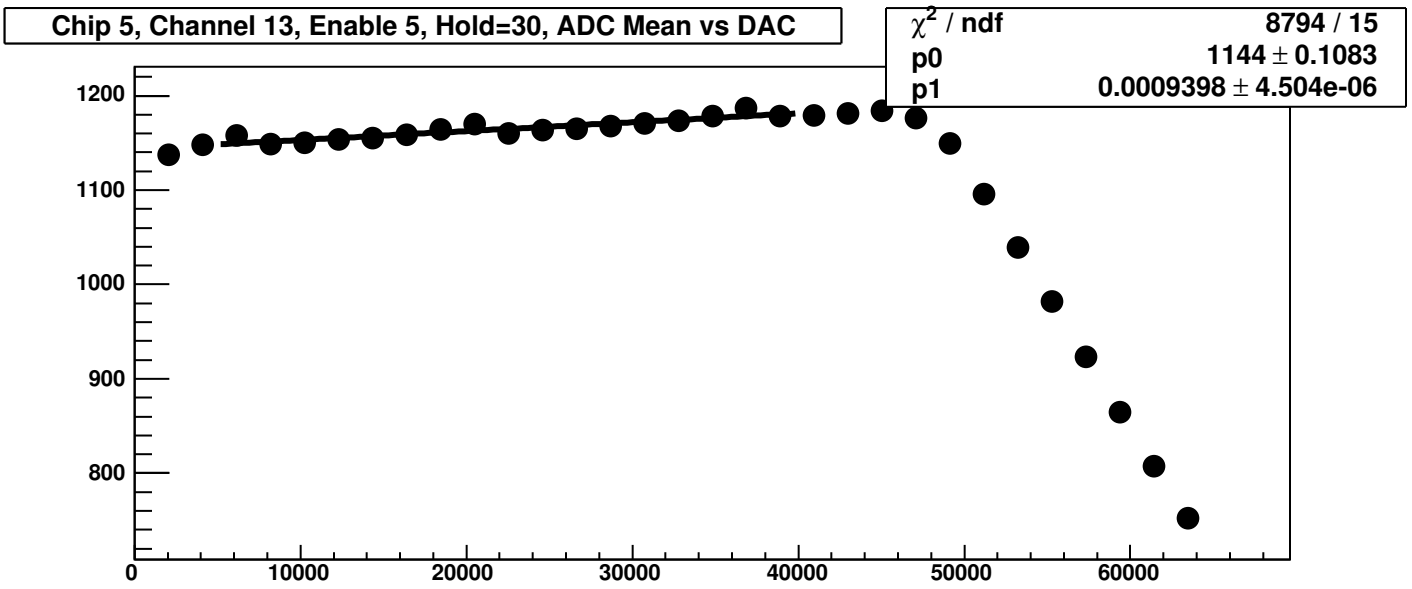

Chip 5, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

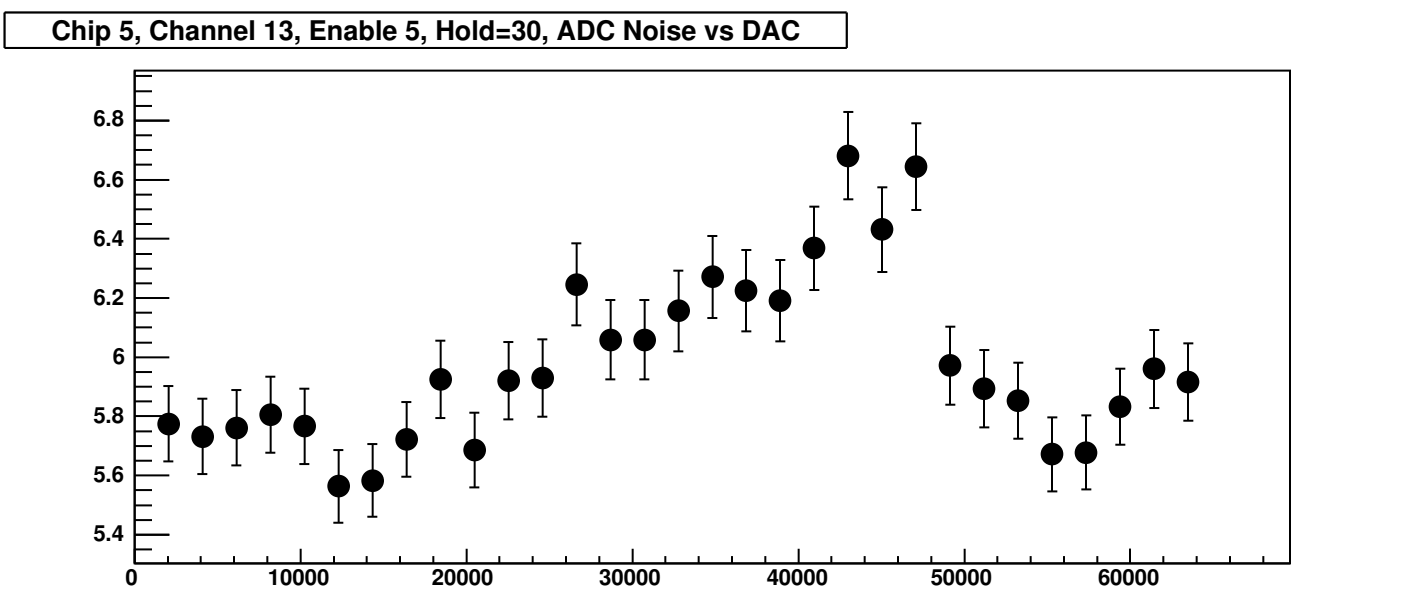

Chip 5, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

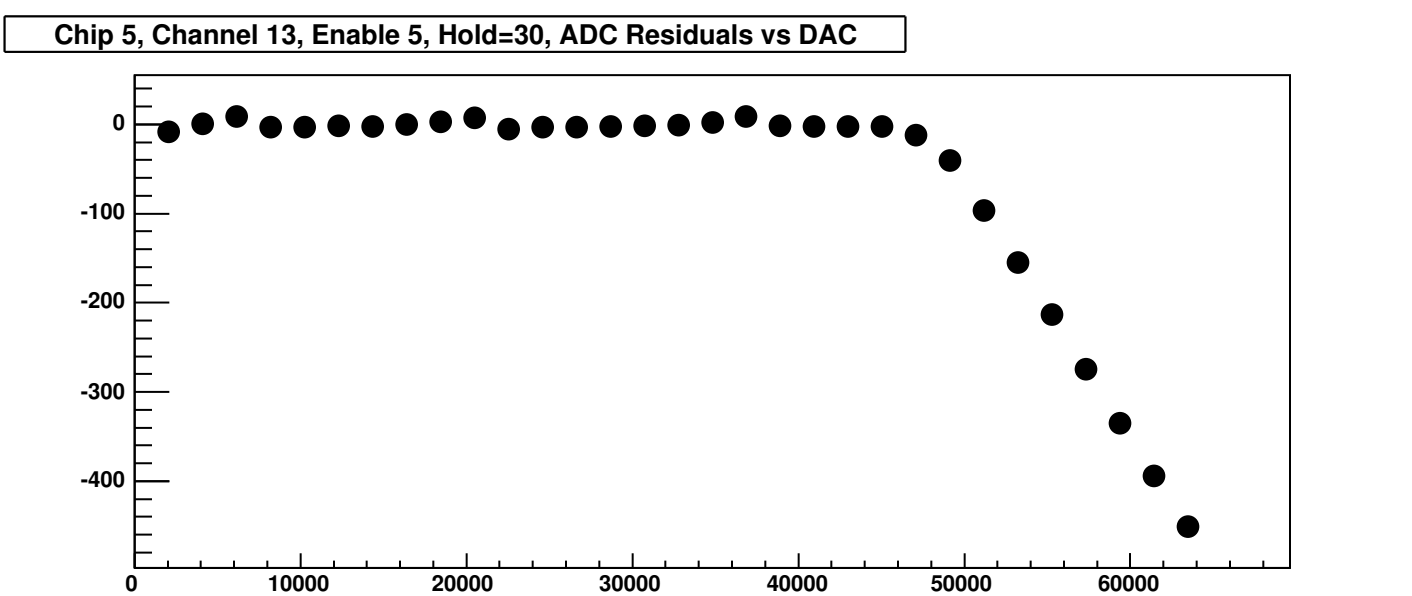

Chip 5, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7058 / 15
p0 1140 ± 0.1145
p1 0.002243 ± 4.783e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

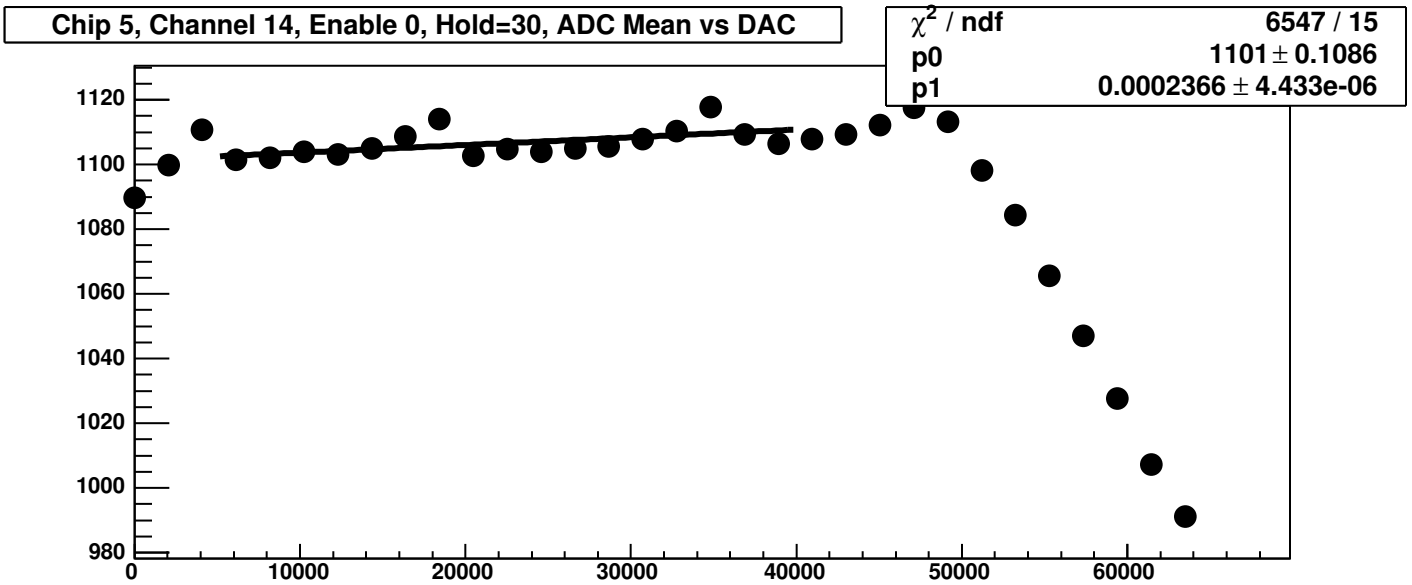

Chip 5, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

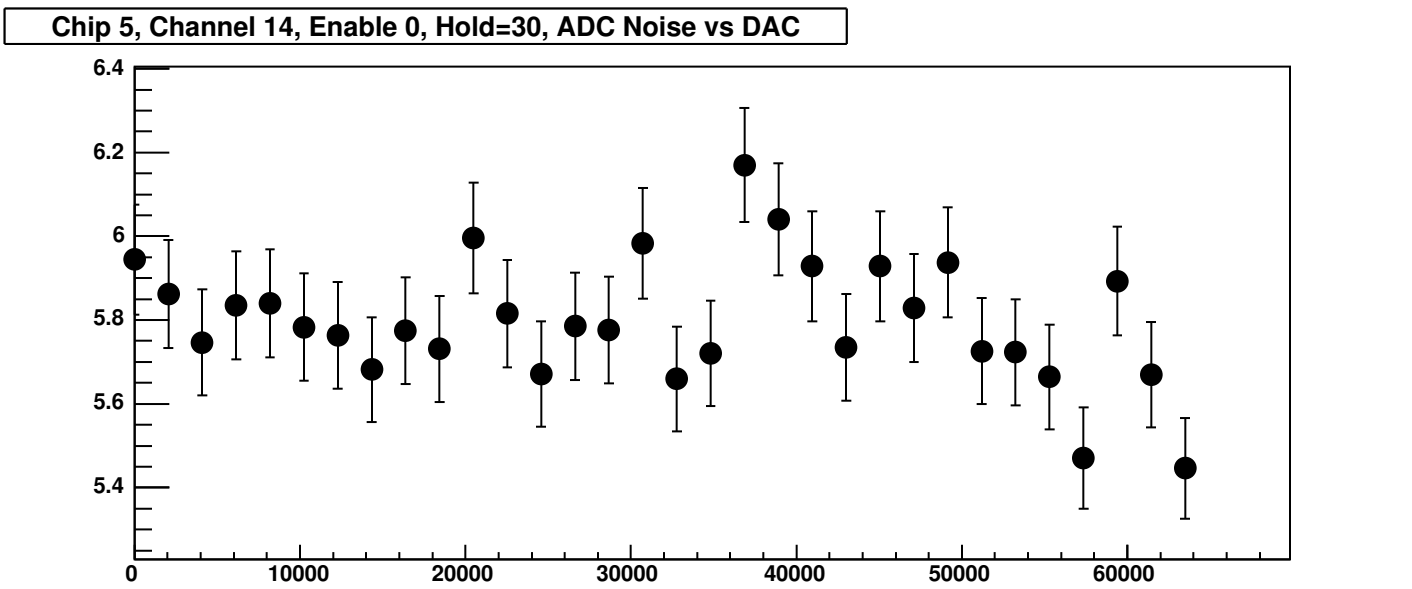

Chip 5, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

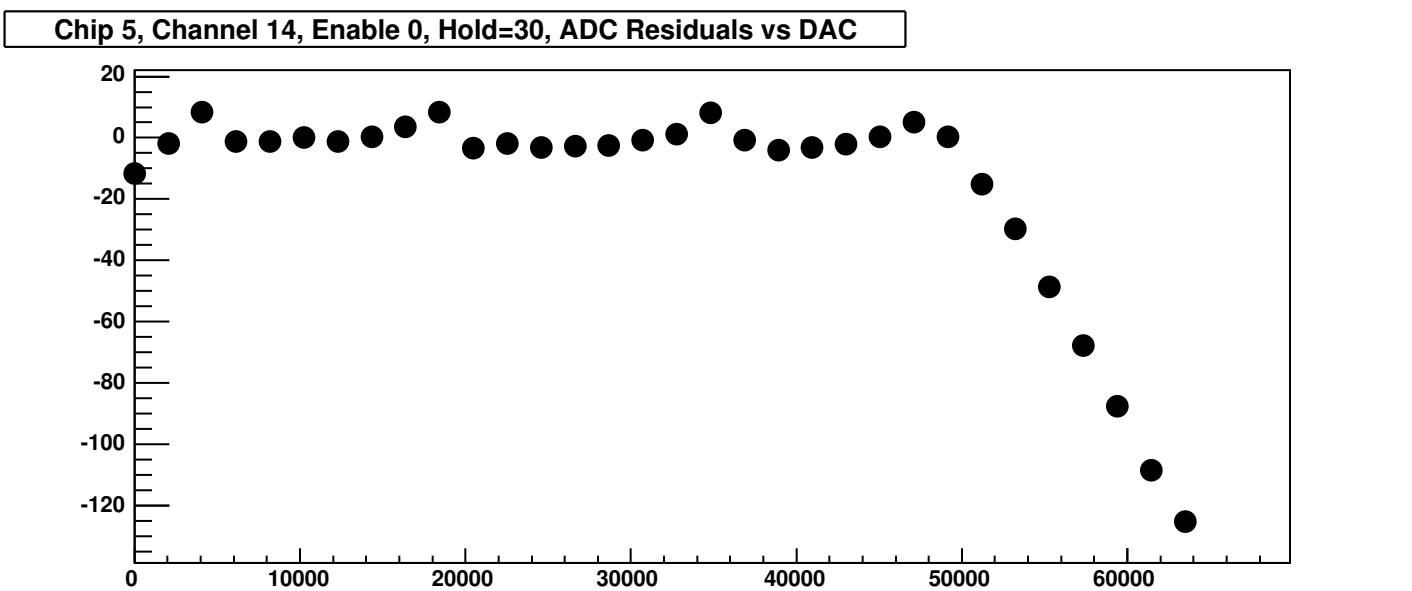

Chip 5, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8794 / 15
p0 1144 ± 0.1083
p1 0.0009398 ± 4.504e-06
1200
1100
1000
900
800
0
10000
20000
30000
40000
50000
60000

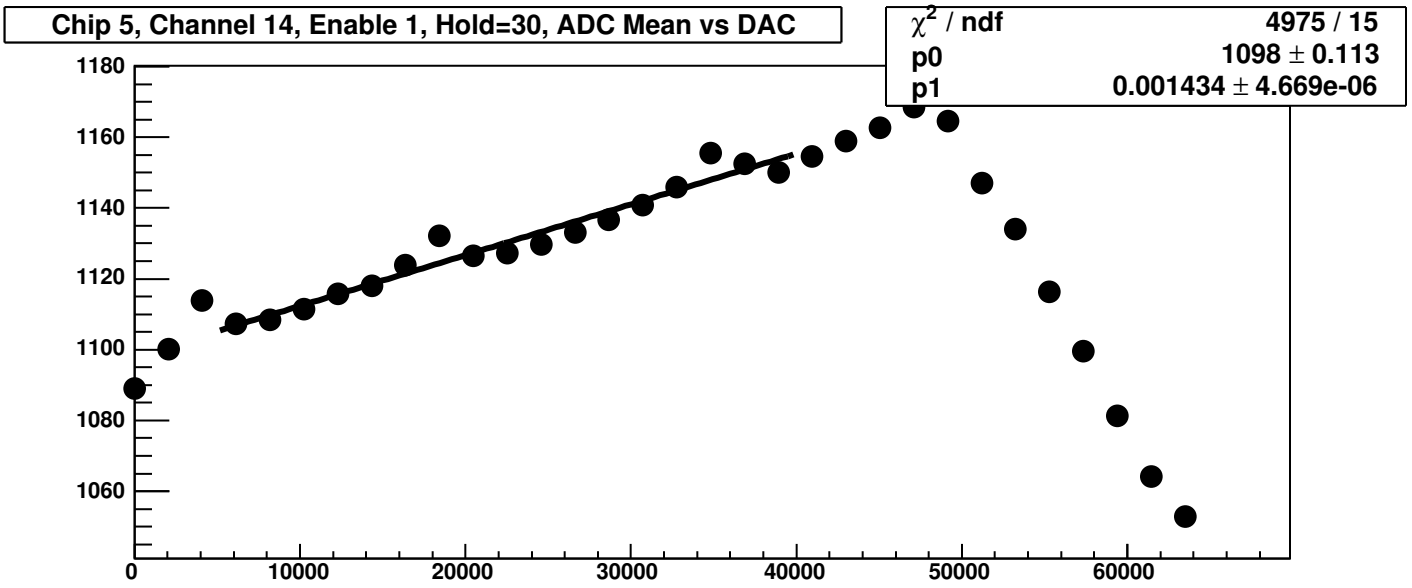

Chip 5, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

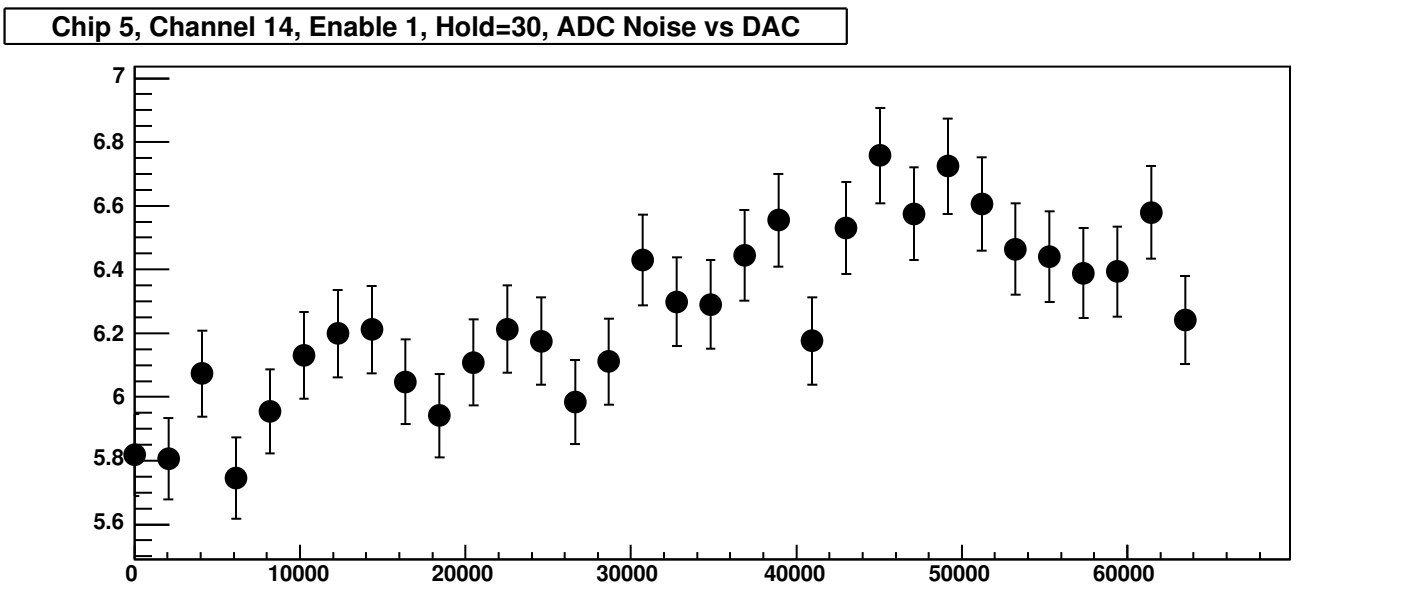

Chip 5, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

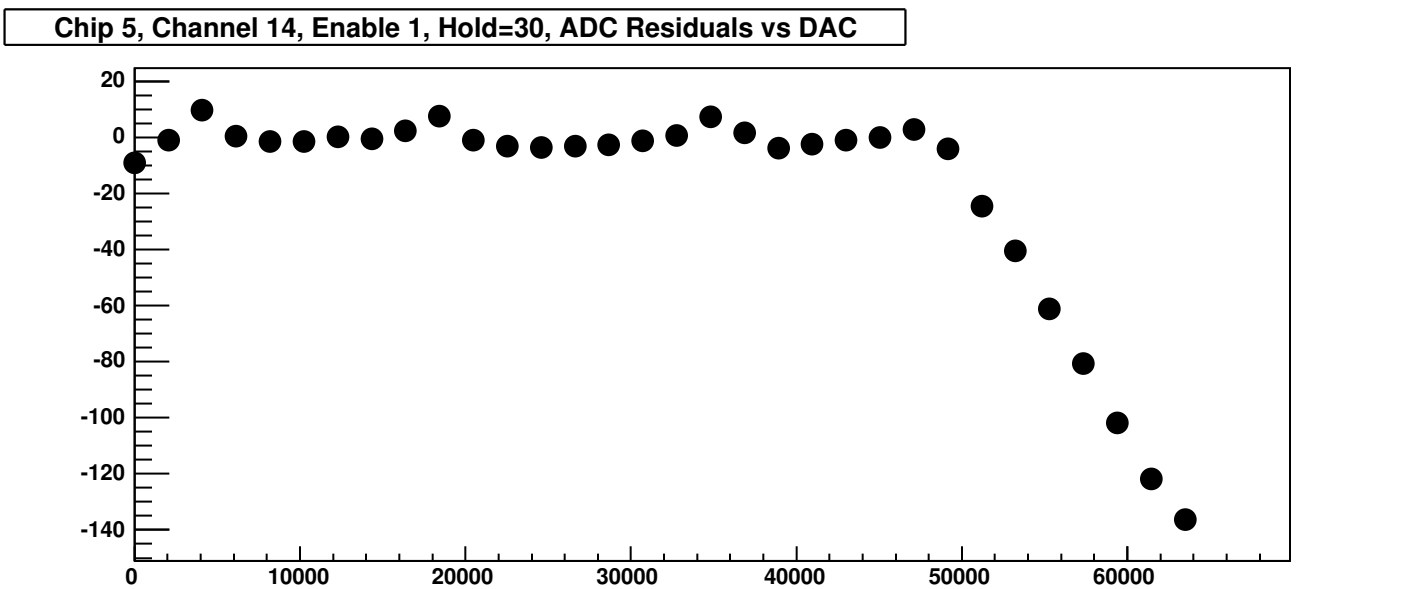

Chip 5, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6547 / 15
p0 1101 ± 0.1086
p1 0.0002366 ± 4.433e-06
1120
1100
1080
1060
1040
1020
1000
980
0
10000
20000
30000
40000
50000
60000

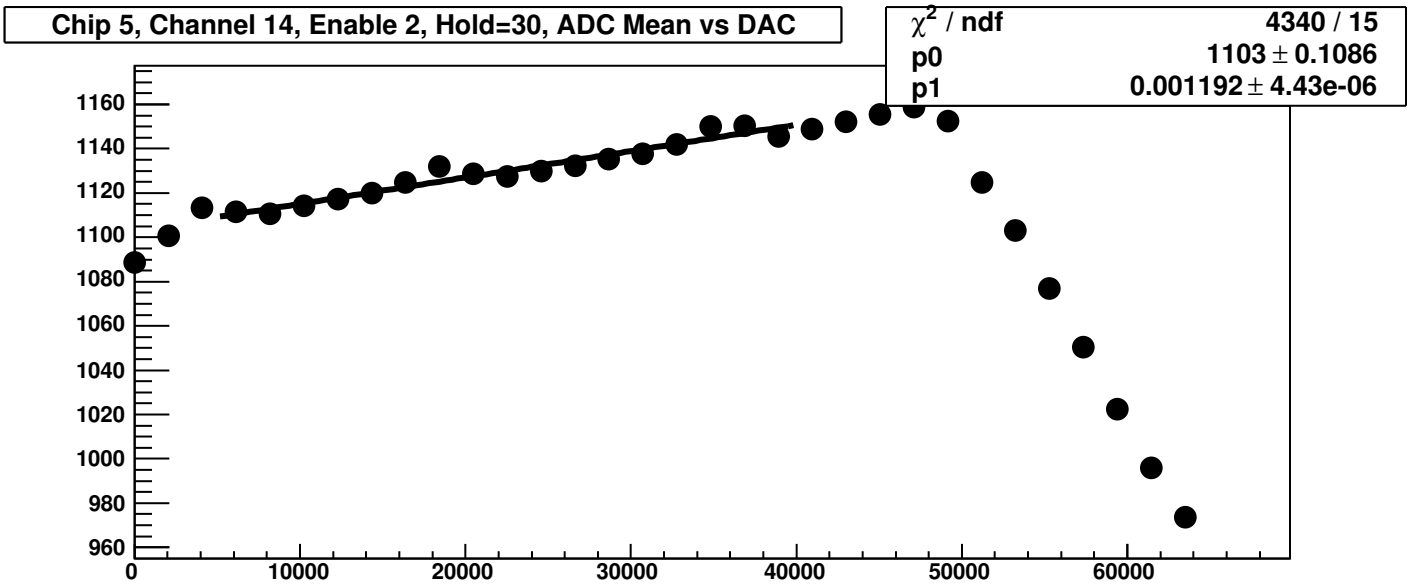

Chip 5, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

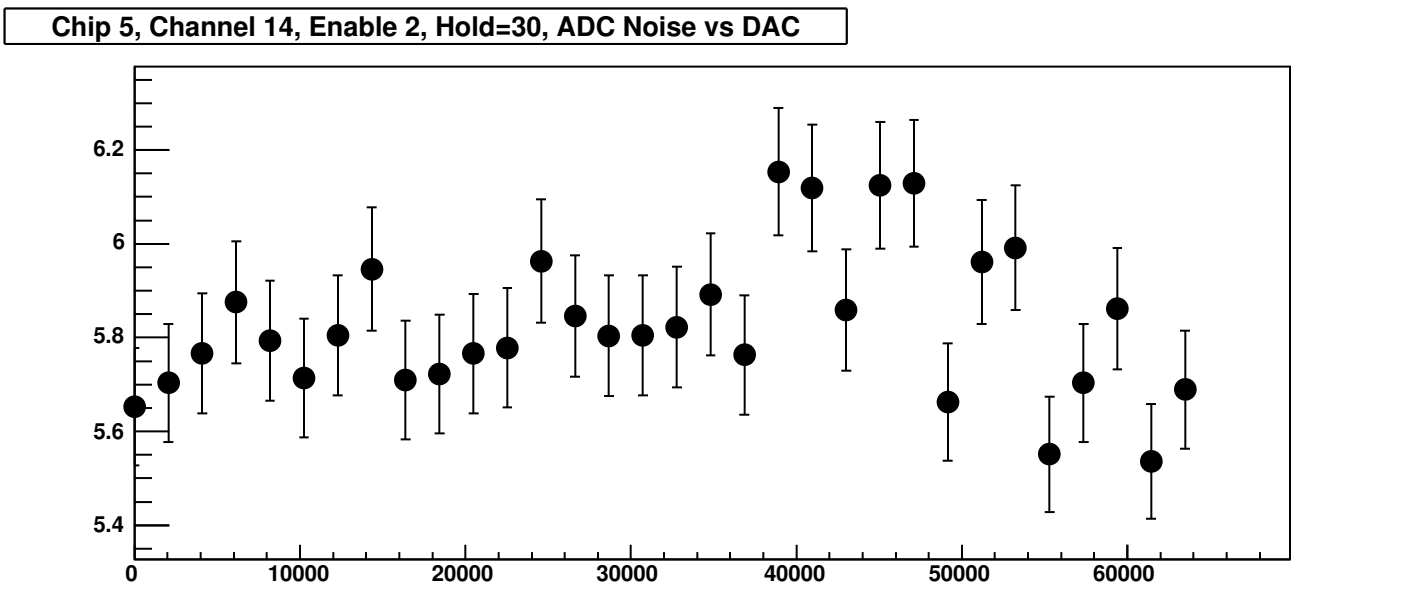

Chip 5, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

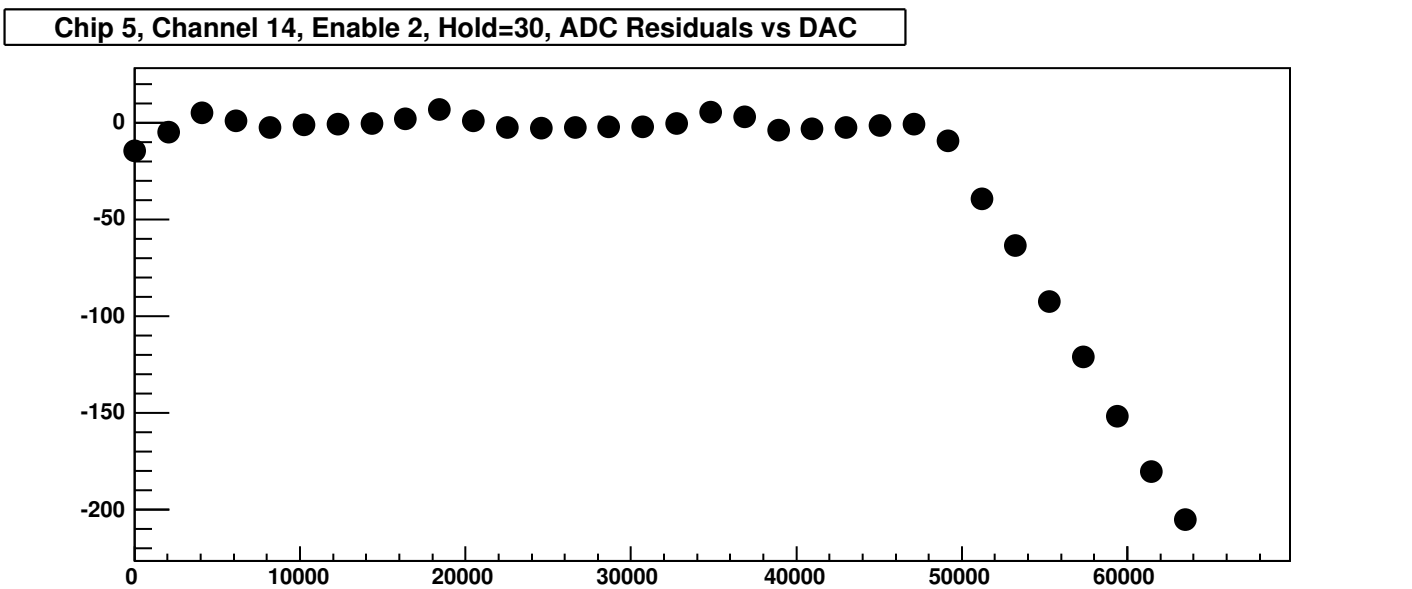

Chip 5, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4975 / 15
p0 1098 ± 0.113
p1 0.001434 ± 4.669e-06
1180
1160
1140
1120
1100
1080
1060
0
10000
20000
30000
40000
50000
60000

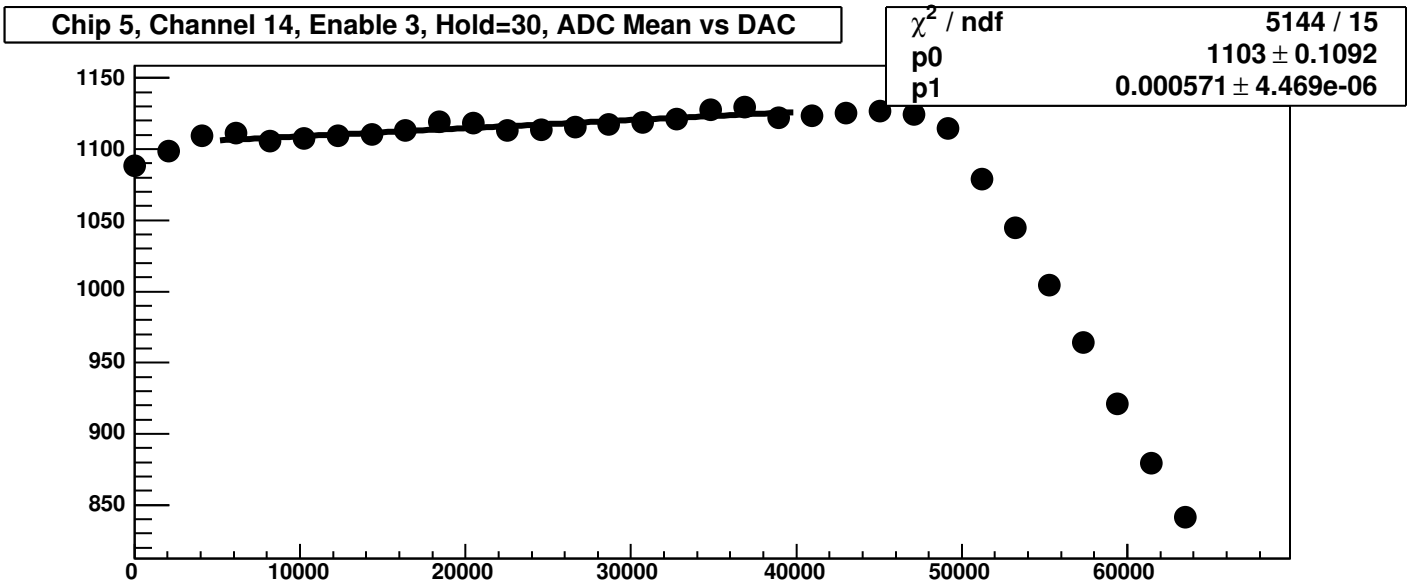

Chip 5, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

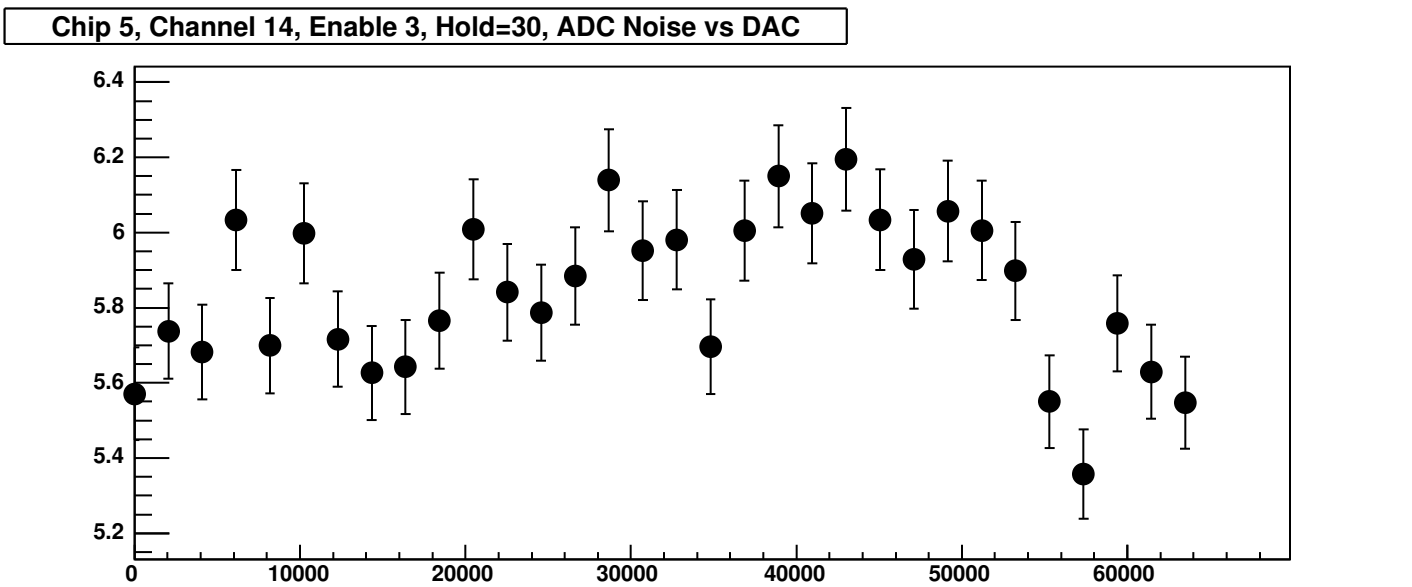

Chip 5, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

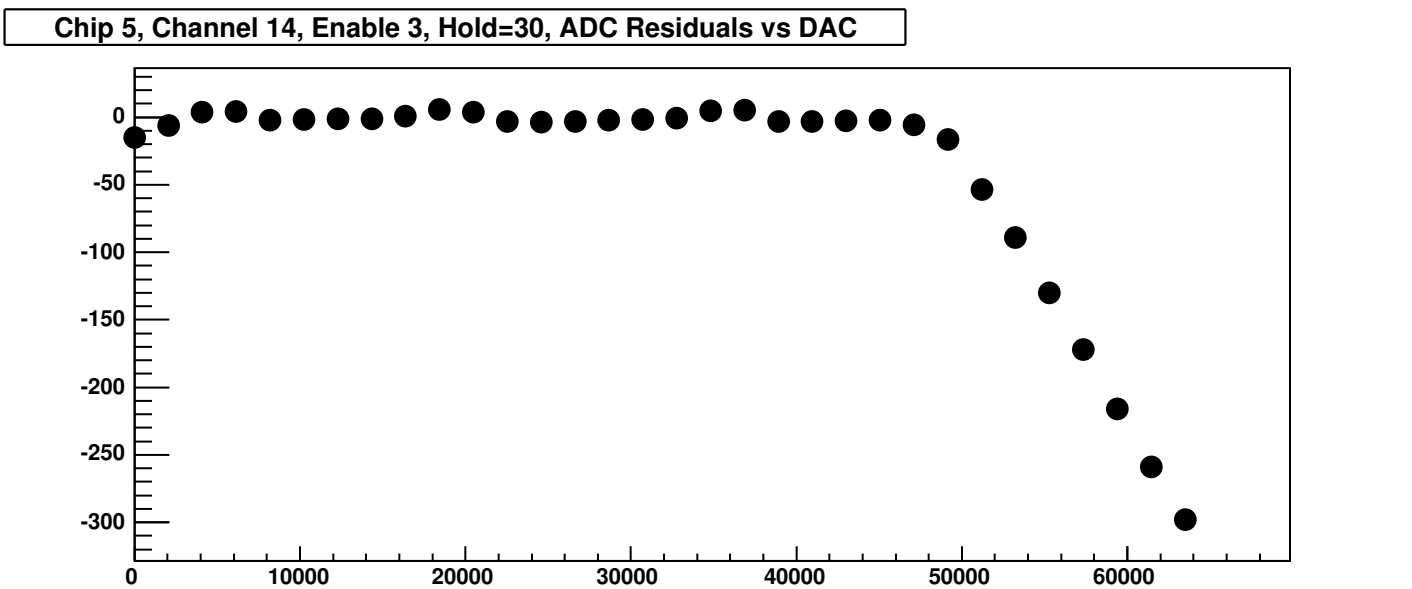

Chip 5, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4340 / 15
p0 1103 ± 0.1086
p1 0.001192 ± 4.43e-06
1160
1140
1120
1100
1080
1060
1040
1020
1000
980
960
0
10000
20000
30000
40000
50000
60000

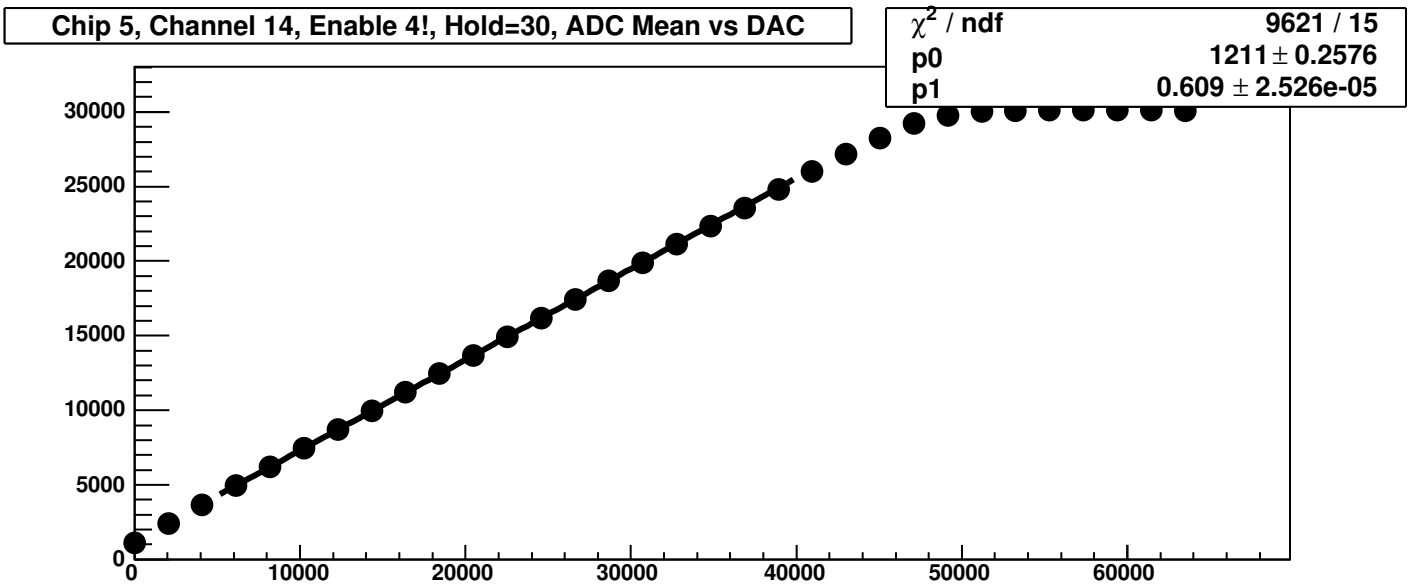

Chip 5, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

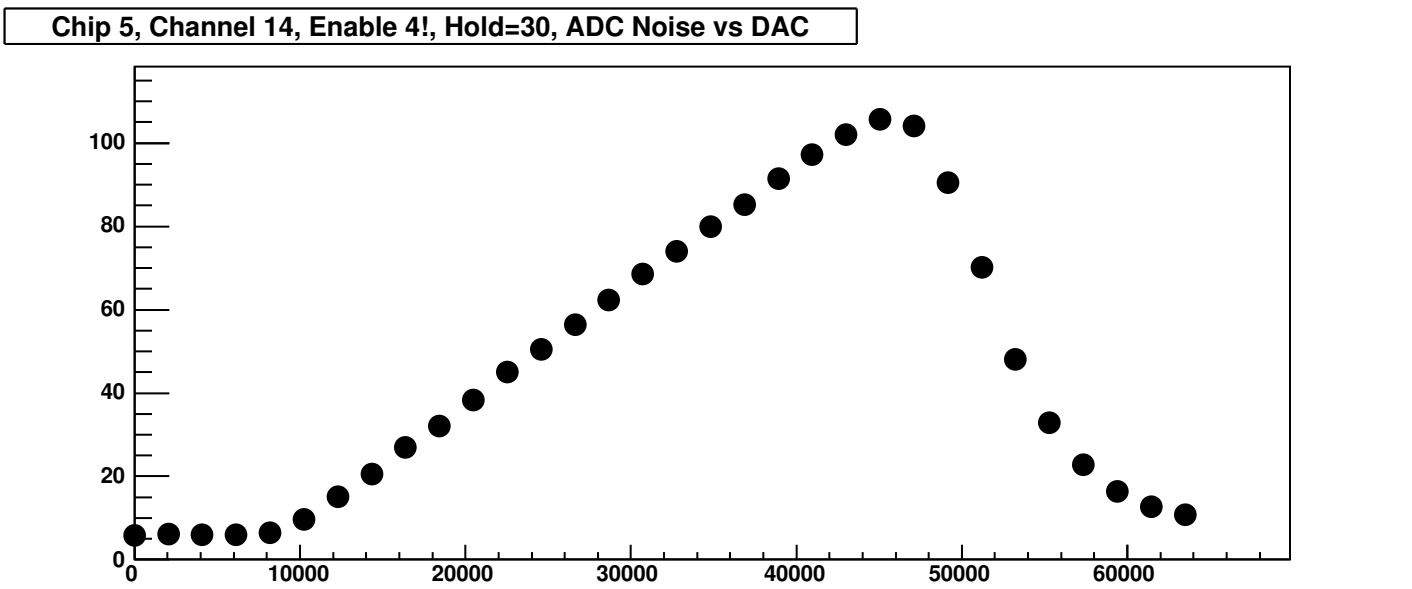

Chip 5, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

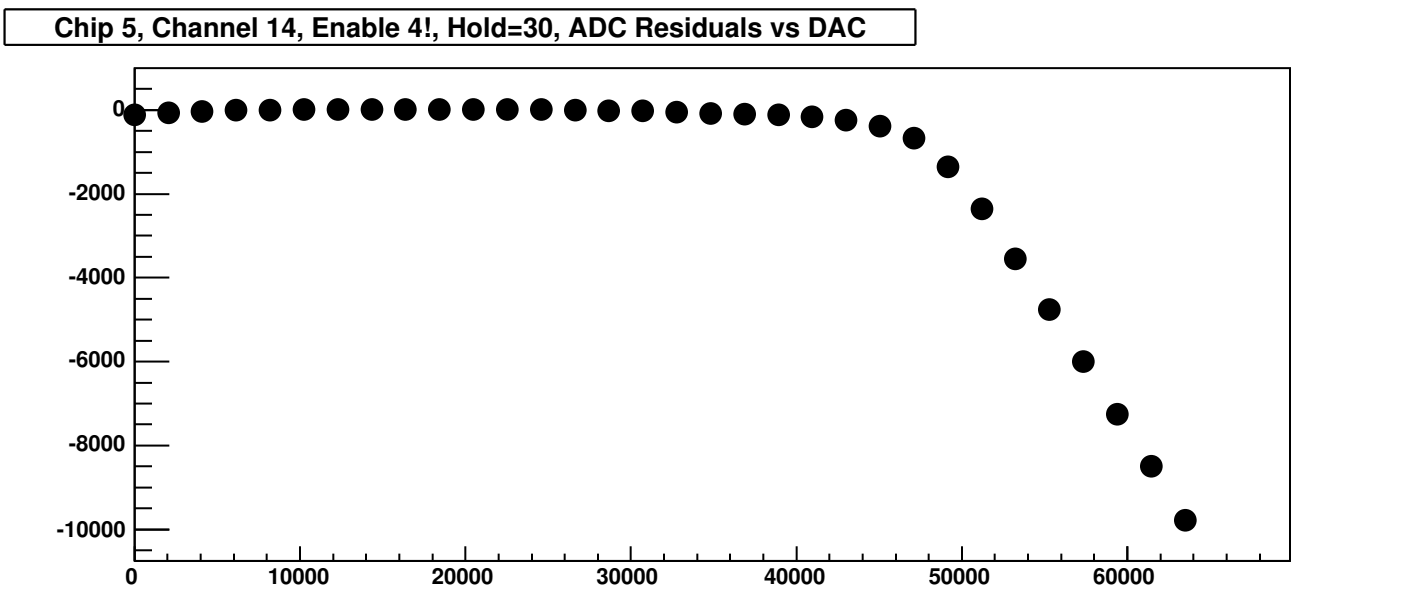

Chip 5, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5144 / 15
p0 1103 ± 0.1092
p1 0.000571 ± 4.469e-06
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

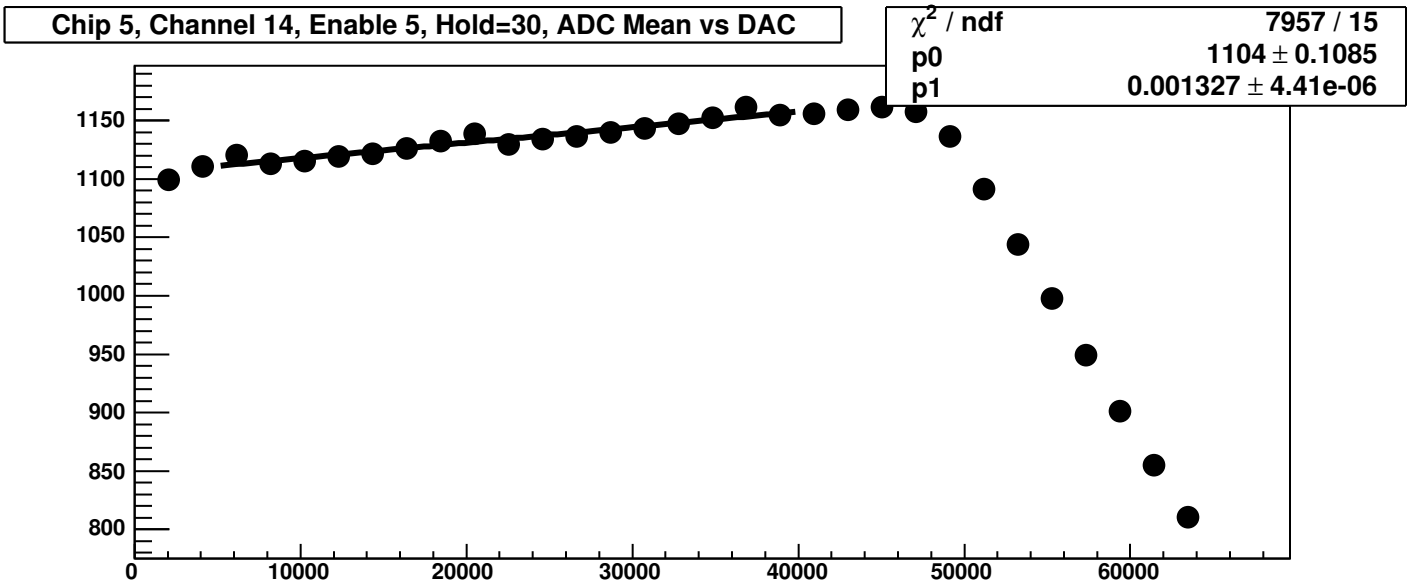

Chip 5, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

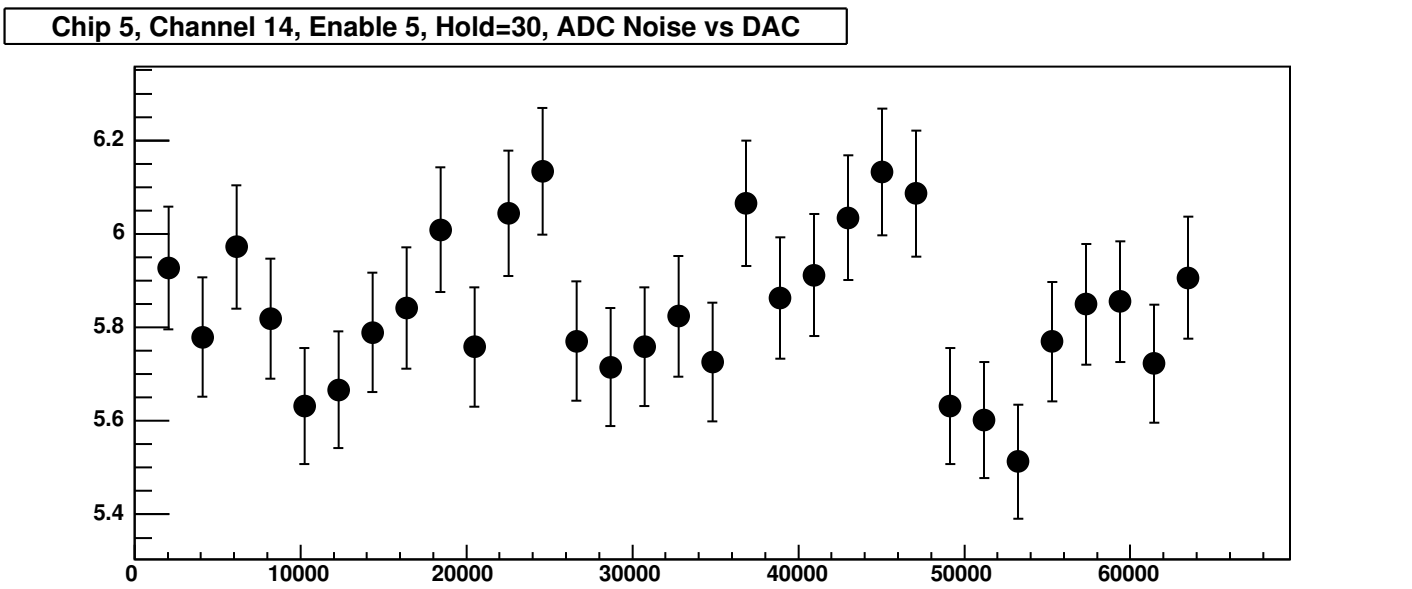

Chip 5, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

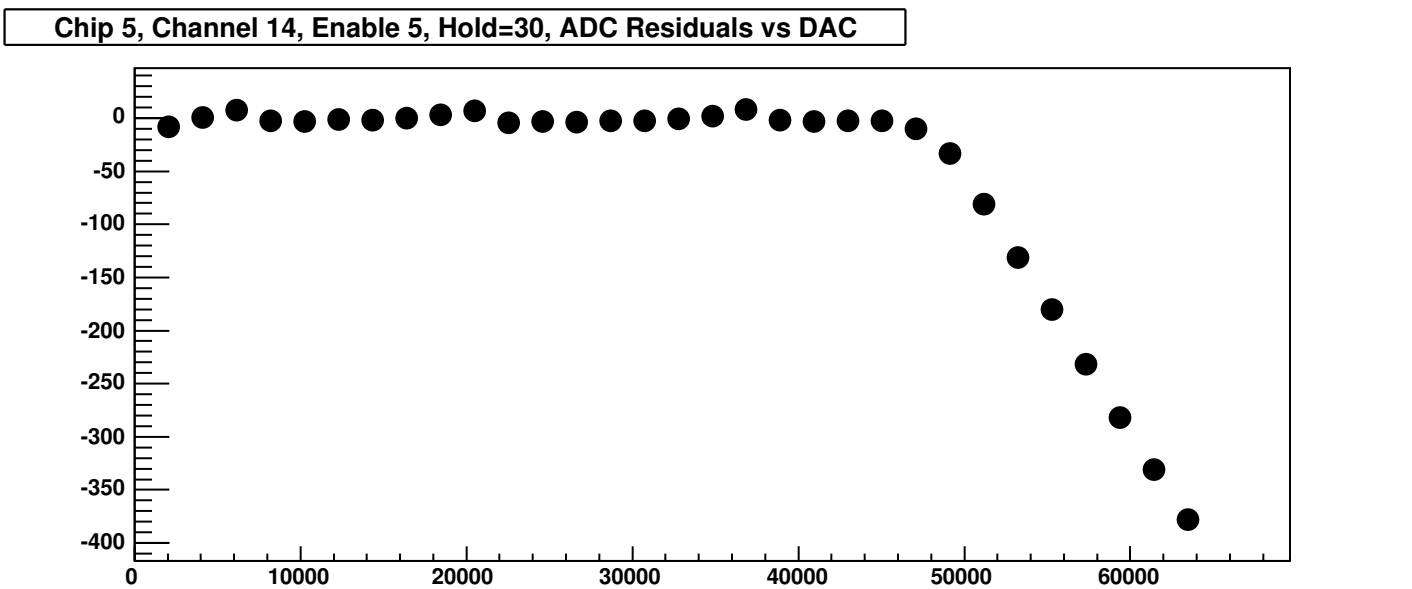

Chip 5, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 9621 / 15
p0 1211 ± 0.2576
p1 0.609 ± 2.526e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

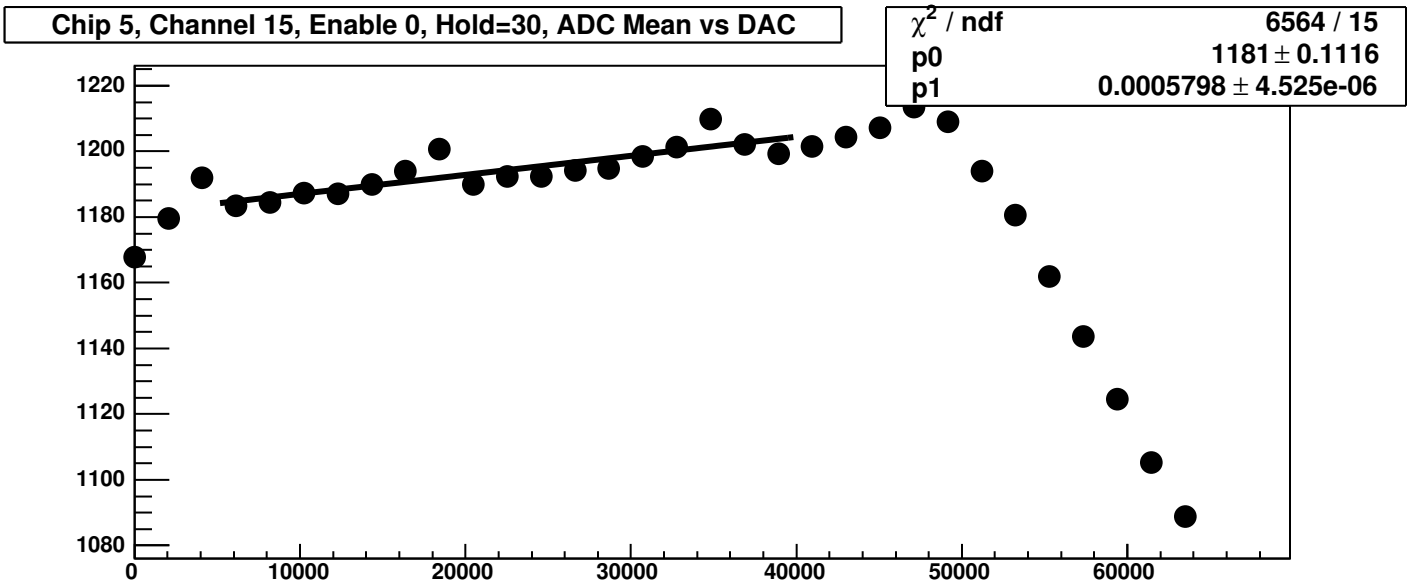

Chip 5, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

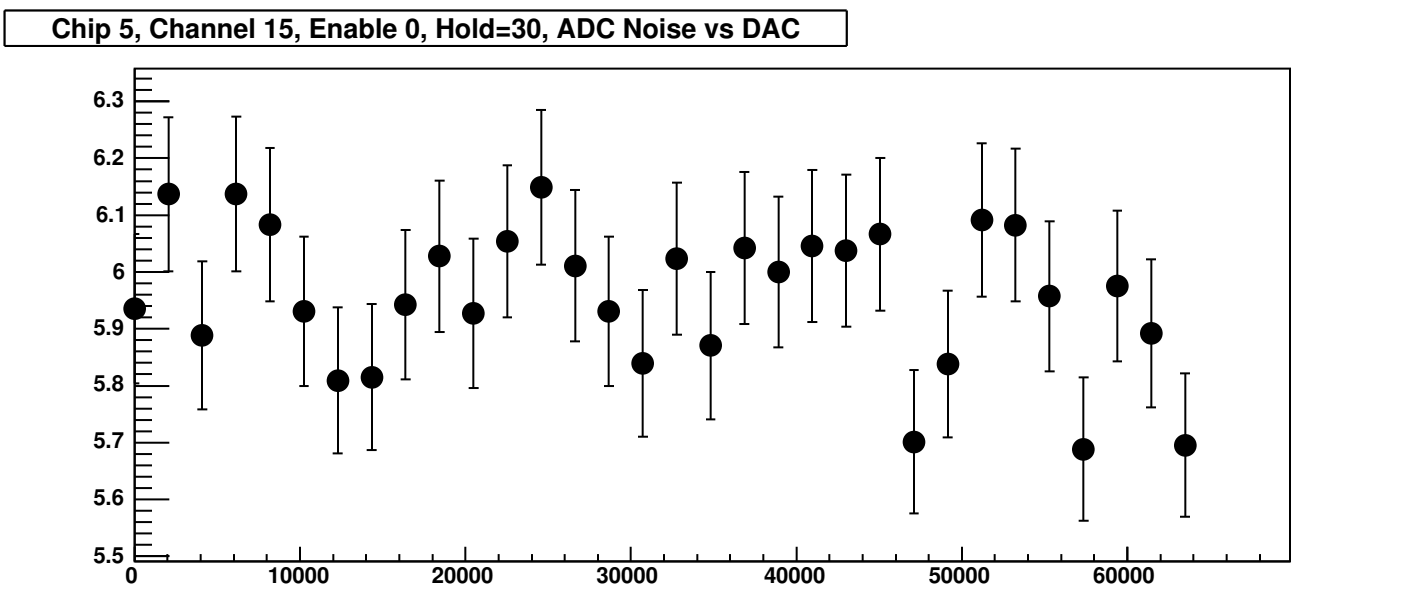

Chip 5, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

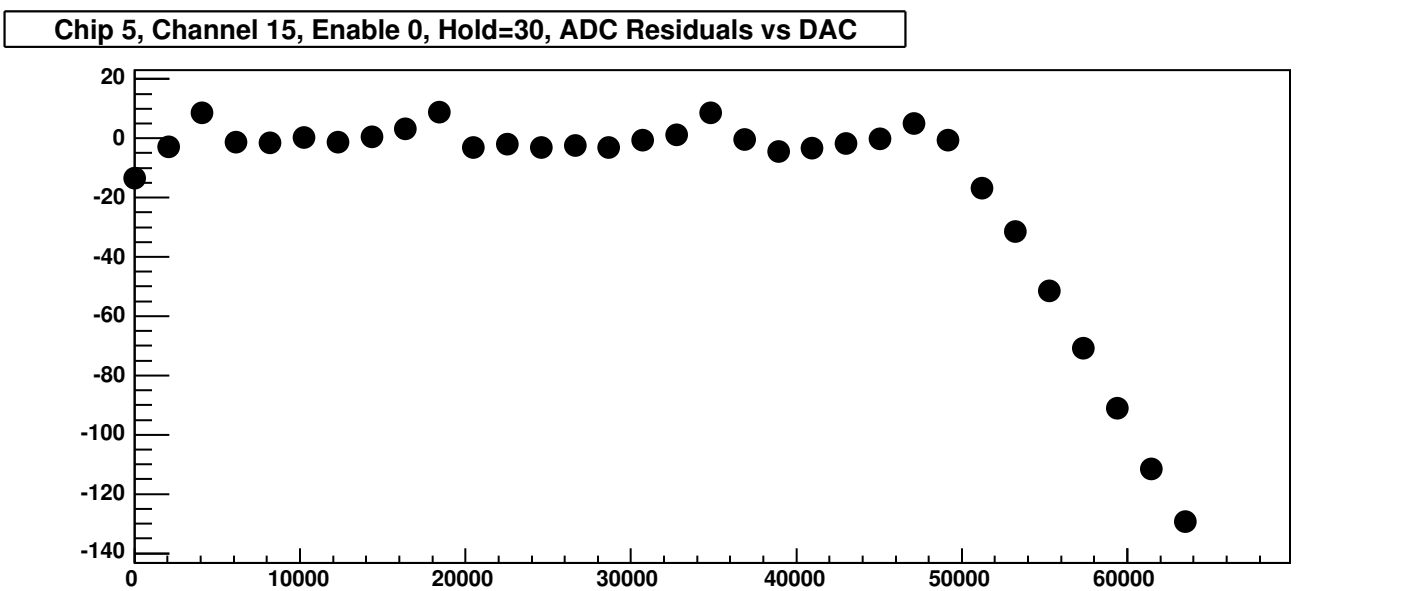

Chip 5, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7957 / 15
p0 1104 ± 0.1085
p1 0.001327 ± 4.41e-06
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

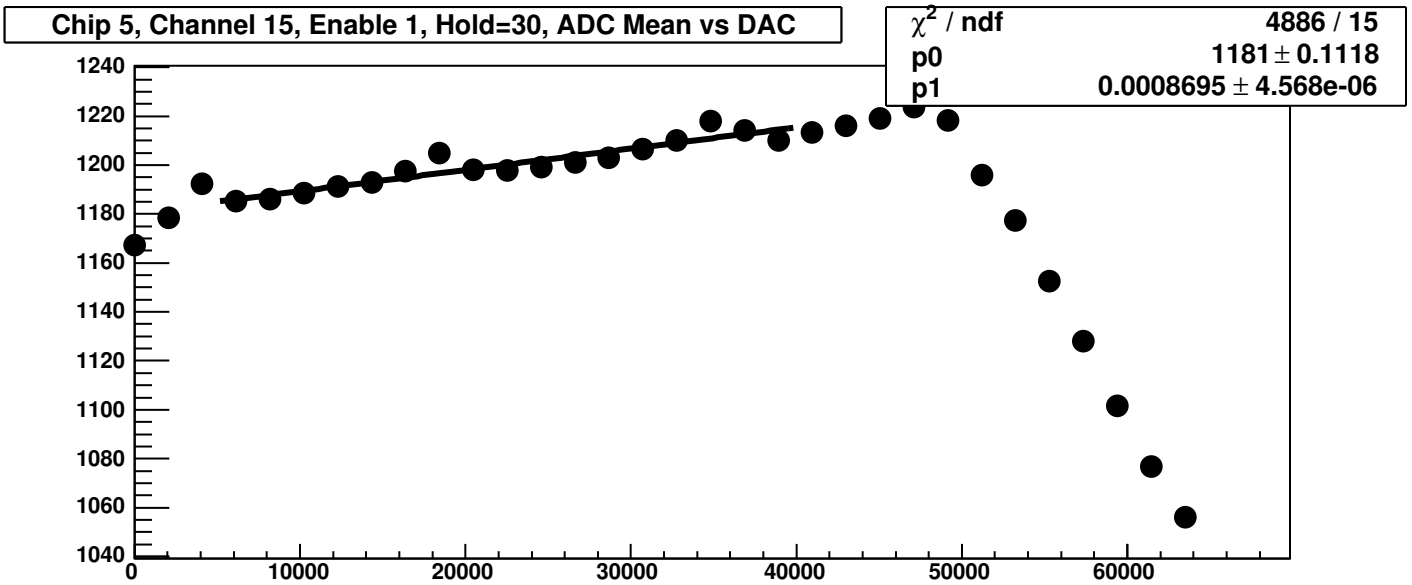

Chip 5, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

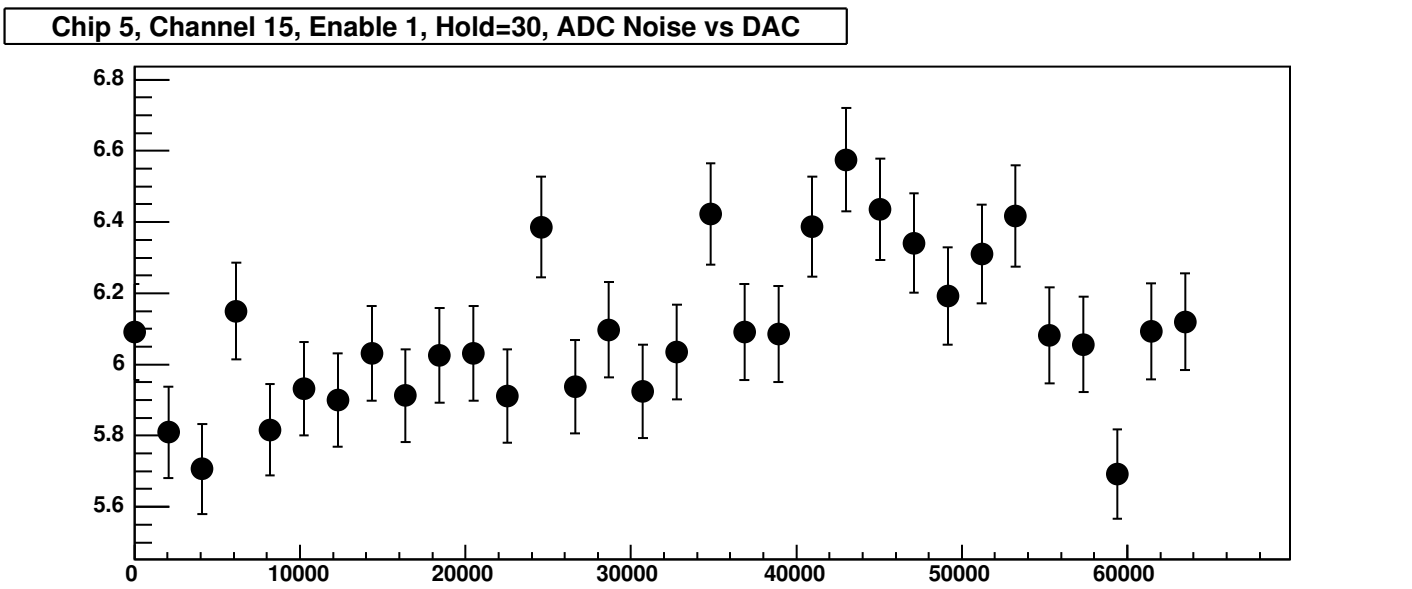

Chip 5, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

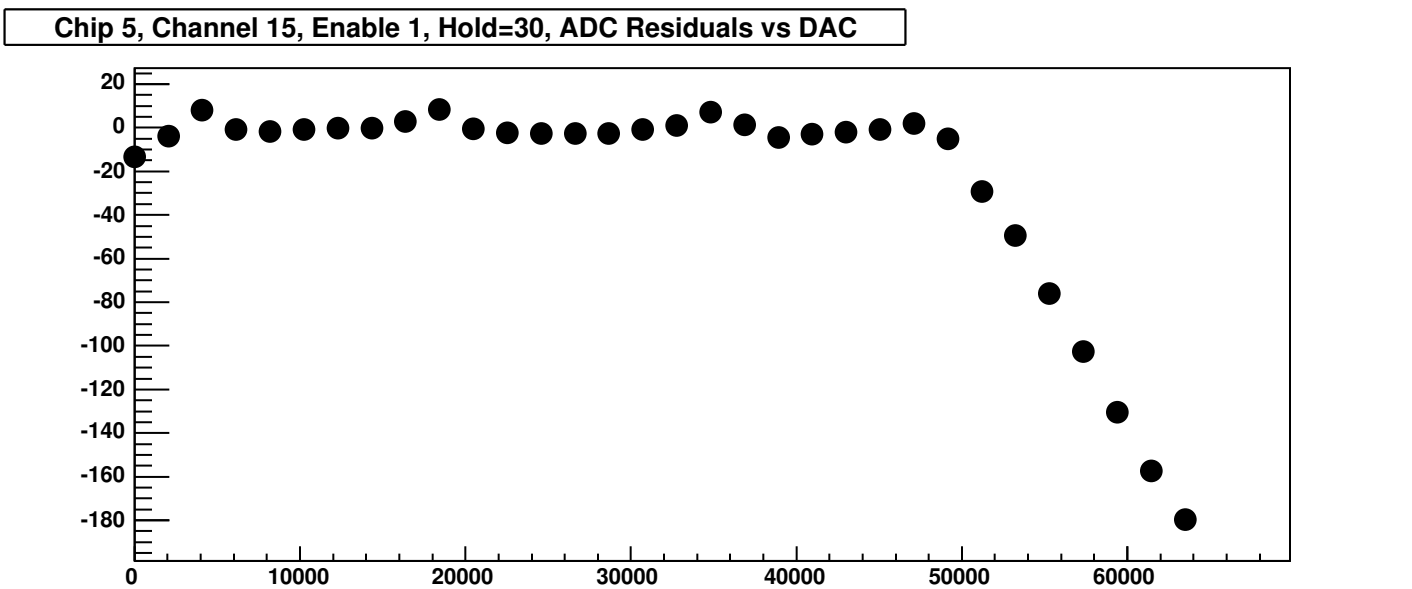

Chip 5, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6564 / 15
p0 1181 ± 0.1116
p1 0.0005798 ± 4.525e-06
1220
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

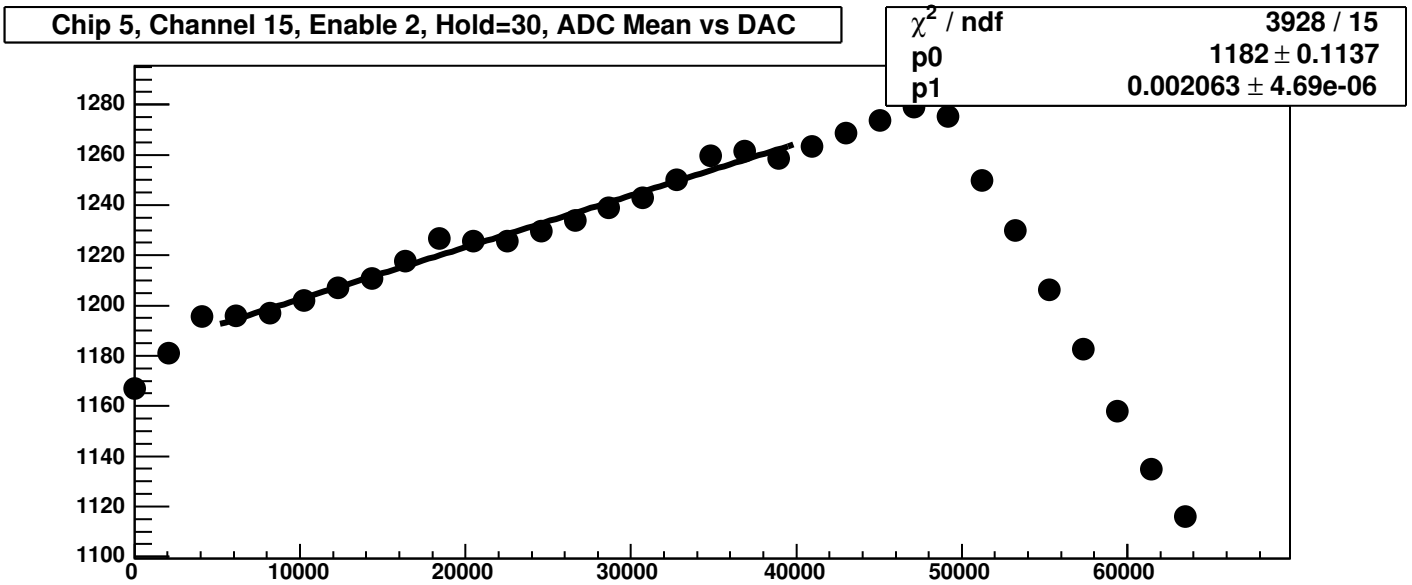

Chip 5, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

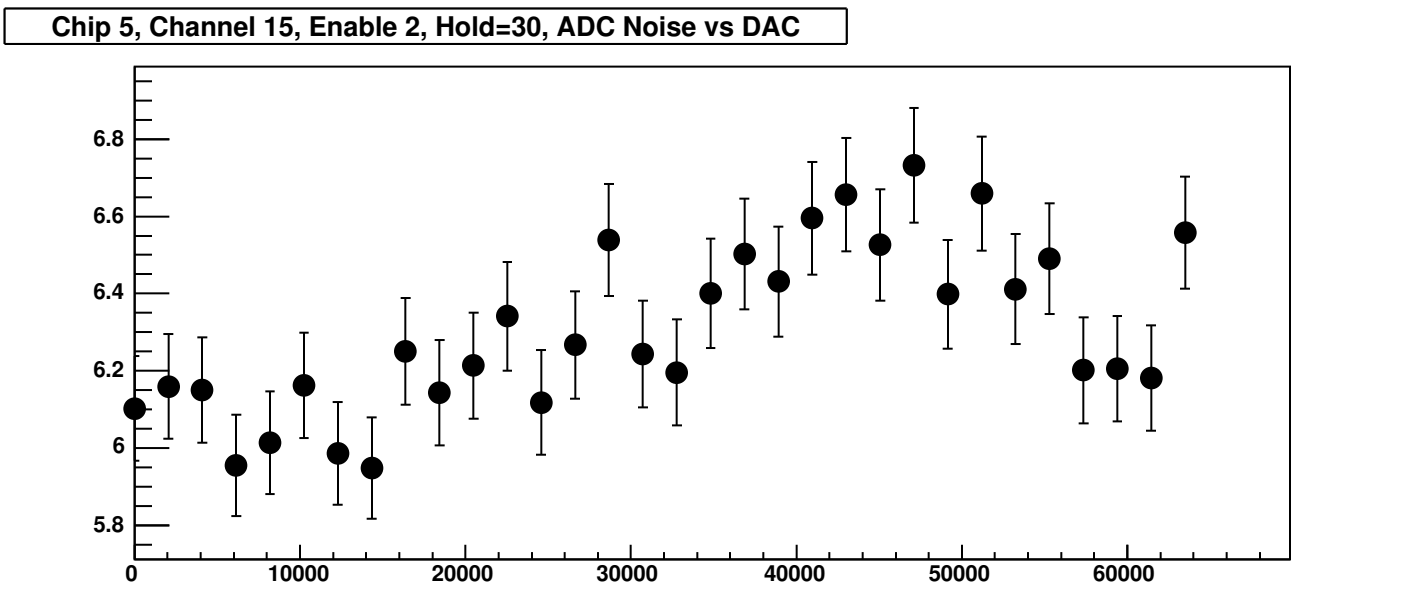

Chip 5, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

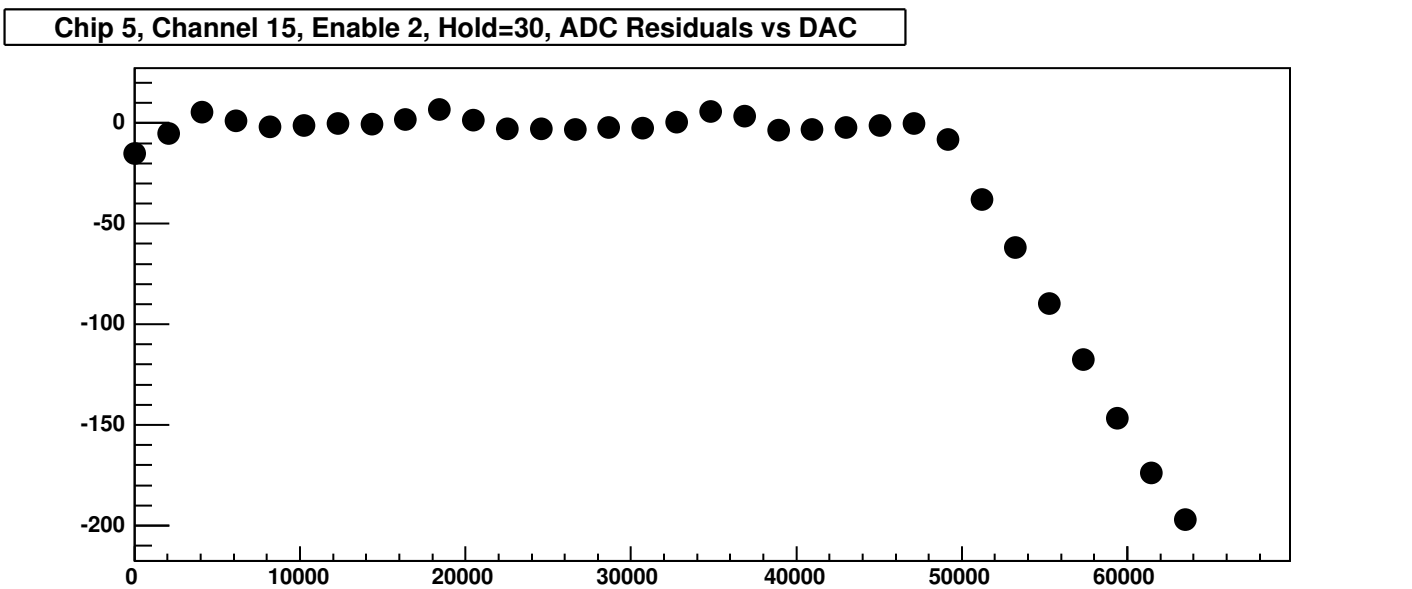

Chip 5, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4886 / 15
p0 1181 ± 0.1118
p1 0.0008695 ± 4.568e-06
1240
1220
1200
1180
1160
1140
1120
1100
1080
1060
1040
0
10000
20000
30000
40000
50000
60000

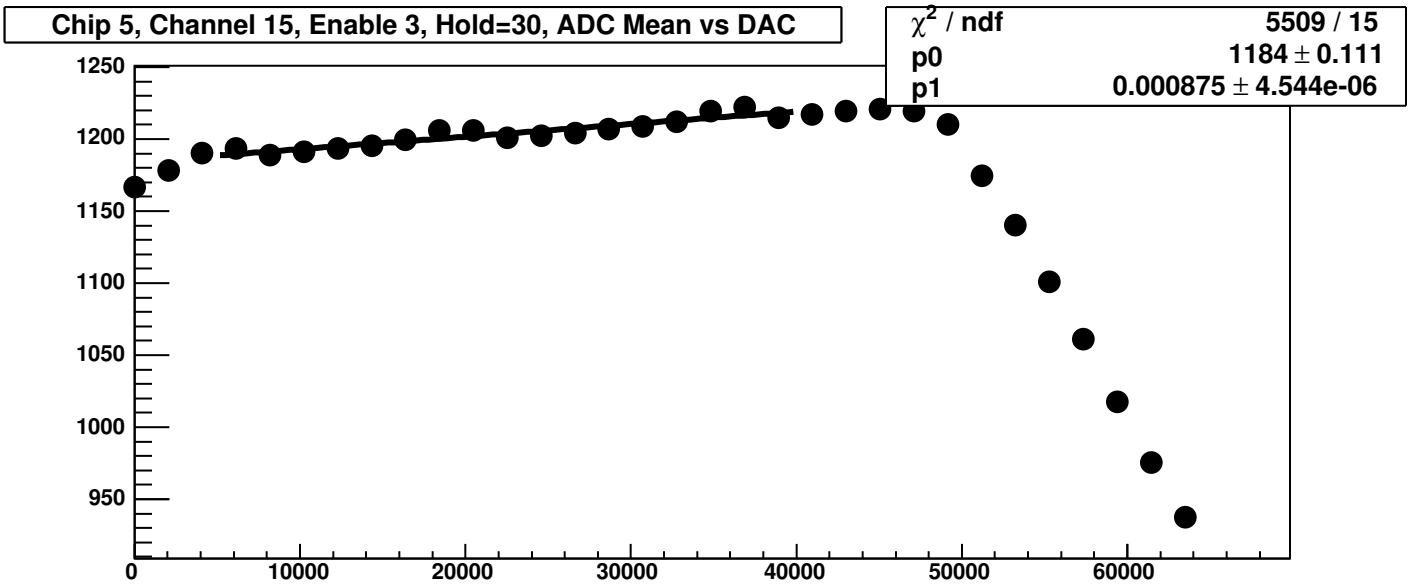

Chip 5, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

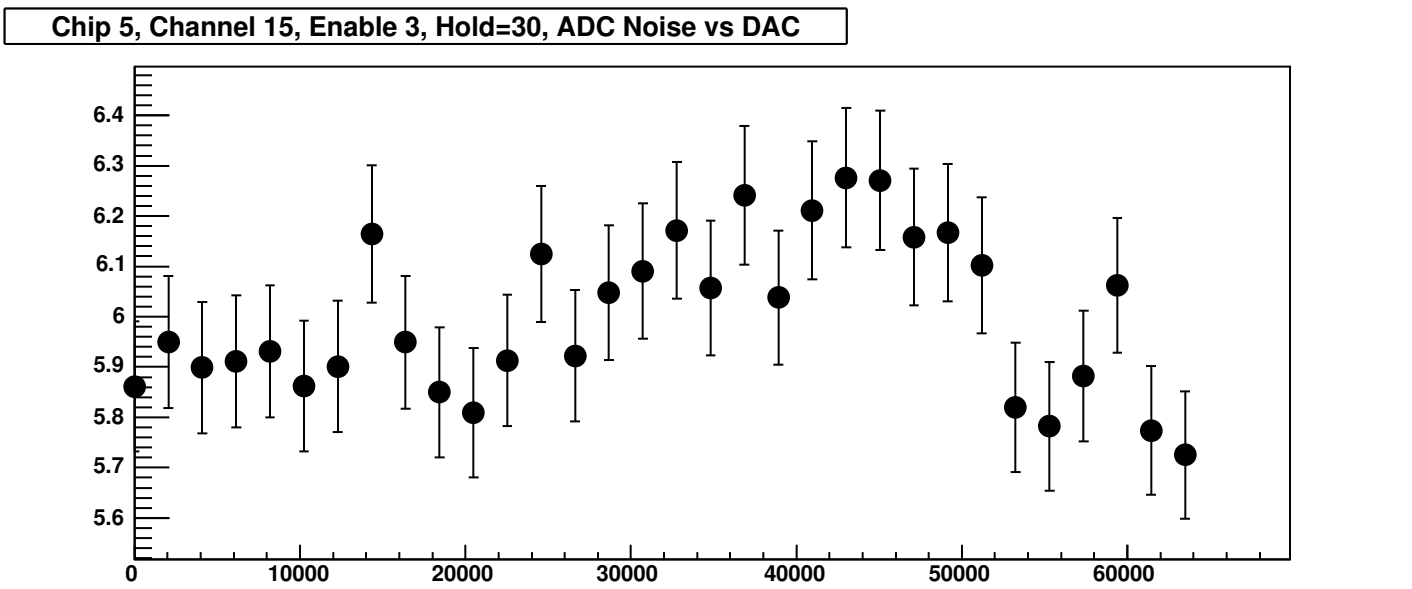

Chip 5, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

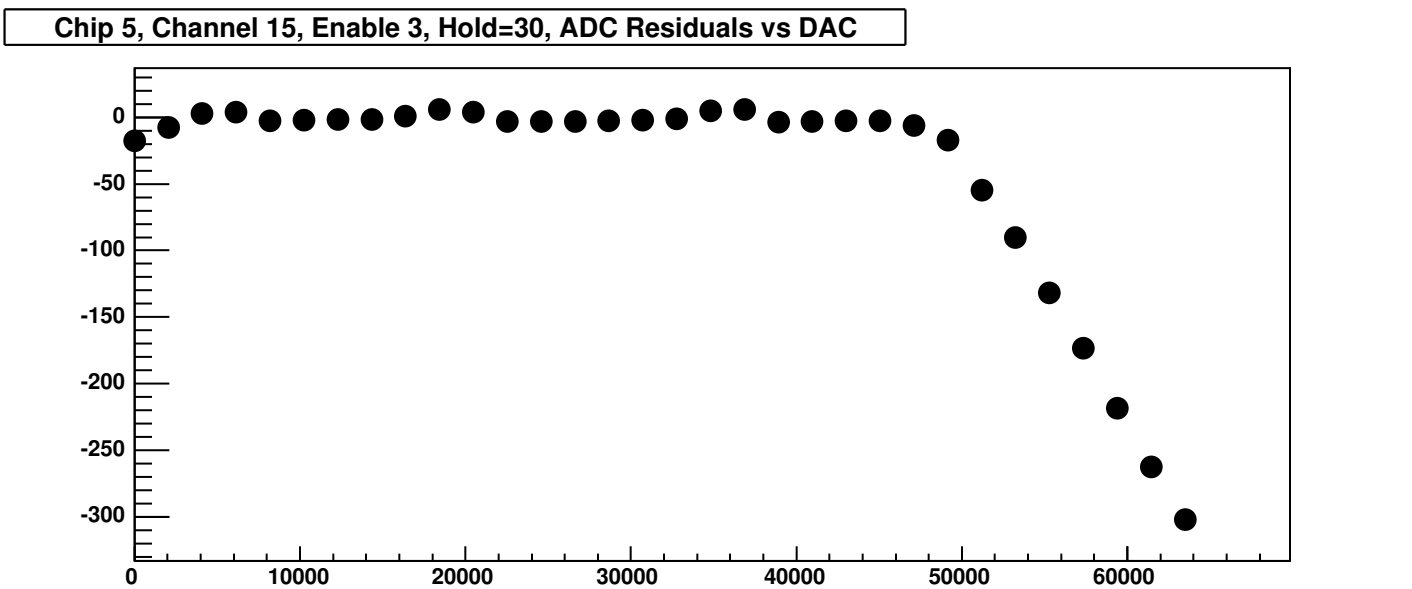

Chip 5, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3928 / 15
p0 1182 ± 0.1137
p1 0.002063 ± 4.69e-06
1280
1260
1240
1220
1200
1180
1160
1140
1120
1100
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

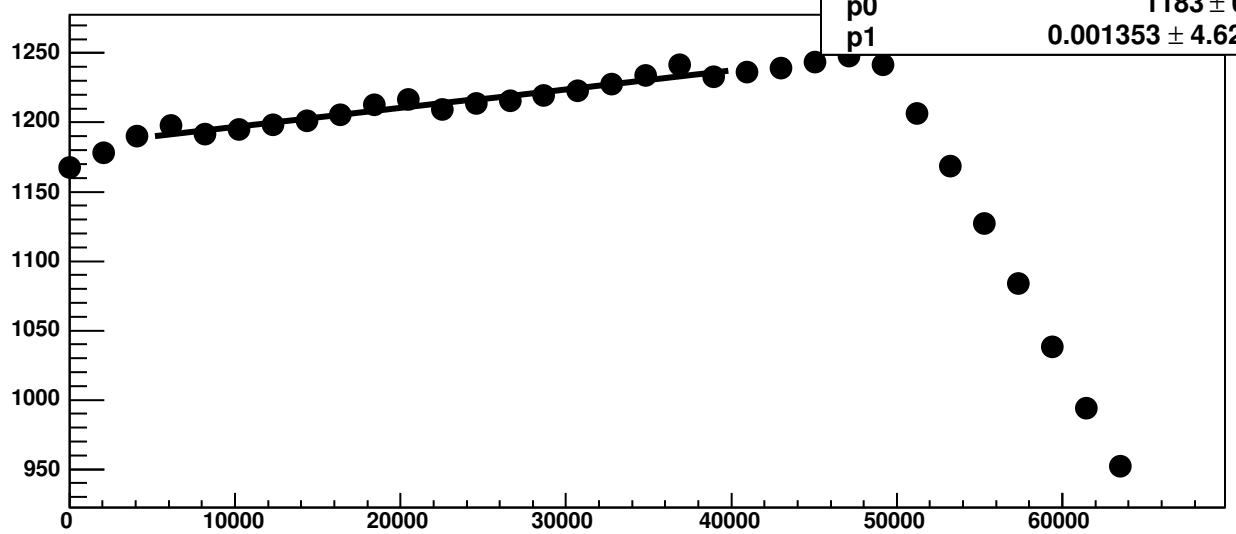

Chip 5, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5509 / 15
p0 1184 ± 0.111
p1 0.000875 ± 4.544e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

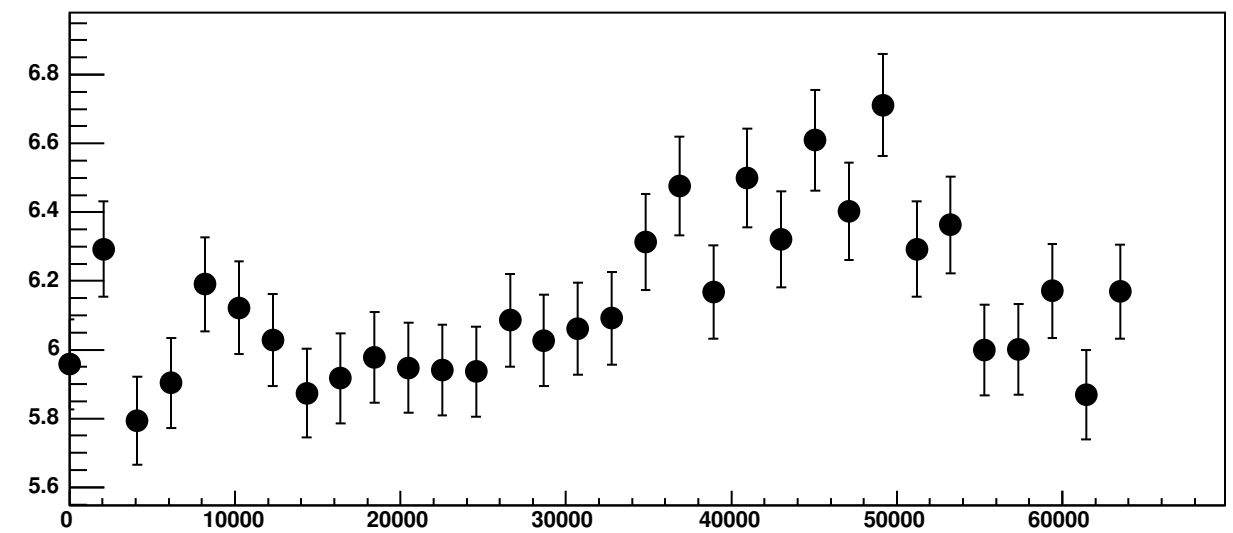

Chip 5, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

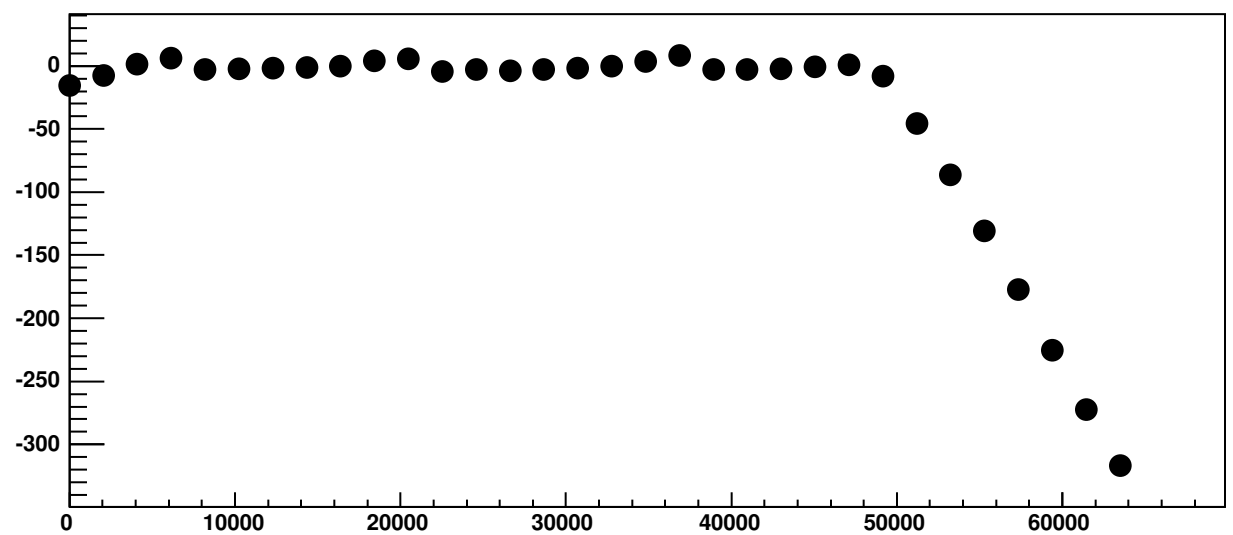
Chip 5, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

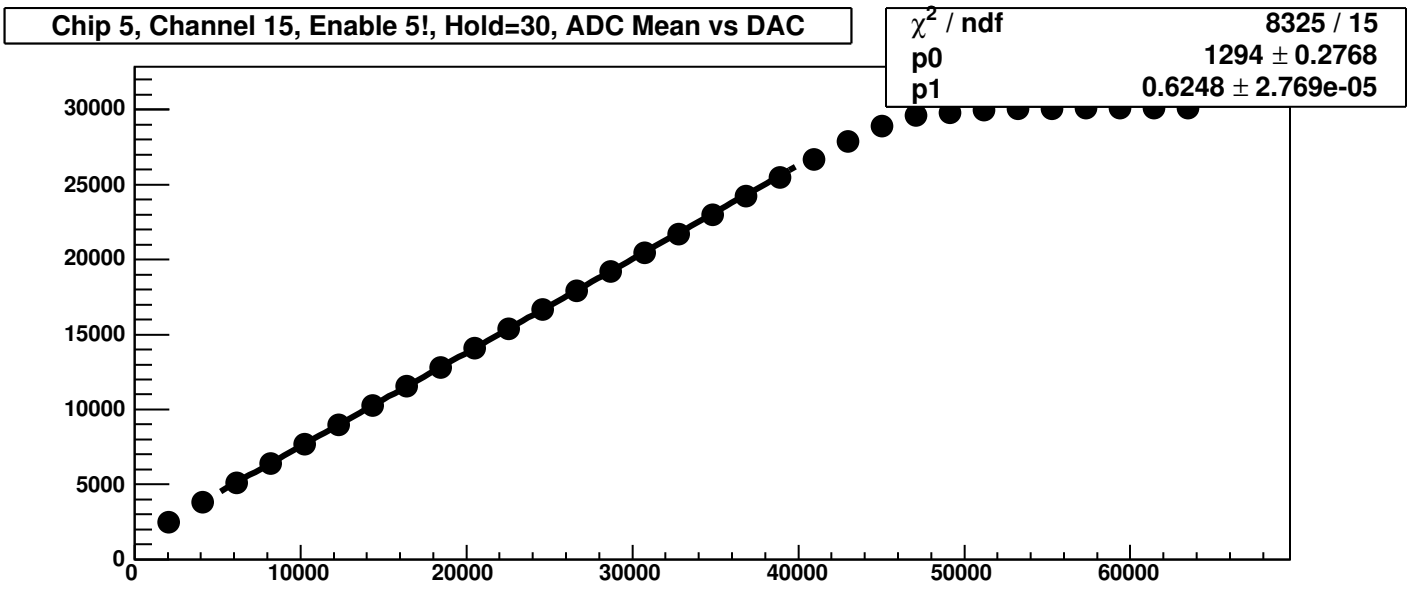


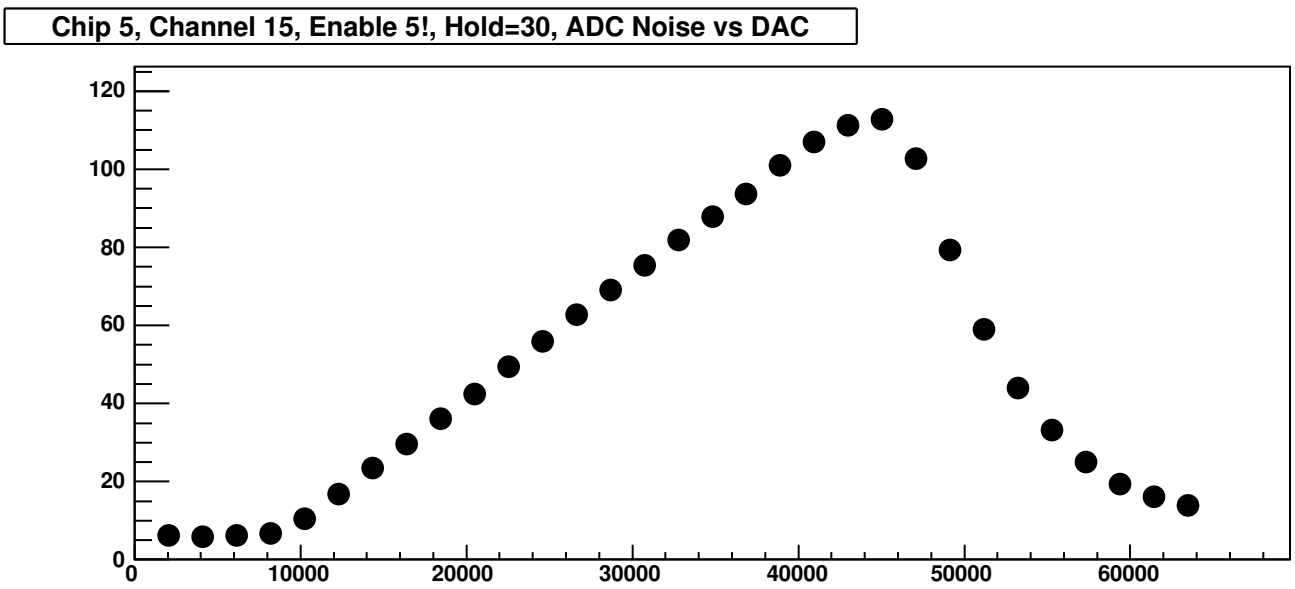
Chip 5, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC

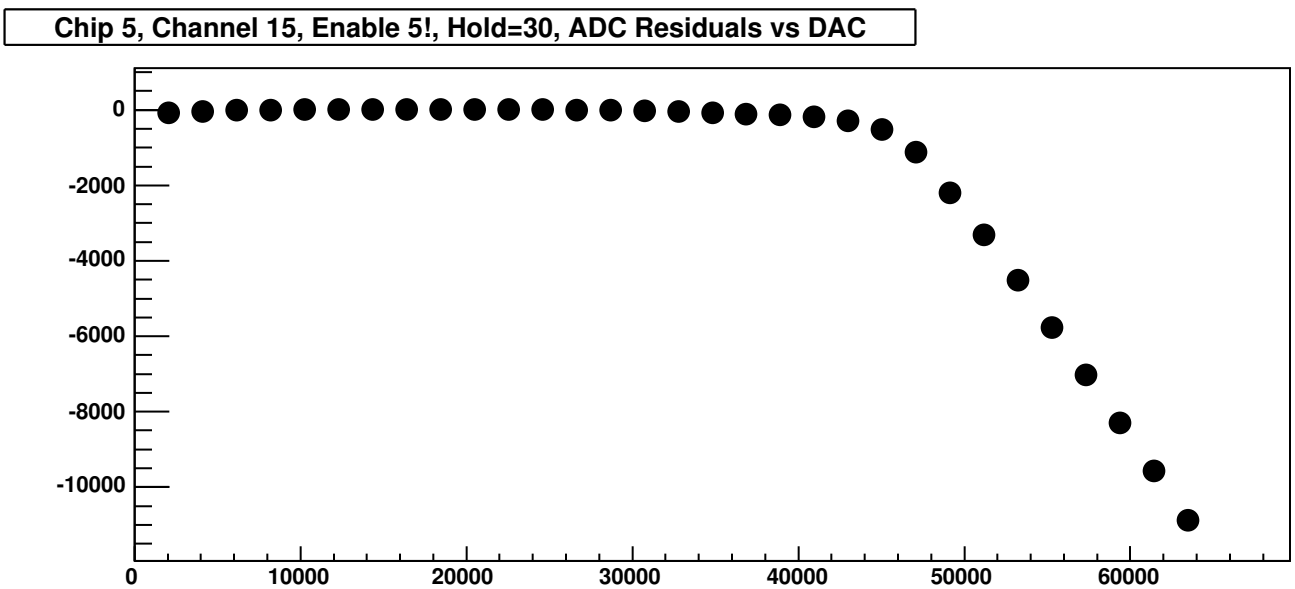


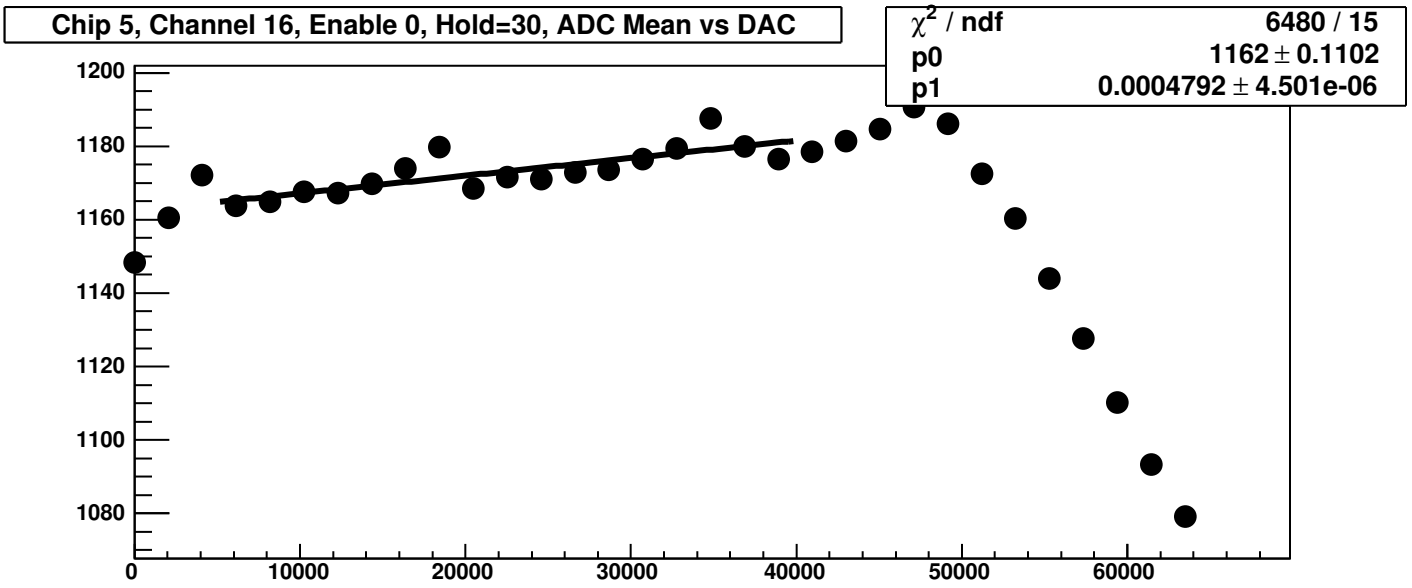
Chip 5, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

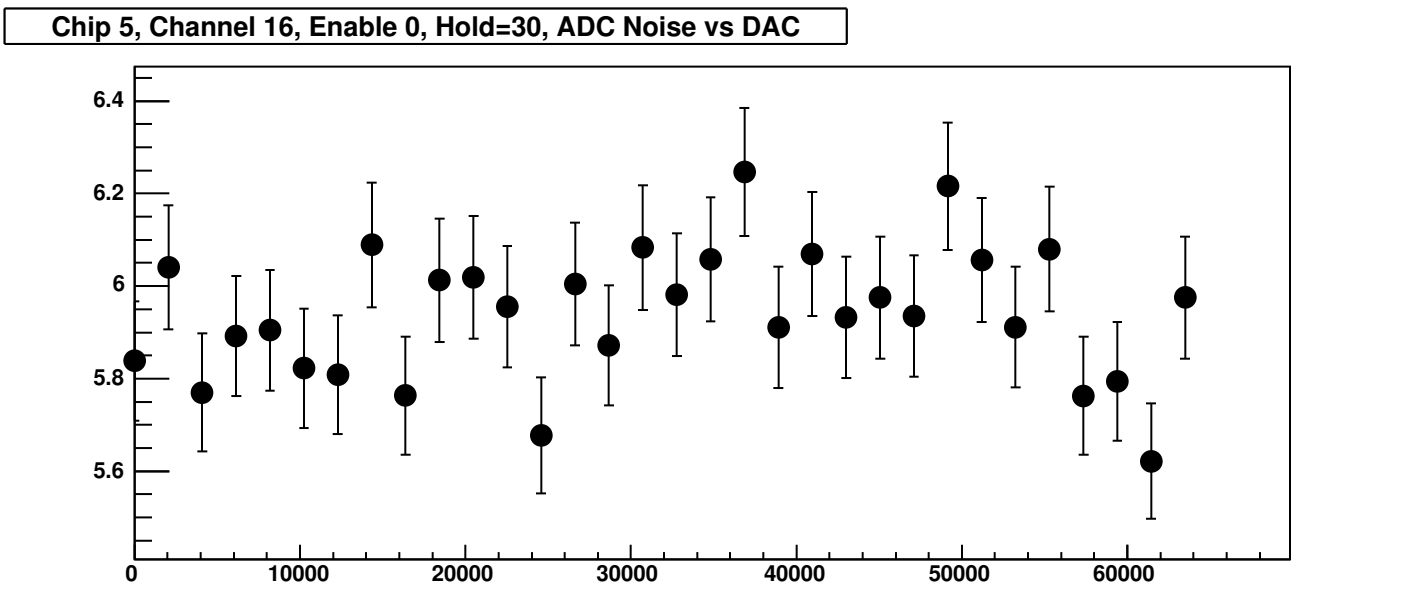


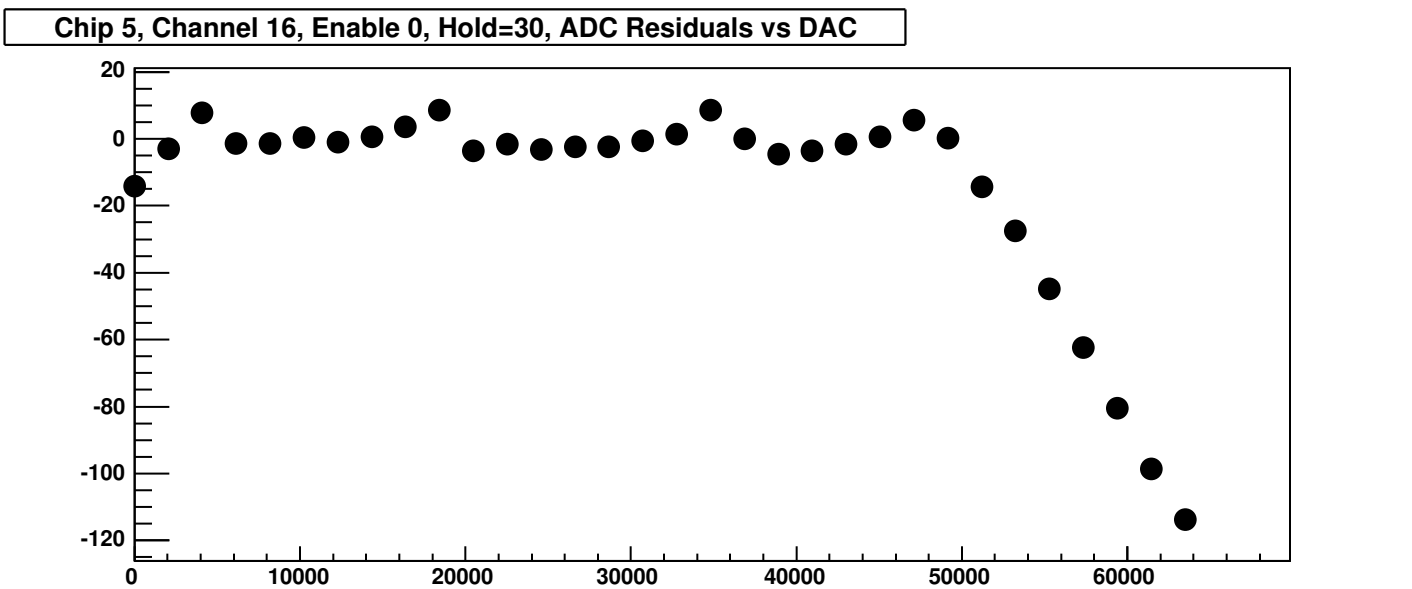

Chip 5, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 8325 / 15
p0 1294 ± 0.2768
p1 0.6248 ± 2.769e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

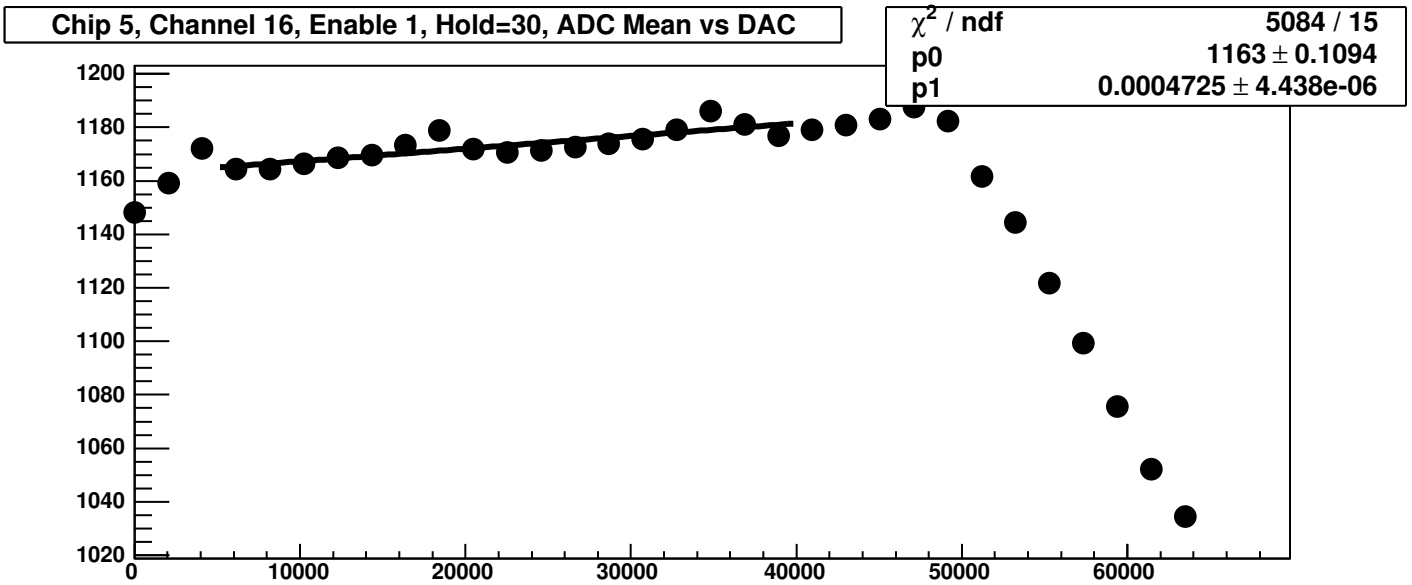

Chip 5, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

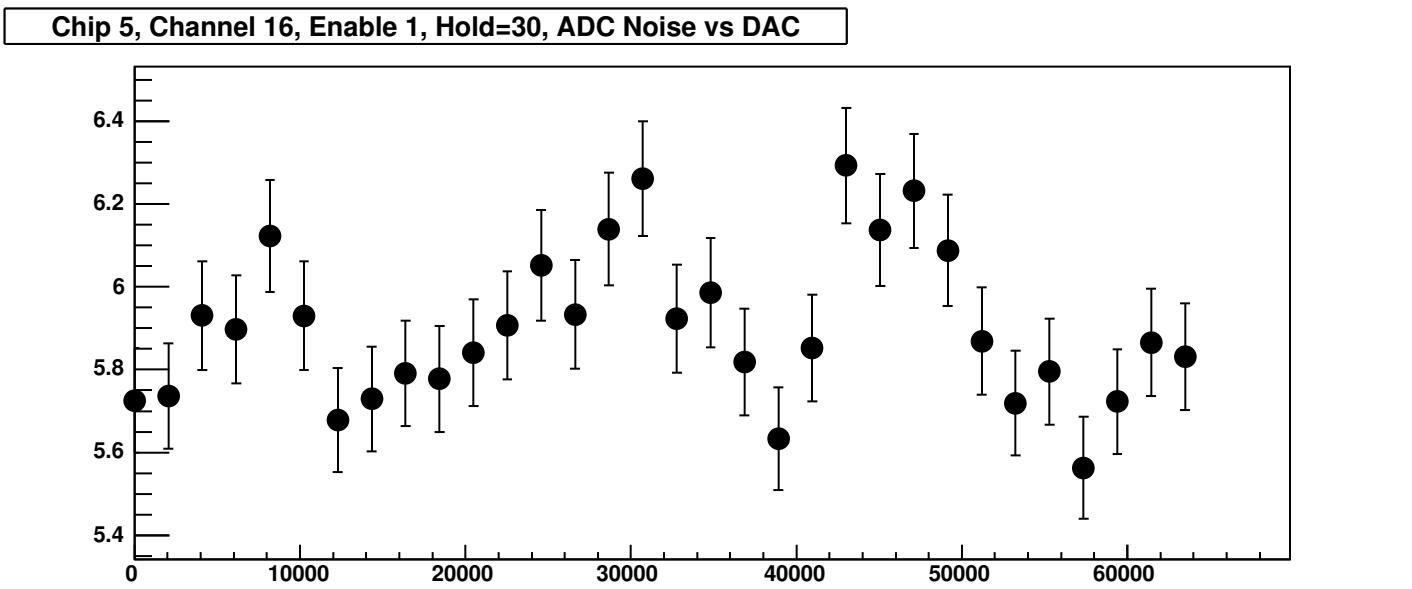

Chip 5, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

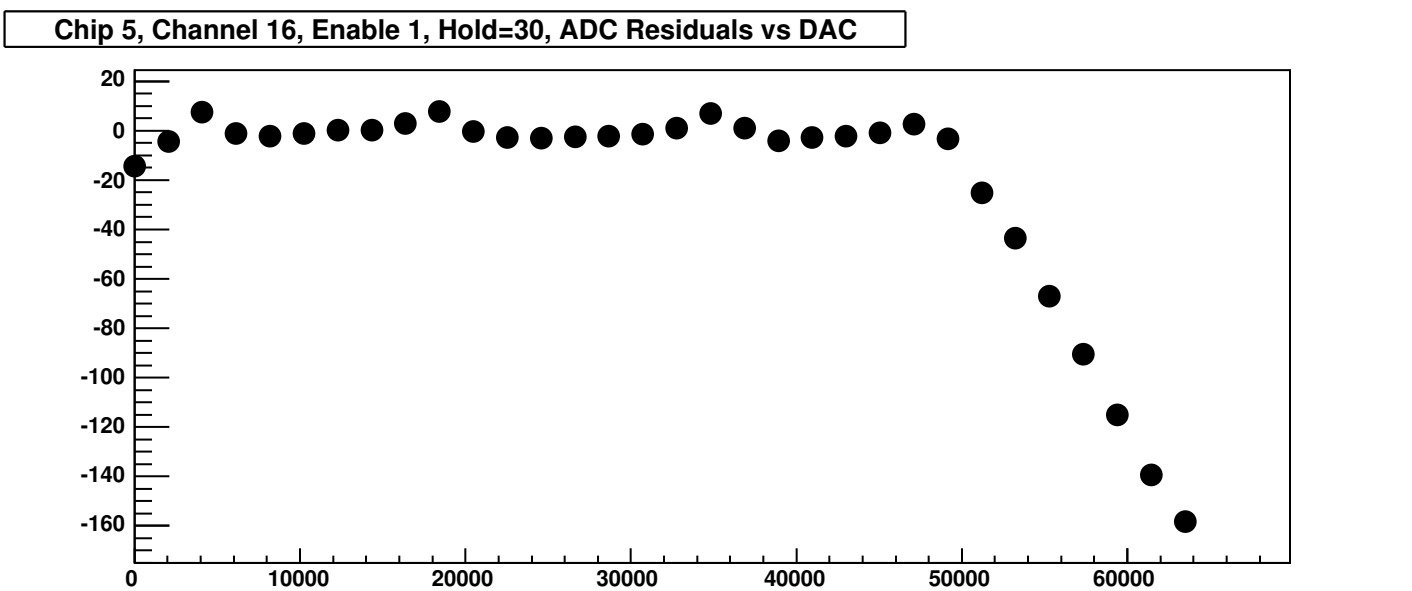

Chip 5, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6480 / 15
p0 1162 ± 0.1102
p1 0.0004792 ± 4.501e-06
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

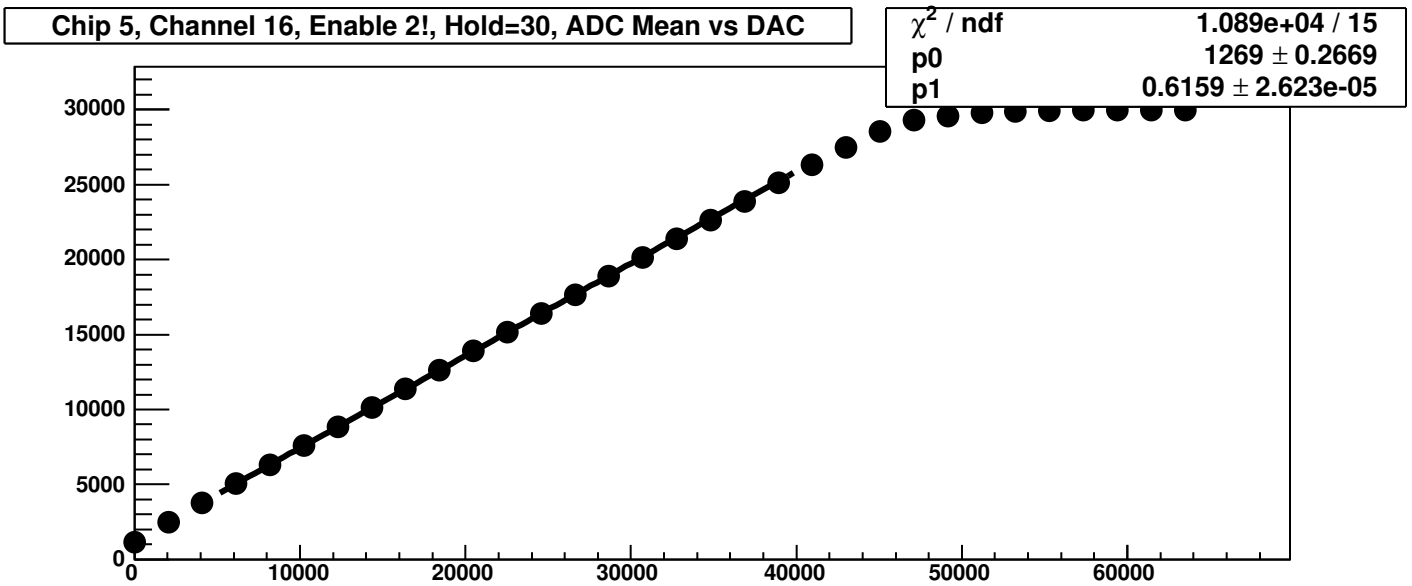

Chip 5, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

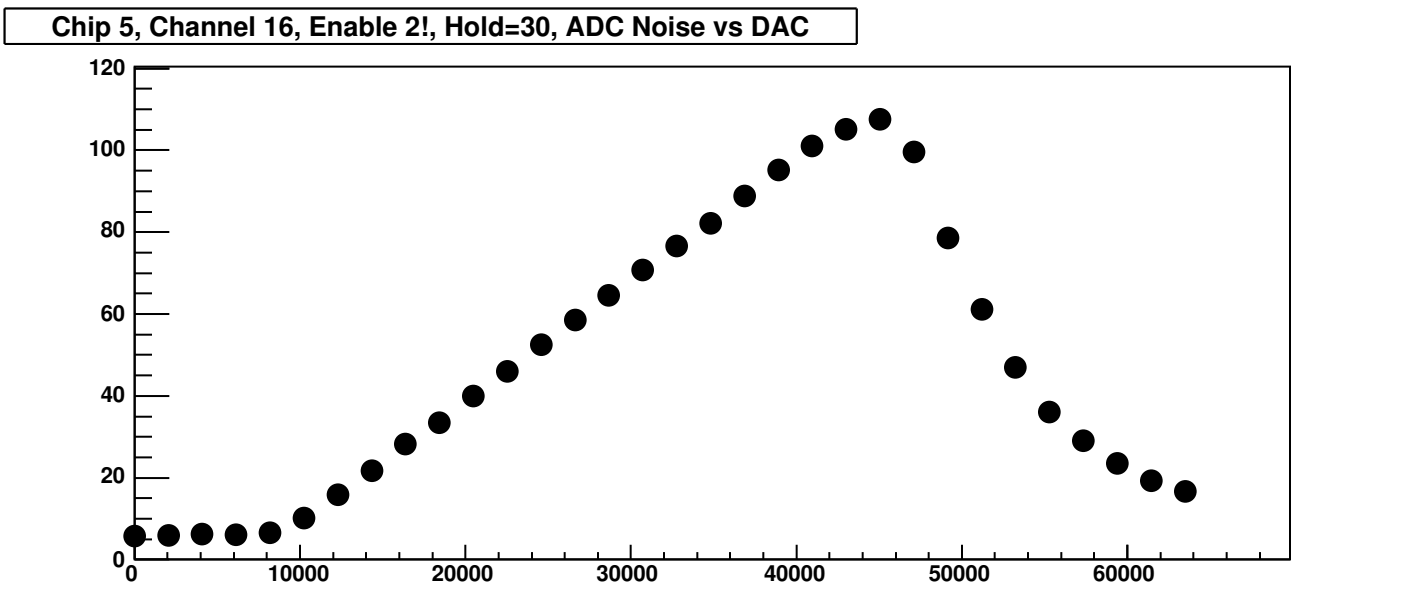

Chip 5, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

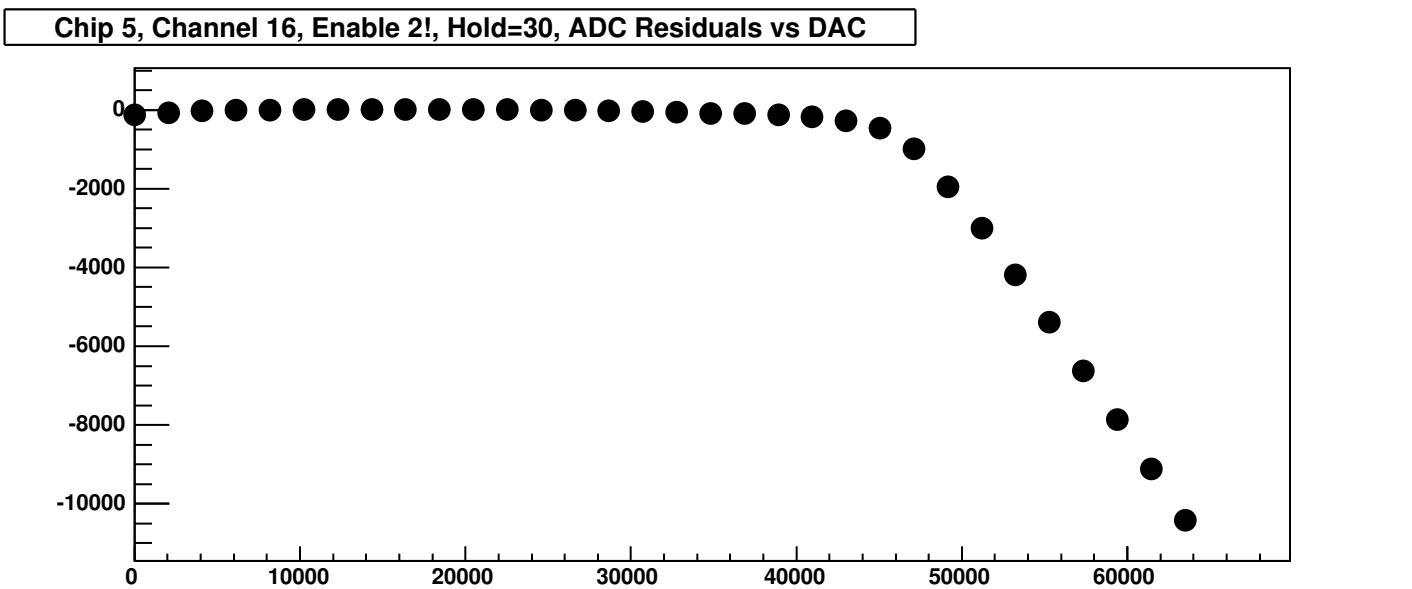

Chip 5, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5084 / 15
p0 1163 ± 0.1094
p1 0.0004725 ± 4.438e-06
1200
1180
1160
1140
1120
1100
1080
1060
1040
1020
0
10000
20000
30000
40000
50000
60000

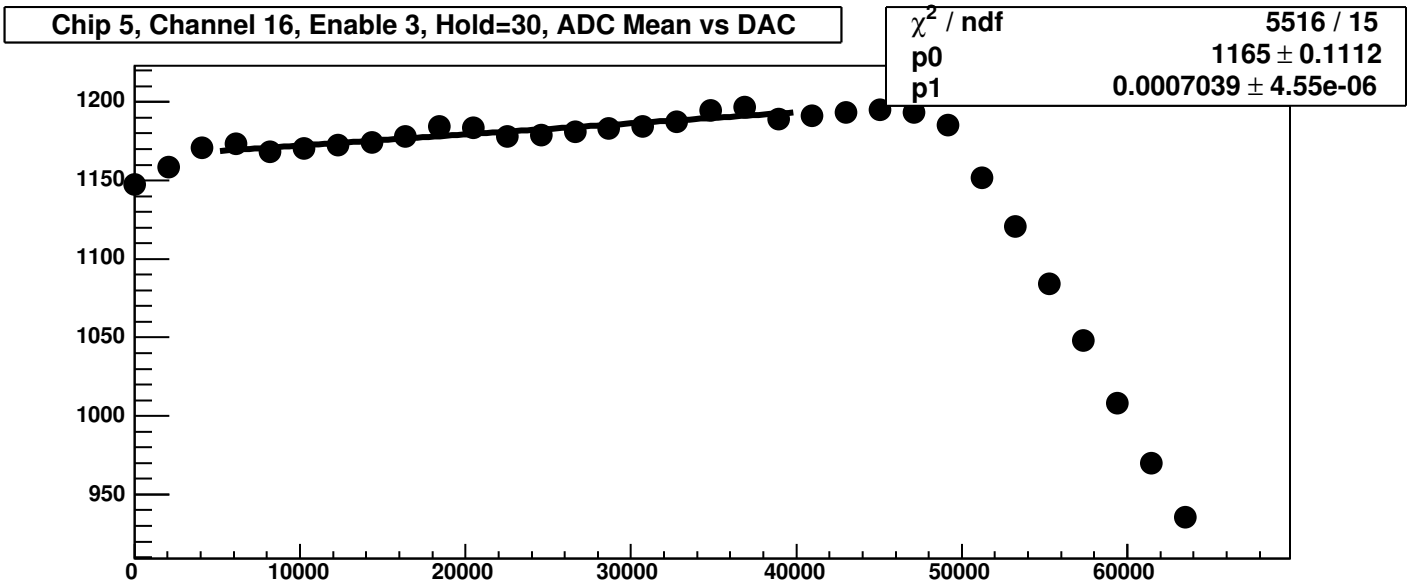

Chip 5, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

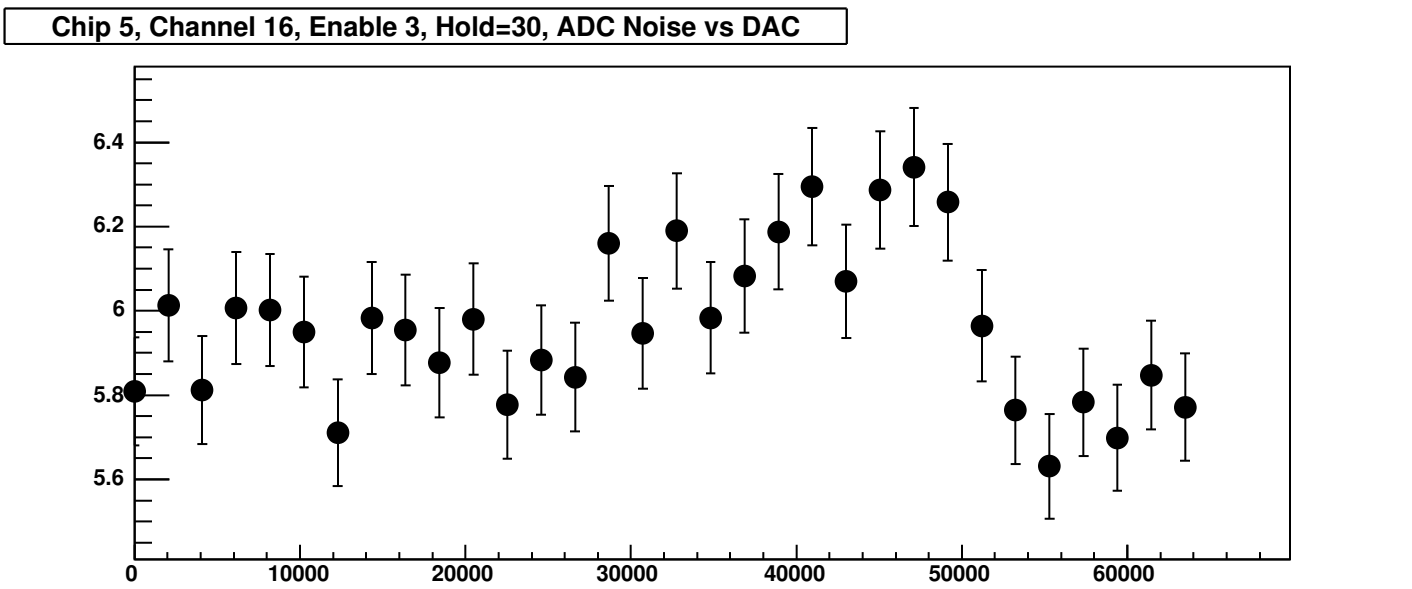

Chip 5, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

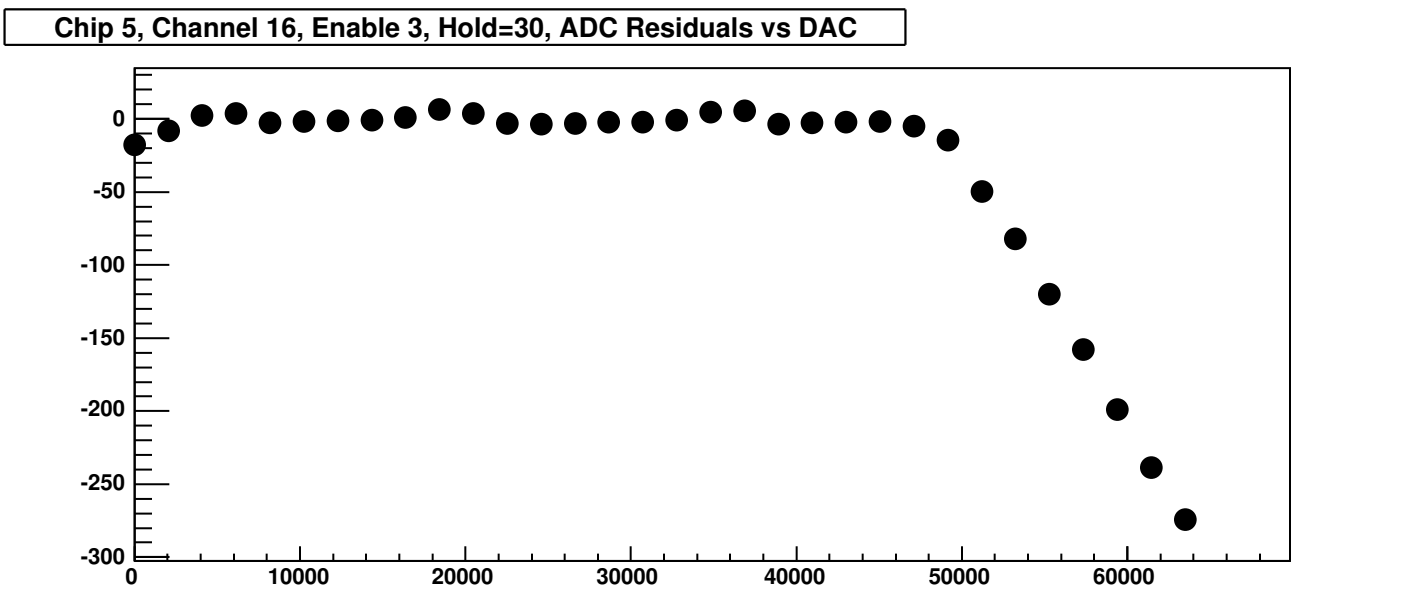

Chip 5, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.089e+04 / 15
p0 1269 ± 0.2669
p1 0.6159 ± 2.623e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

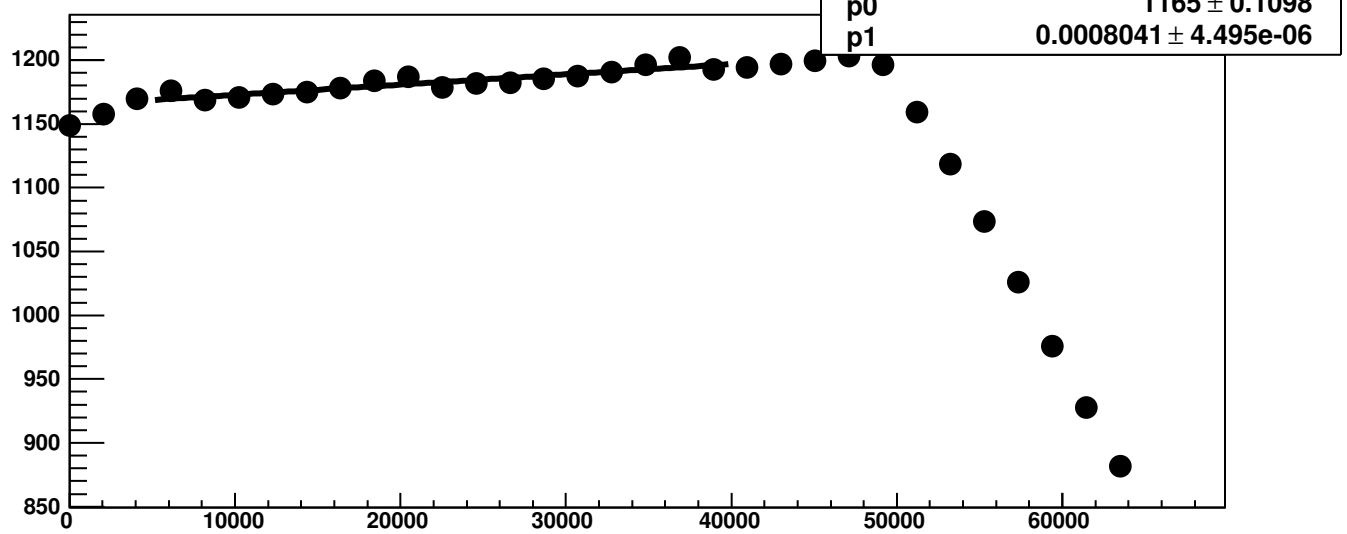

Chip 5, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5516 / 15
p0 1165 ± 0.1112
p1 0.0007039 ± 4.55e-06
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

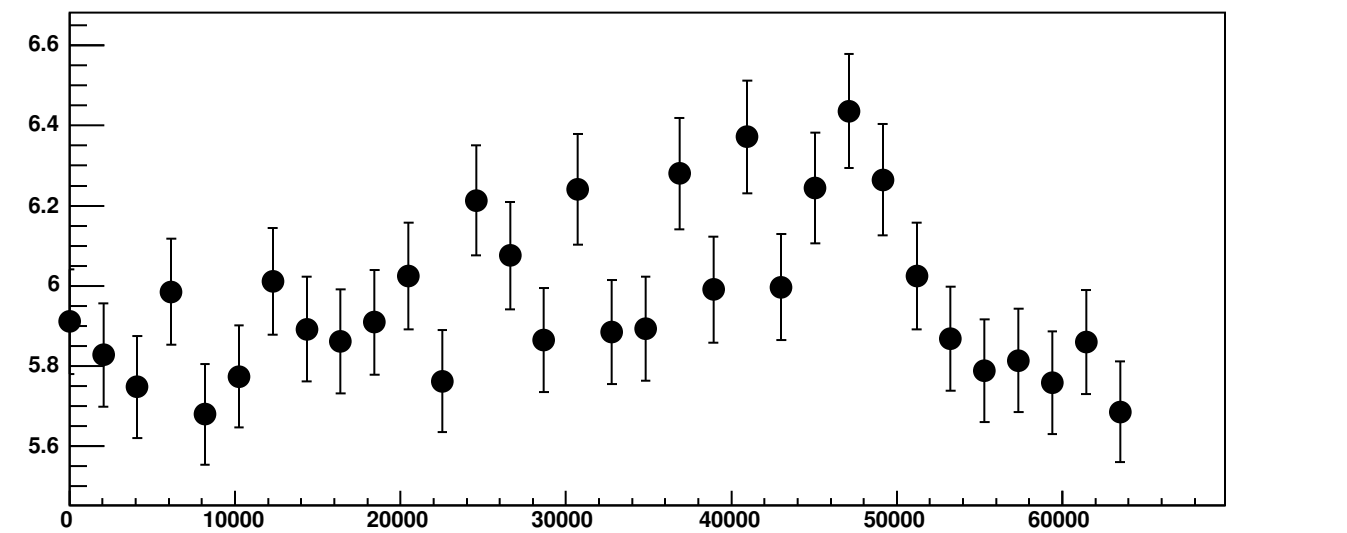

Chip 5, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

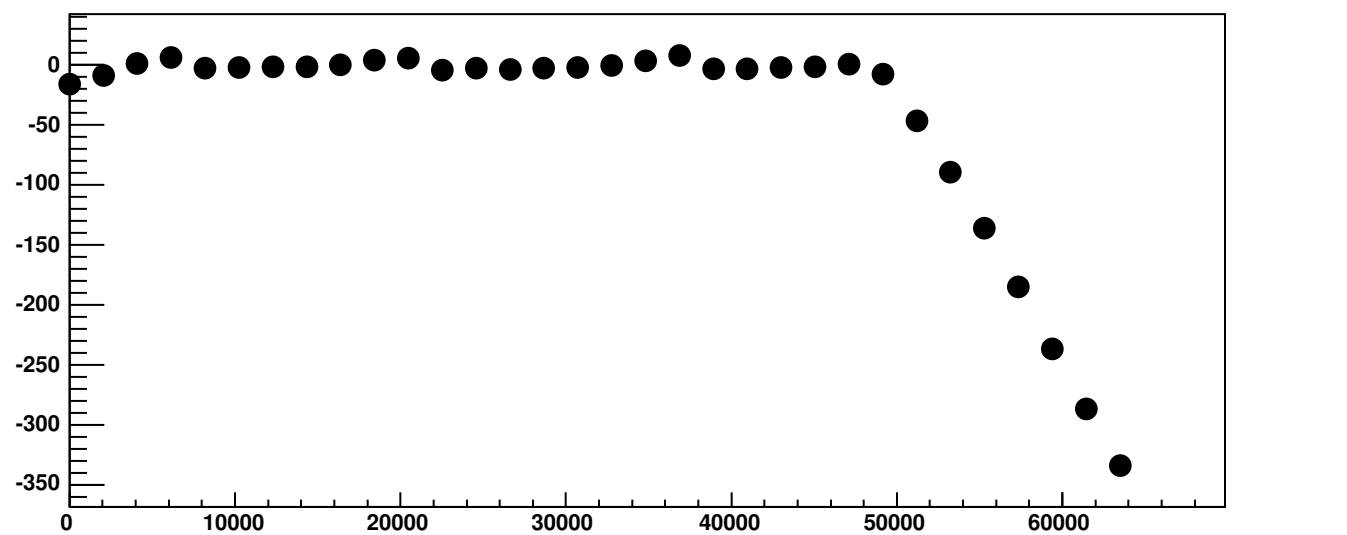
Chip 5, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC



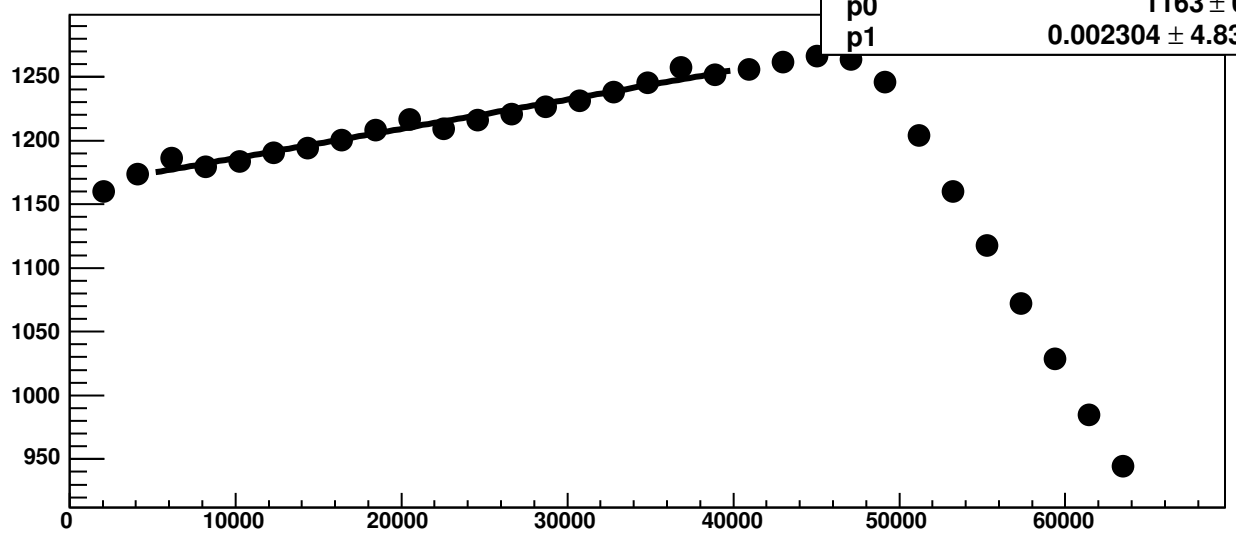
Chip 5, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC



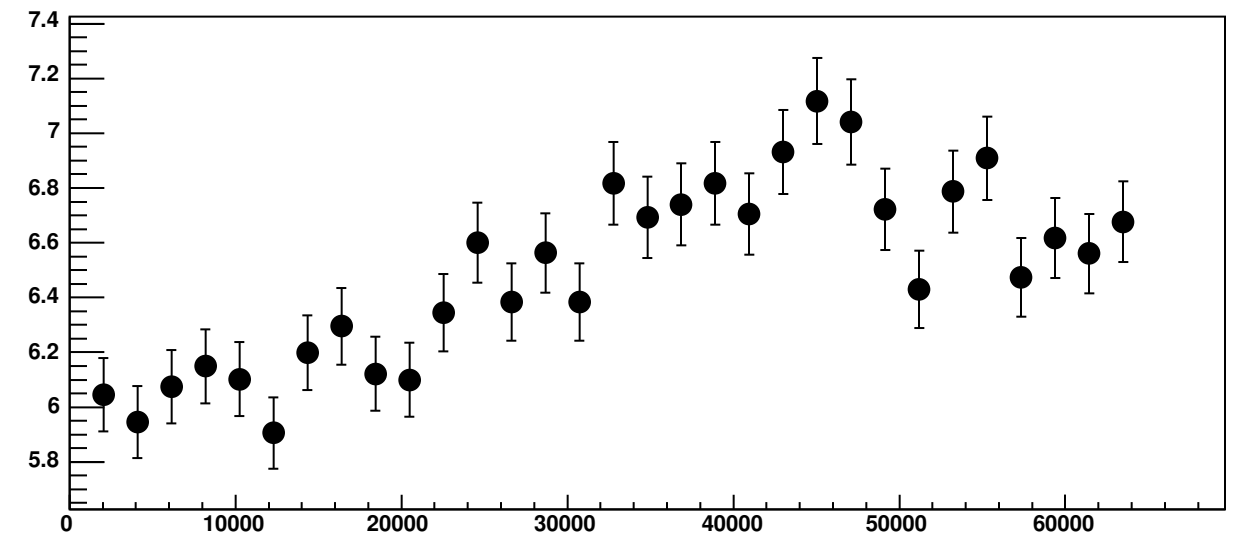
Chip 5, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC



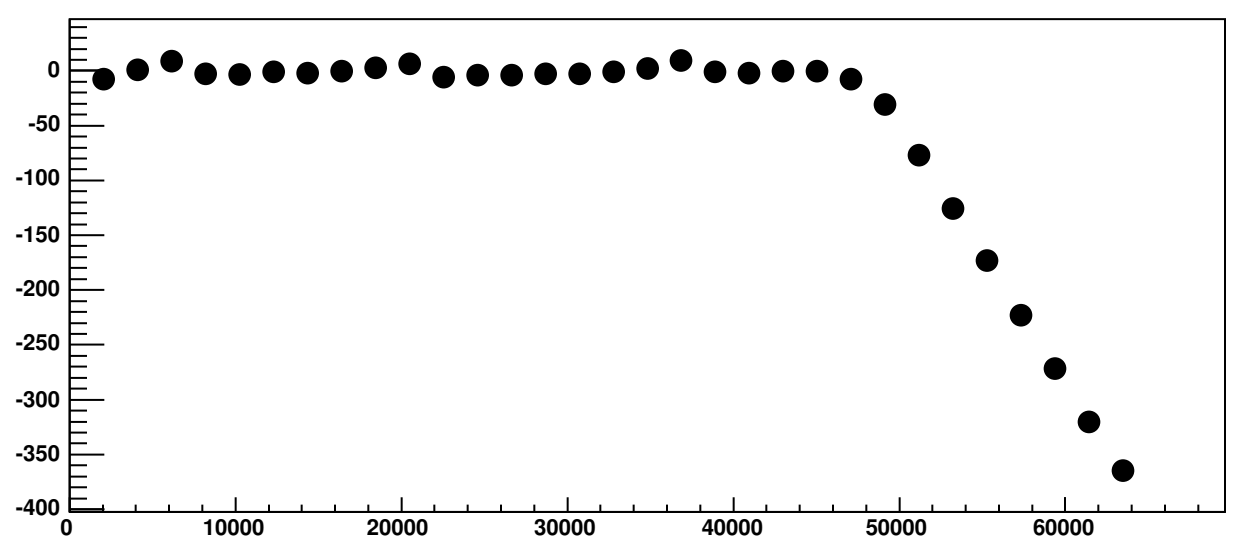
Chip 5, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC

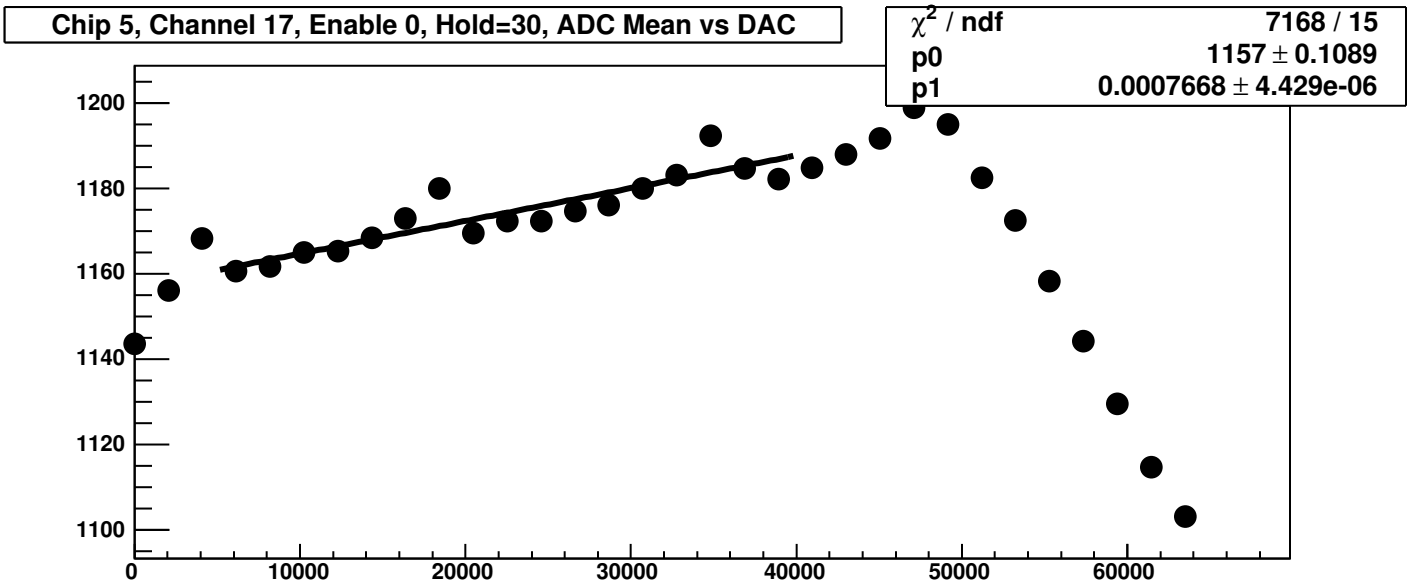


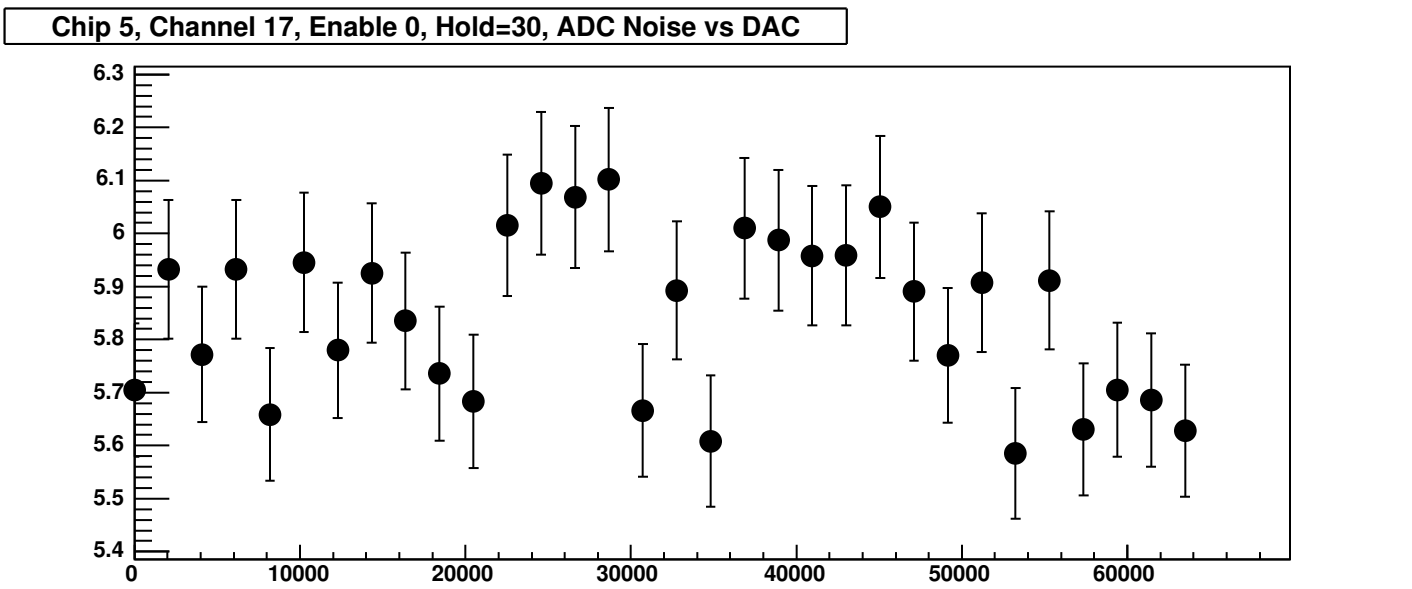
Chip 5, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC

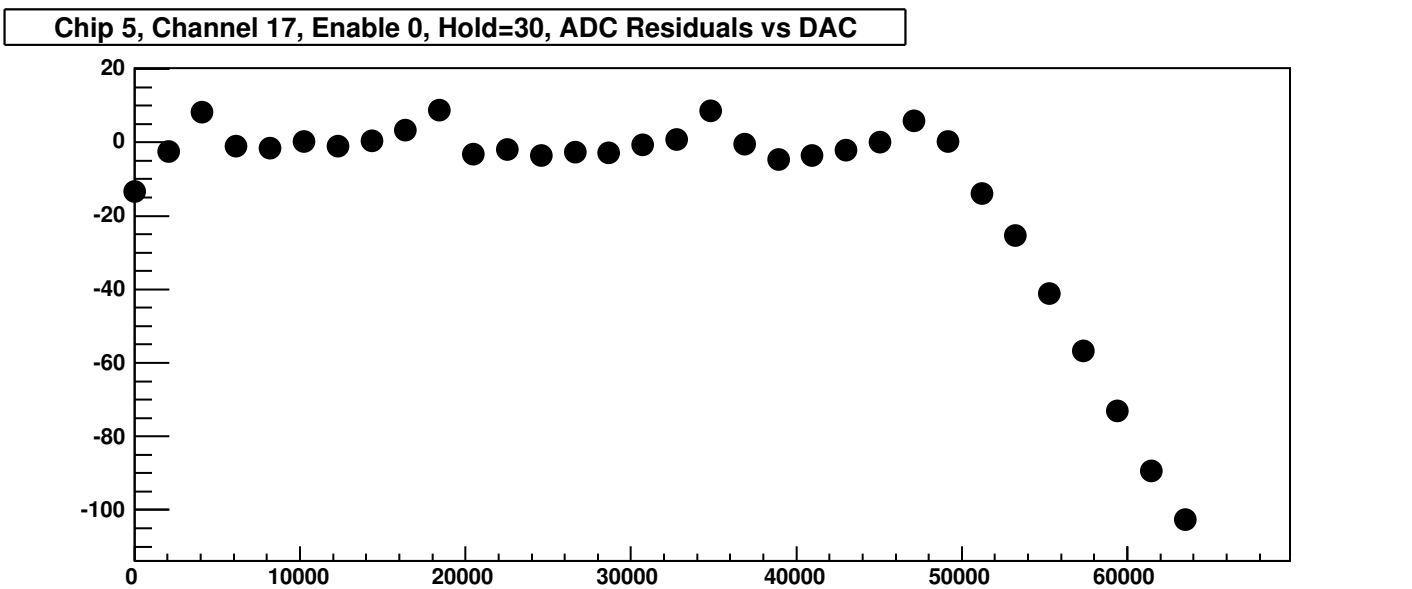


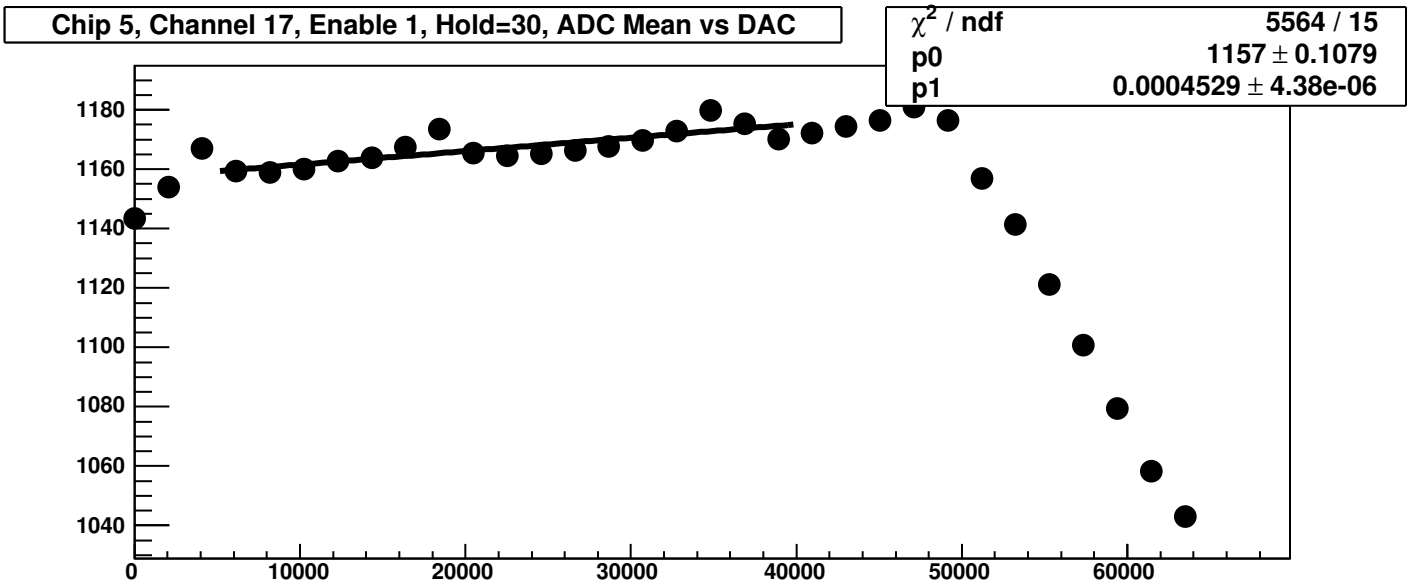
Chip 5, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC

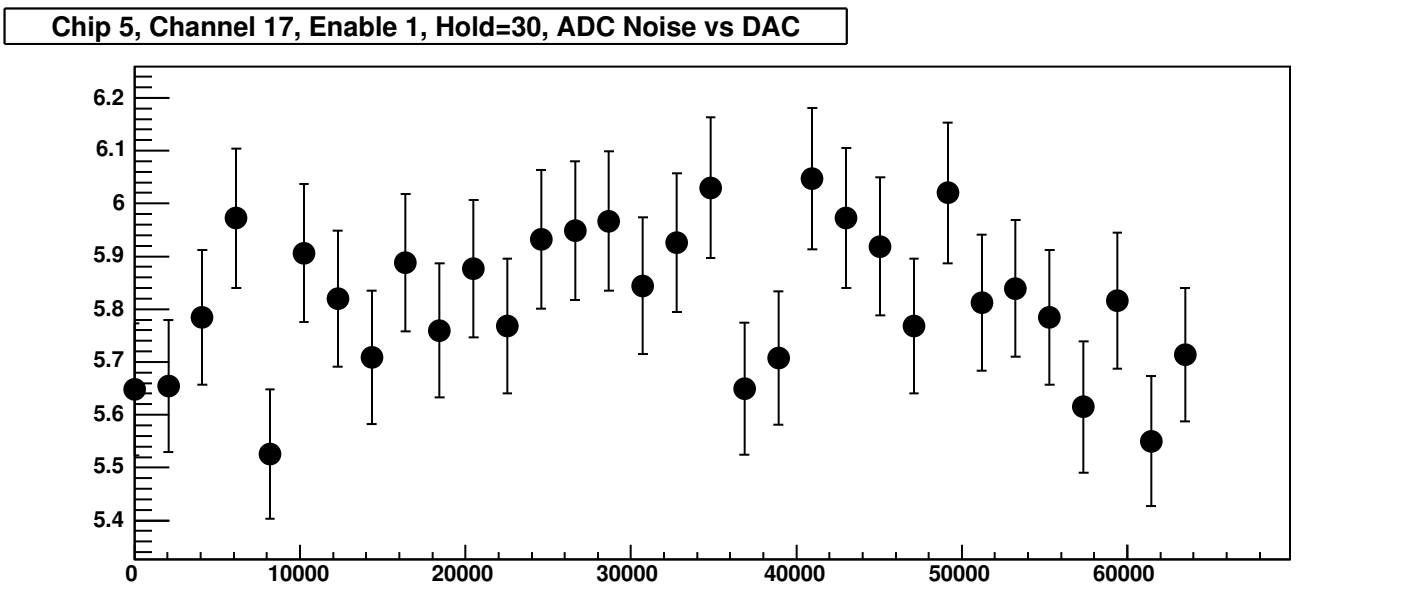


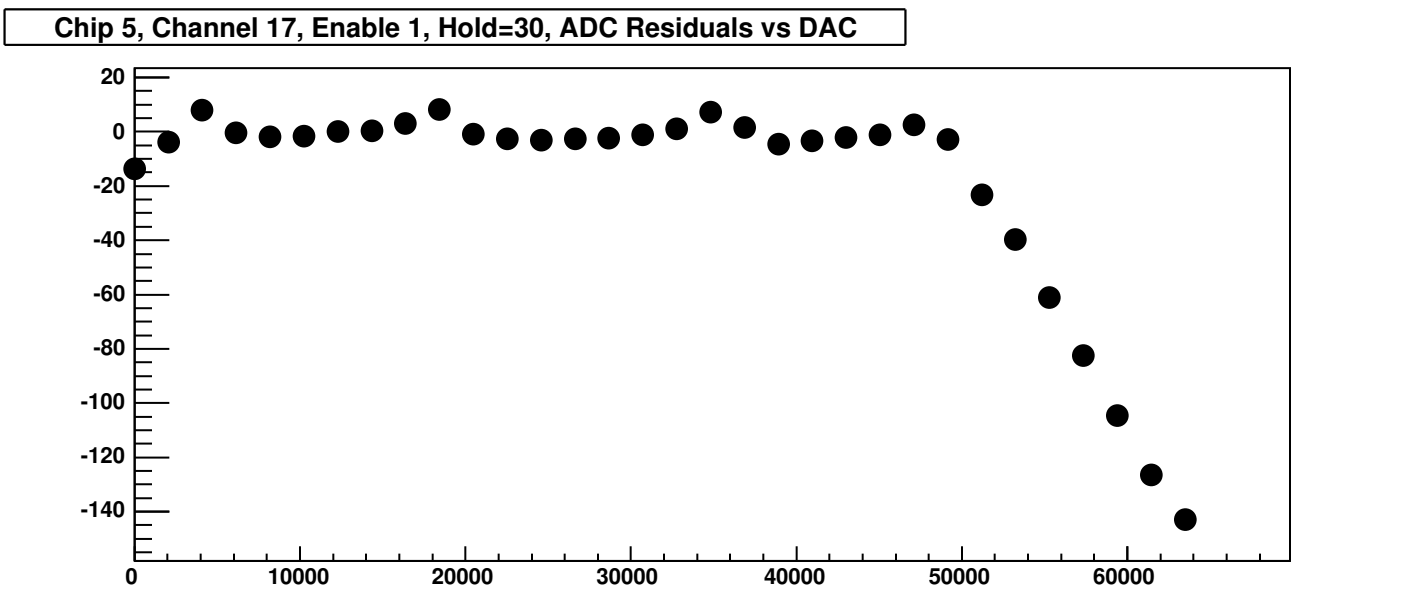

Chip 5, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7168 / 15
p0 1157 ± 0.1089
p1 0.0007668 ± 4.429e-06
1200
1180
1160
1140
1120
1100
0
10000
20000
30000
40000
50000
60000

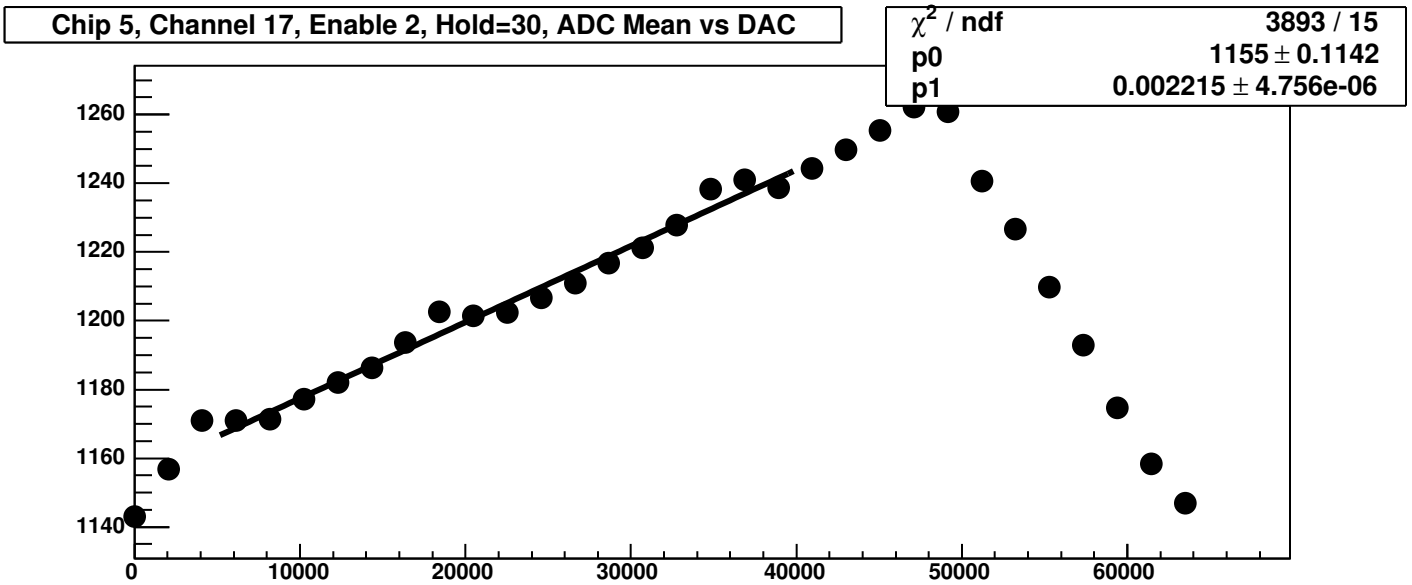

Chip 5, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

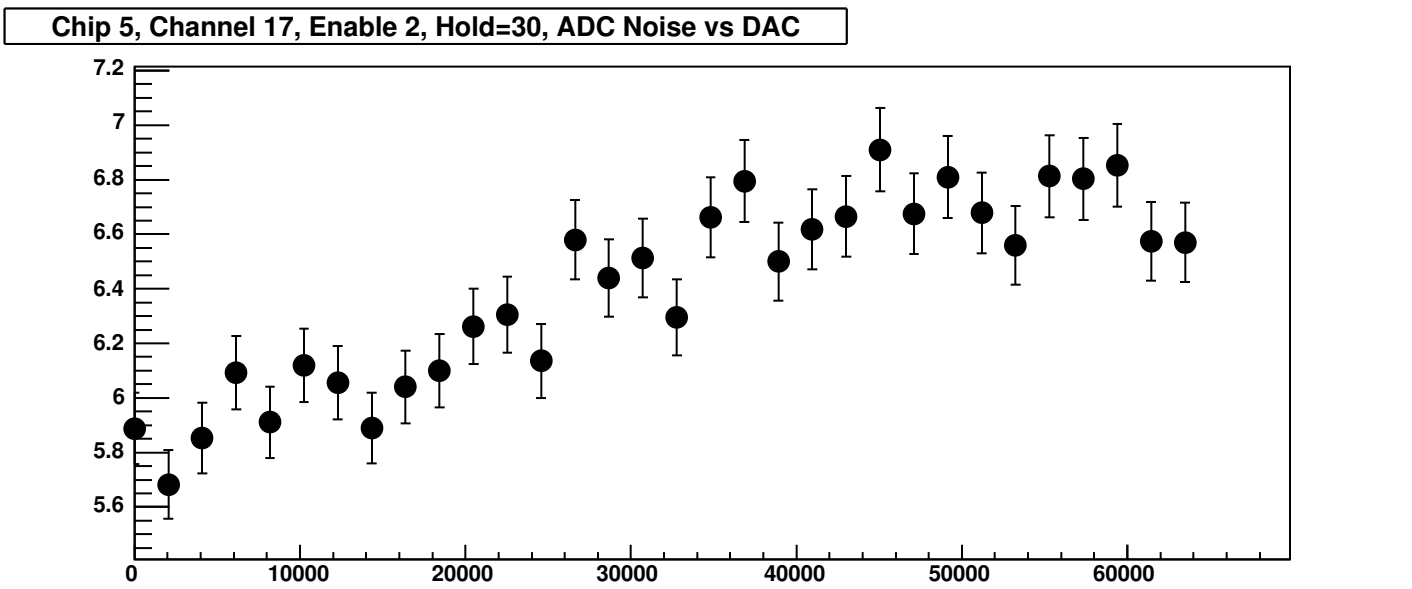

Chip 5, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

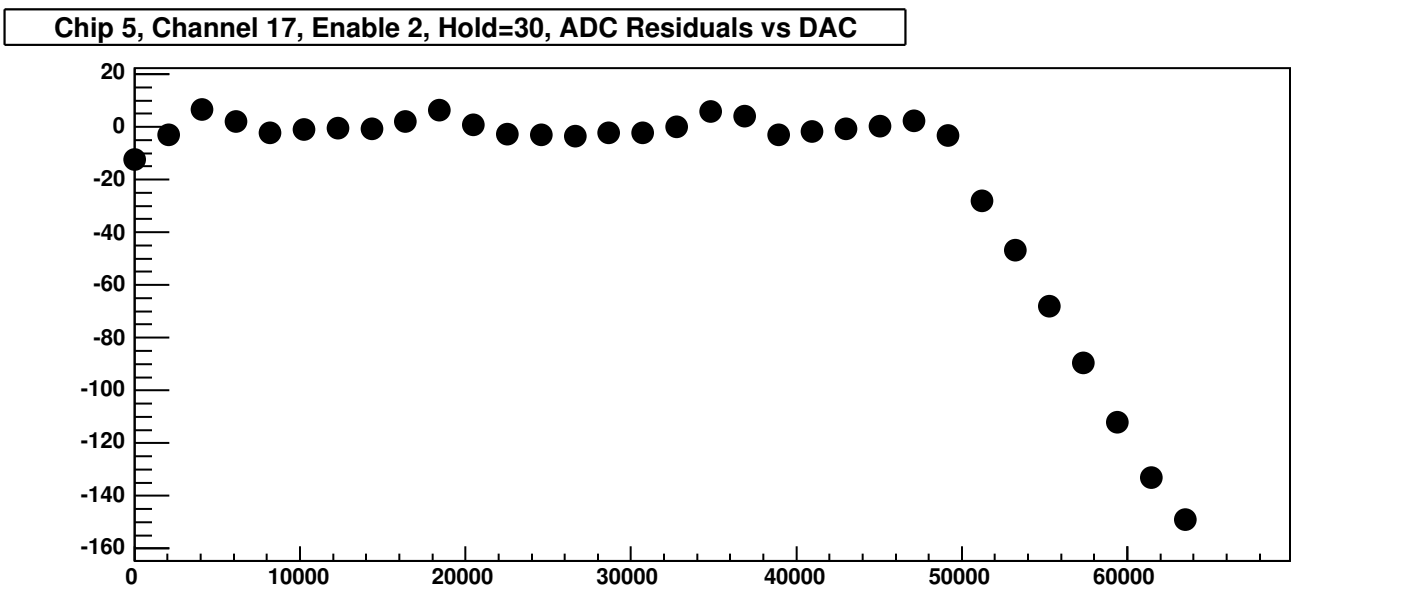

Chip 5, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5564 / 15
p0 1157 ± 0.1079
p1 0.0004529 ± 4.38e-06
1180
1160
1140
1120
1100
1080
1060
1040
0
10000
20000
30000
40000
50000
60000

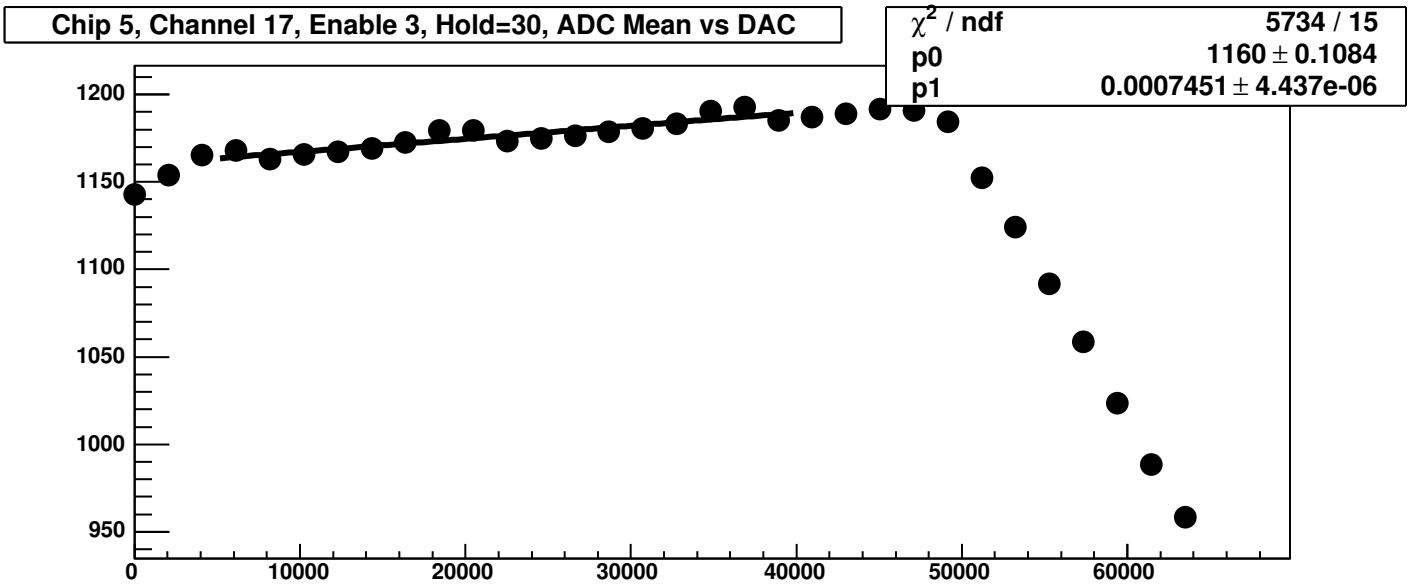

Chip 5, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

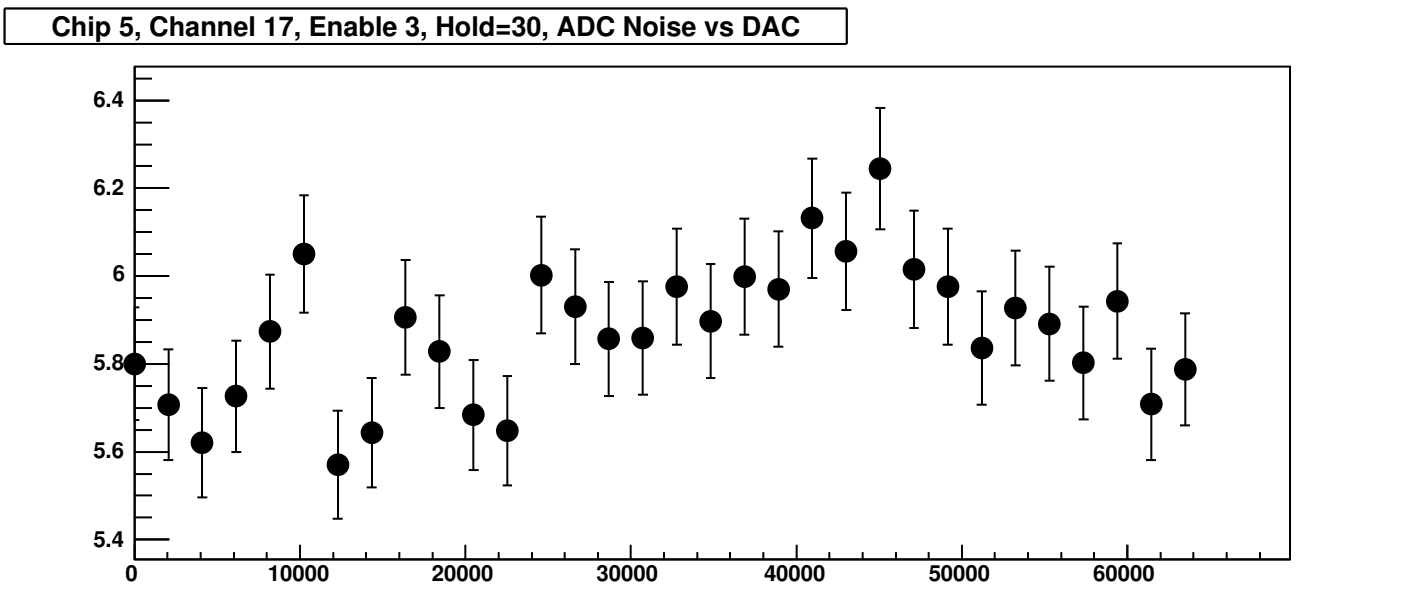

Chip 5, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

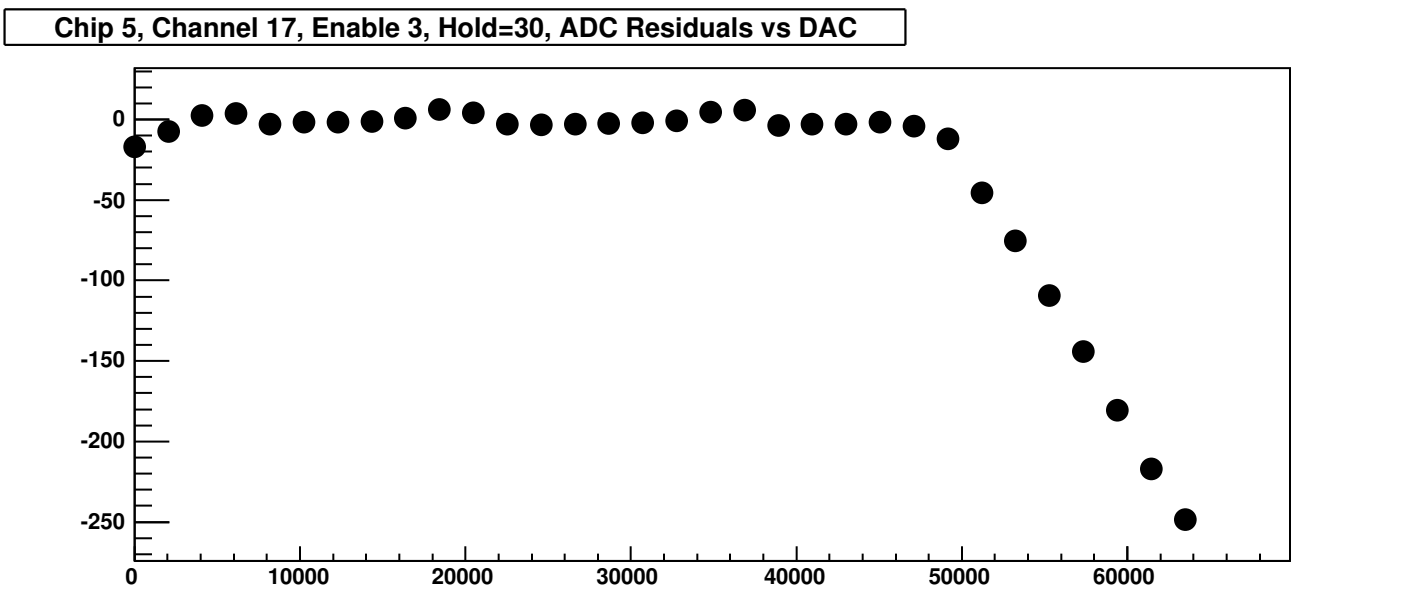

Chip 5, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3893 / 15
p0 1155 ± 0.1142
p1 0.002215 ± 4.756e-06
1260
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

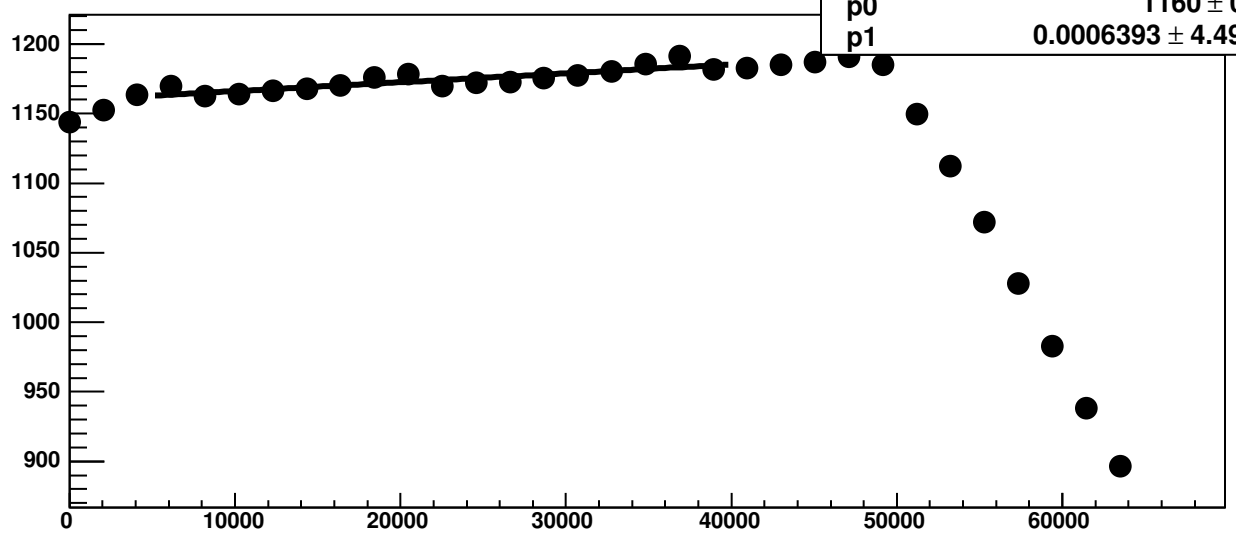

Chip 5, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5734 / 15
p0 1160 ± 0.1084
p1 0.0007451 ± 4.437e-06
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

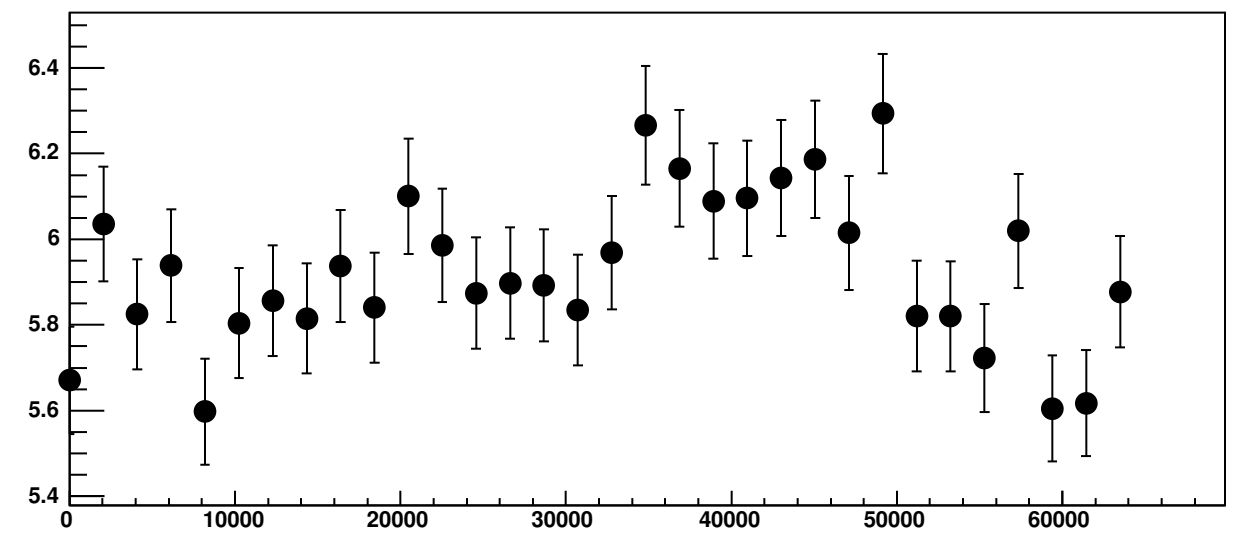

Chip 5, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

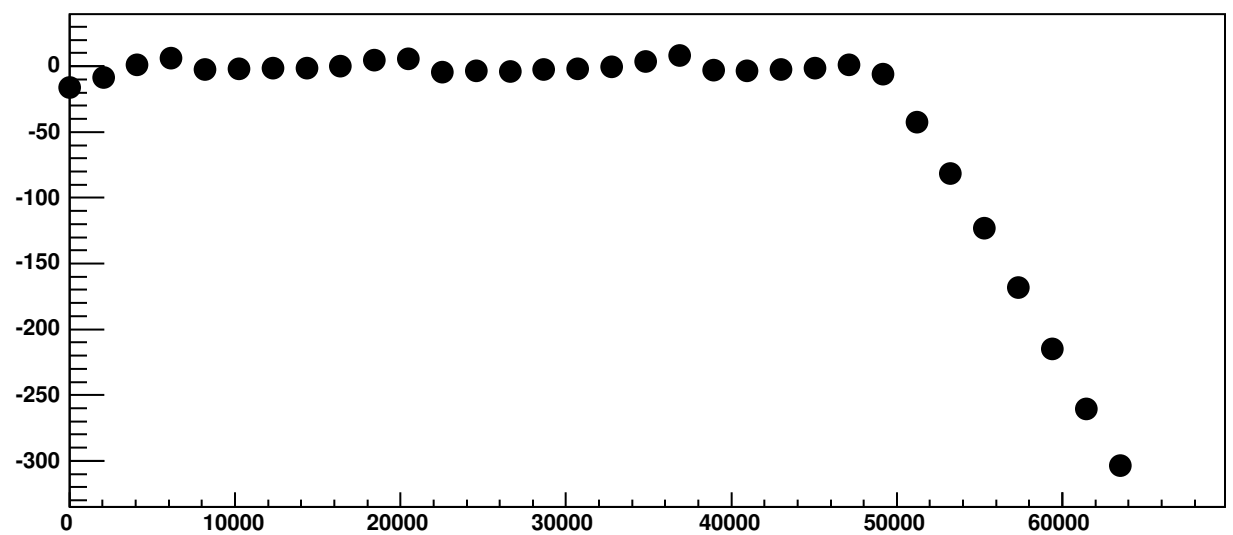
Chip 5, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

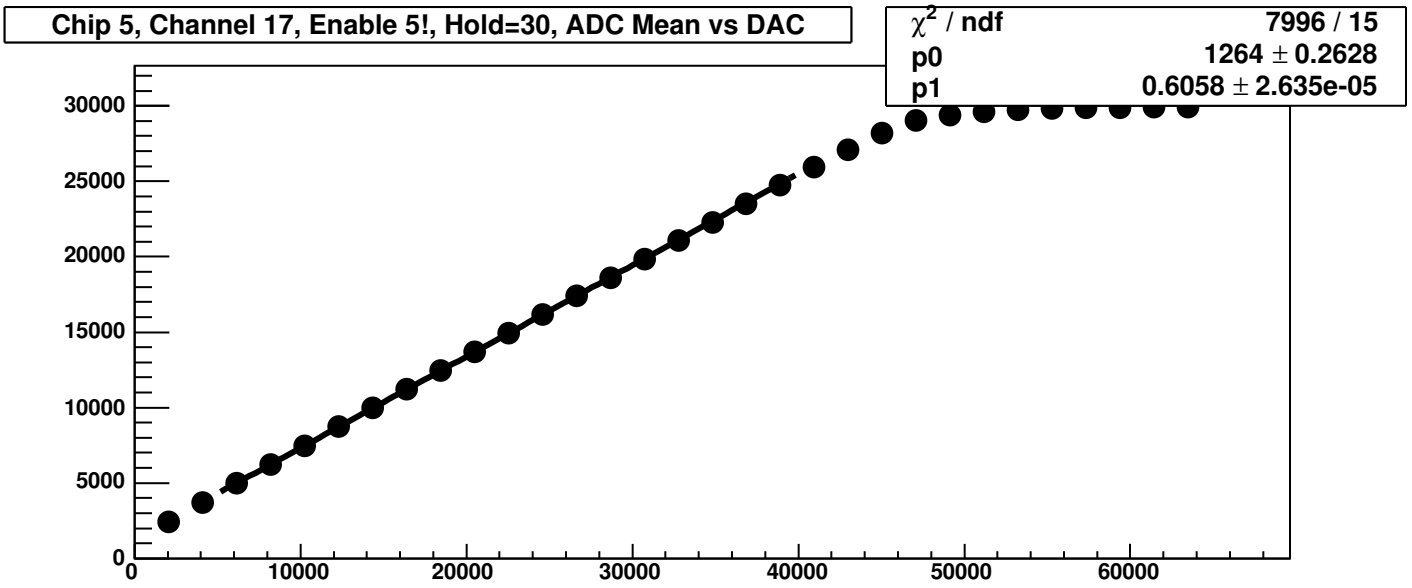


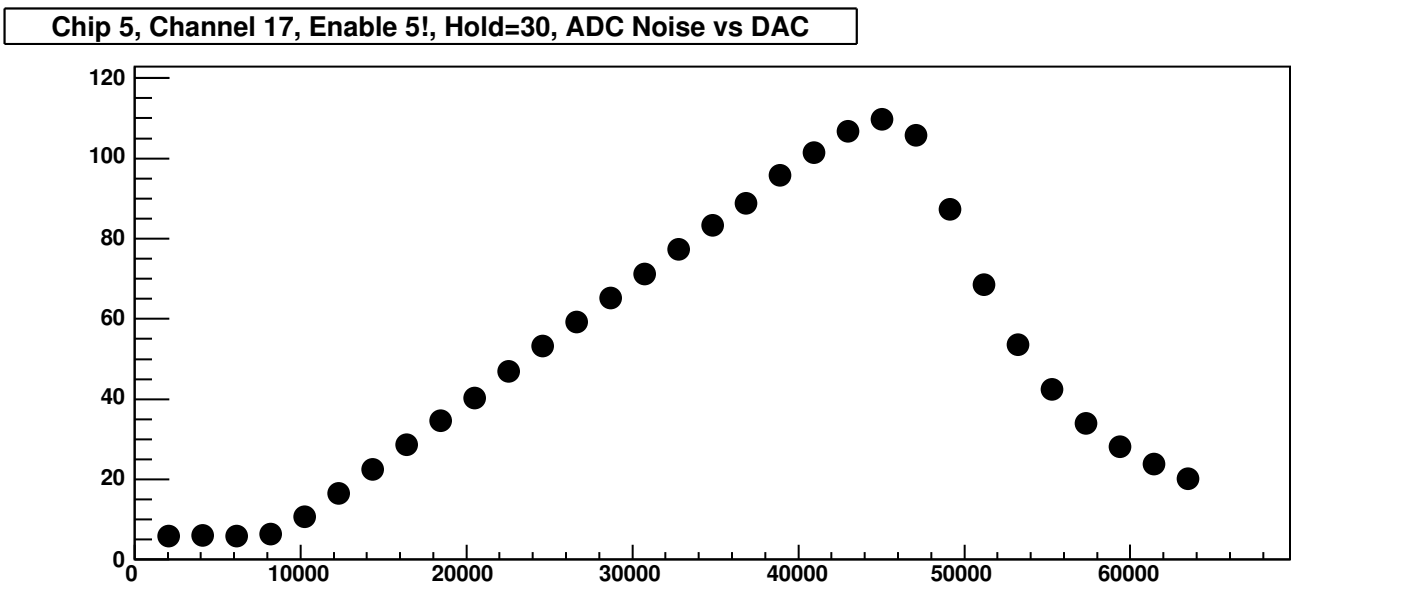
Chip 5, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC

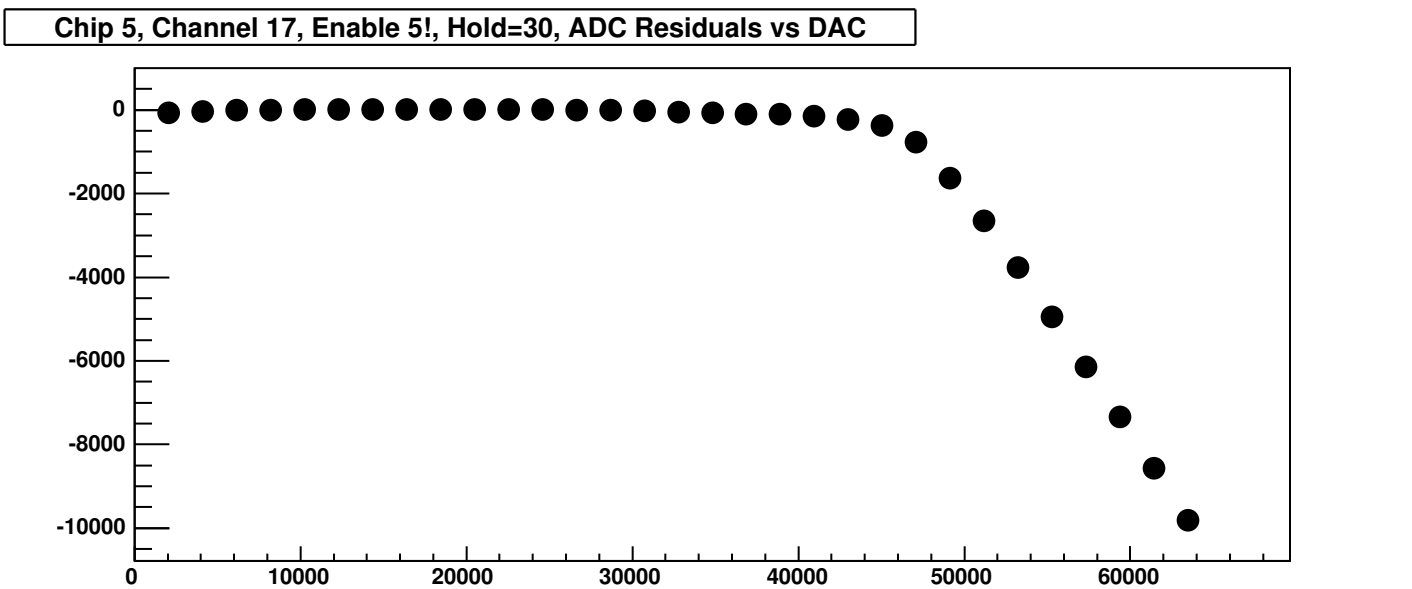


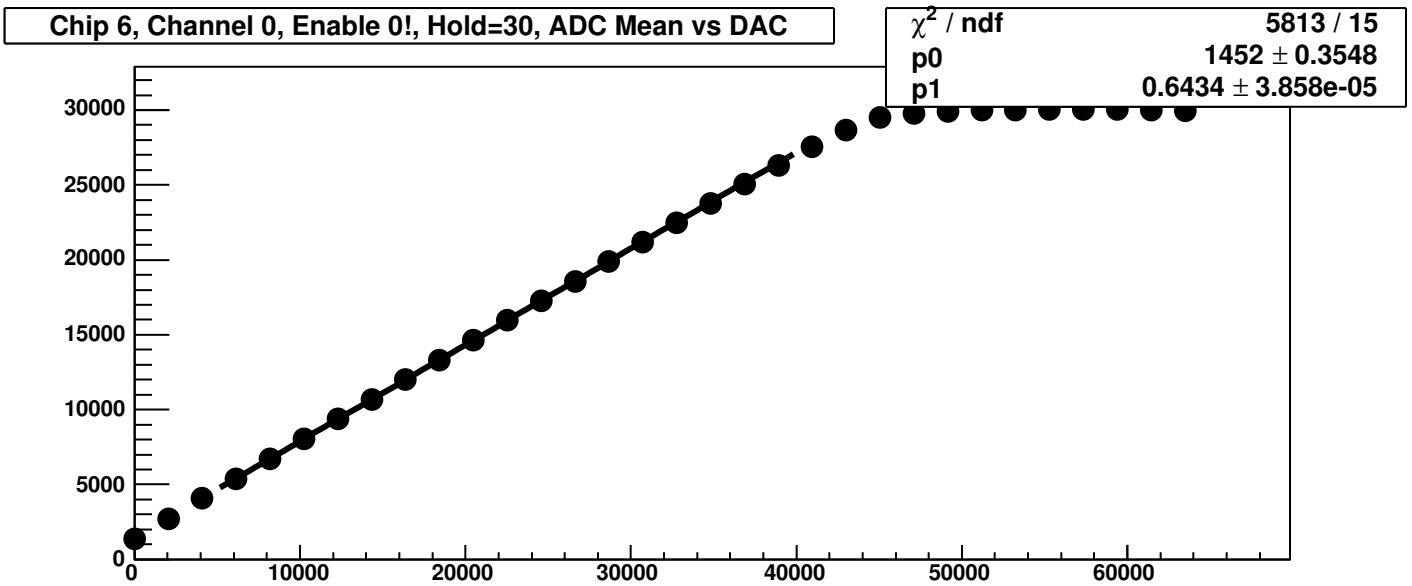
Chip 5, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC

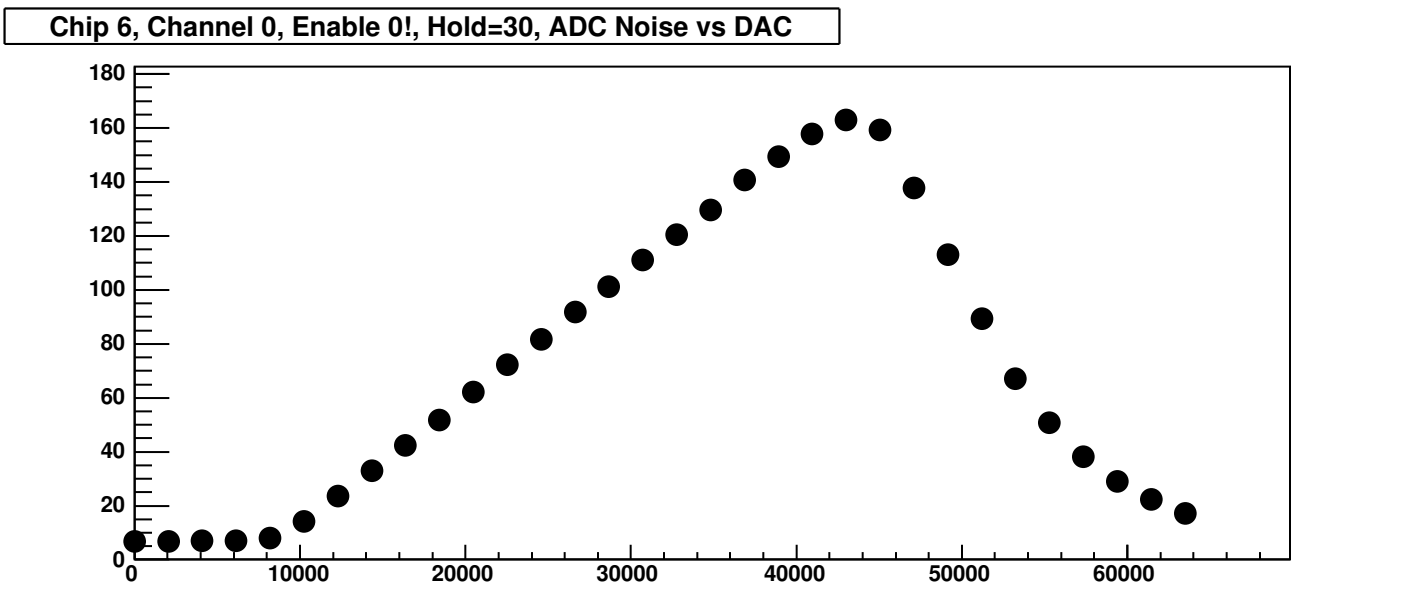


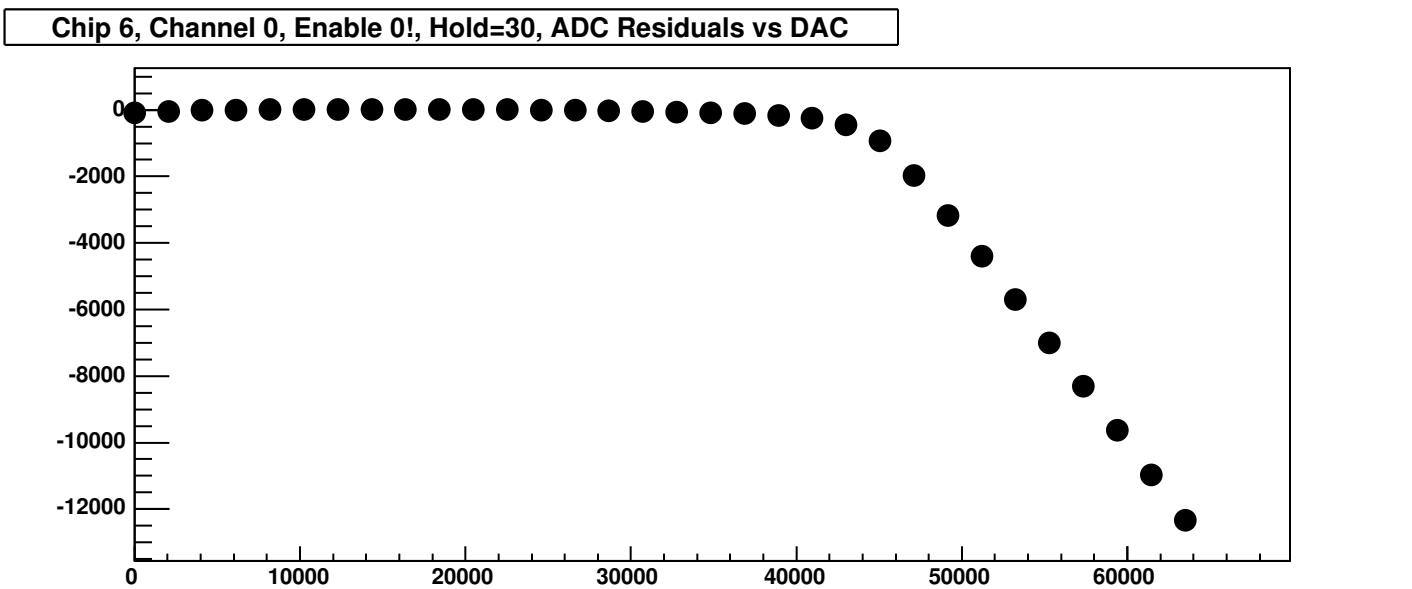

Chip 5, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7996 / 15
p0 1264 ± 0.2628
p1 0.6058 ± 2.635e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

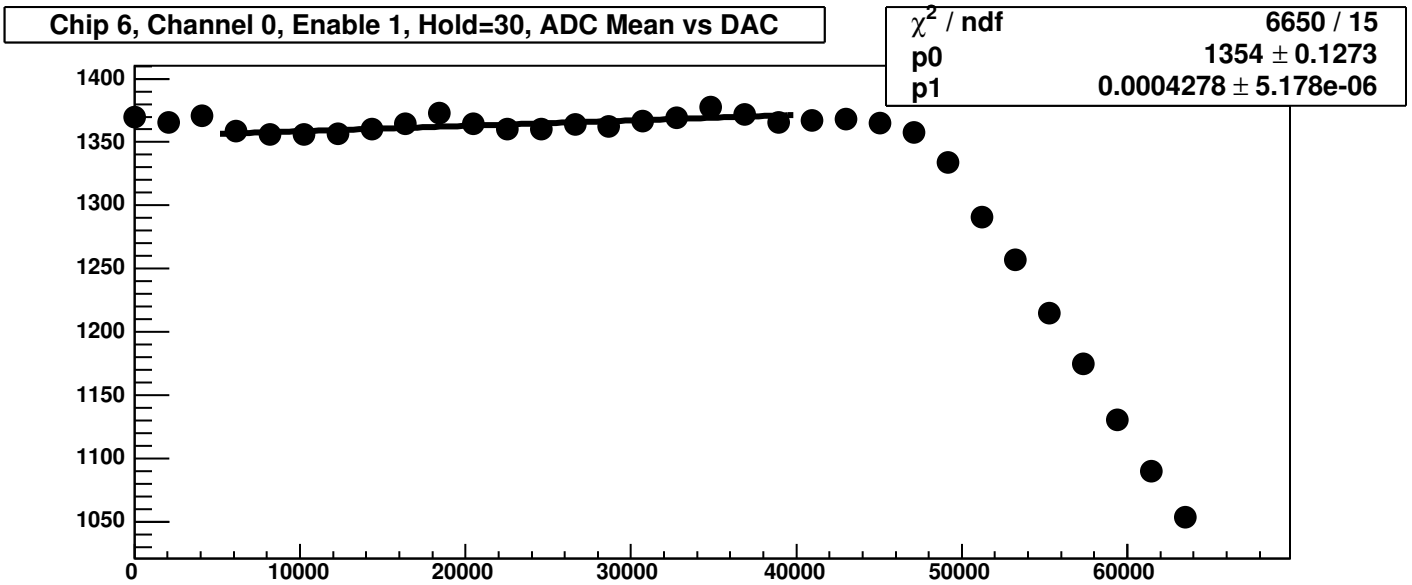

Chip 5, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

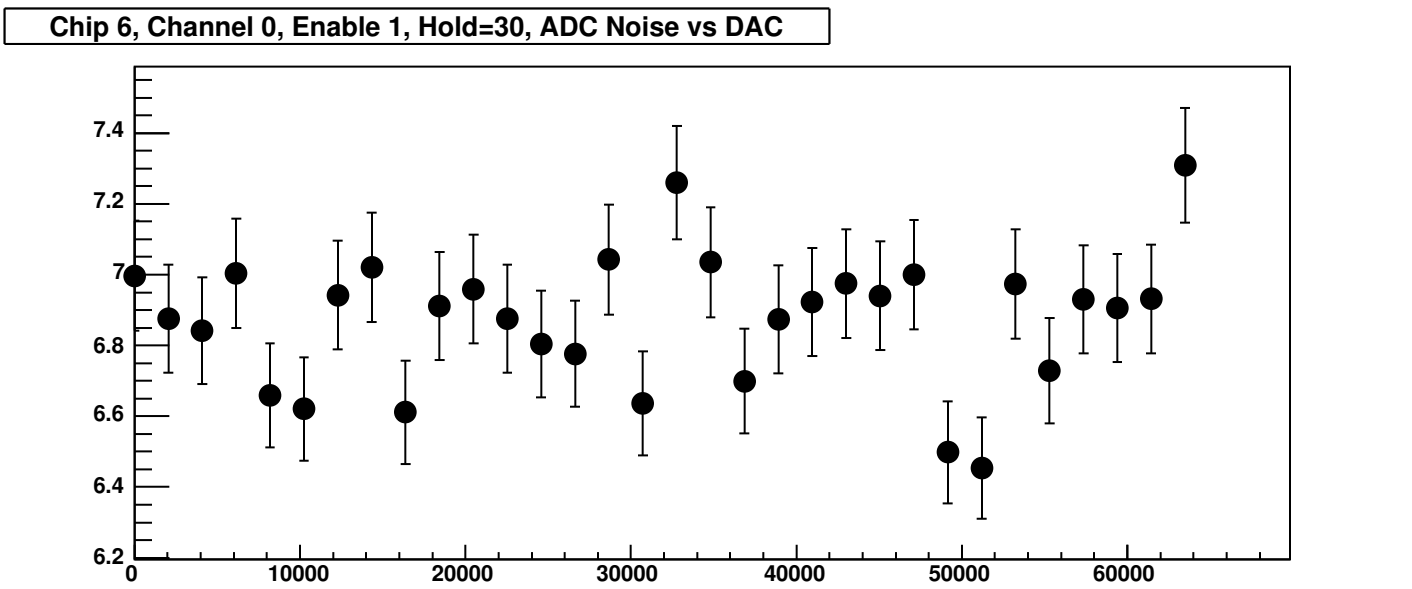

Chip 5, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

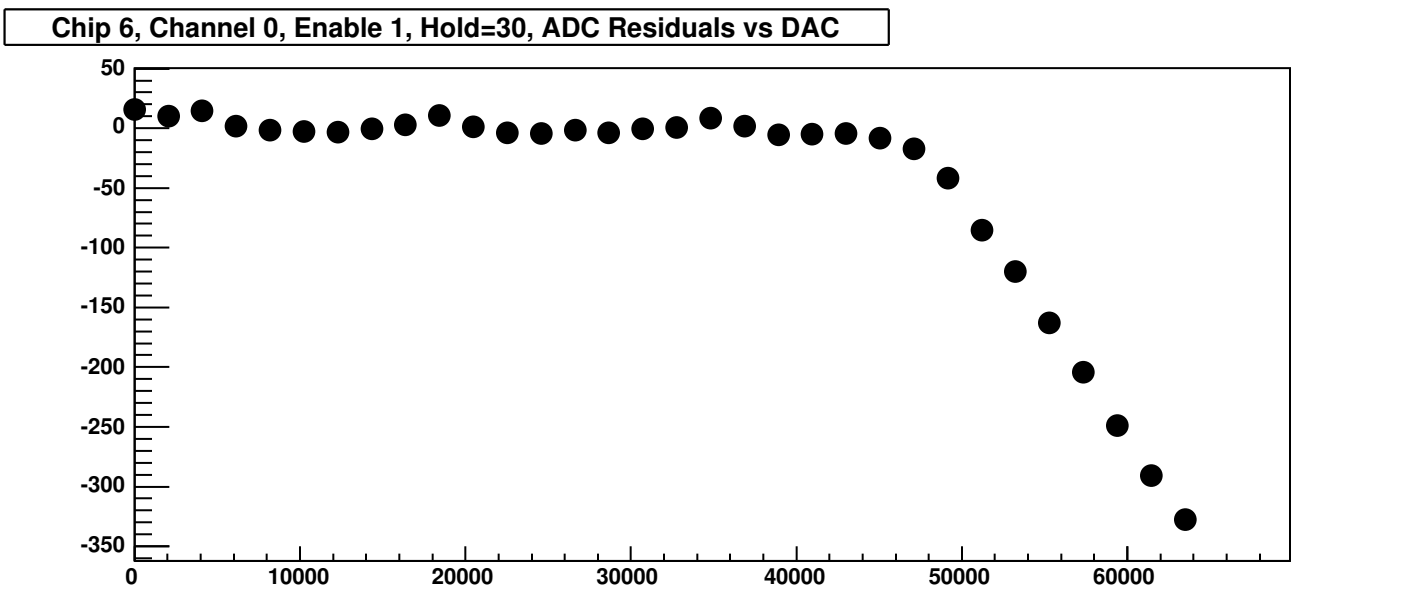

Chip 6, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 5813 / 15
p0 1452 ± 0.3548
p1 0.6434 ± 3.858e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

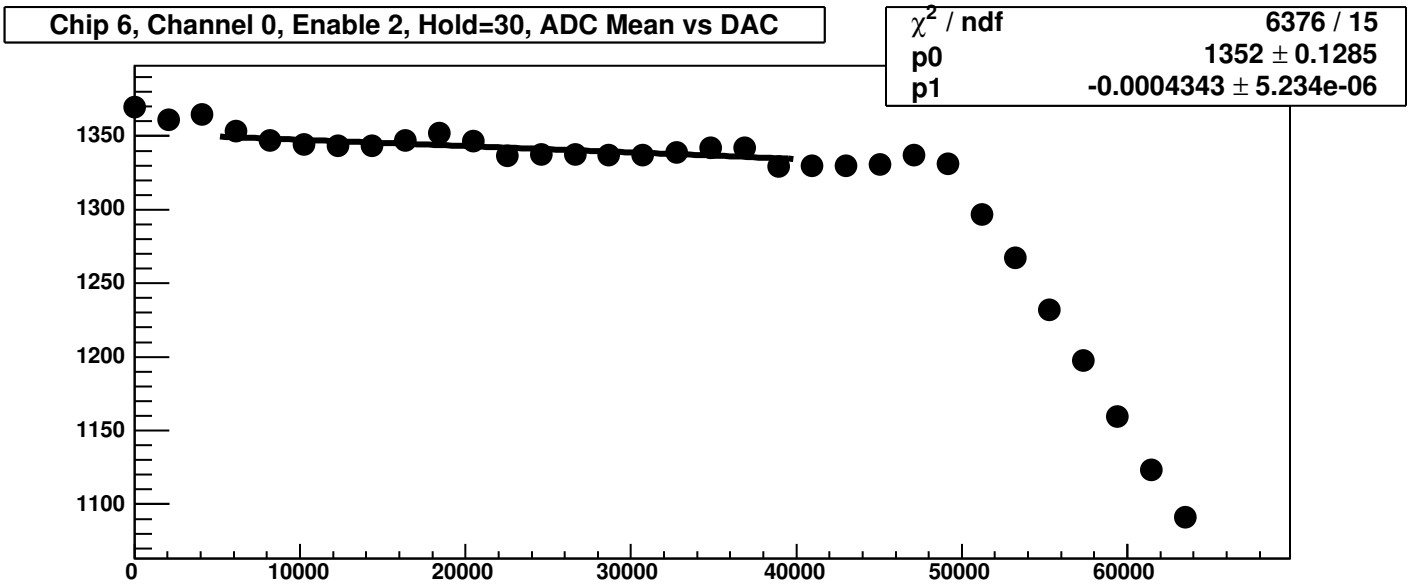

Chip 6, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

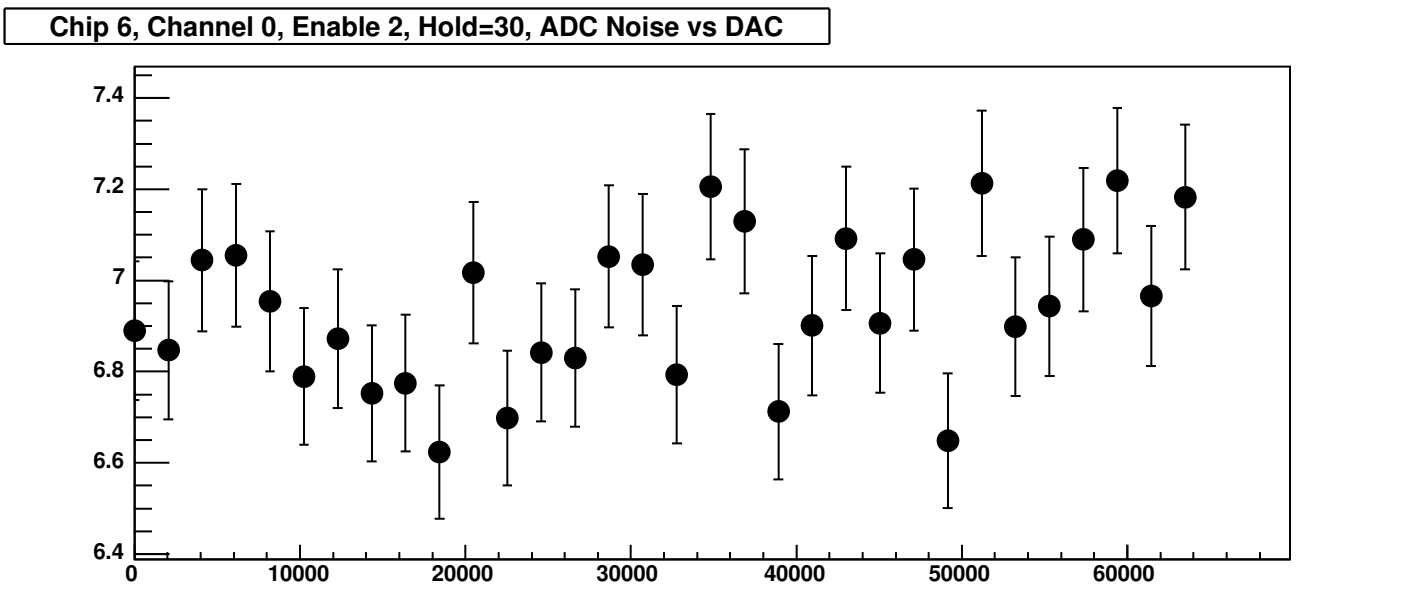

Chip 6, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

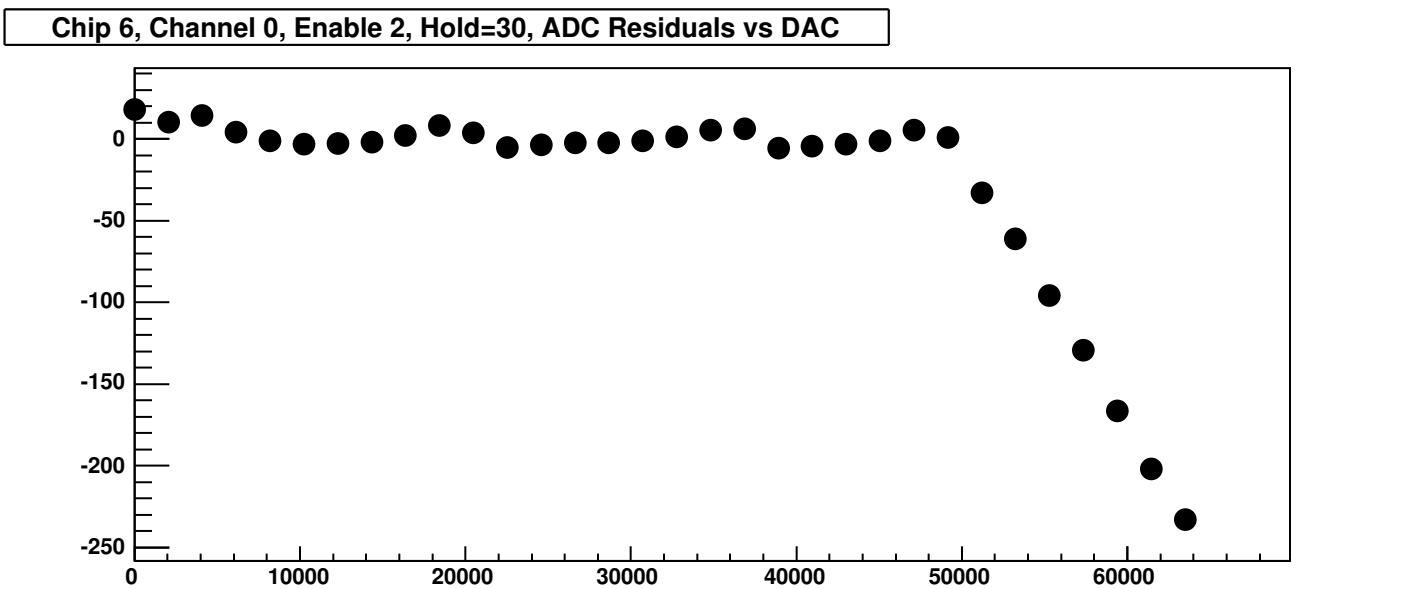

Chip 6, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6650 / 15
p0 1354 ± 0.1273
p1 0.0004278 ± 5.178e-06
1400
1350
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

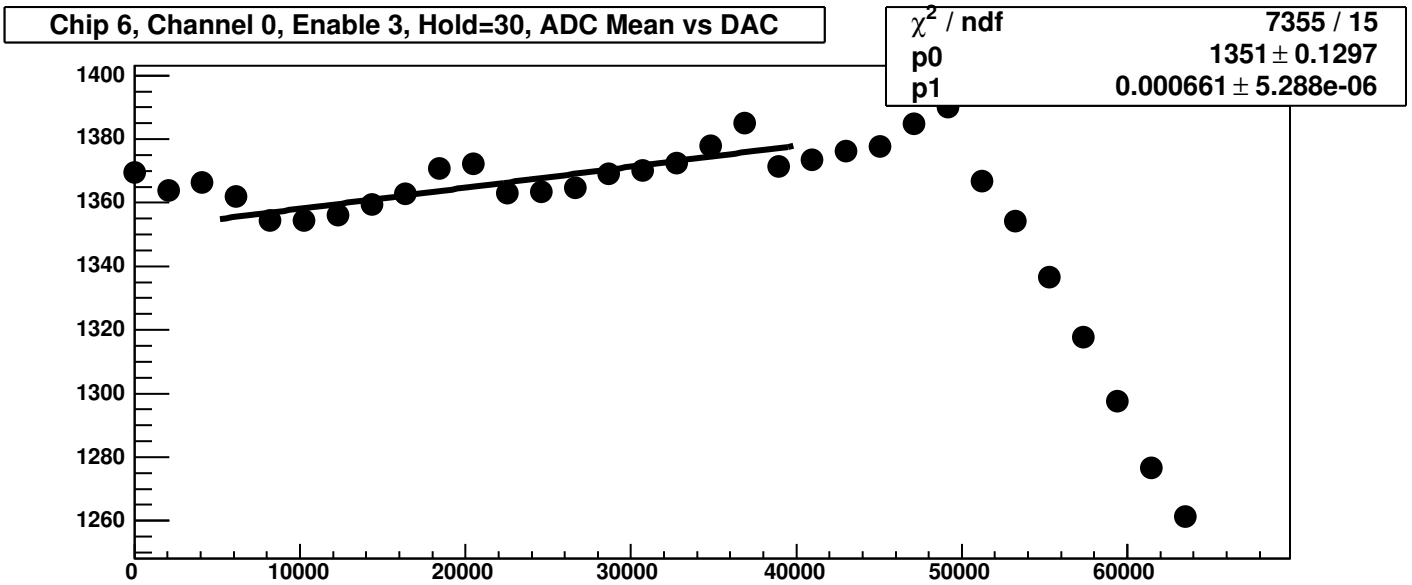

Chip 6, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

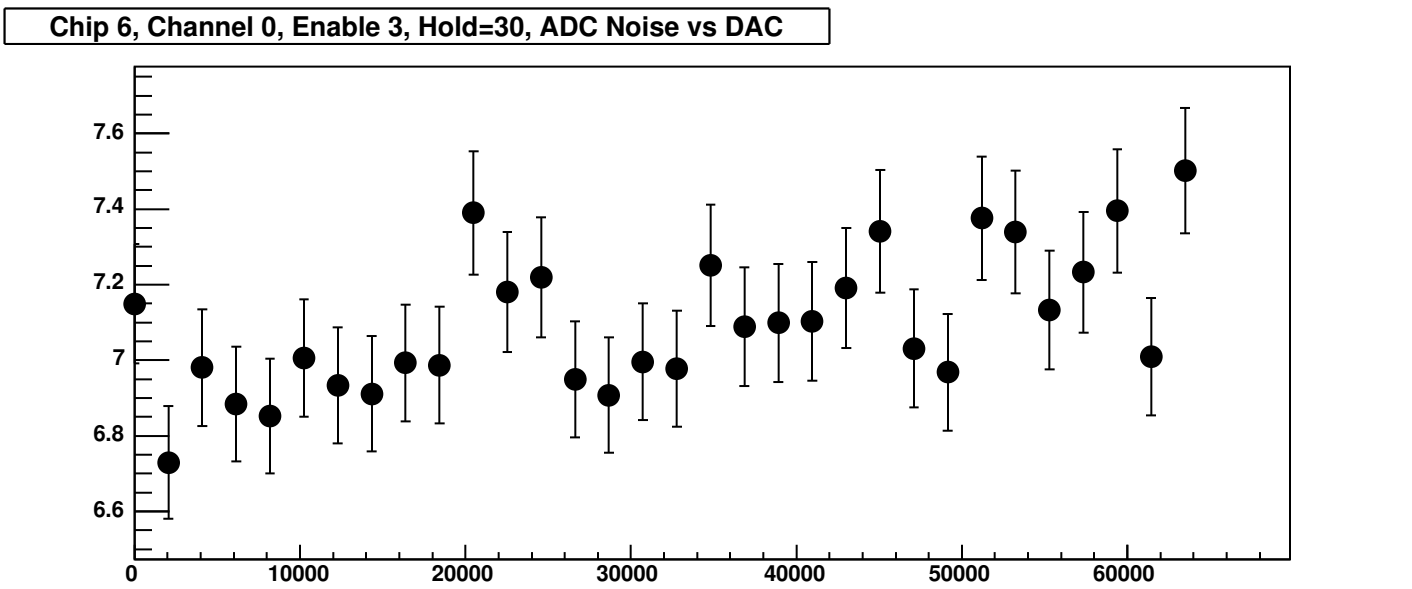

Chip 6, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

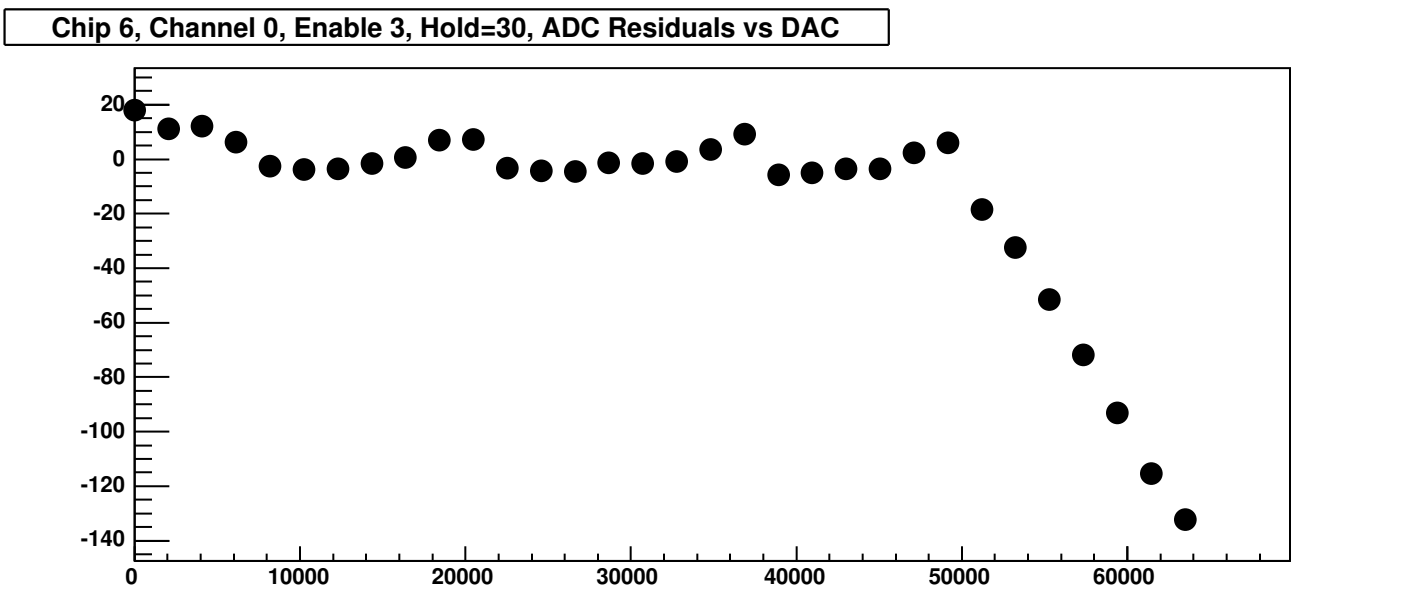

Chip 6, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6376 / 15
p0 1352 ± 0.1285
p1 -0.0004343 ± 5.234e-06
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

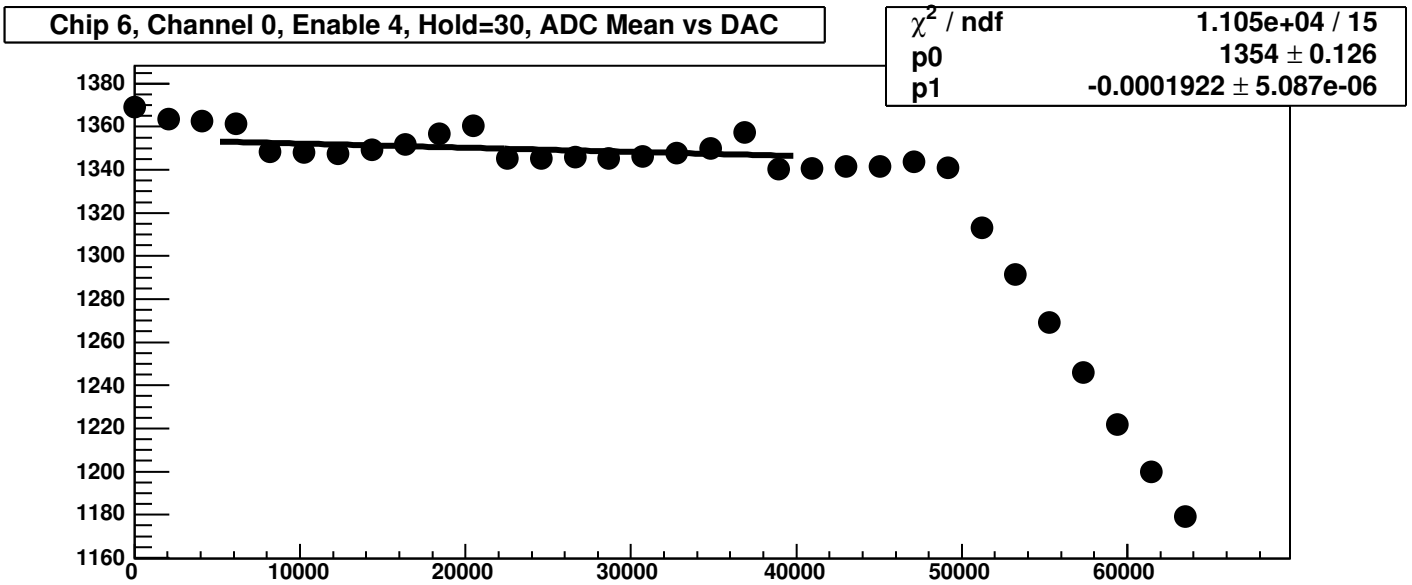

Chip 6, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

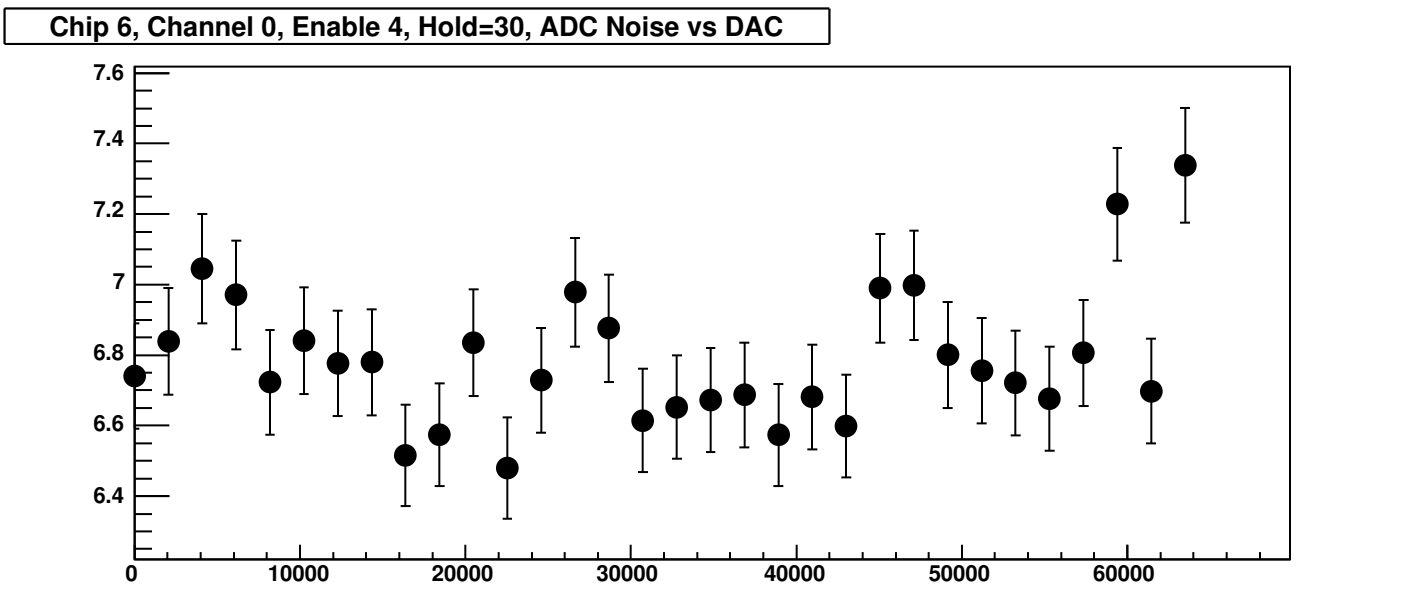

Chip 6, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

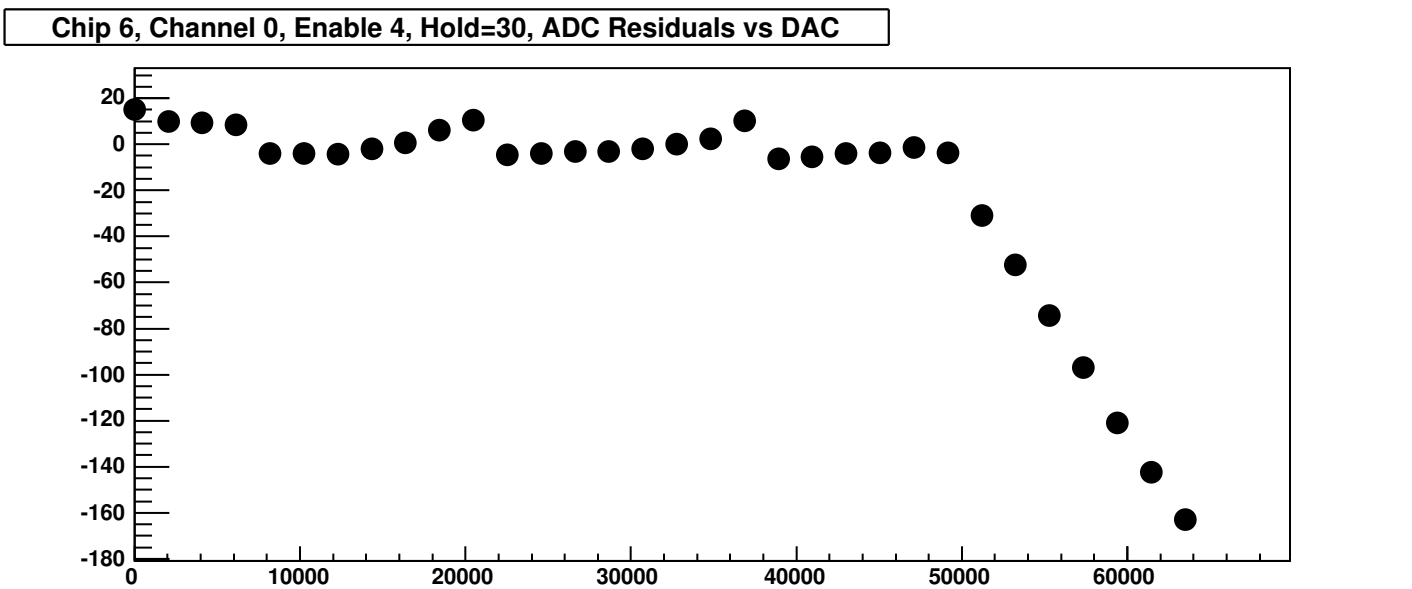

Chip 6, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7355 / 15
p0 1351 ± 0.1297
p1 0.000661 ± 5.288e-06
1400
1380
1360
1340
1320
1300
1280
1260
0
10000
20000
30000
40000
50000
60000

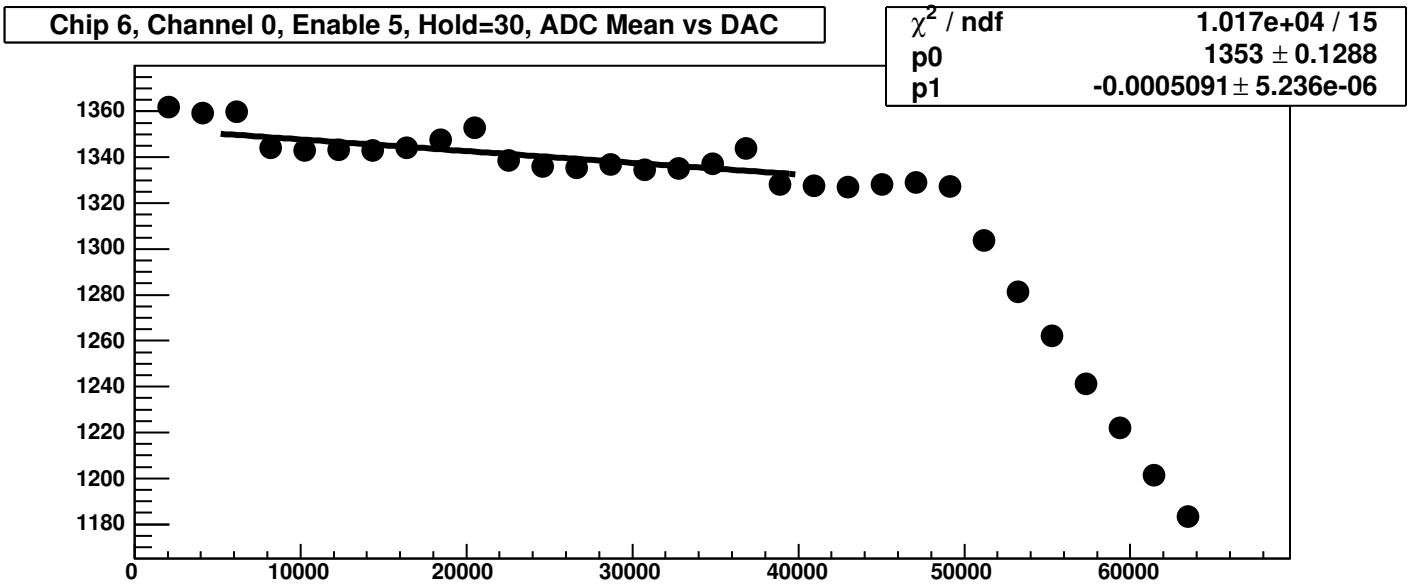

Chip 6, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
0
10000
20000
30000
40000
50000
60000

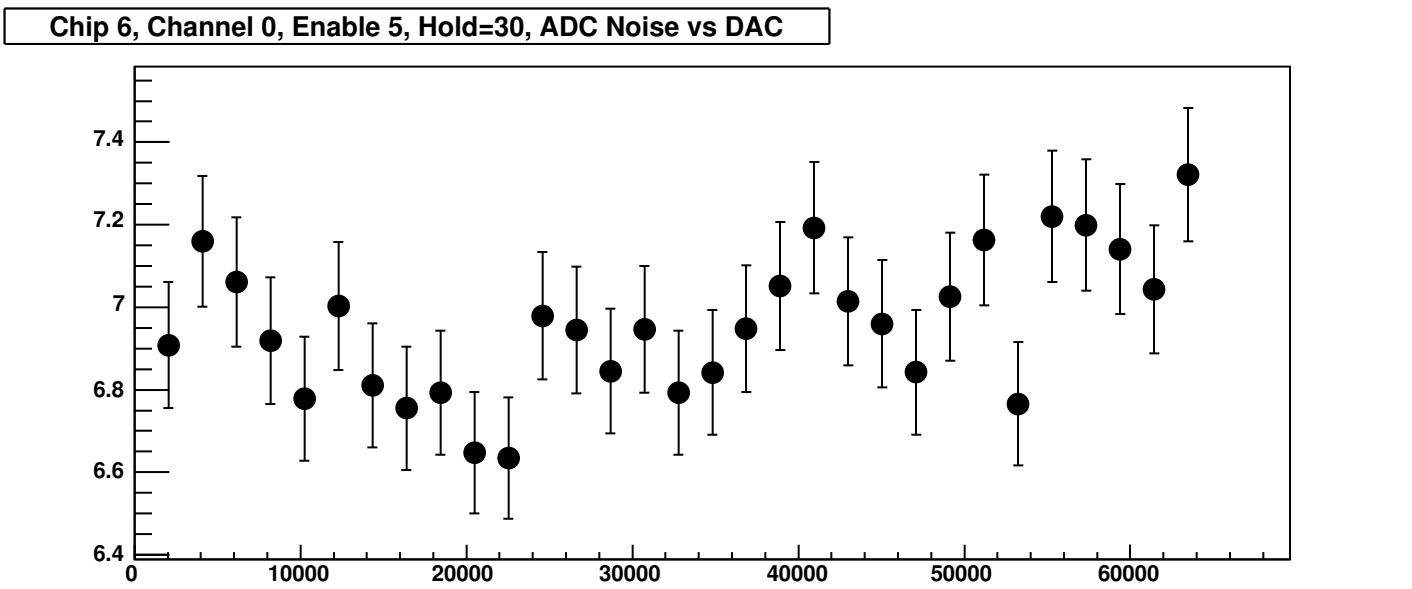

Chip 6, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

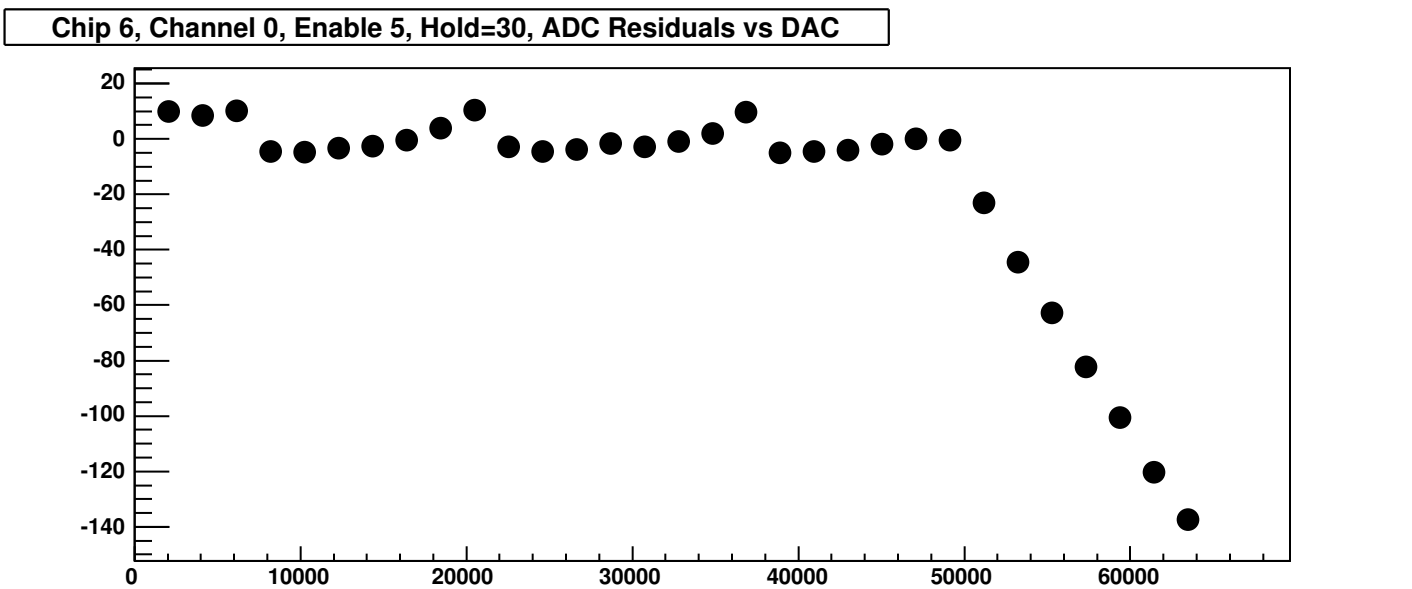

Chip 6, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.105e+04 / 15
p0 1354 ± 0.126
p1 -0.0001922 ± 5.087e-06
1380
1360
1340
1320
1300
1280
1260
1240
1220
1200
1180
1160
0
10000
20000
30000
40000
50000
60000

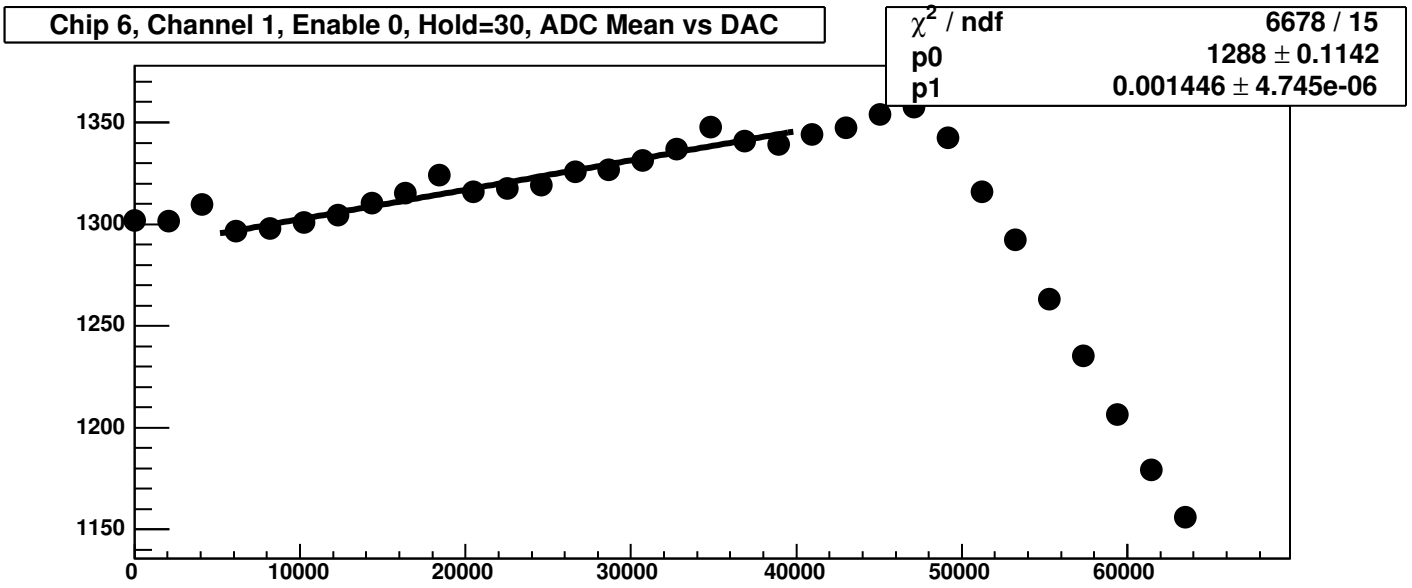

Chip 6, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

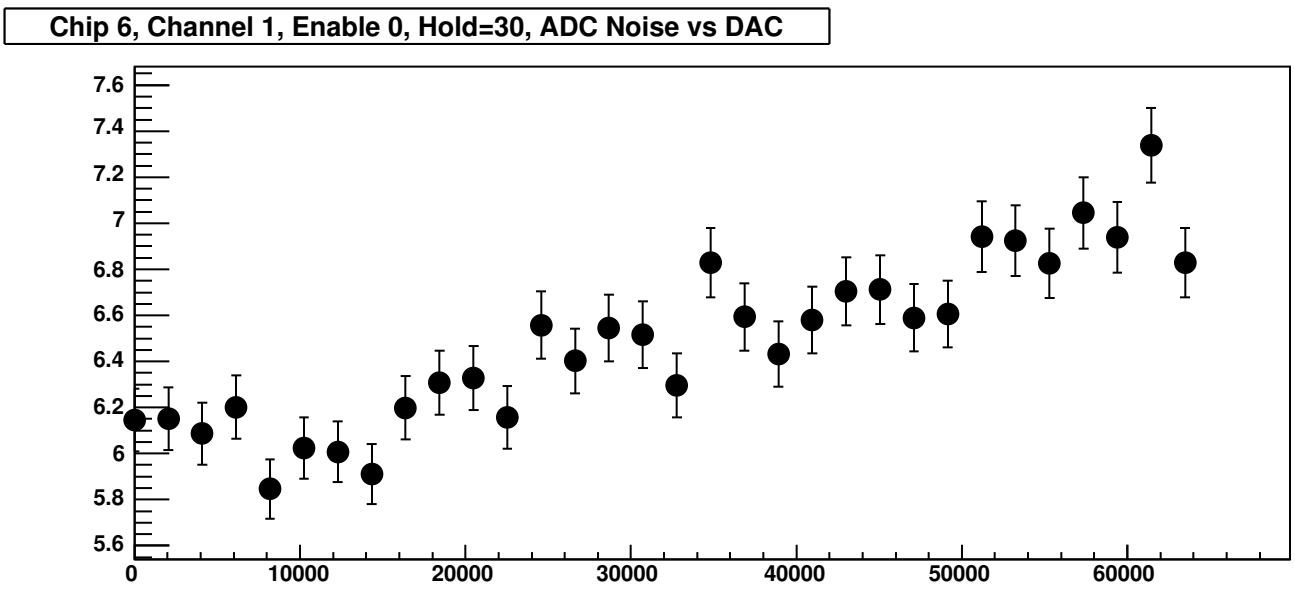

Chip 6, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

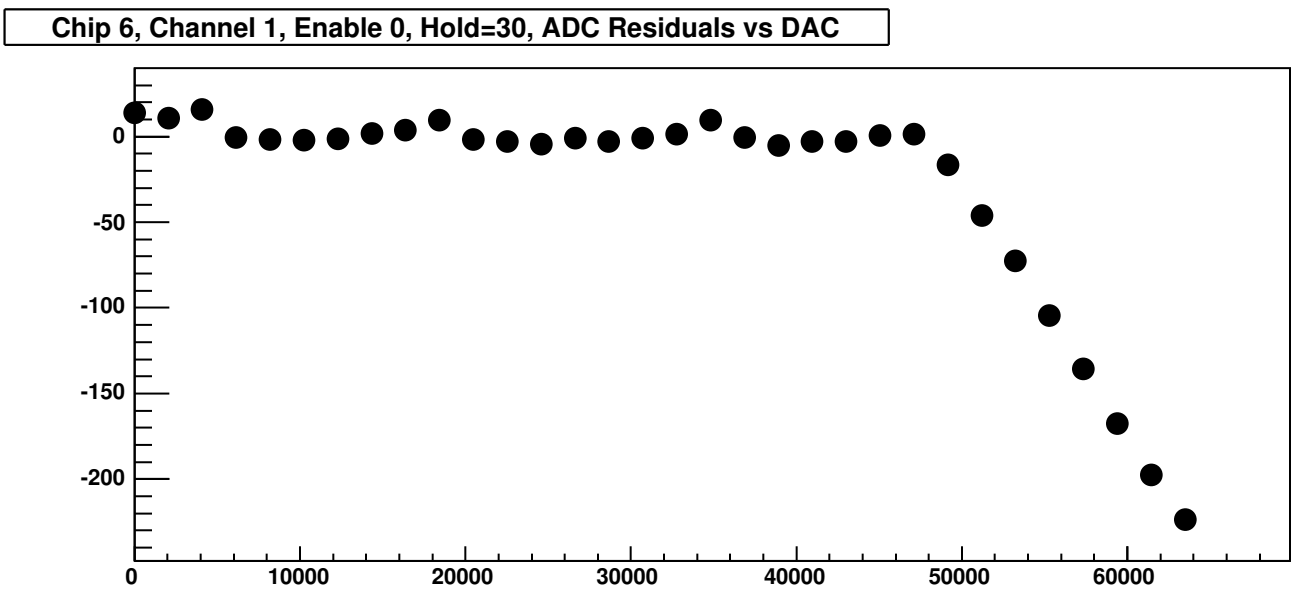

Chip 6, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.017e+04 / 15
p0 1353 ± 0.1288
p1 -0.0005091 ± 5.236e-06
1360
1340
1320
1300
1280
1260
1240
1220
1200
1180
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
0
10000
20000
30000
40000
50000
60000

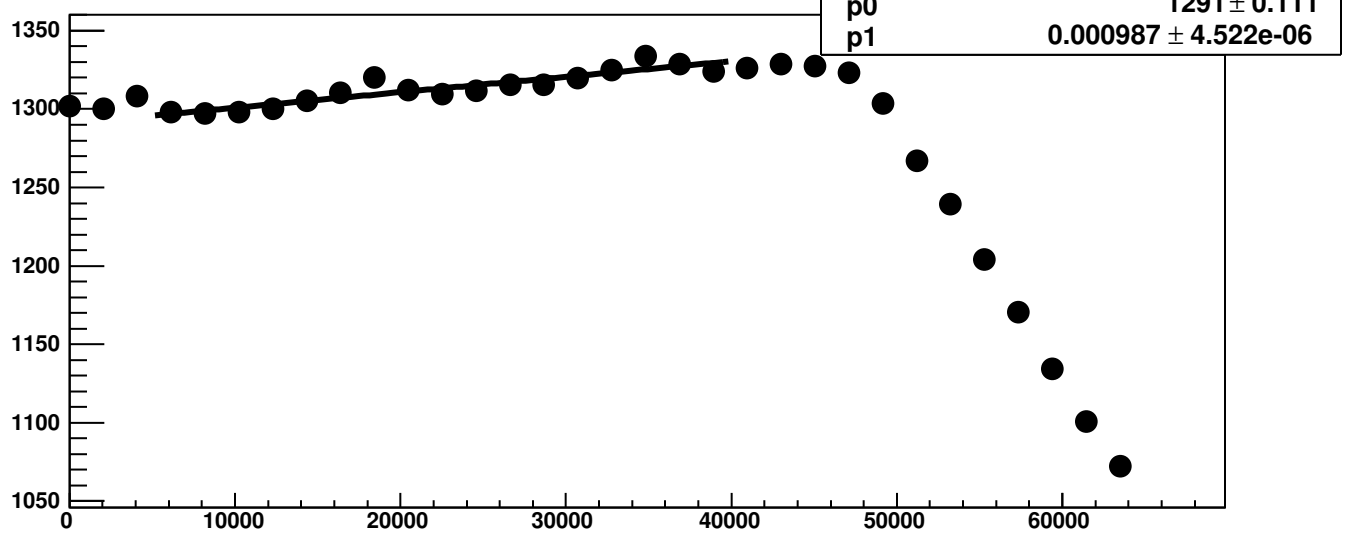

Chip 6, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6678 / 15
p0 1288 ± 0.1142
p1 0.001446 ± 4.745e-06
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

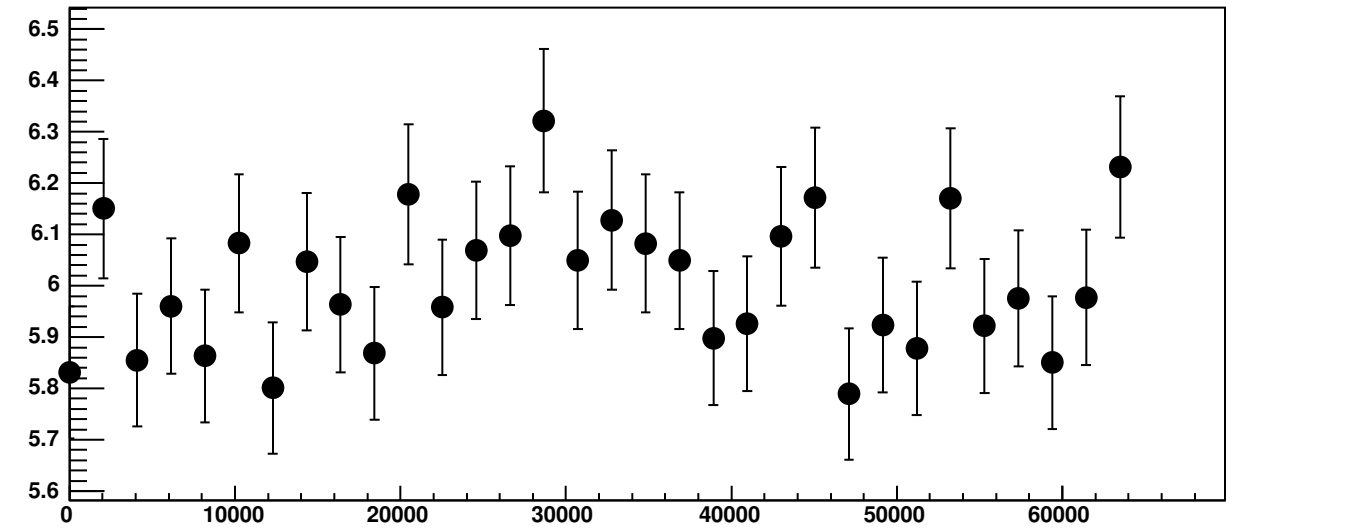

Chip 6, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

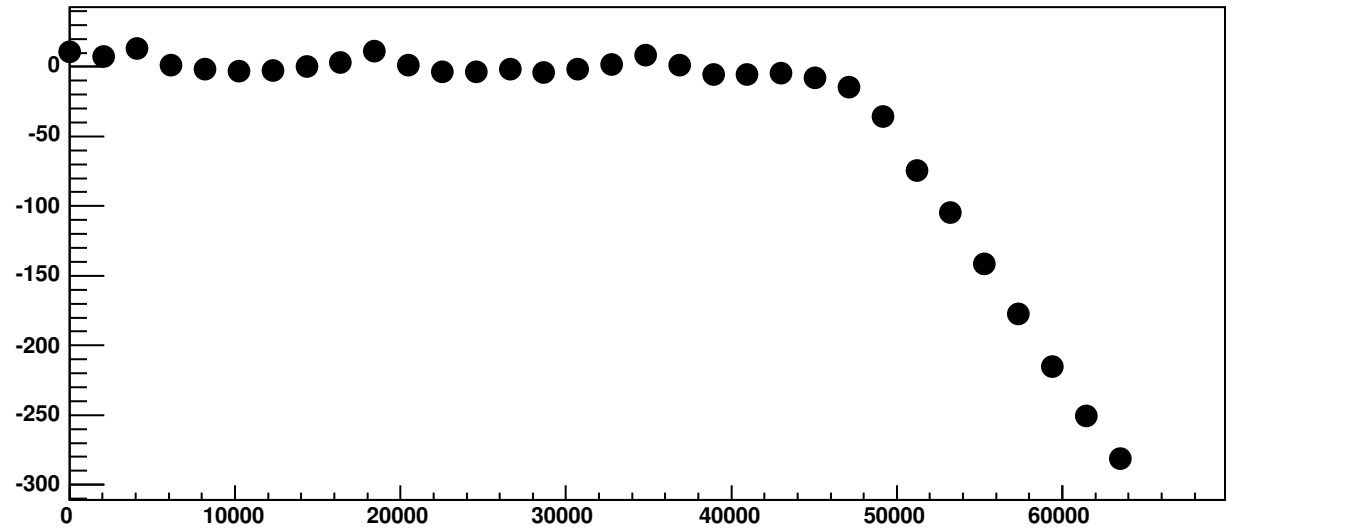
Chip 6, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

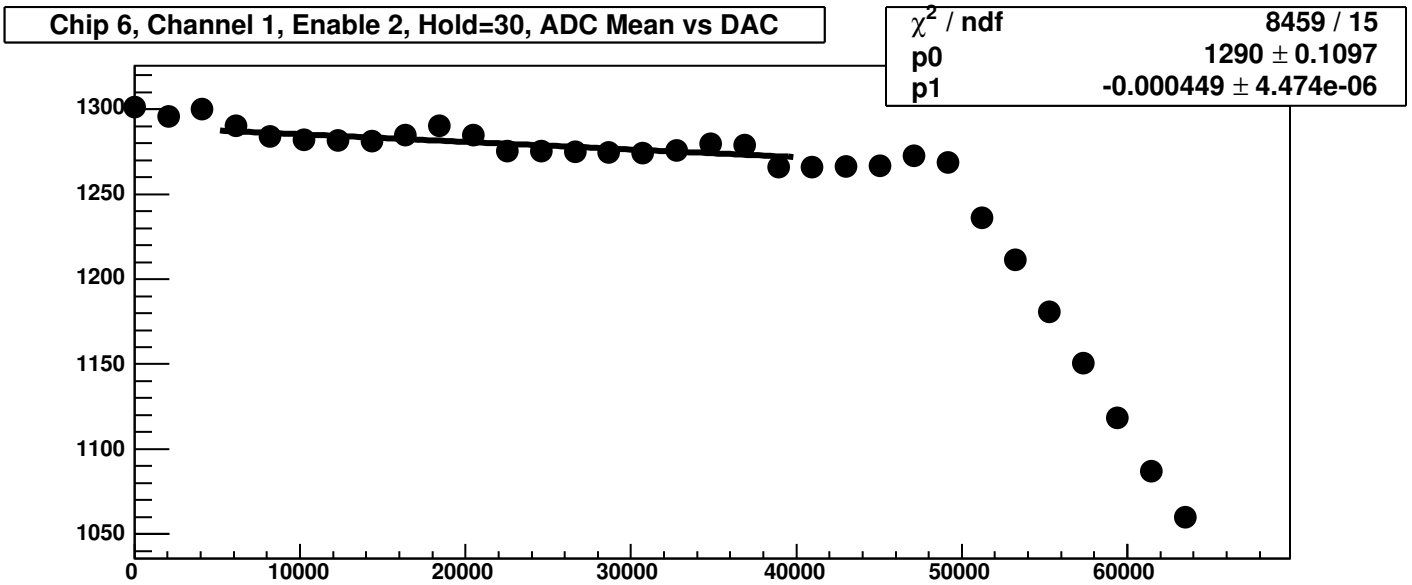


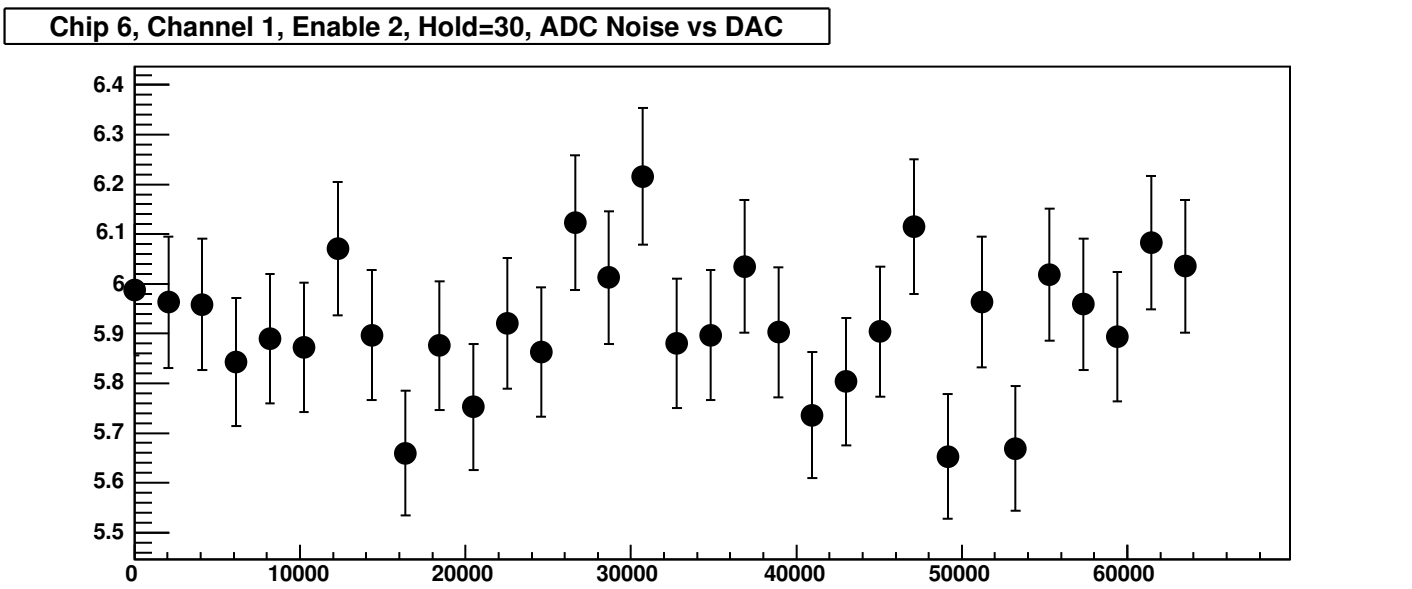
Chip 6, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC

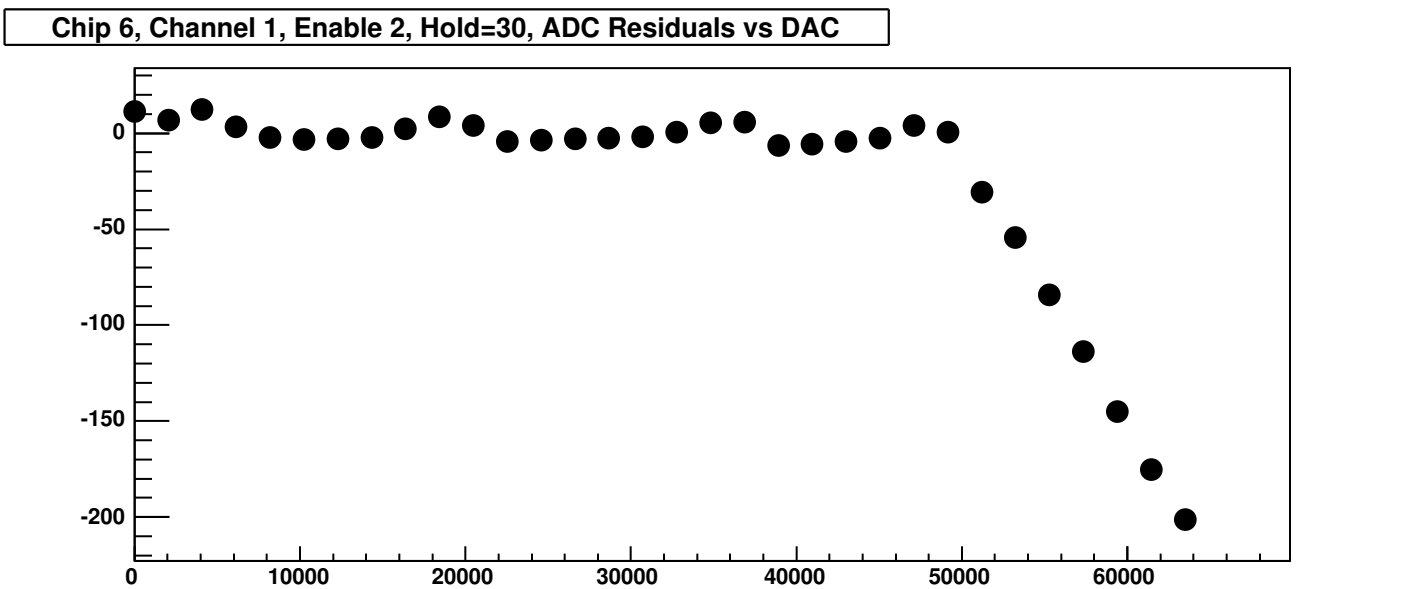


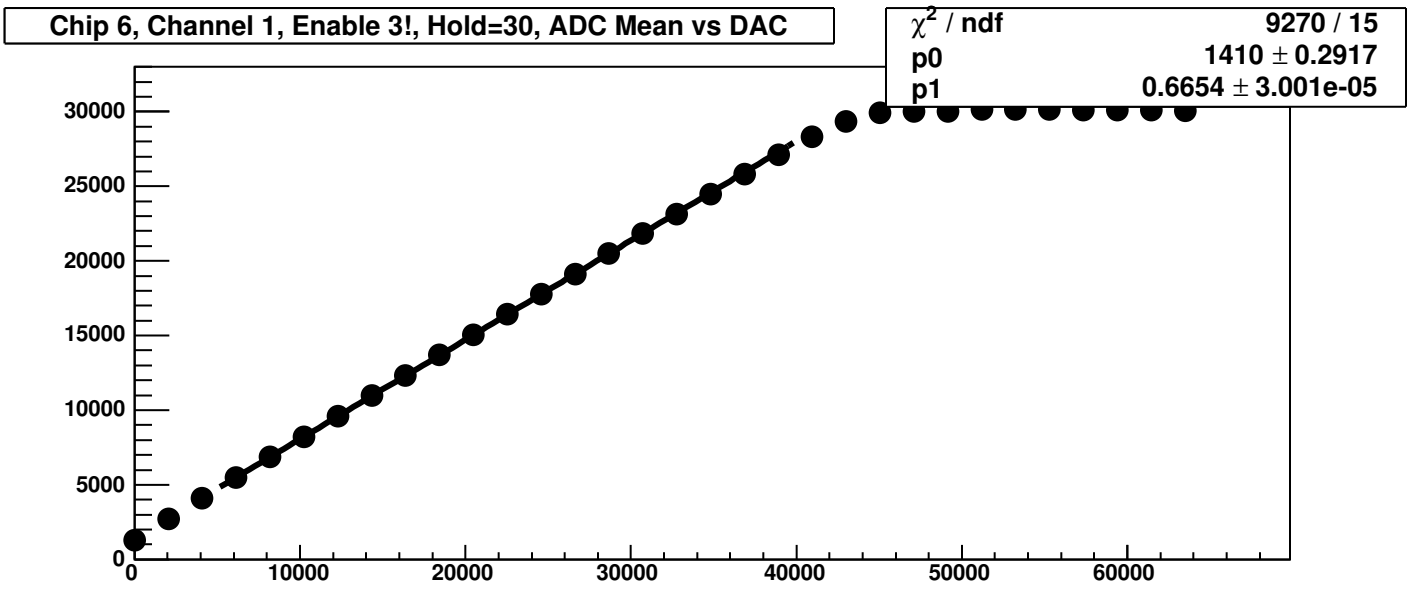
Chip 6, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

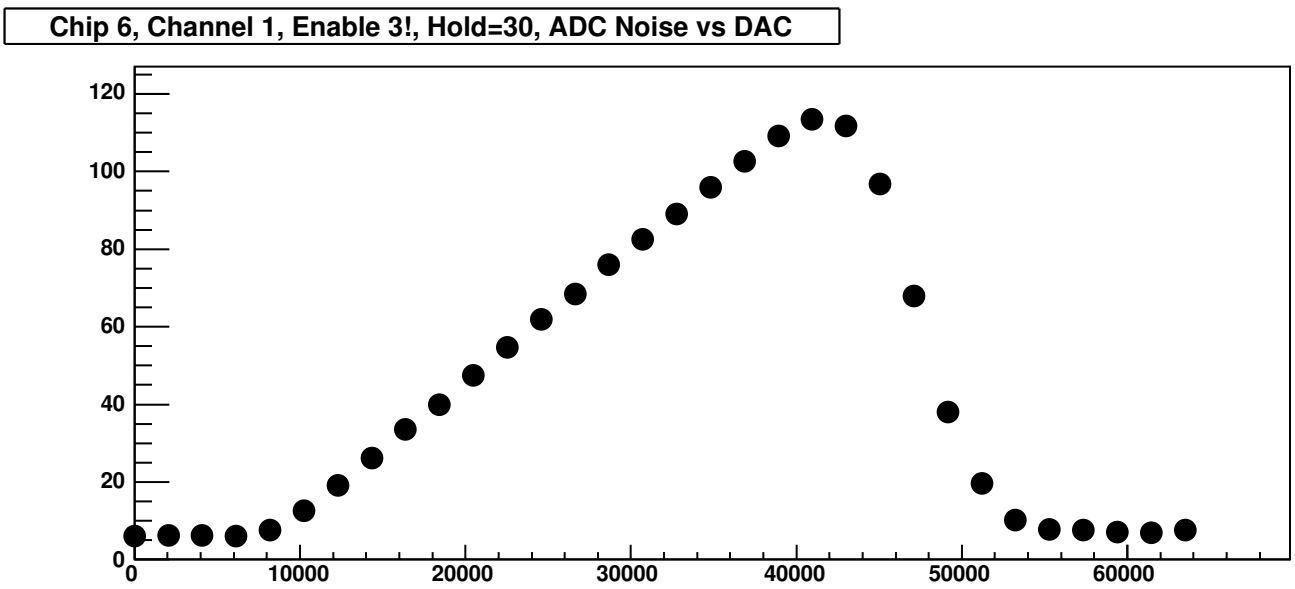


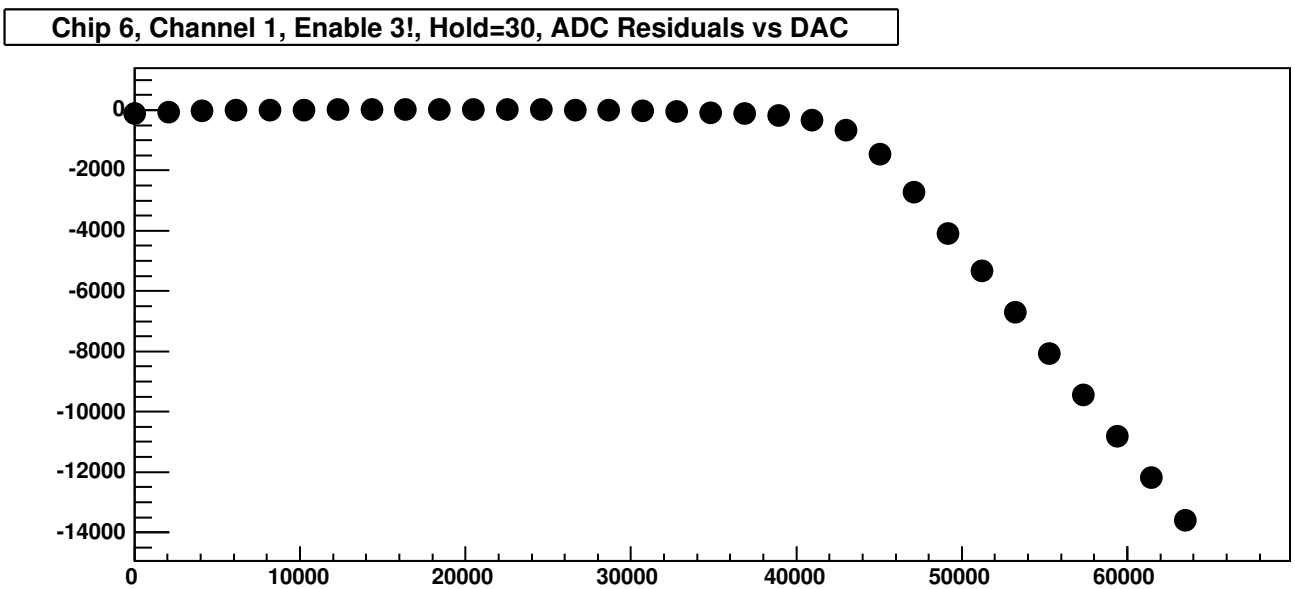

Chip 6, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8459 / 15
p0 1290 ± 0.1097
p1 -0.000449 ± 4.474e-06
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

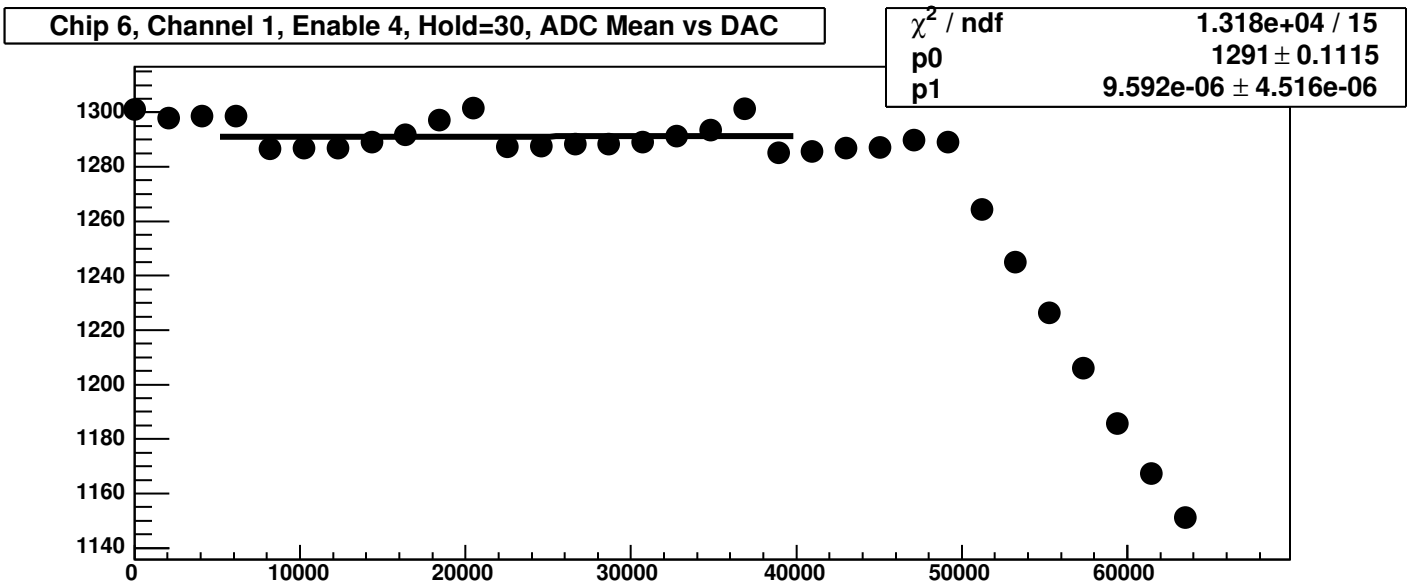

Chip 6, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

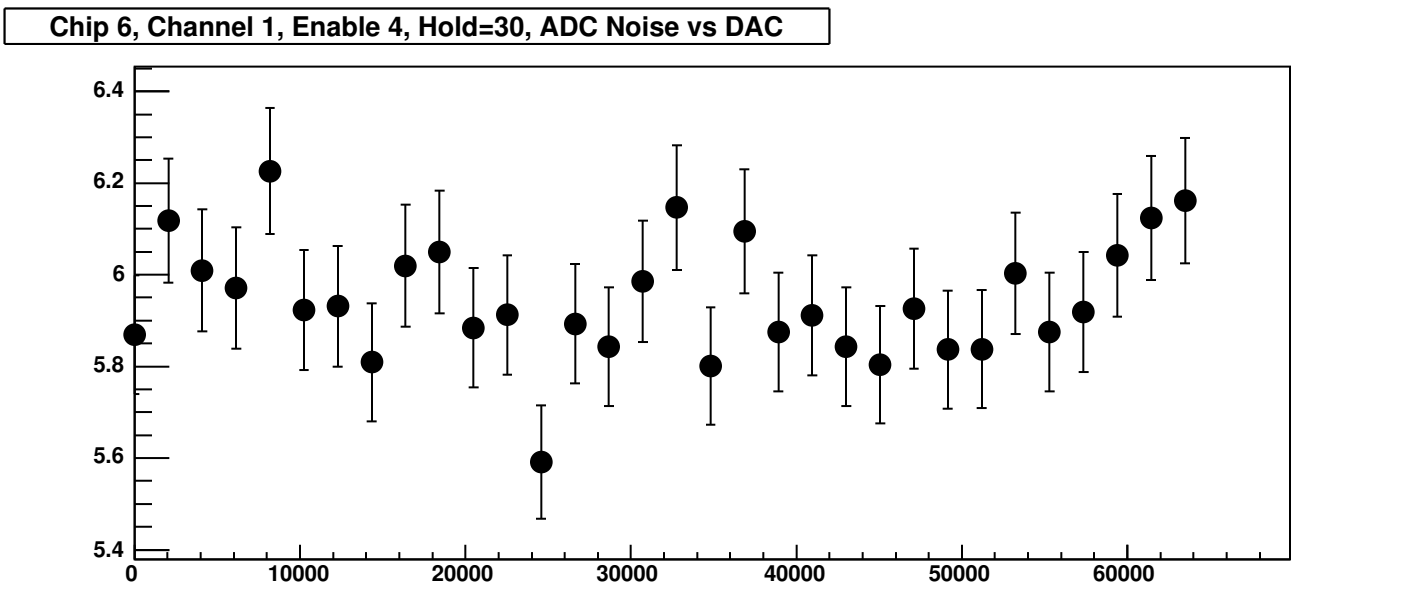

Chip 6, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

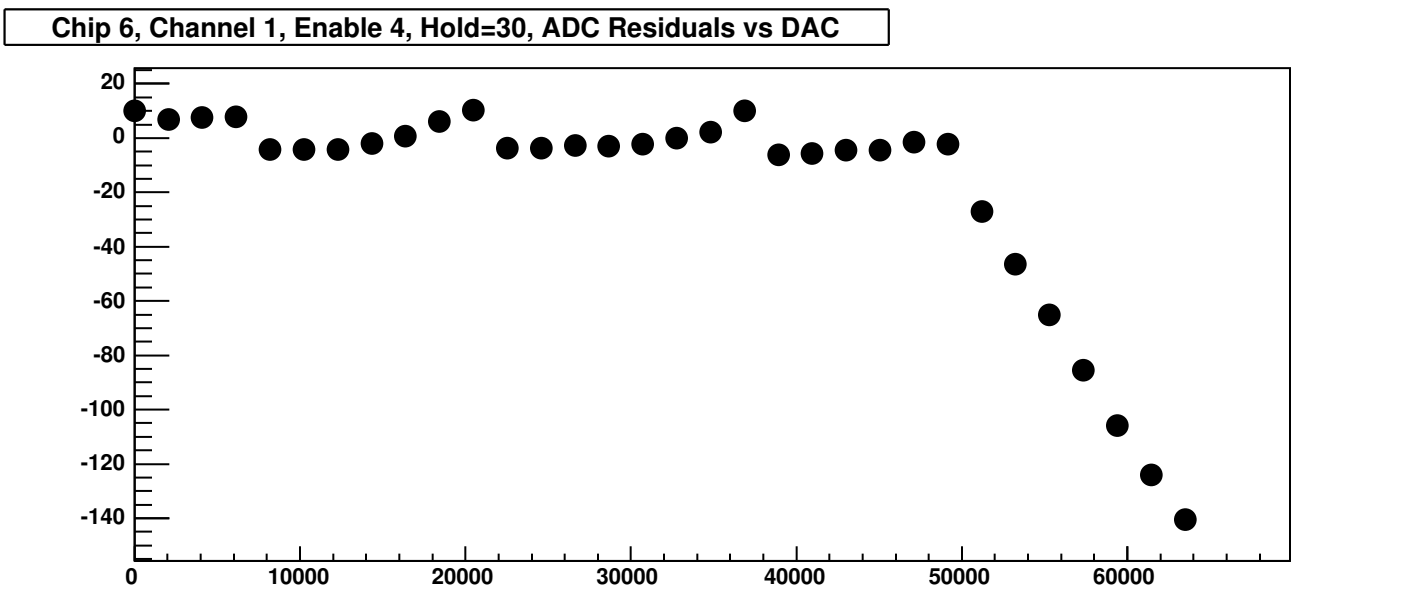

Chip 6, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 9270 / 15
p0 1410 ± 0.2917
p1 0.6654 ± 3.001e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

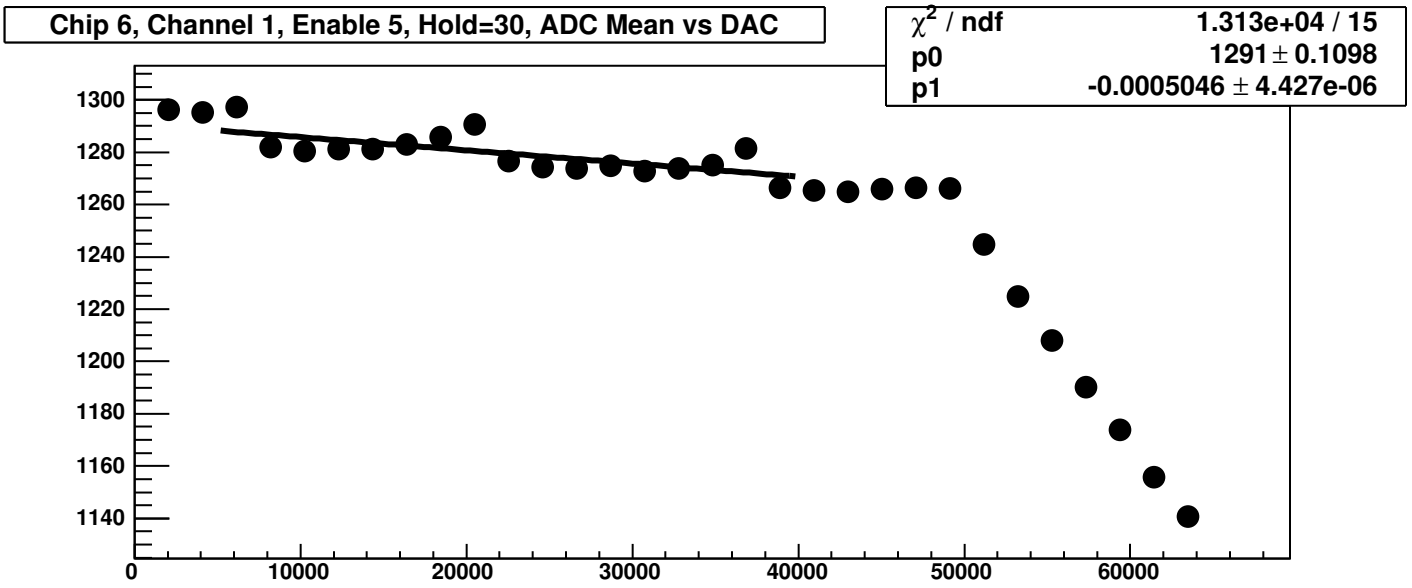

Chip 6, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

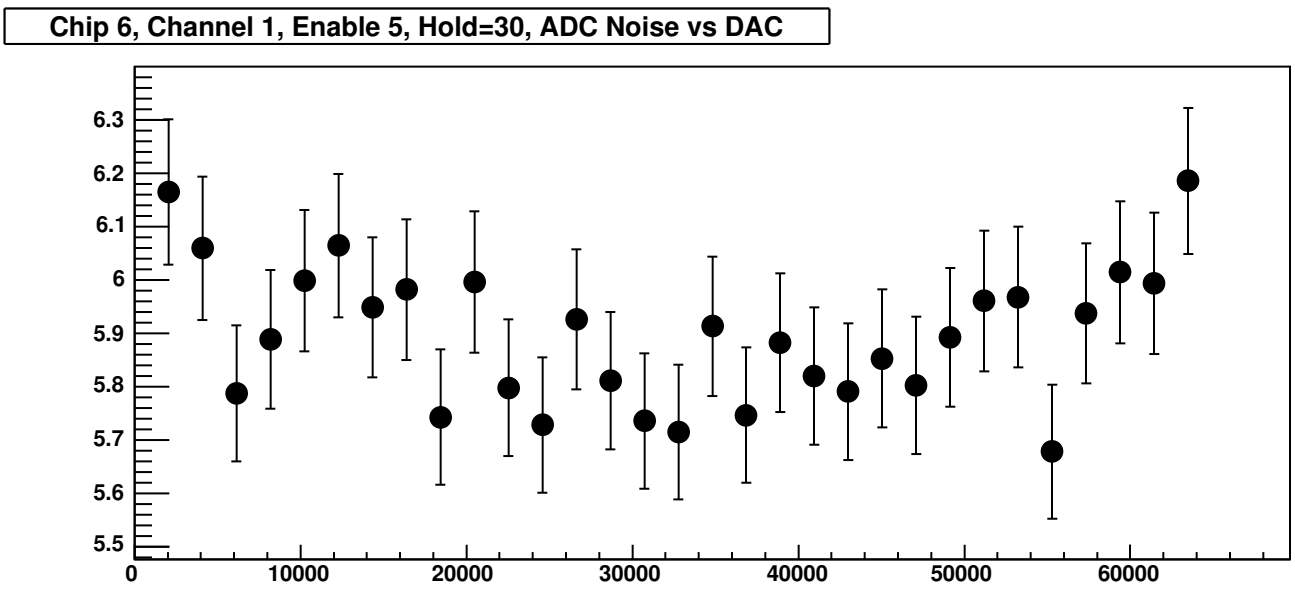

Chip 6, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

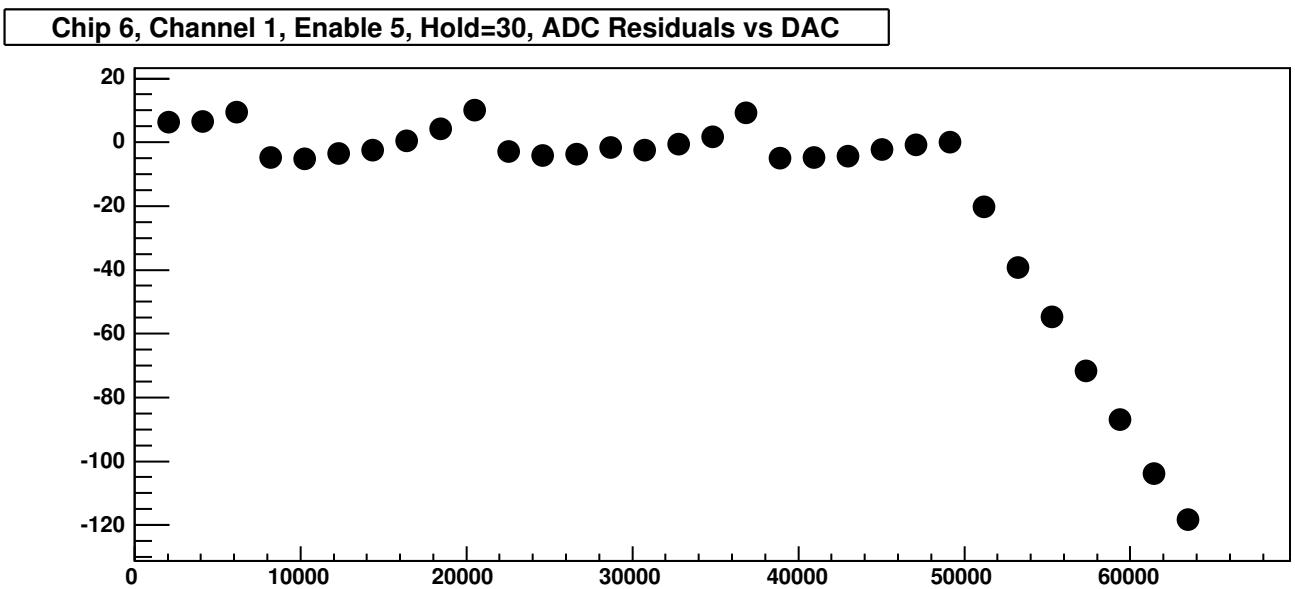

Chip 6, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.318e+04 / 15
p0 1291 ± 0.1115
p1 9.592e-06 ± 4.516e-06
1300
1280
1260
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

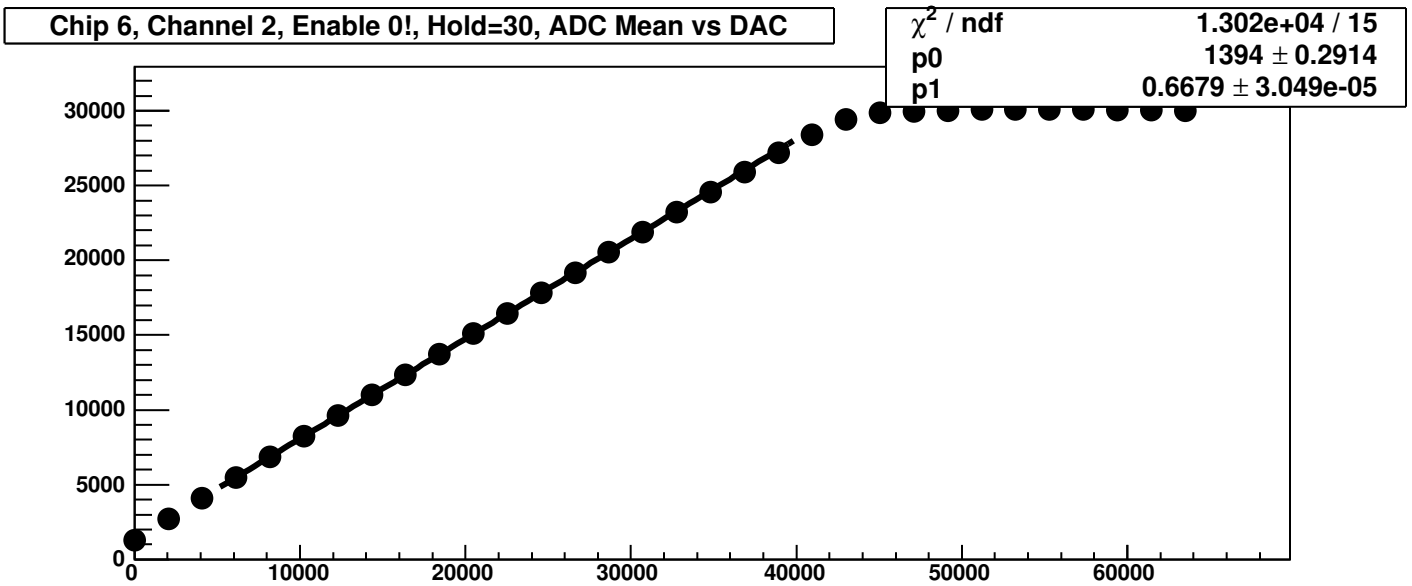

Chip 6, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

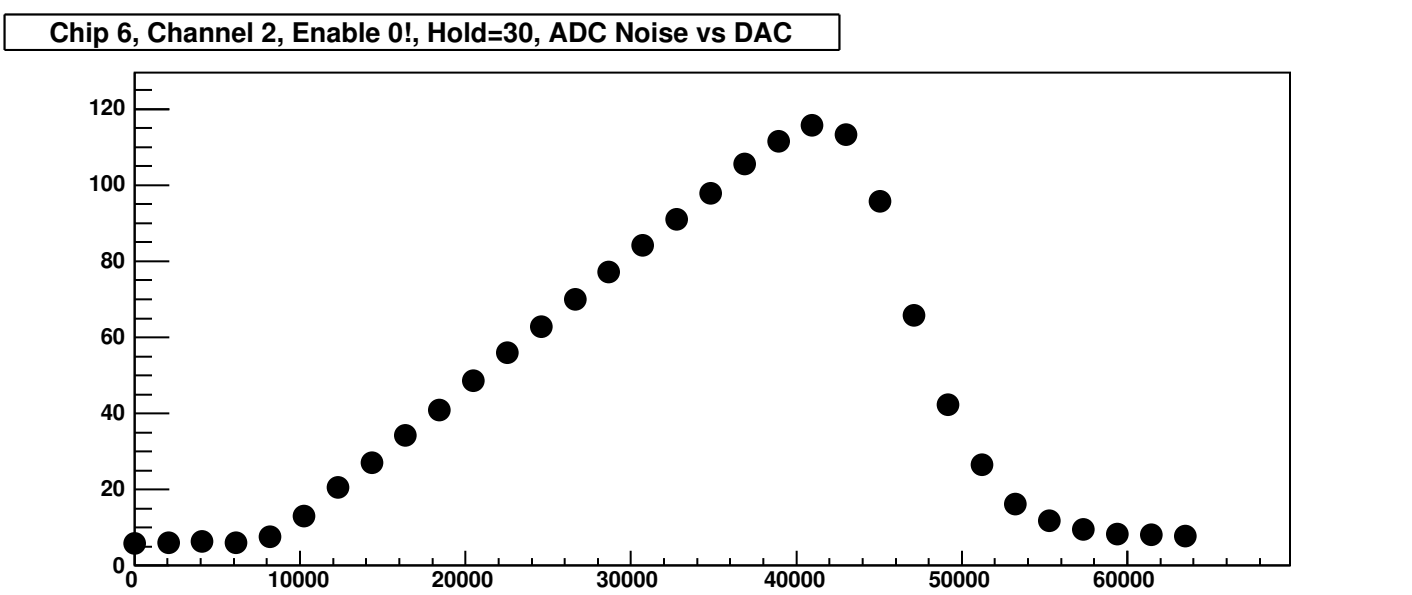

Chip 6, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

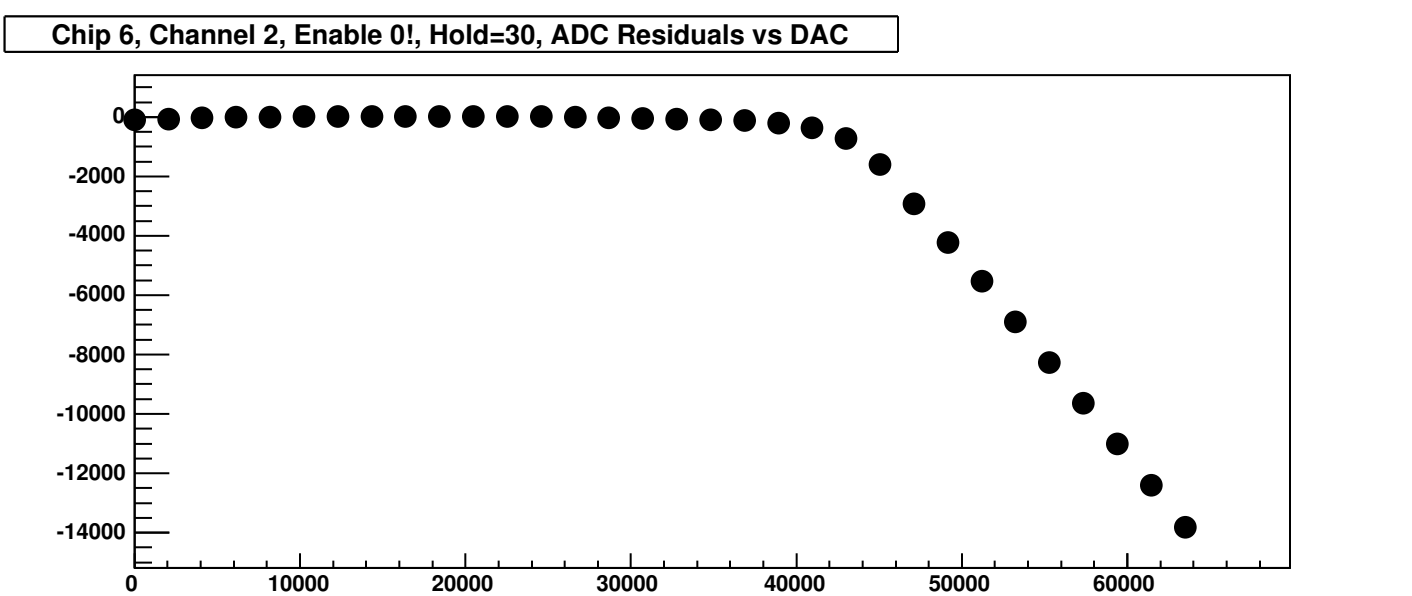

Chip 6, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.313e+04 / 15
p0 1291 ± 0.1098
p1 -0.0005046 ± 4.427e-06
1300
1280
1260
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

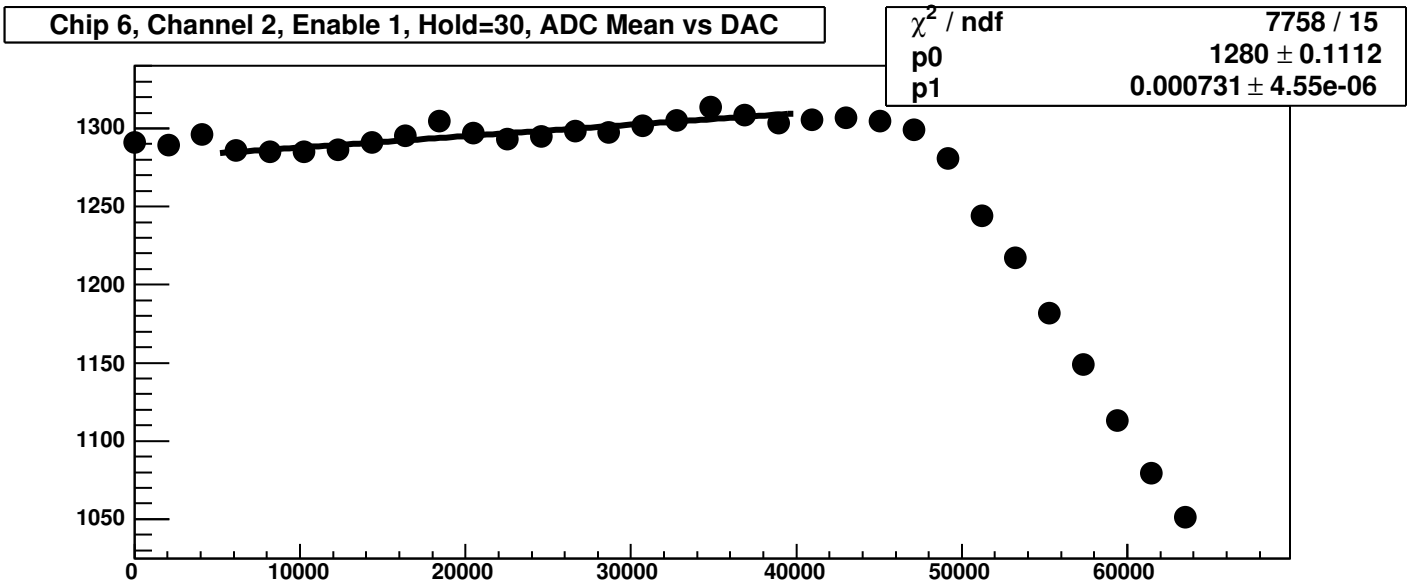

Chip 6, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

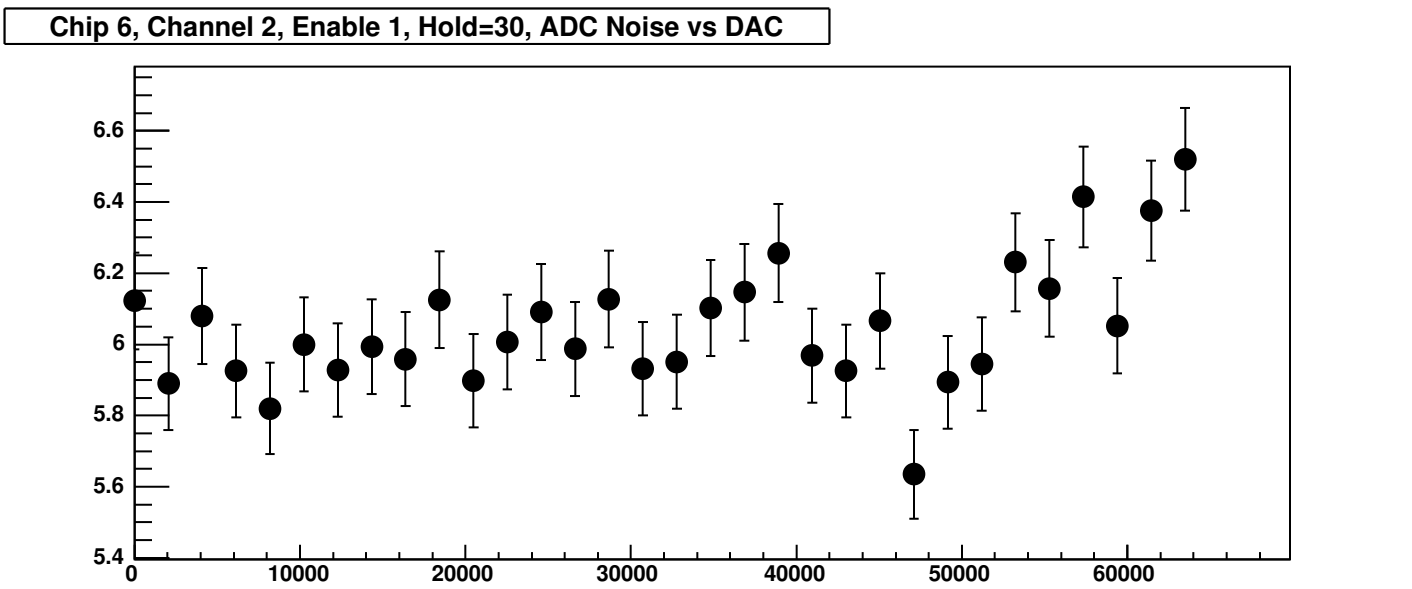

Chip 6, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

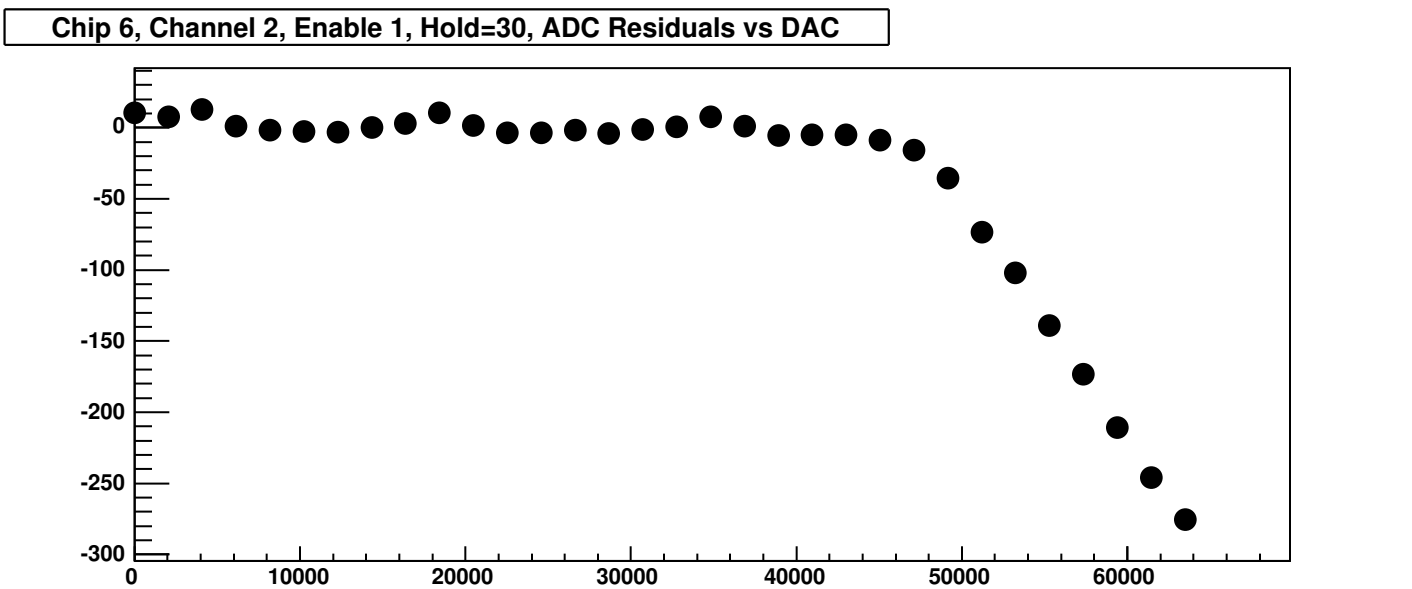

Chip 6, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.302e+04 / 15
p0 1394 ± 0.2914
p1 0.6679 ± 3.049e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

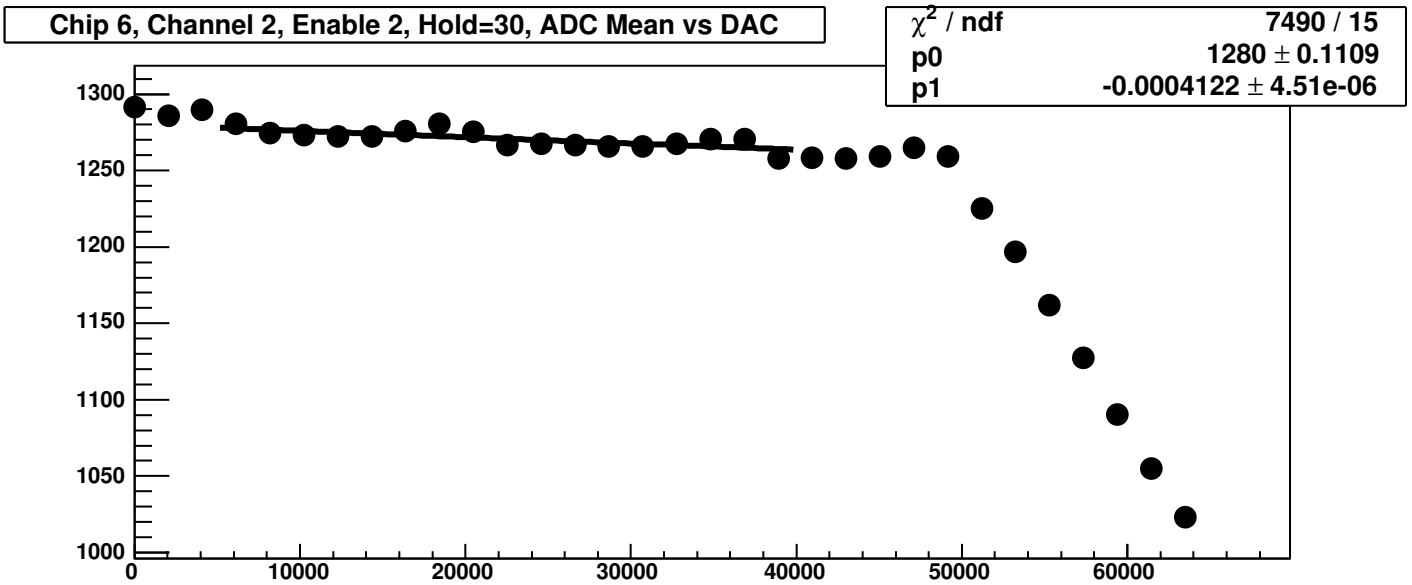

Chip 6, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

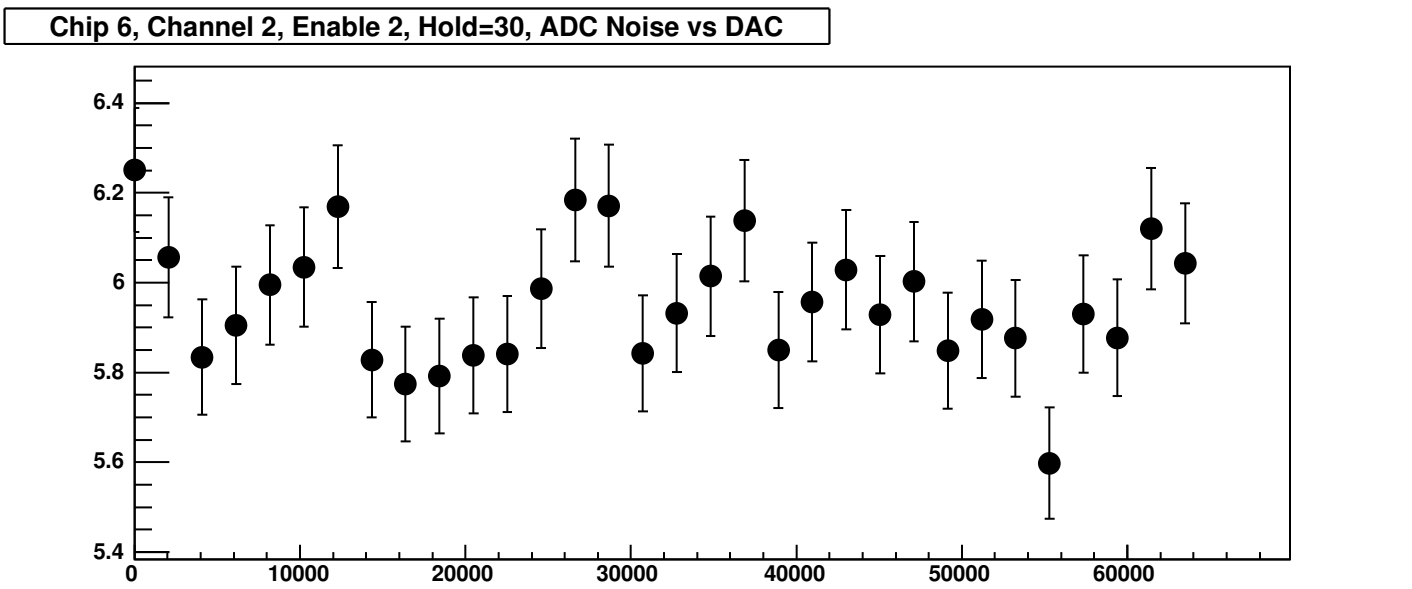

Chip 6, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

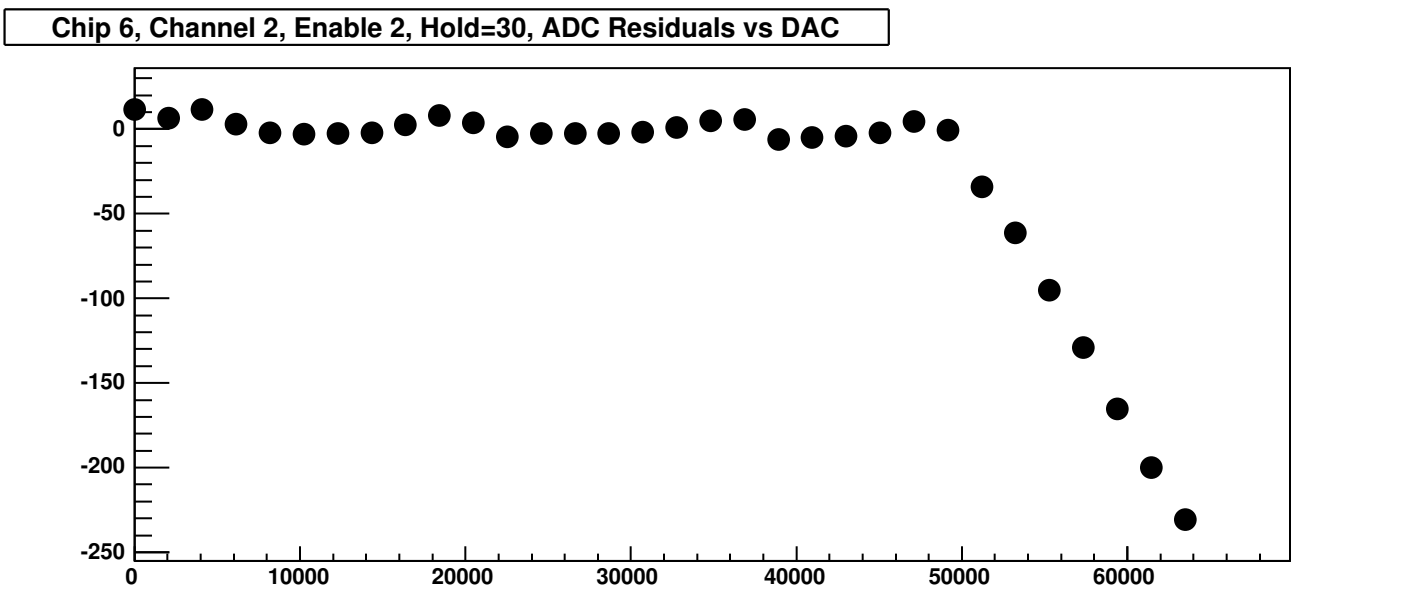

Chip 6, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7758 / 15
p0 1280 ± 0.1112
p1 0.000731 ± 4.55e-06
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

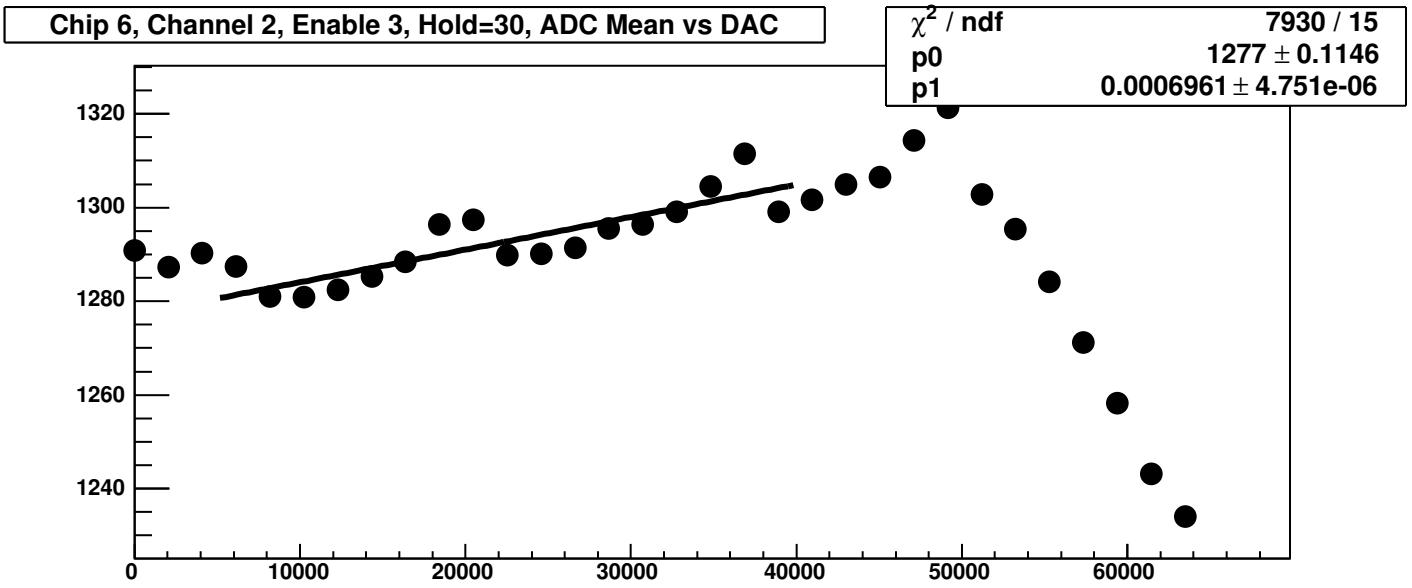

Chip 6, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

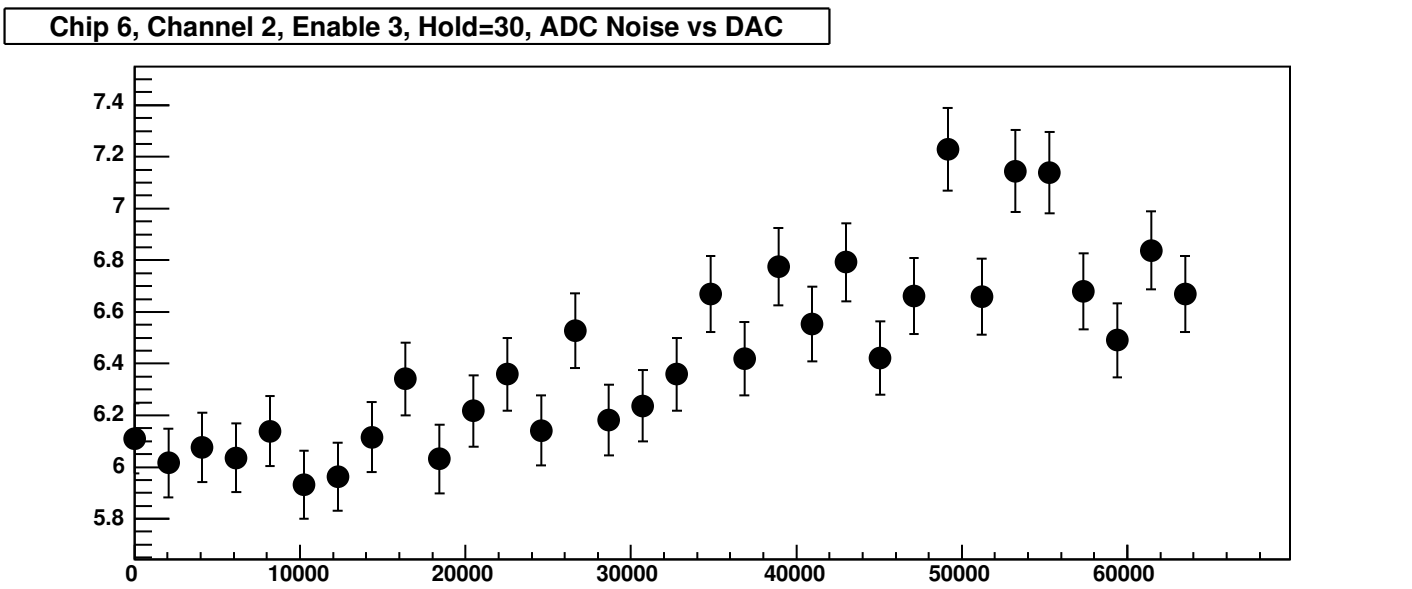

Chip 6, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

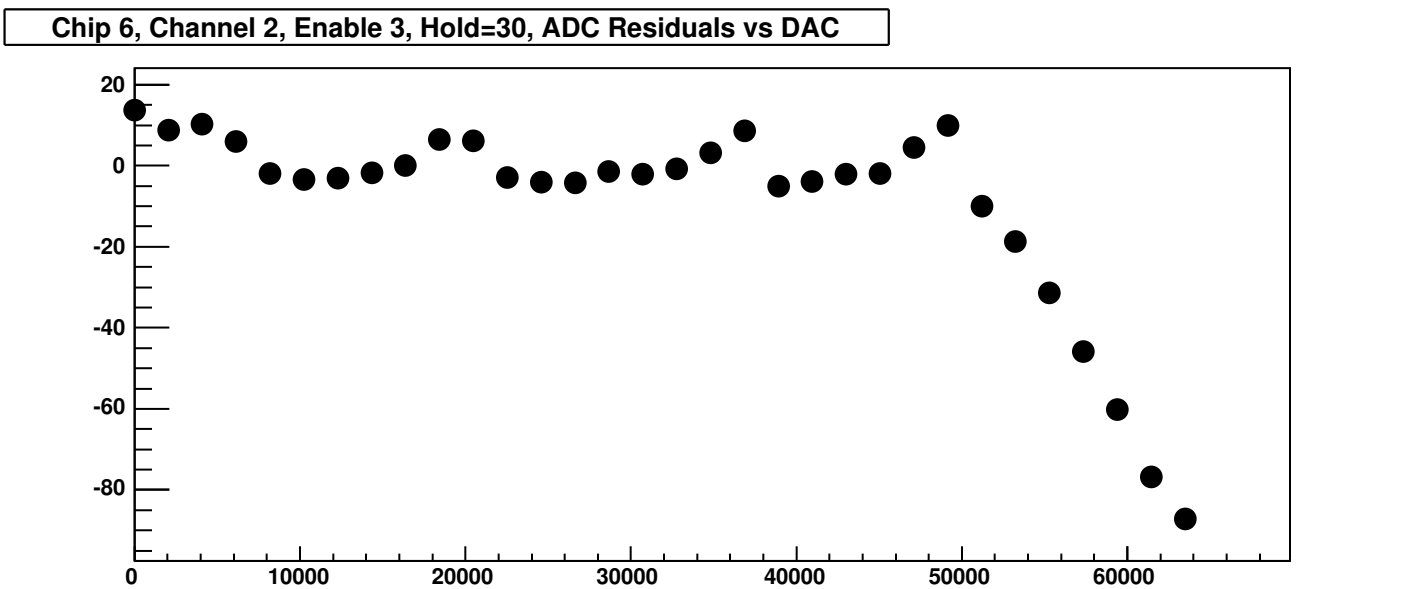

Chip 6, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7490 / 15
p0 1280 ± 0.1109
p1 -0.0004122 ± 4.51e-06
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

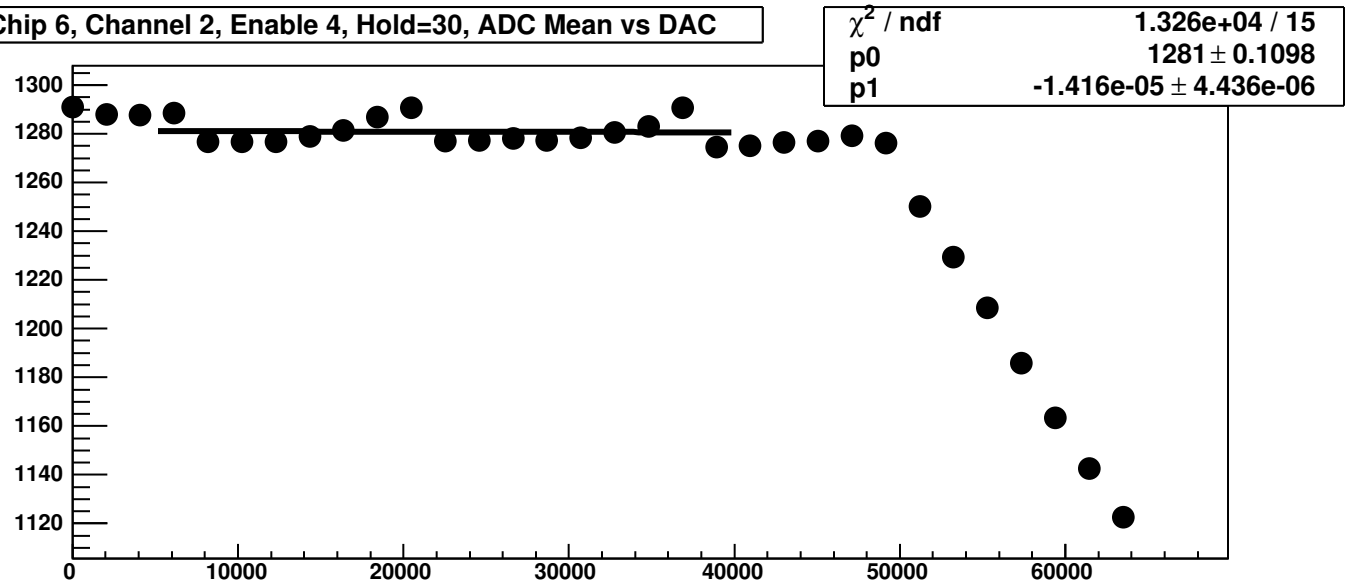

Chip 6, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7930 / 15
p0 1277 ± 0.1146
p1 0.0006961 ± 4.751e-06
1320
1300
1280
1260
1240
0
10000
20000
30000
40000
50000
60000

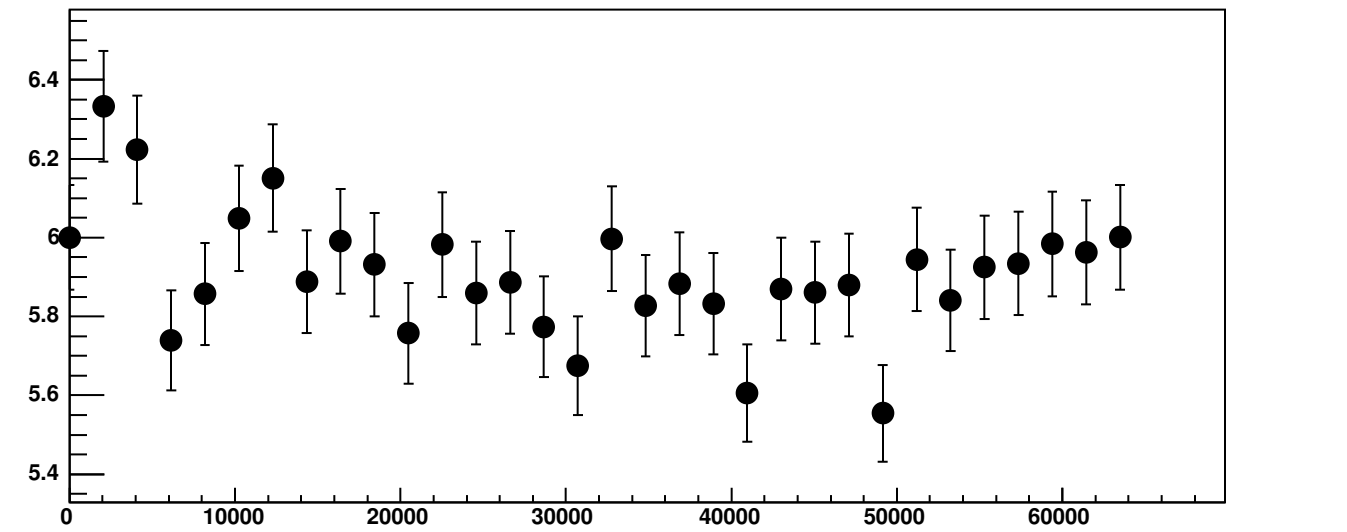

Chip 6, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

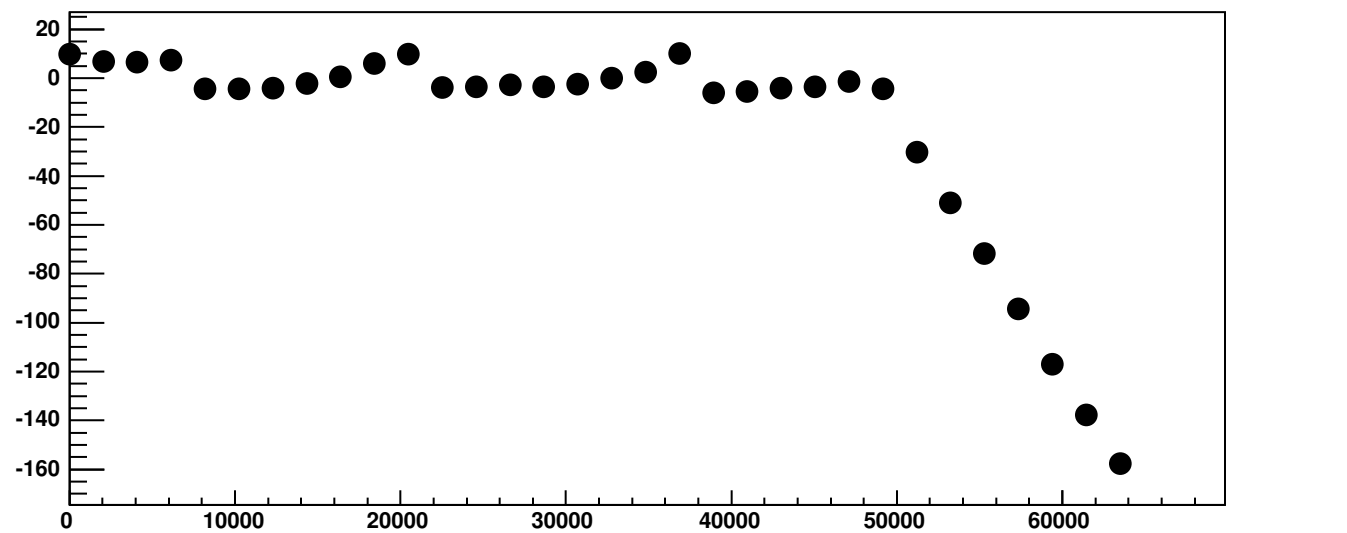
Chip 6, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

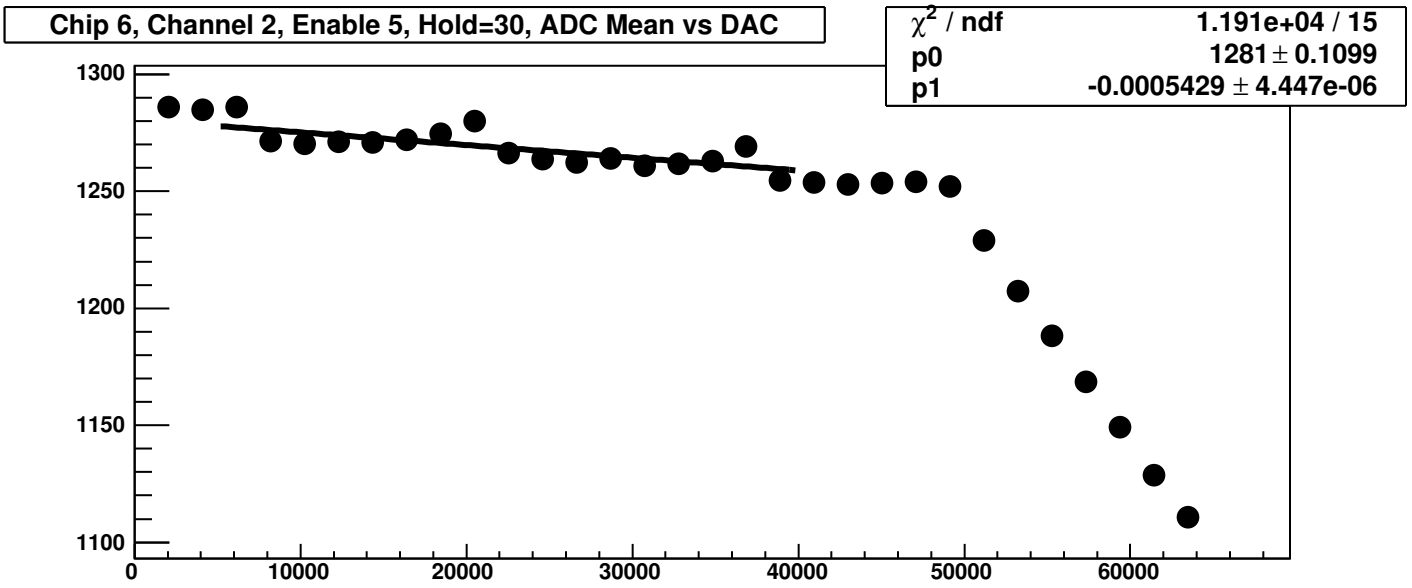


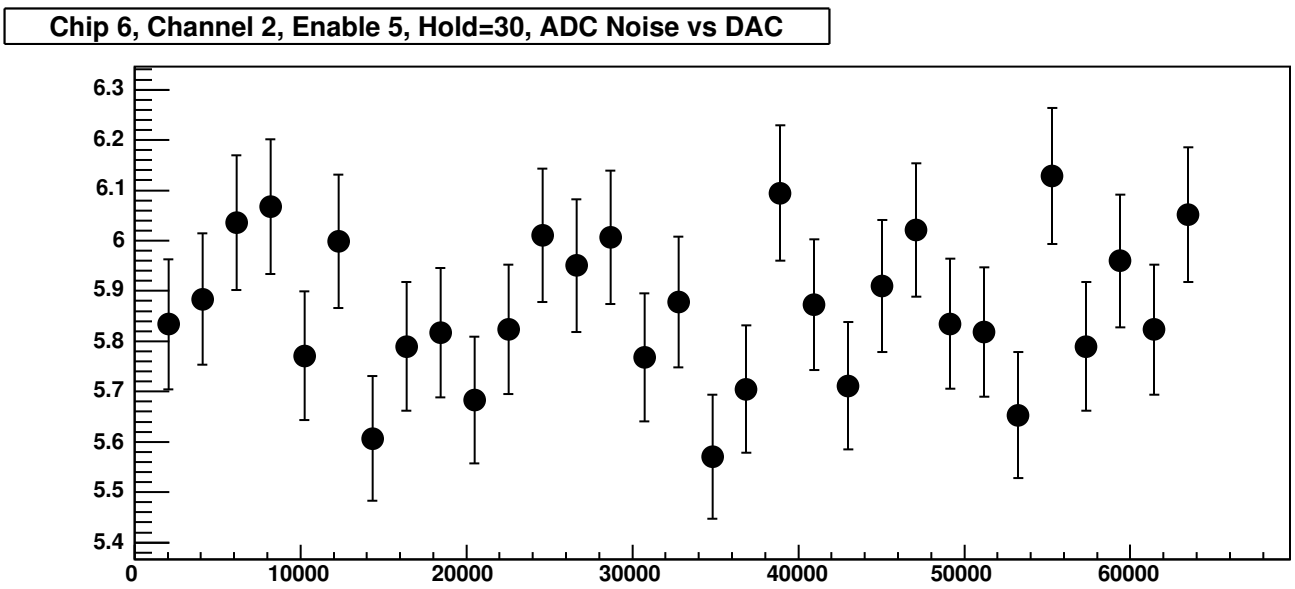
Chip 6, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC

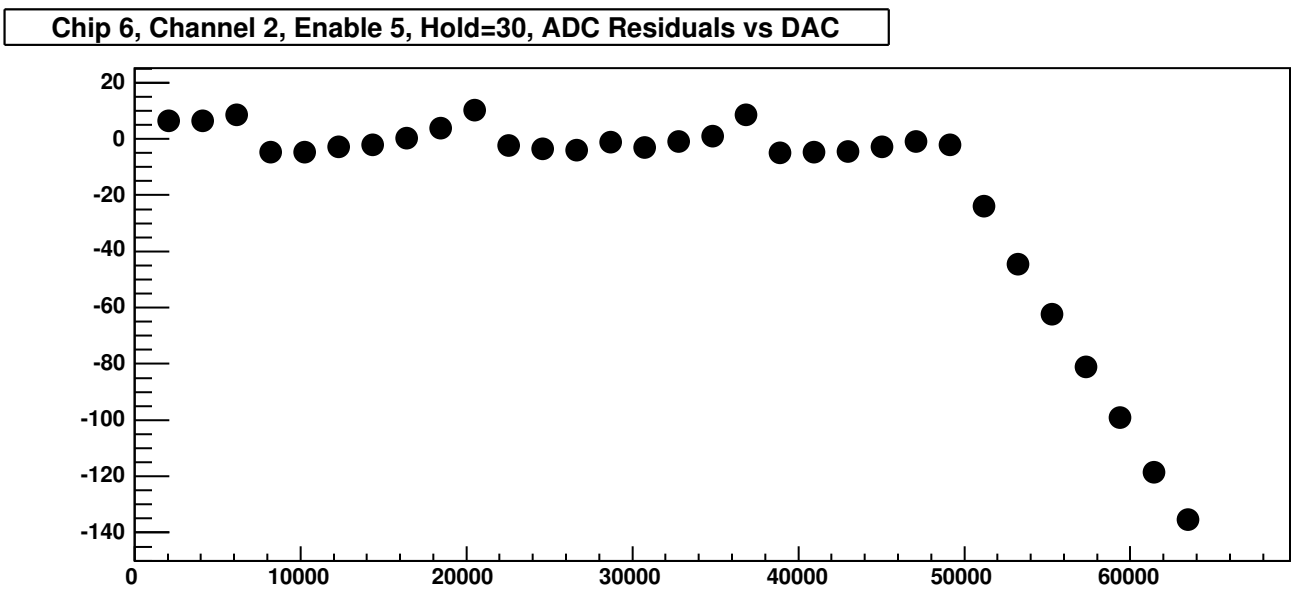


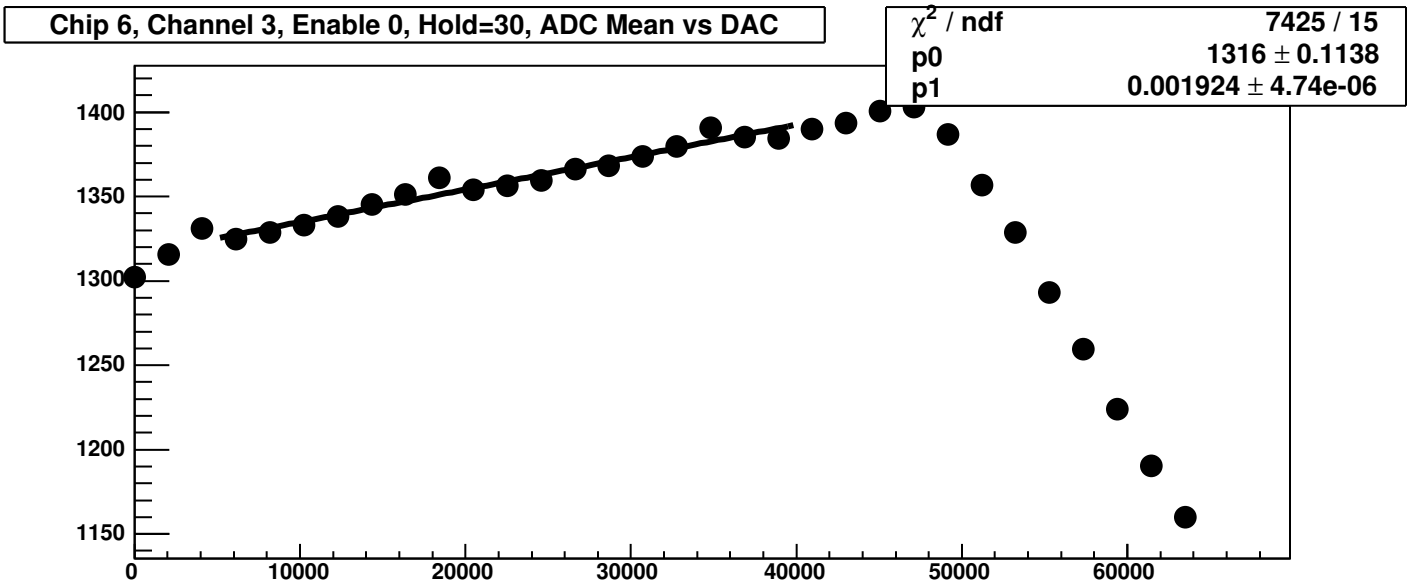
Chip 6, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

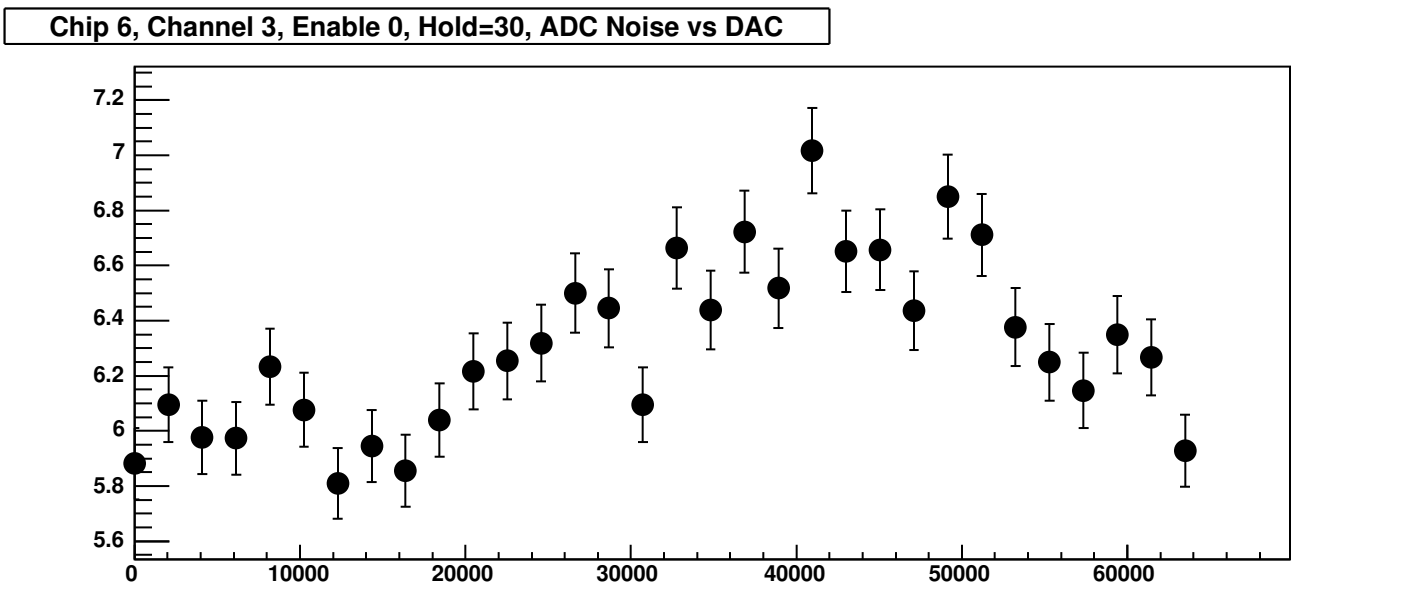


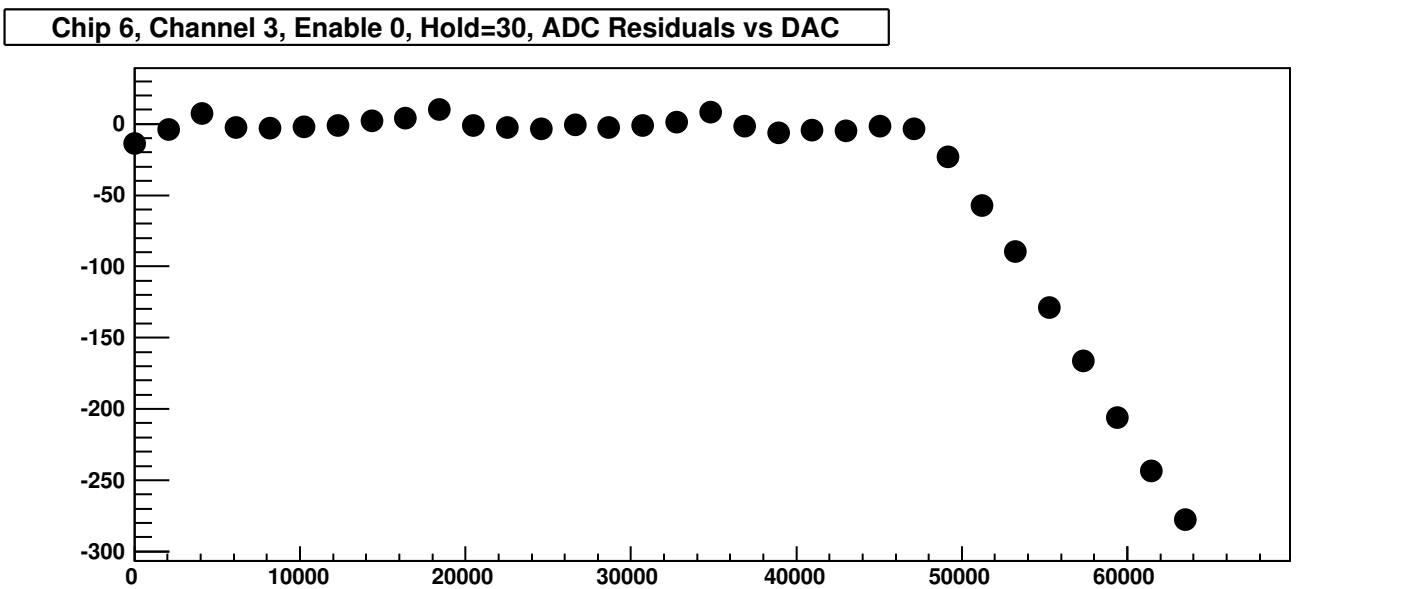

Chip 6, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.191e+04 / 15
p0 1281 ± 0.1099
p1 -0.0005429 ± 4.447e-06
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

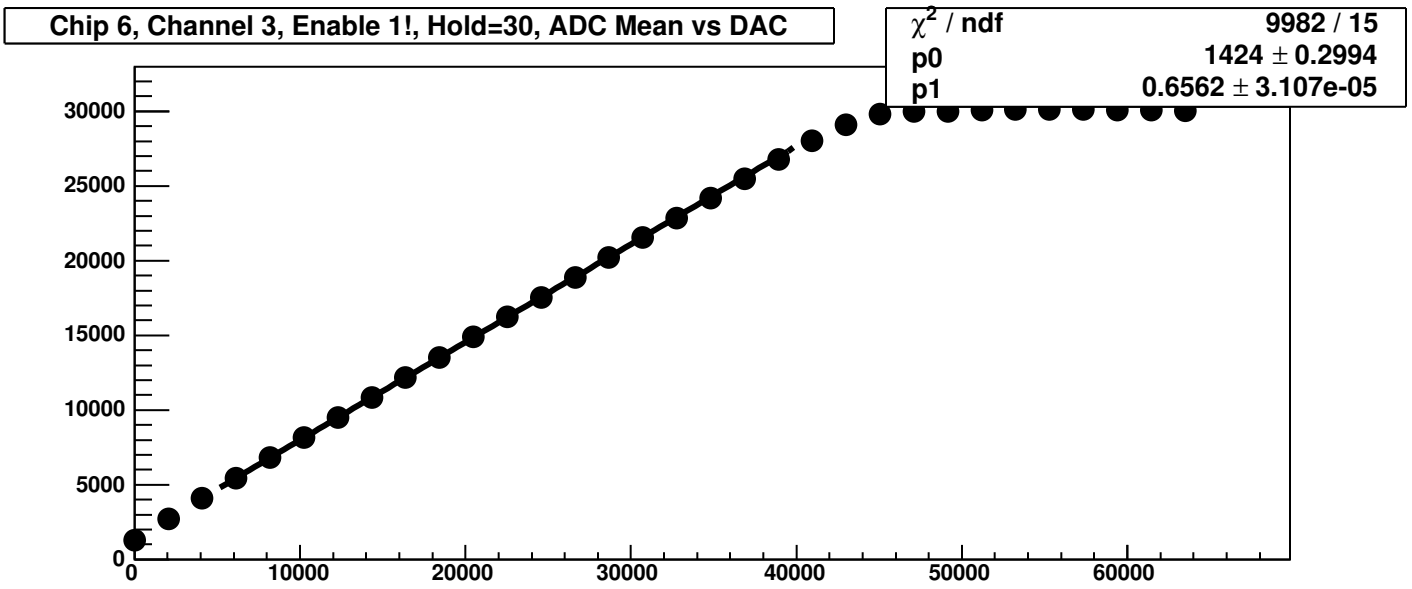

Chip 6, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

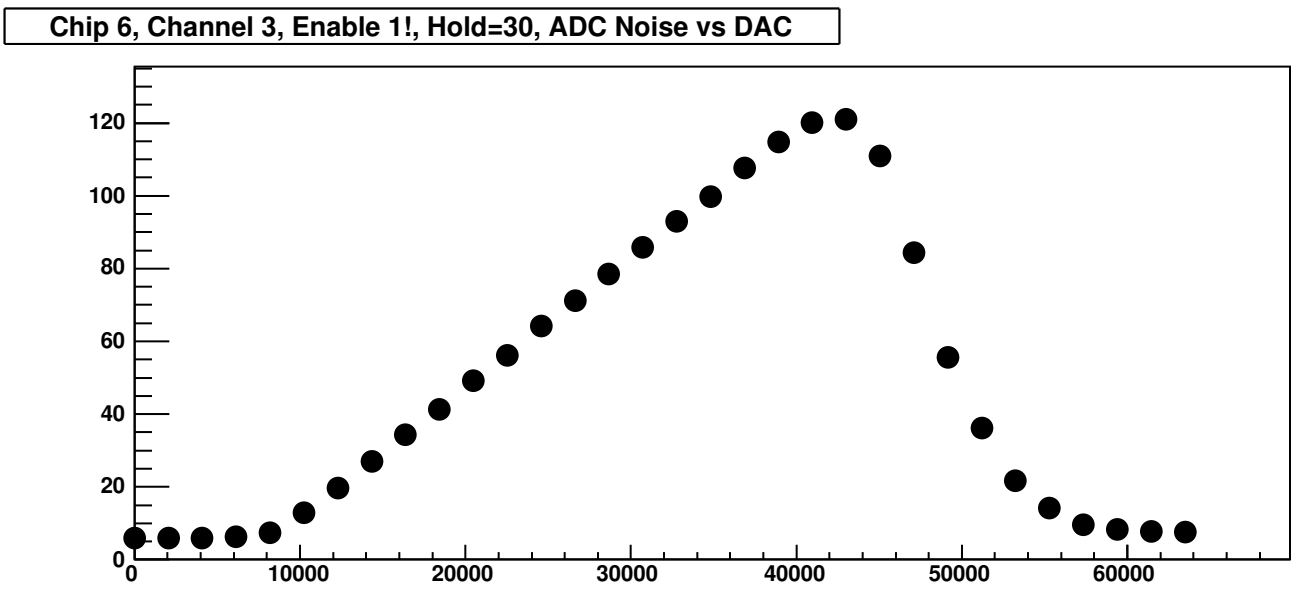

Chip 6, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

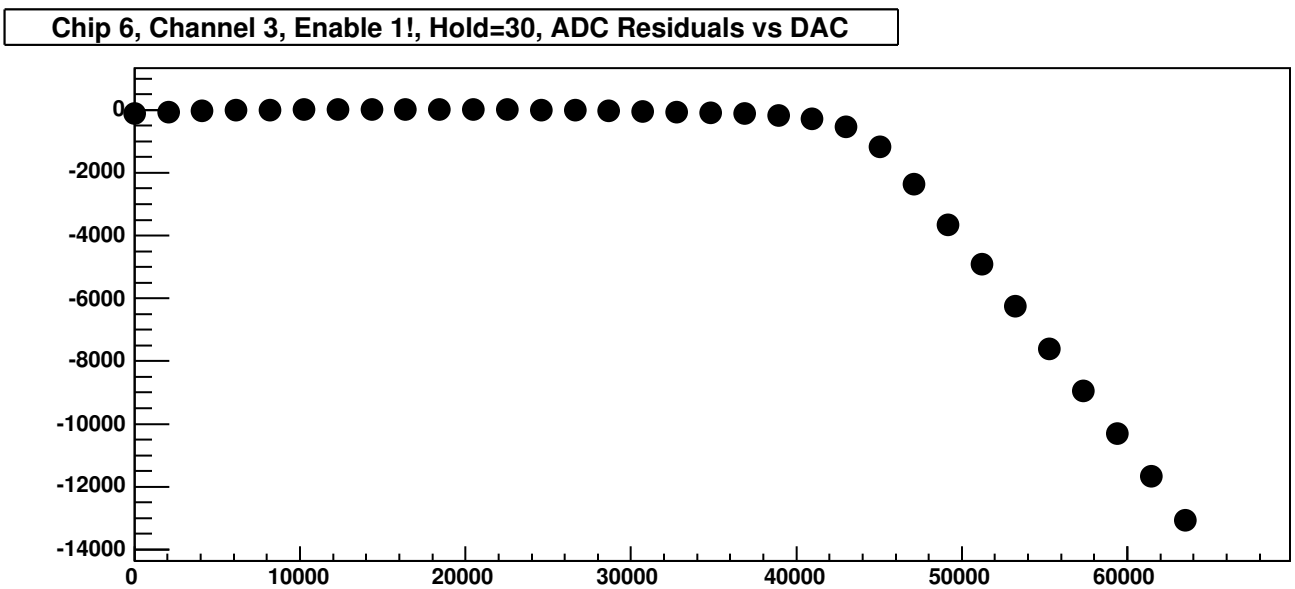

Chip 6, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7425 / 15
p0 1316 ± 0.1138
p1 0.001924 ± 4.74e-06
1400
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

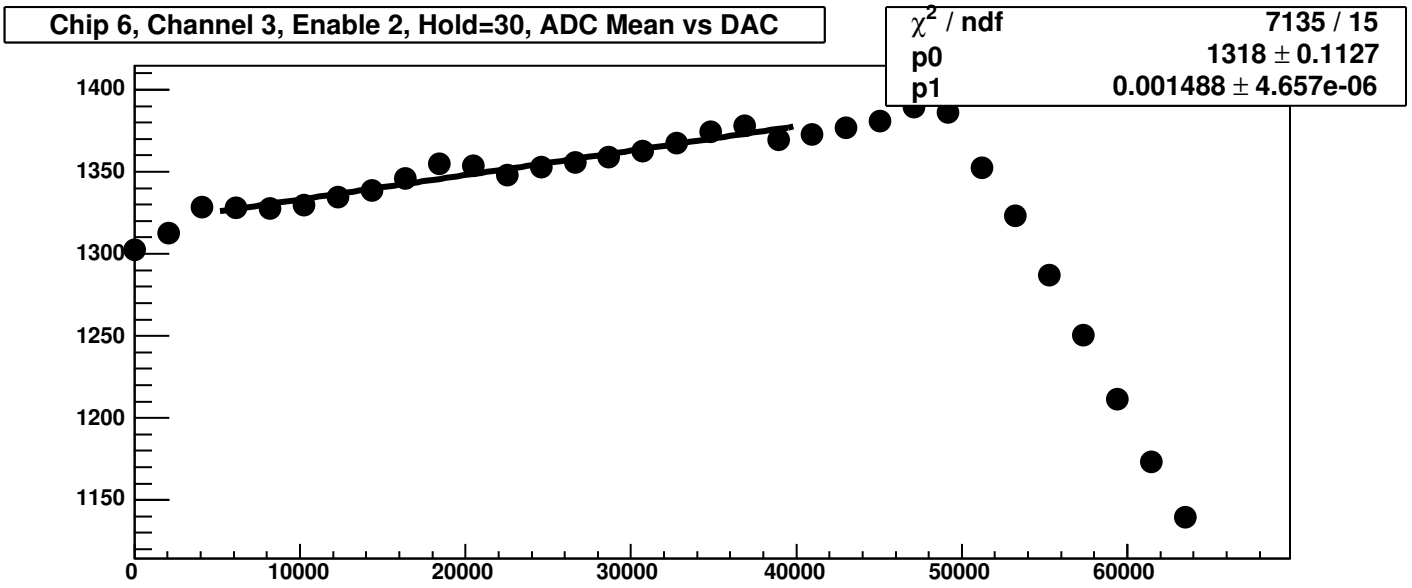

Chip 6, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

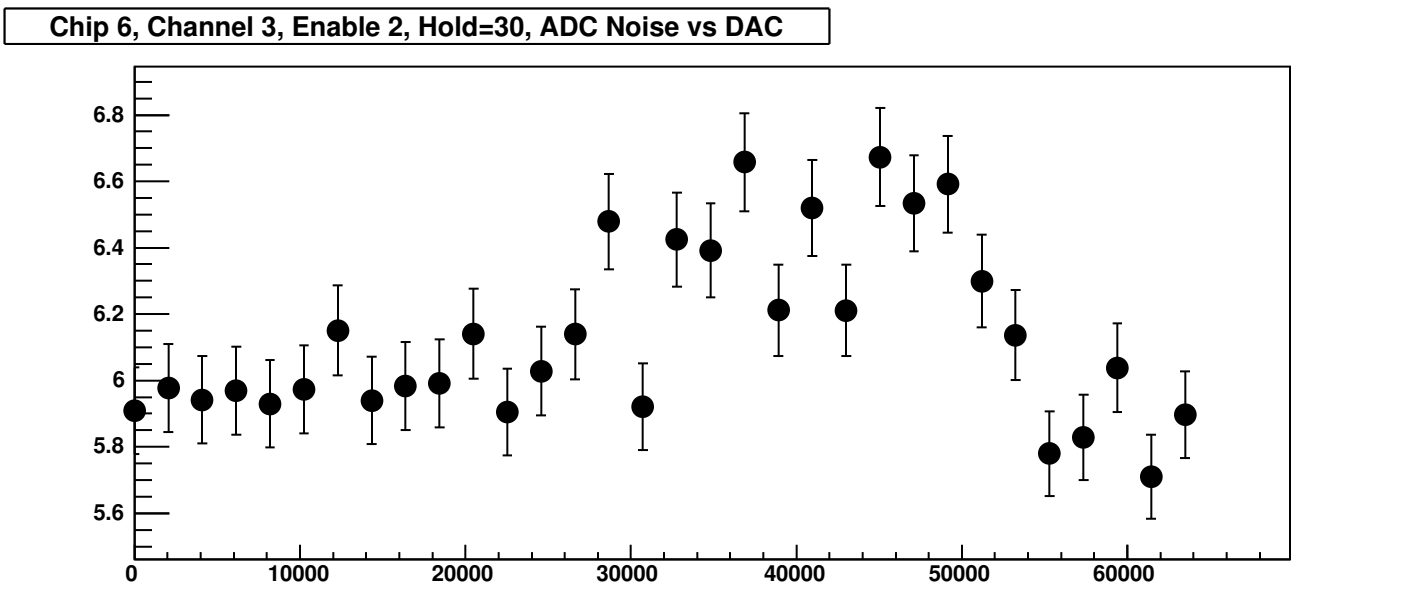

Chip 6, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

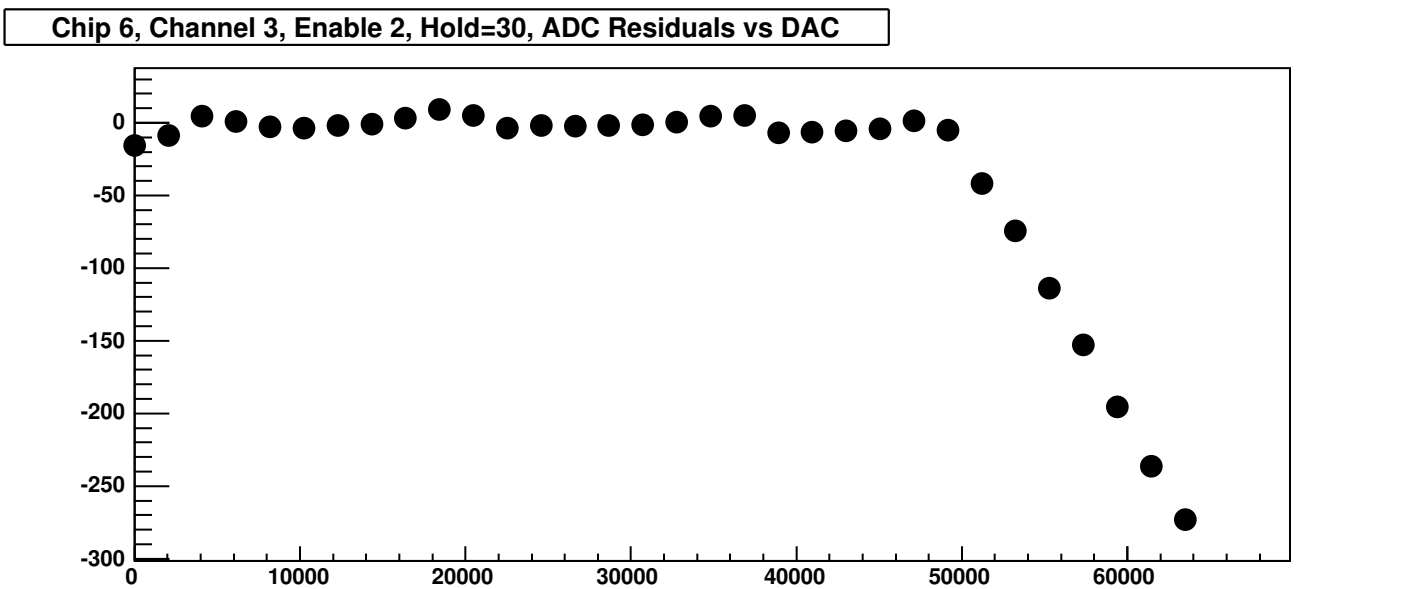

Chip 6, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 9982 / 15
p0 1424 ± 0.2994
p1 0.6562 ± 3.107e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

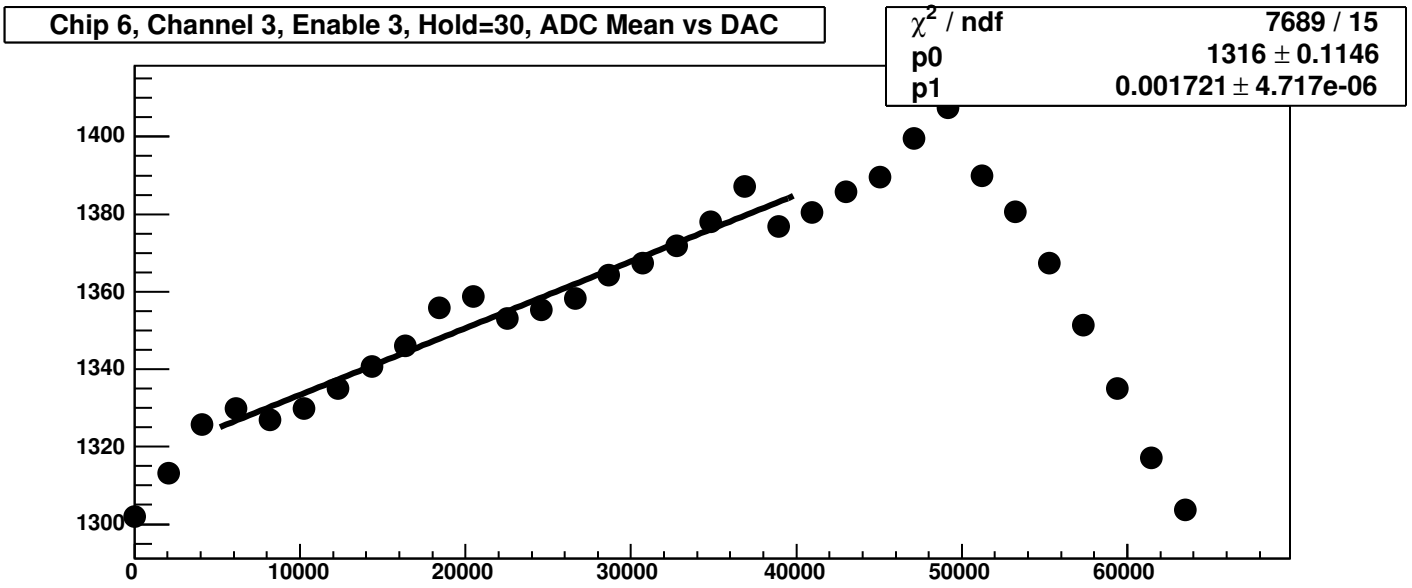

Chip 6, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

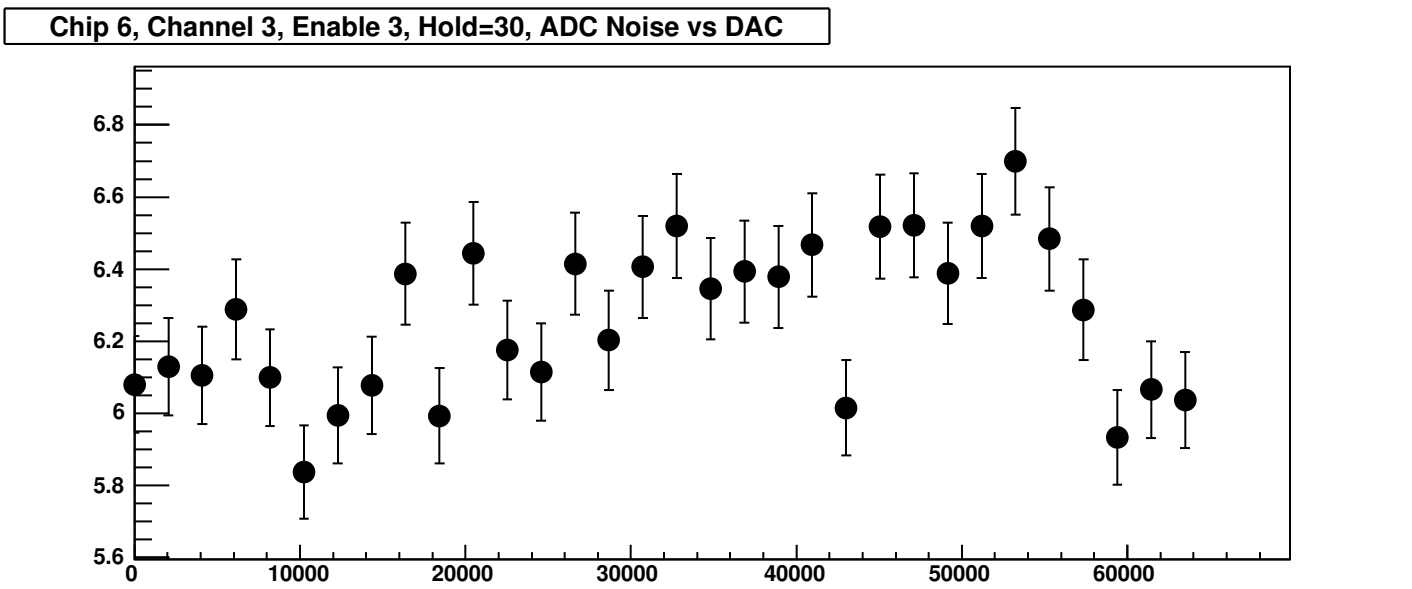

Chip 6, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

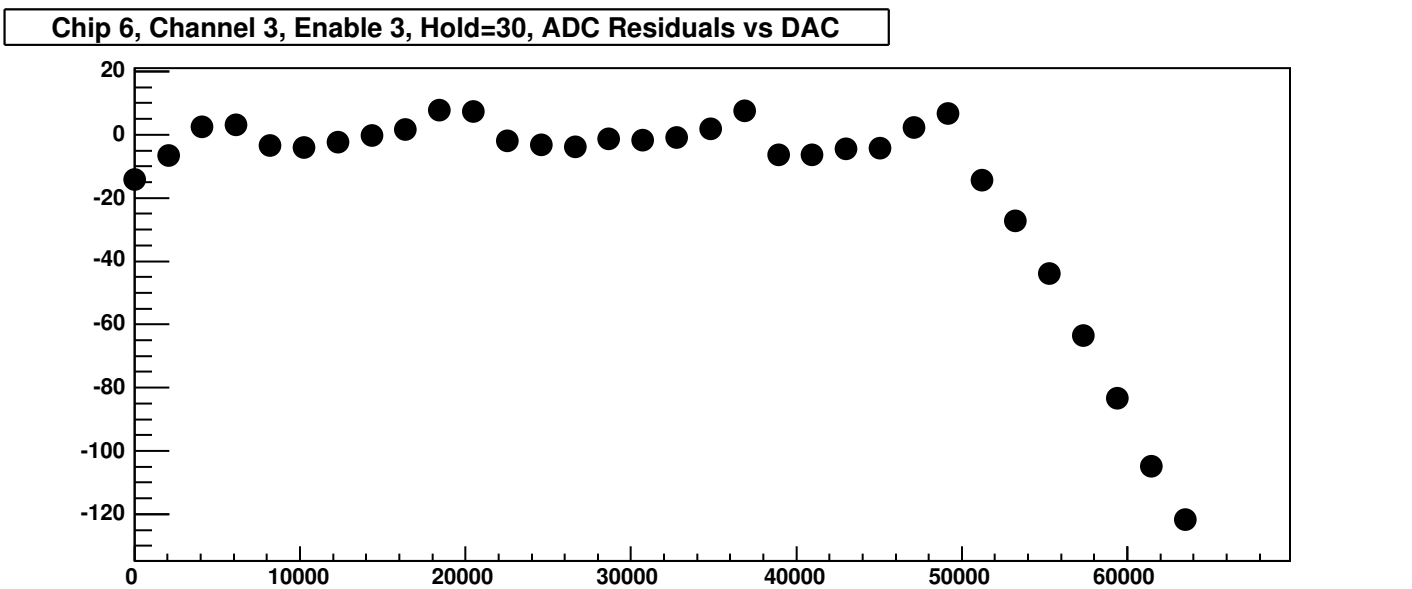

Chip 6, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7135 / 15
p0 1318 ± 0.1127
p1 0.001488 ± 4.657e-06
1400
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

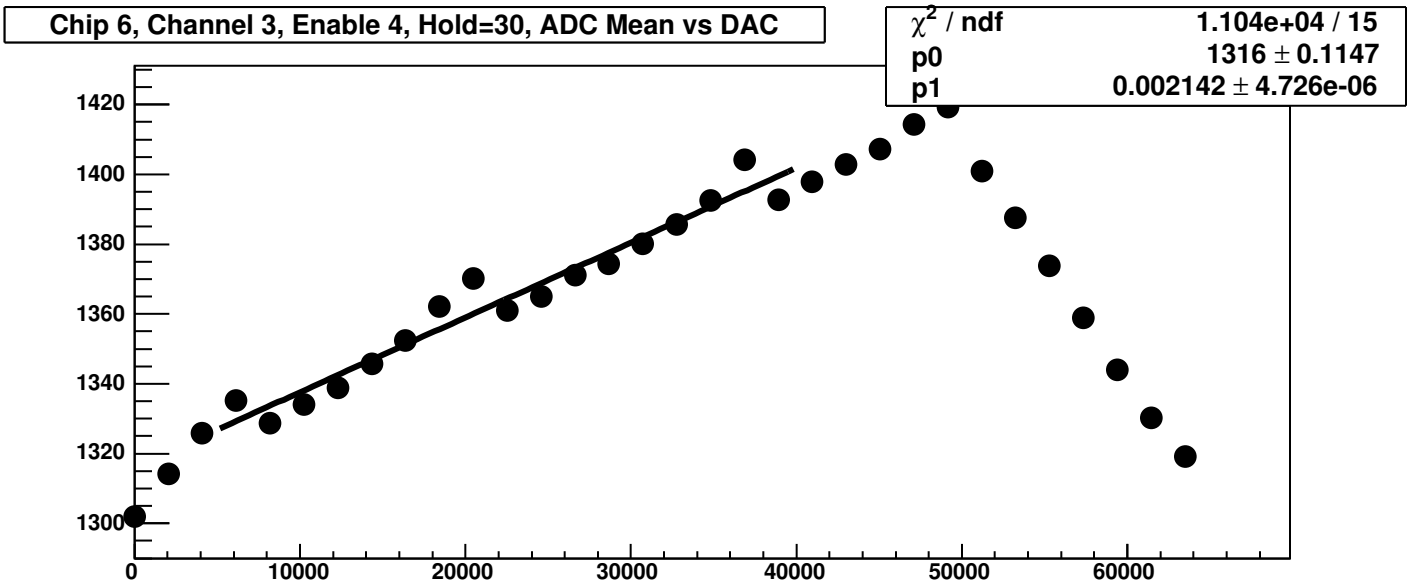

Chip 6, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

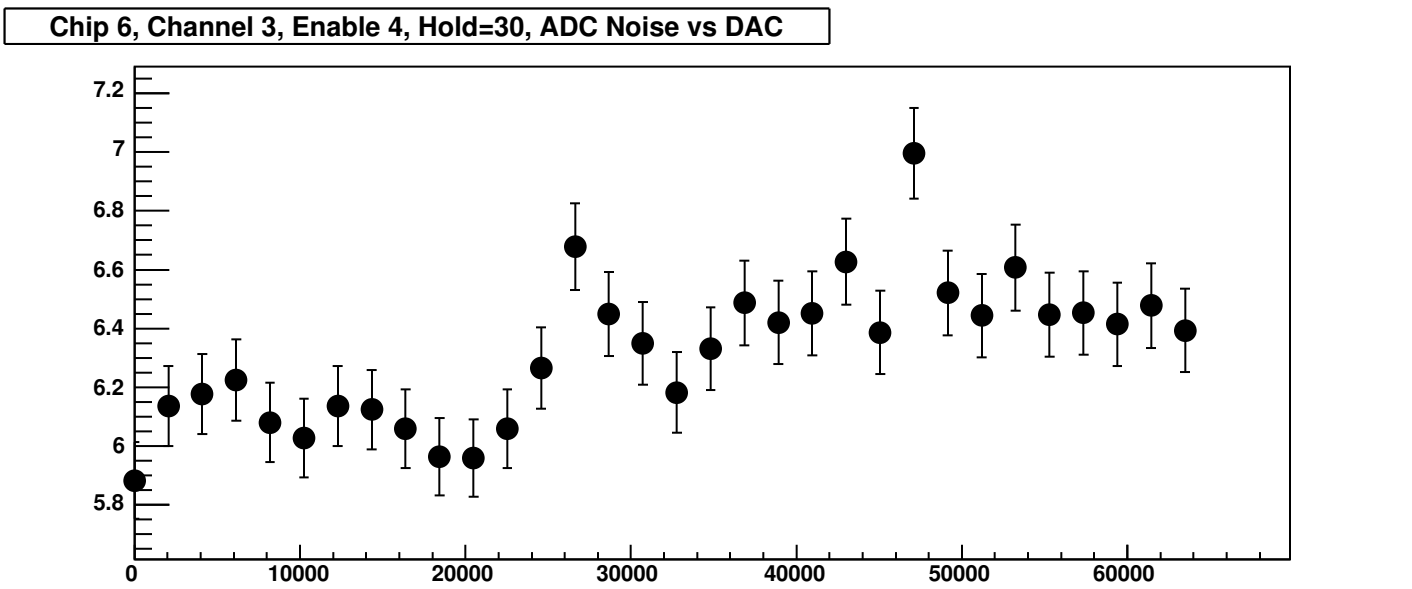

Chip 6, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

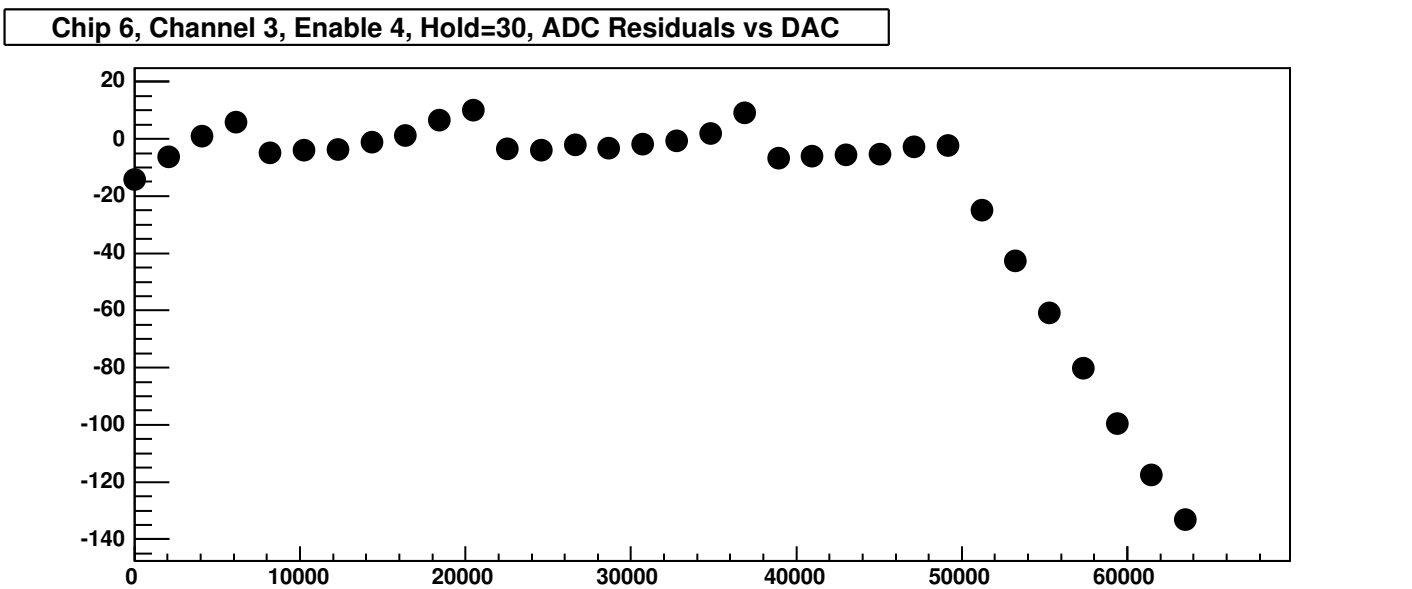

Chip 6, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7689 / 15
p0 1316 ± 0.1146
p1 0.001721 ± 4.717e-06
1400
1380
1360
1340
1320
1300
0
10000
20000
30000
40000
50000
60000

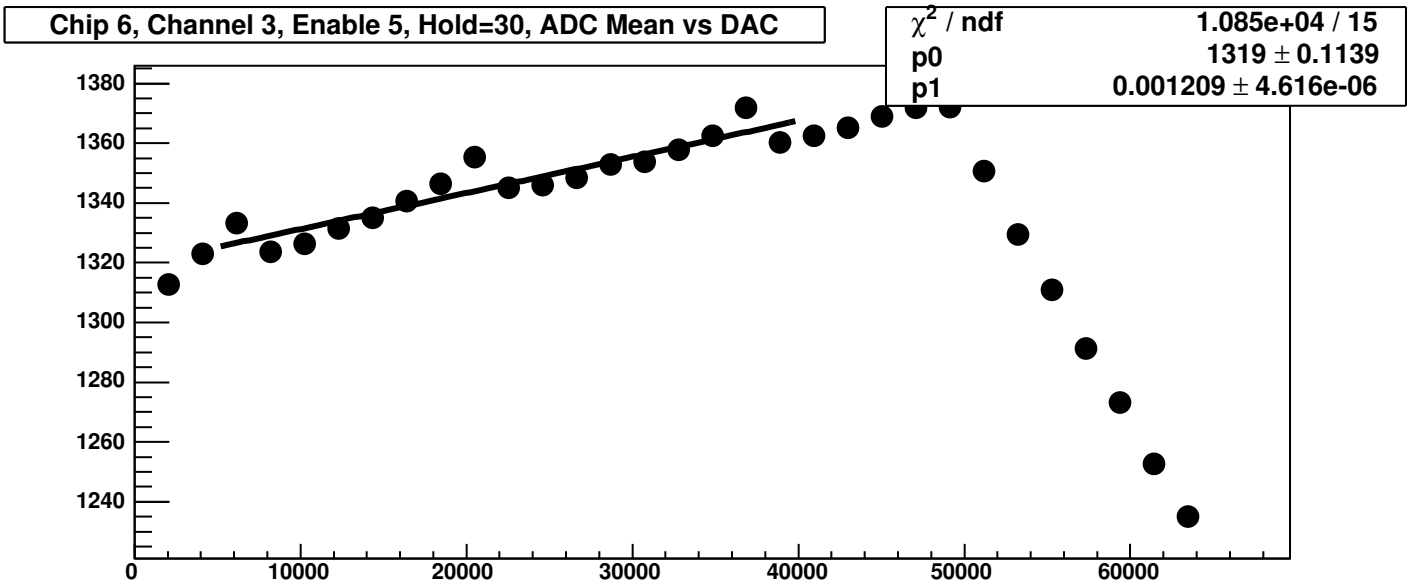

Chip 6, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

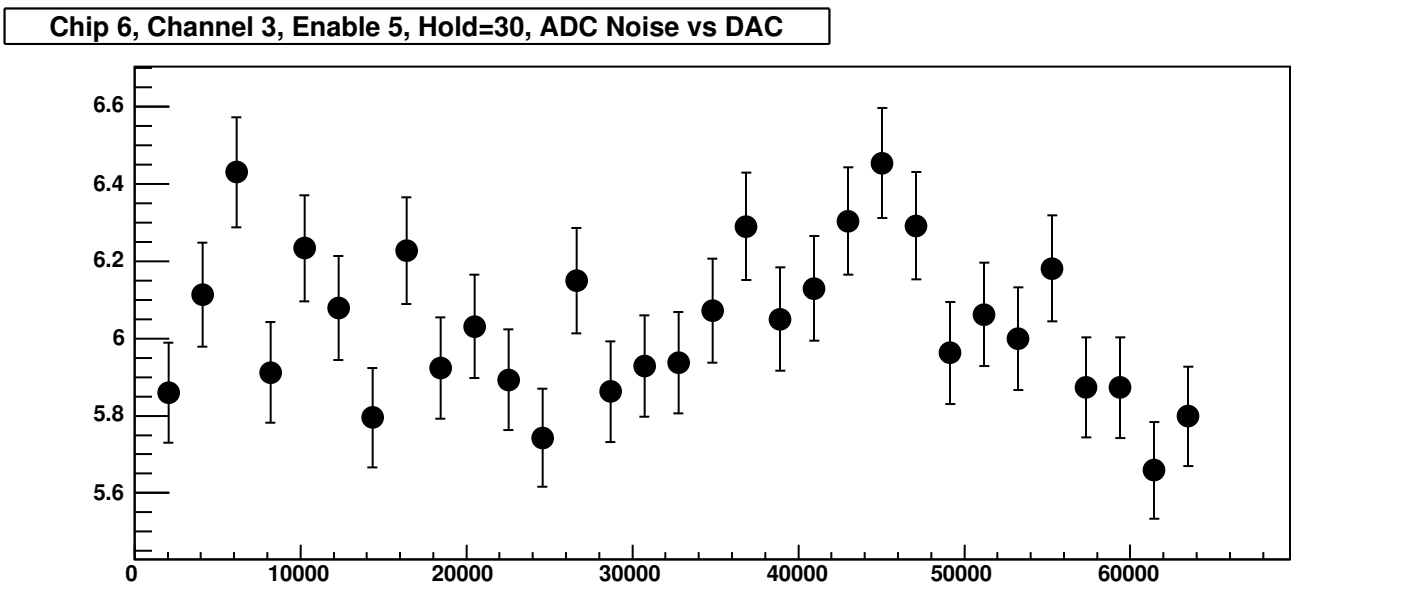

Chip 6, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

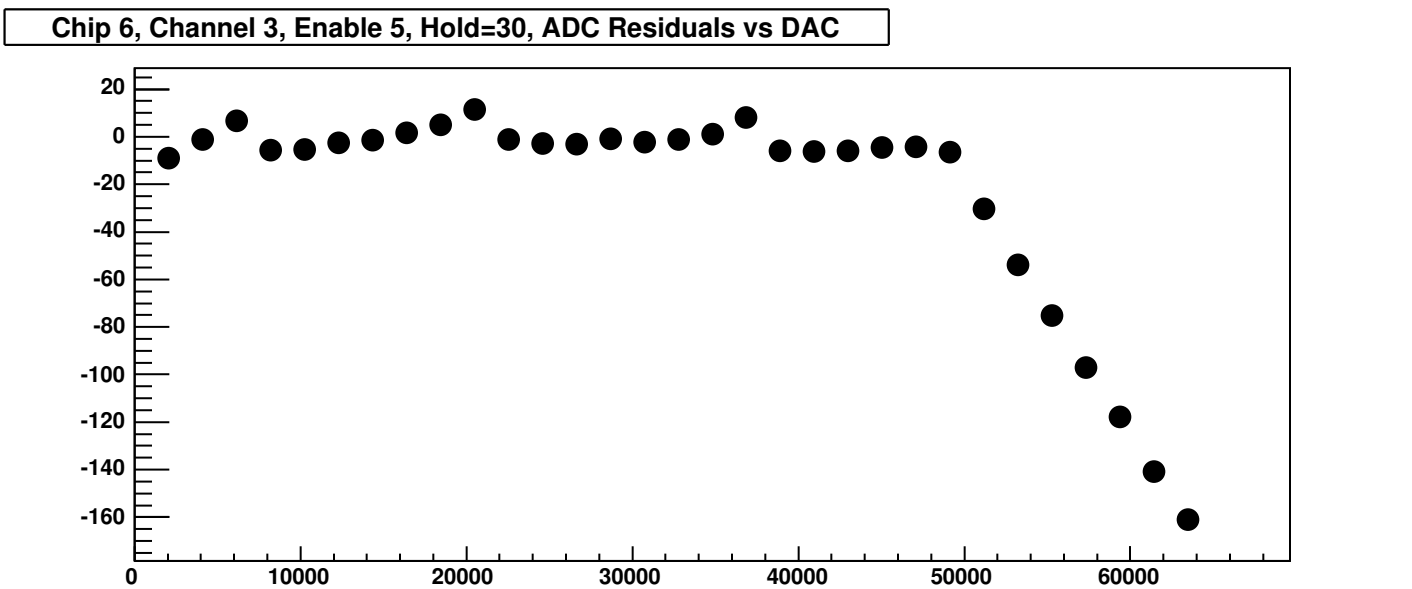

Chip 6, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.104e+04 / 15
p0 1316 ± 0.1147
p1 0.002142 ± 4.726e-06
1420
1400
1380
1360
1340
1320
1300
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

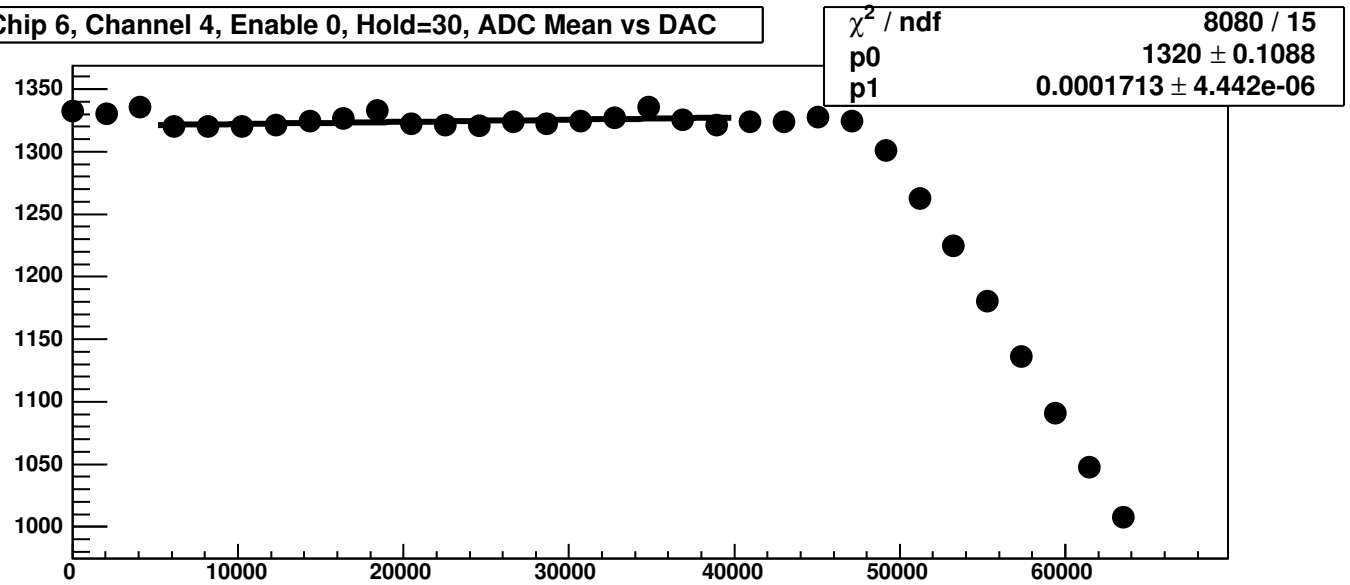

Chip 6, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.085e+04 / 15
p0 1319 ± 0.1139
p1 0.001209 ± 4.616e-06
1380
1360
1340
1320
1300
1280
1260
1240
0
10000
20000
30000
40000
50000
60000

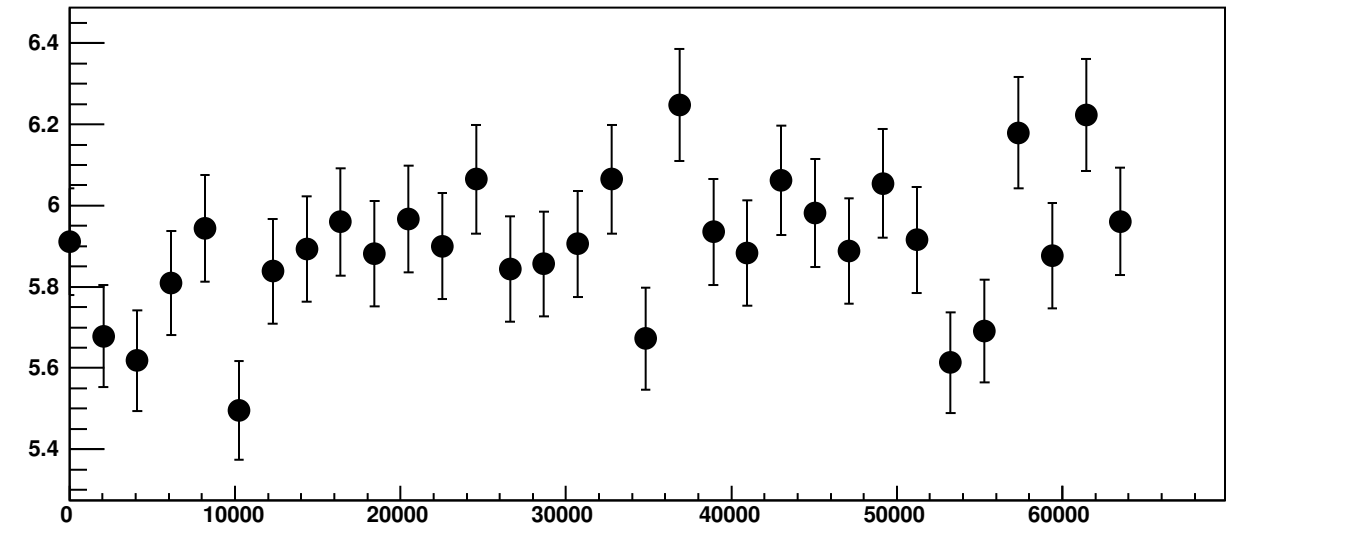

Chip 6, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

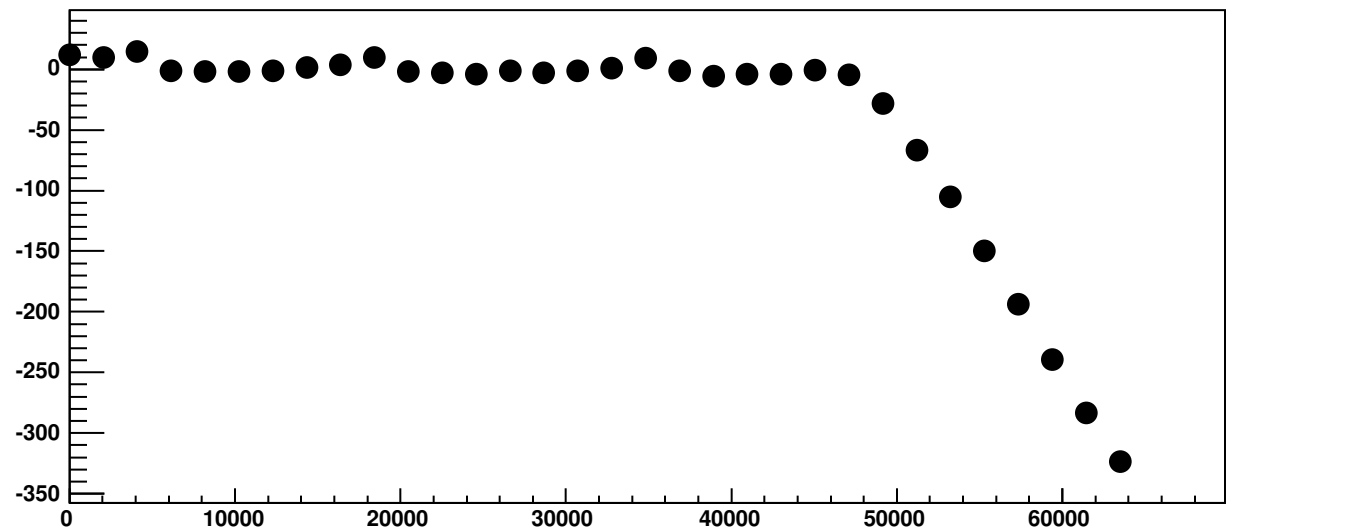
Chip 6, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



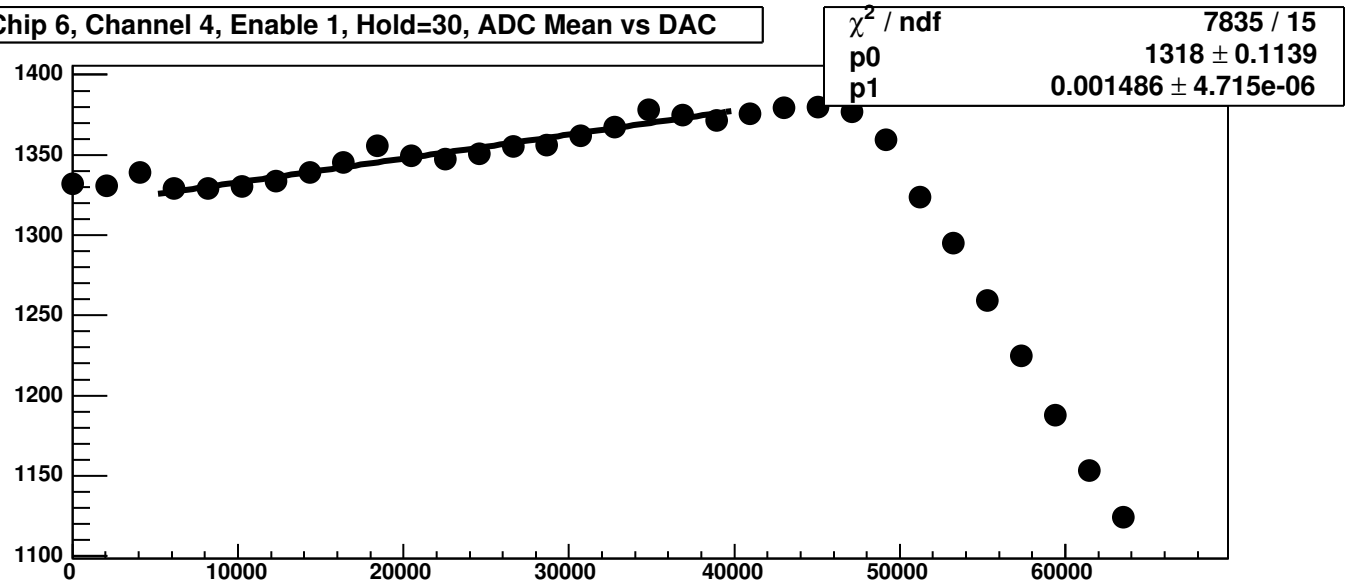
Chip 6, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



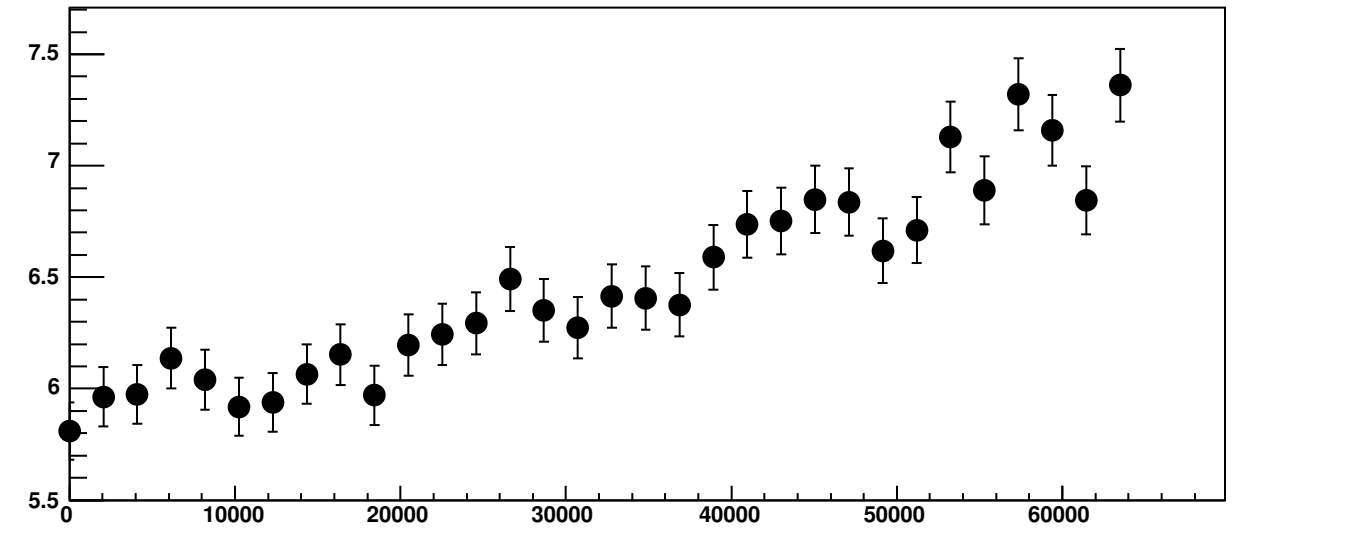
Chip 6, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC



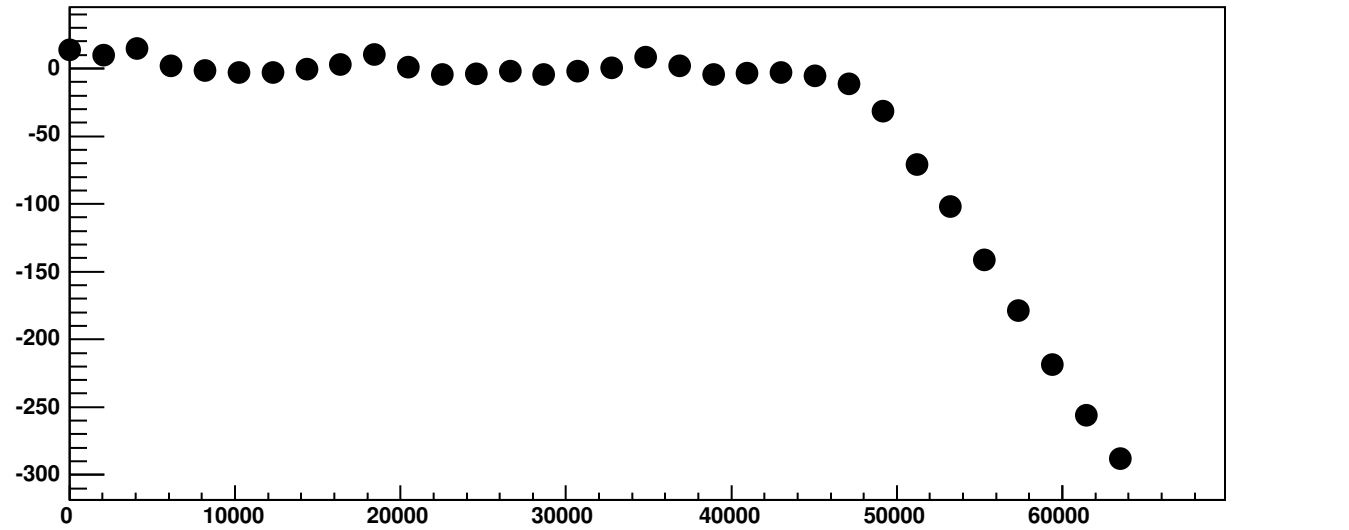
Chip 6, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC

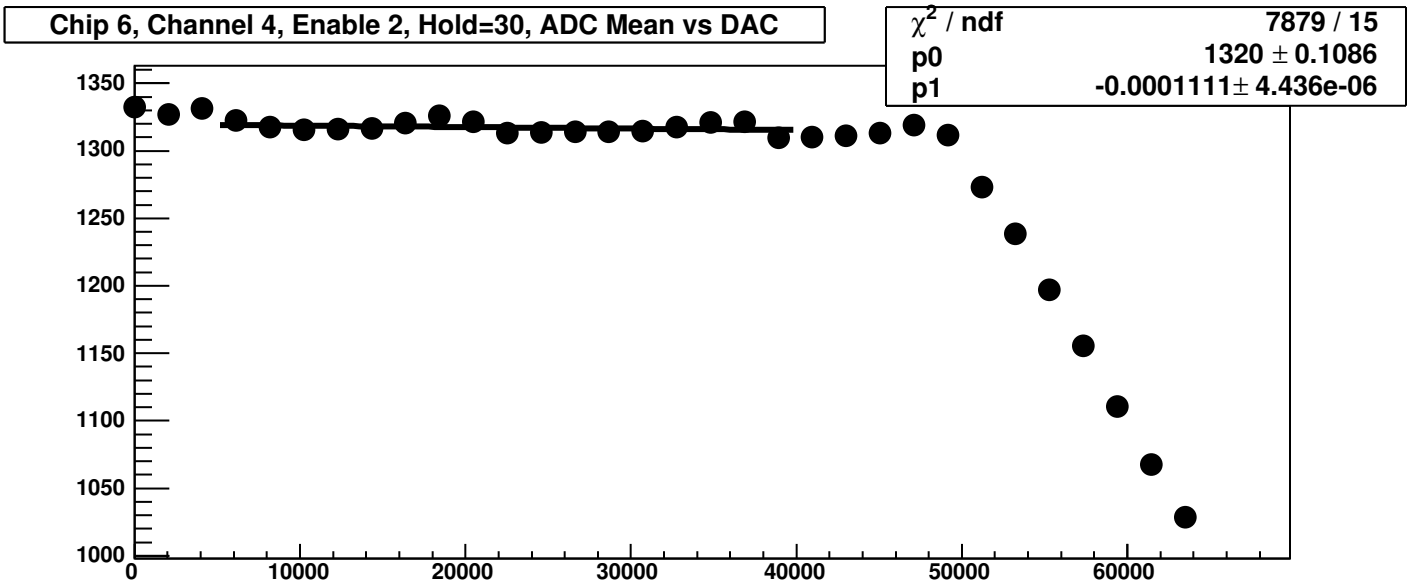


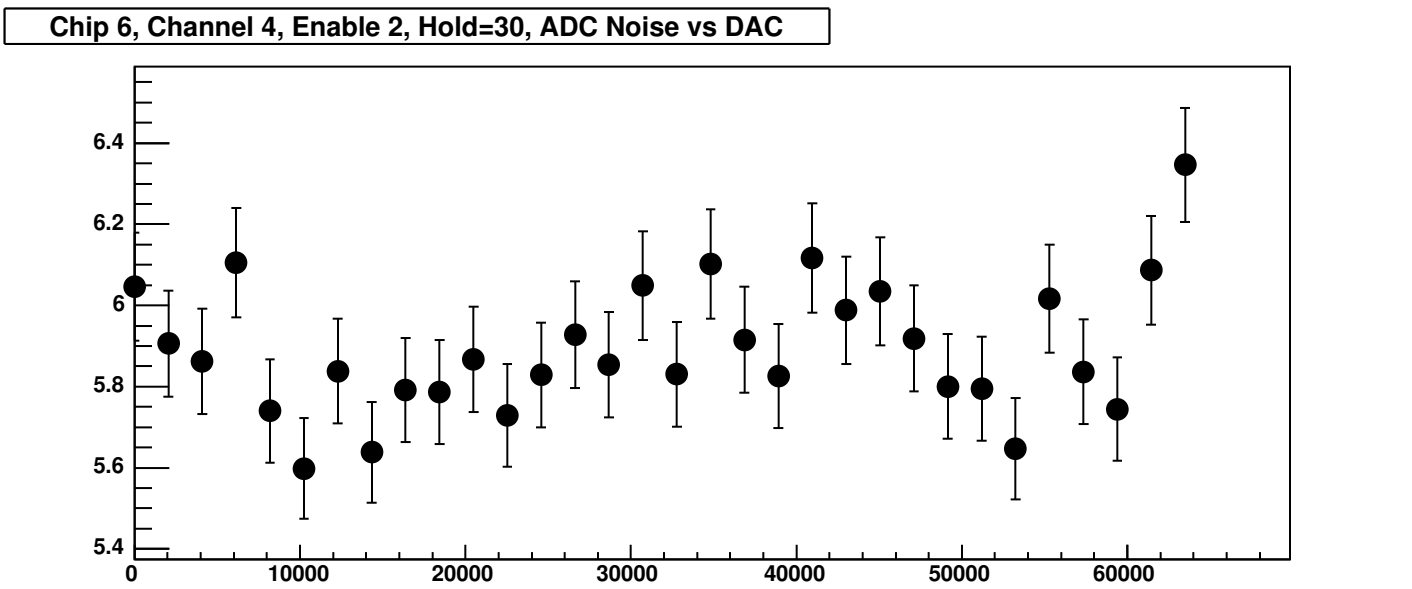
Chip 6, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC

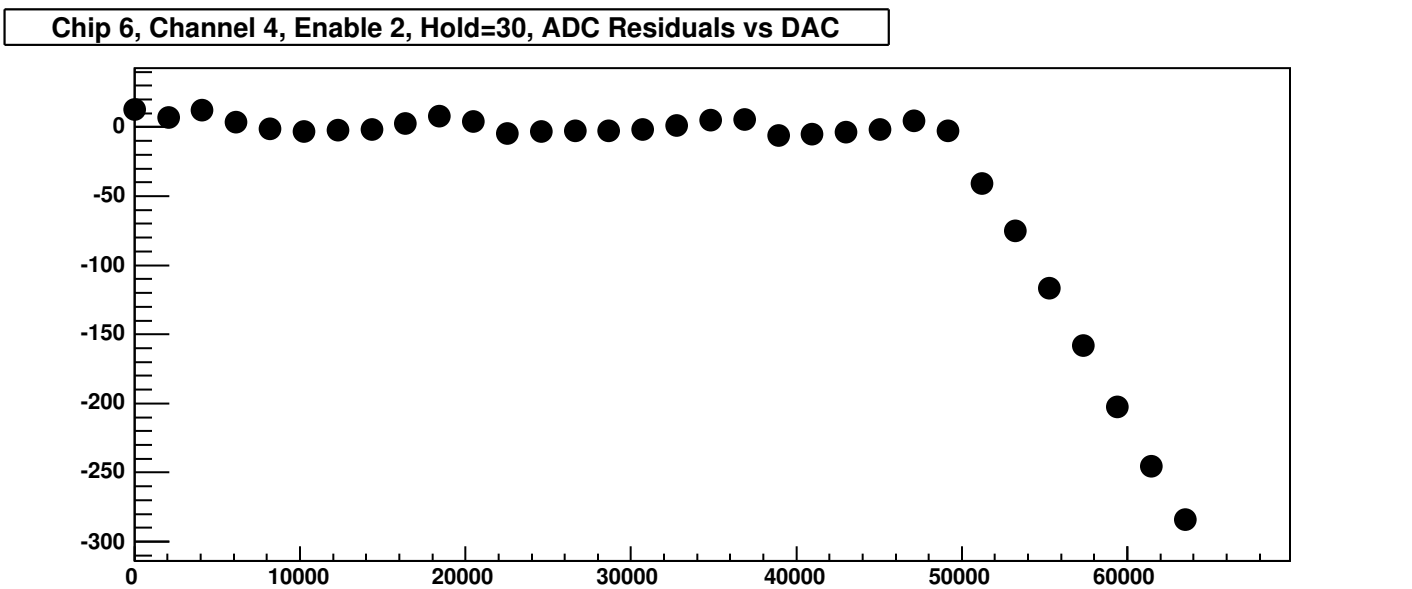


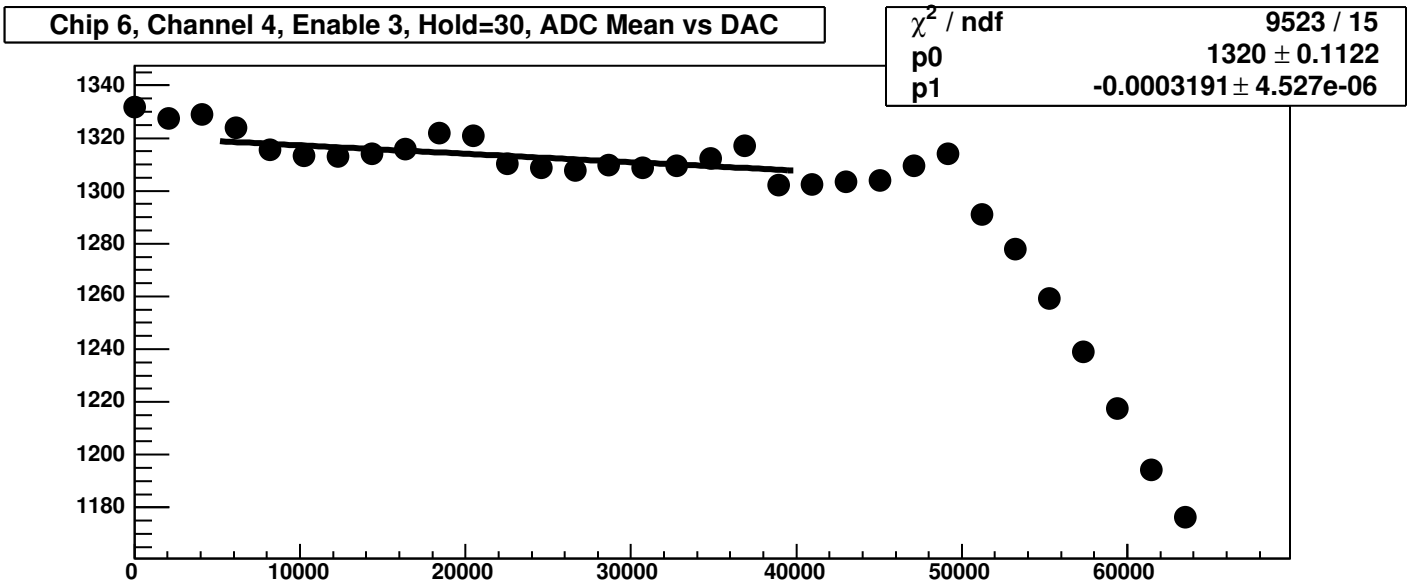
Chip 6, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC

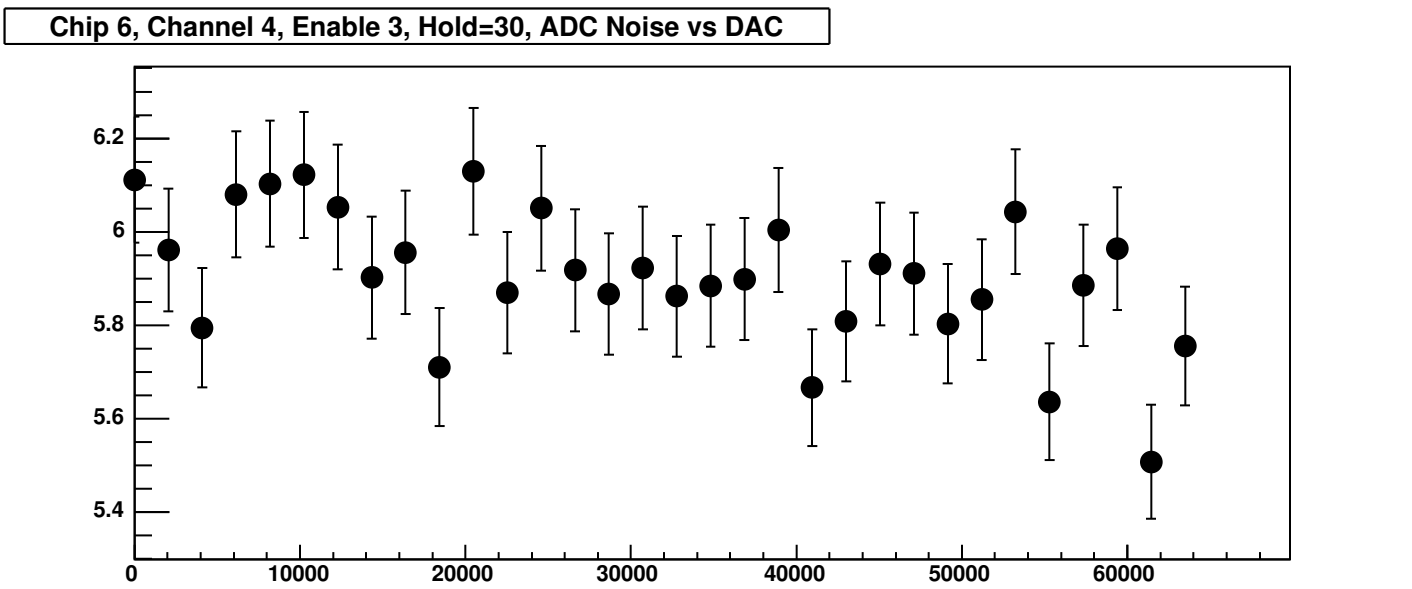


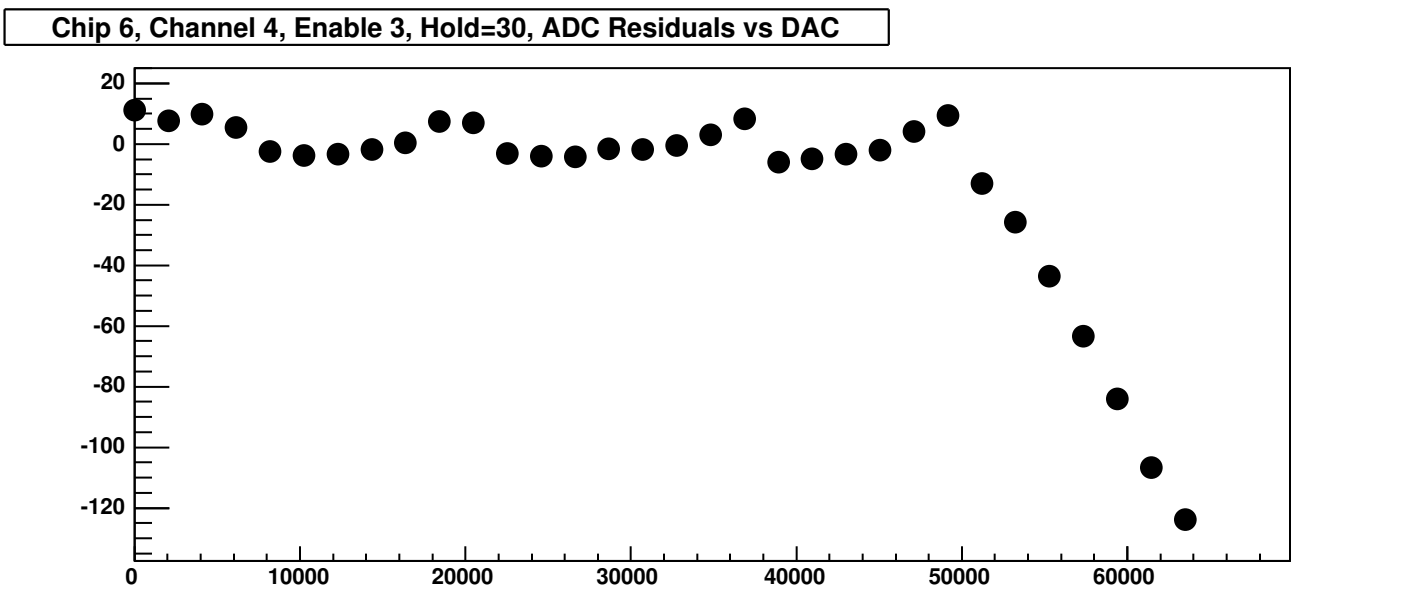

Chip 6, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7879 / 15
p0 1320 ± 0.1086
p1 -0.0001111 ± 4.436e-06
1350
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

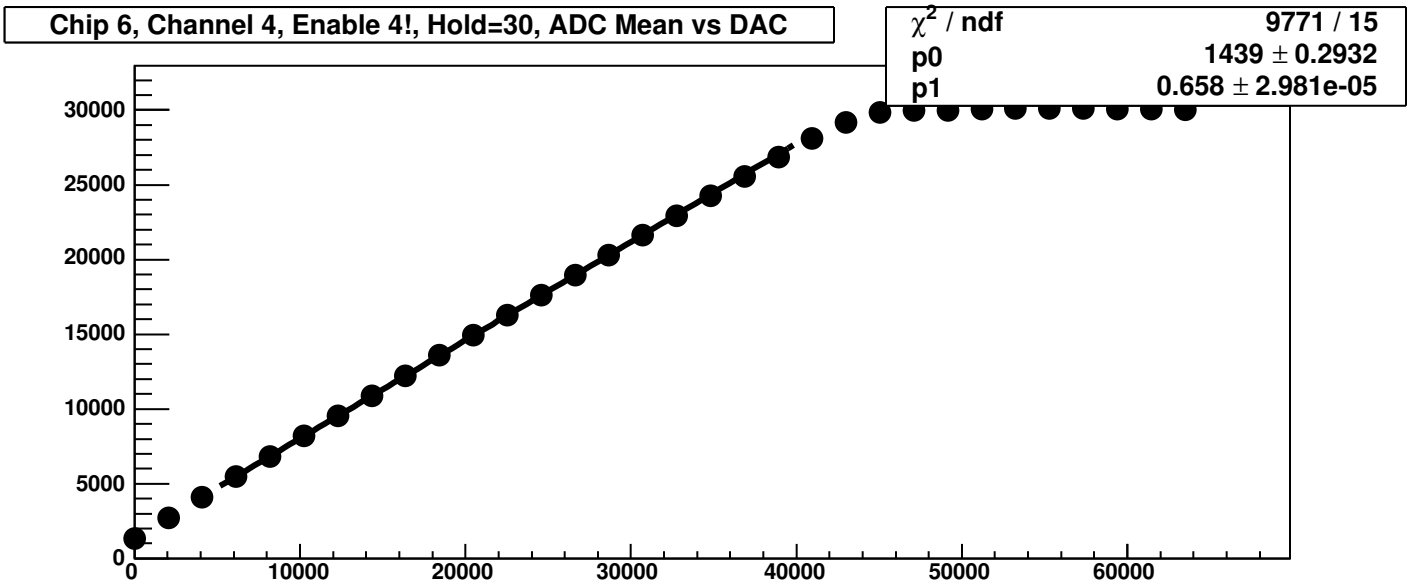

Chip 6, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

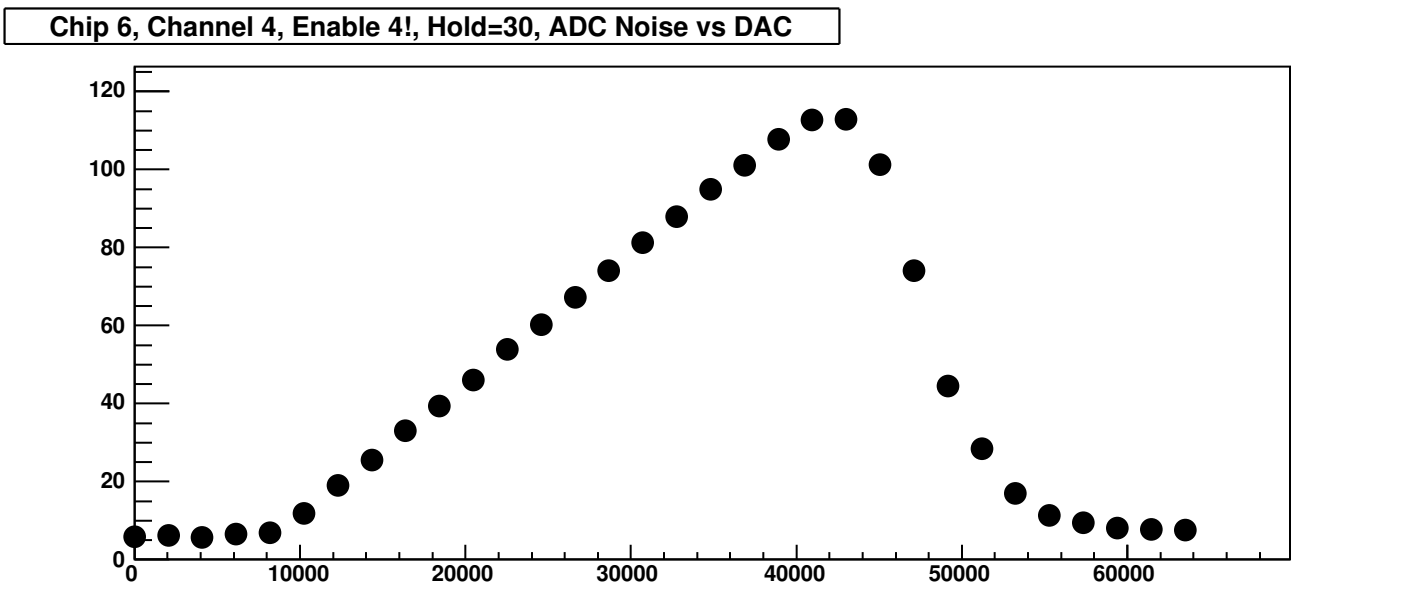

Chip 6, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

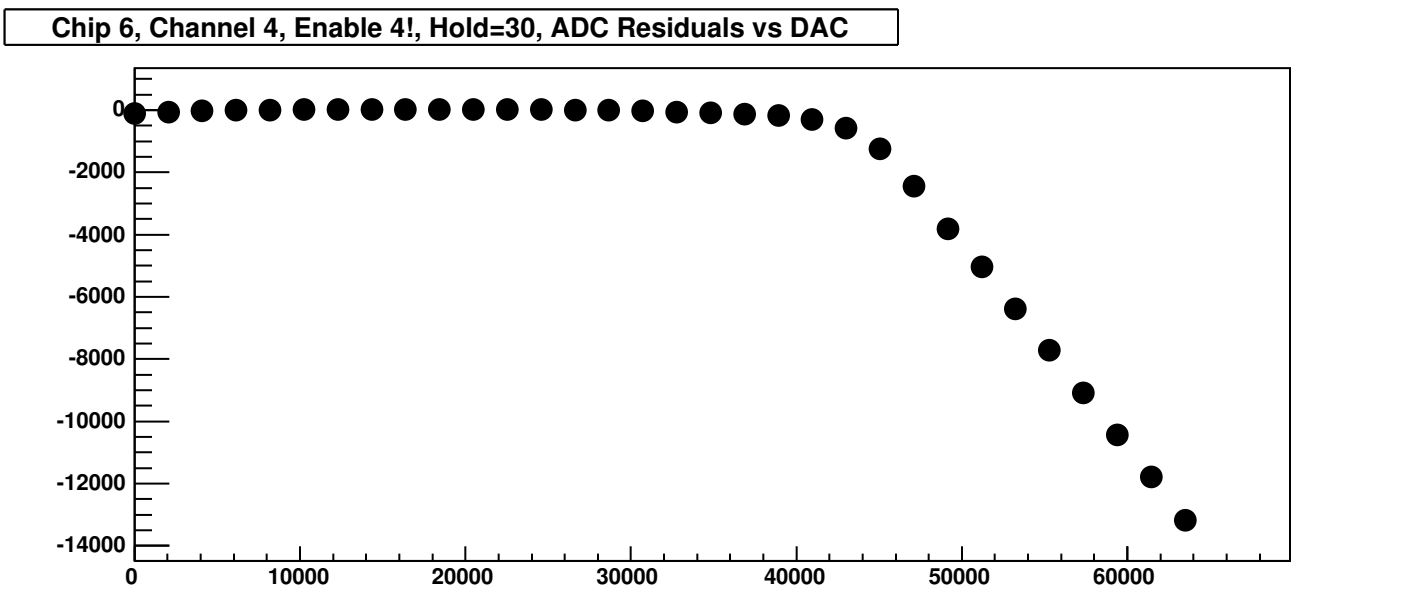

Chip 6, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9523 / 15
p0 1320 ± 0.1122
p1 -0.0003191 ± 4.527e-06
1340
1320
1300
1280
1260
1240
1220
1200
1180
0
10000
20000
30000
40000
50000
60000

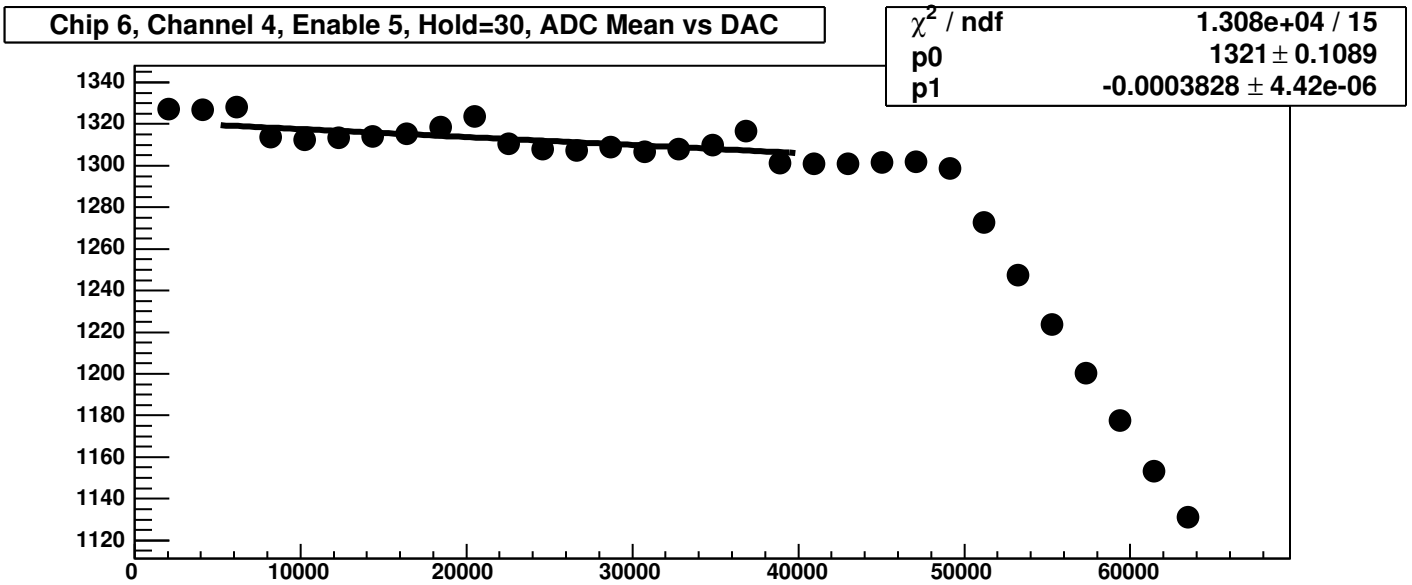

Chip 6, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

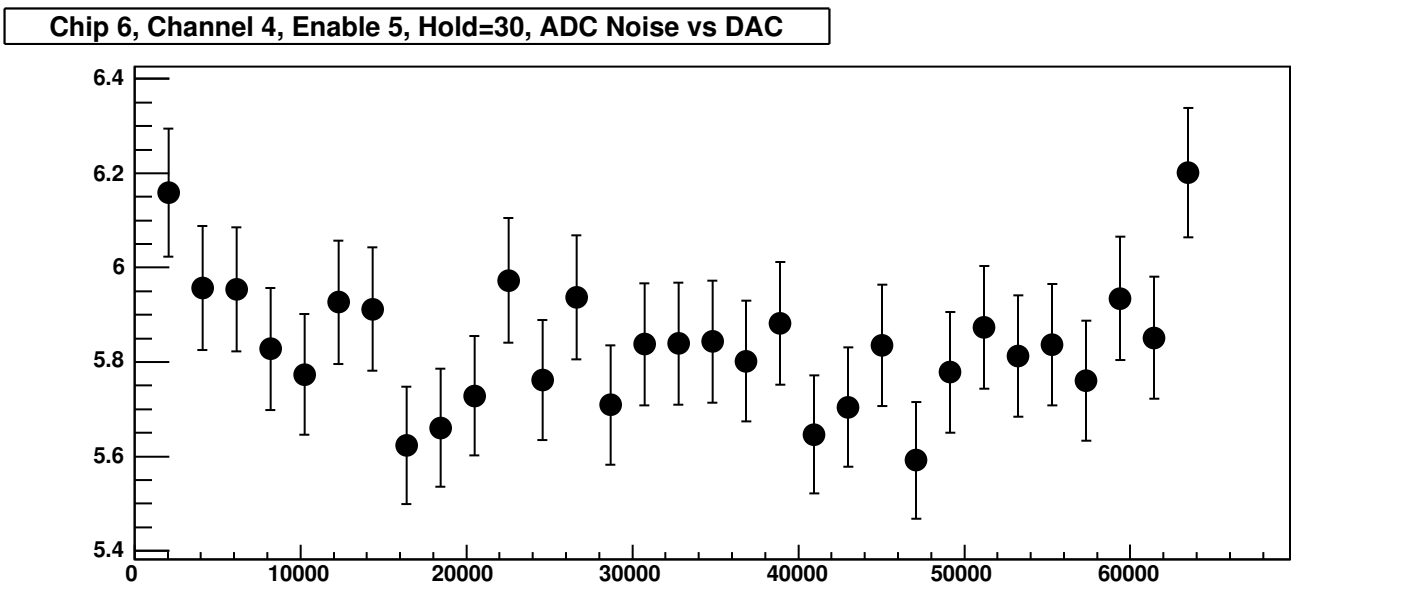

Chip 6, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

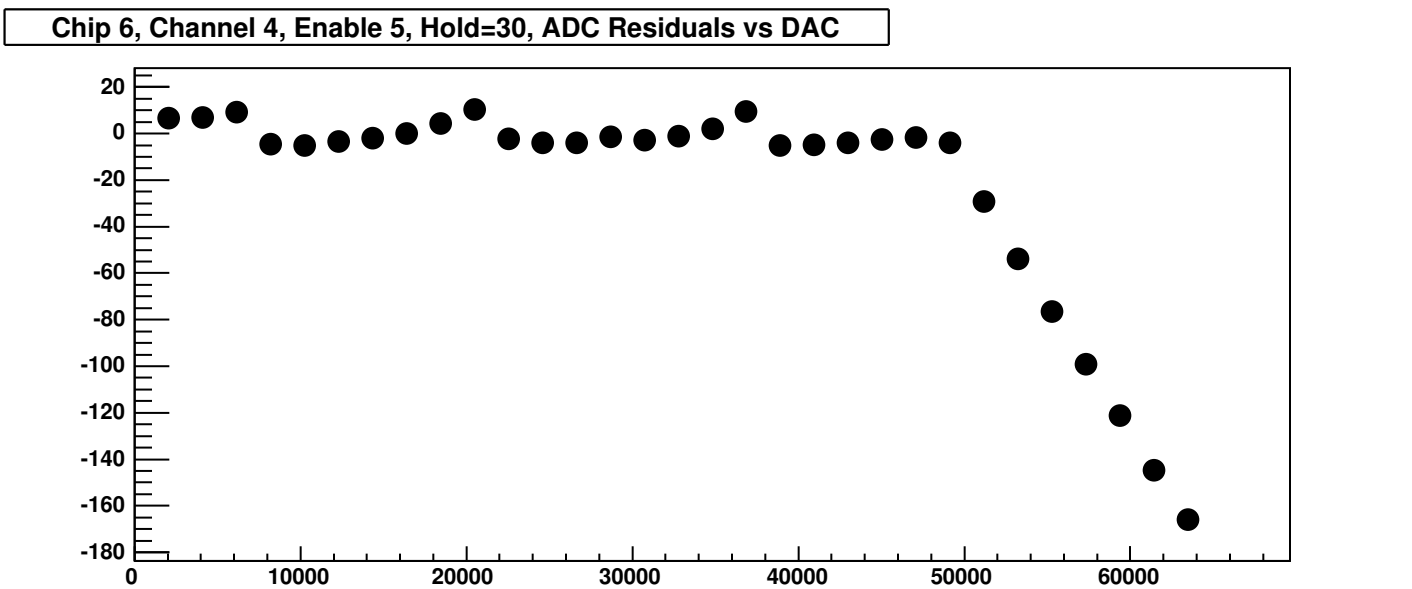

Chip 6, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 9771 / 15
p0 1439 ± 0.2932
p1 0.658 ± 2.981e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

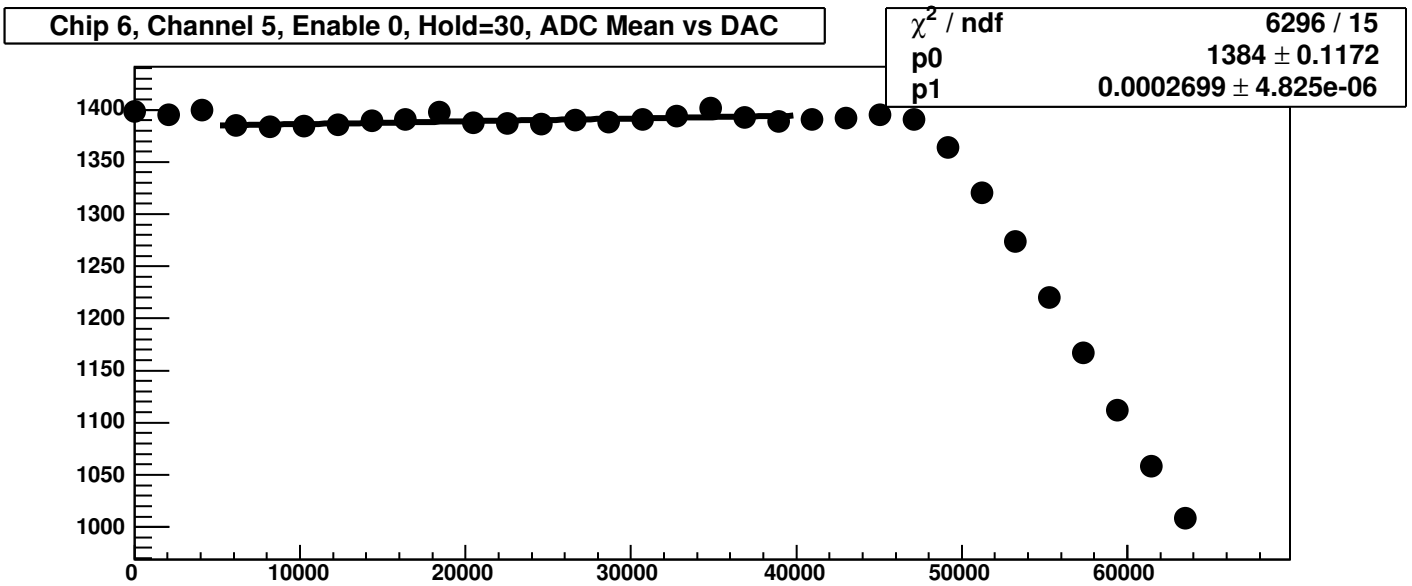

Chip 6, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

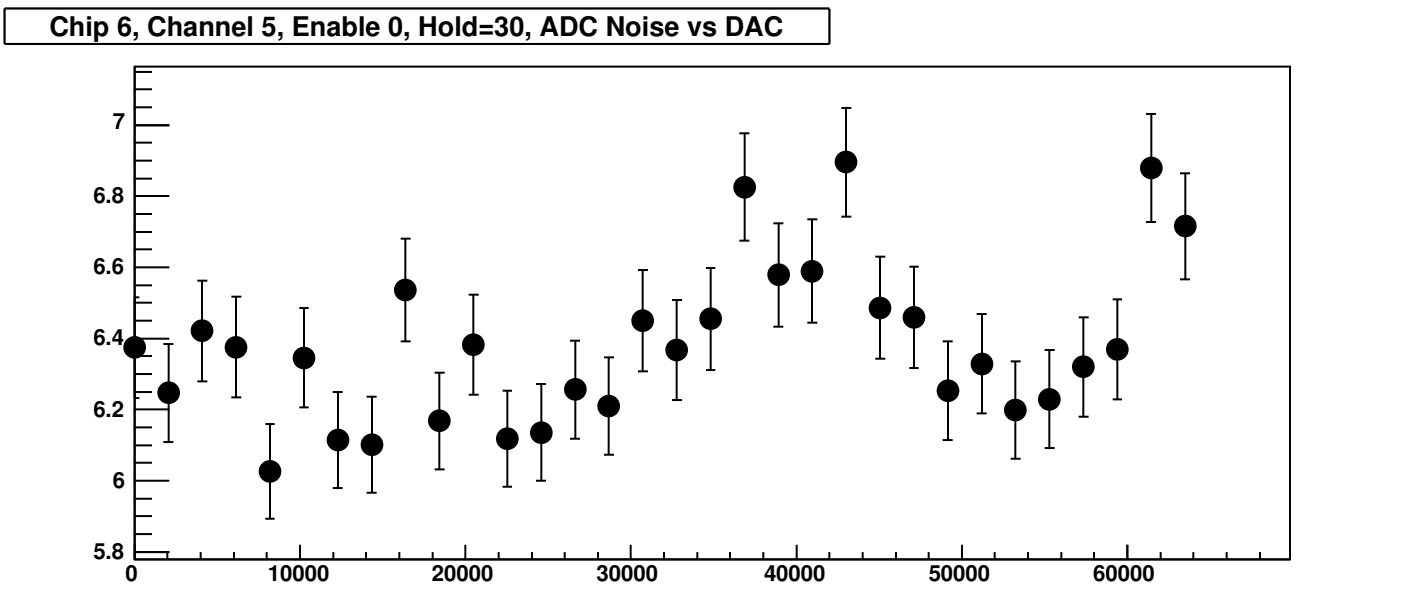

Chip 6, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

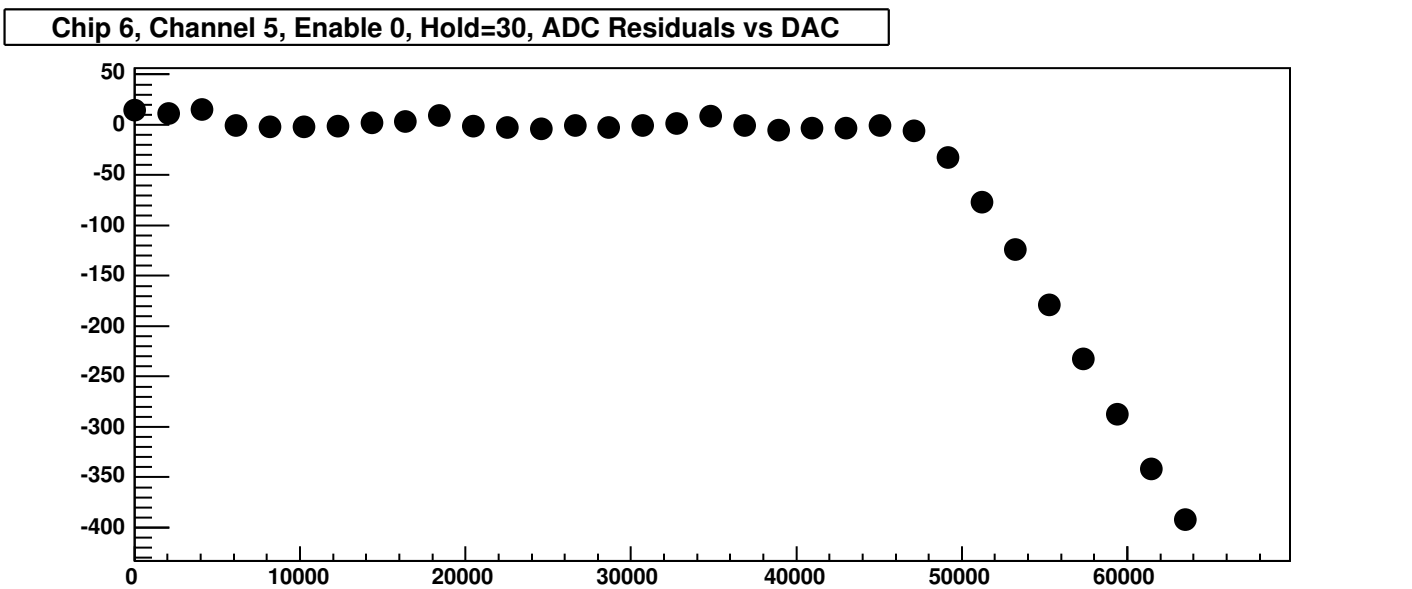

Chip 6, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.308e+04 / 15
p0 1321 ± 0.1089
p1 -0.0003828 ± 4.42e-06
1340
1320
1300
1280
1260
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000

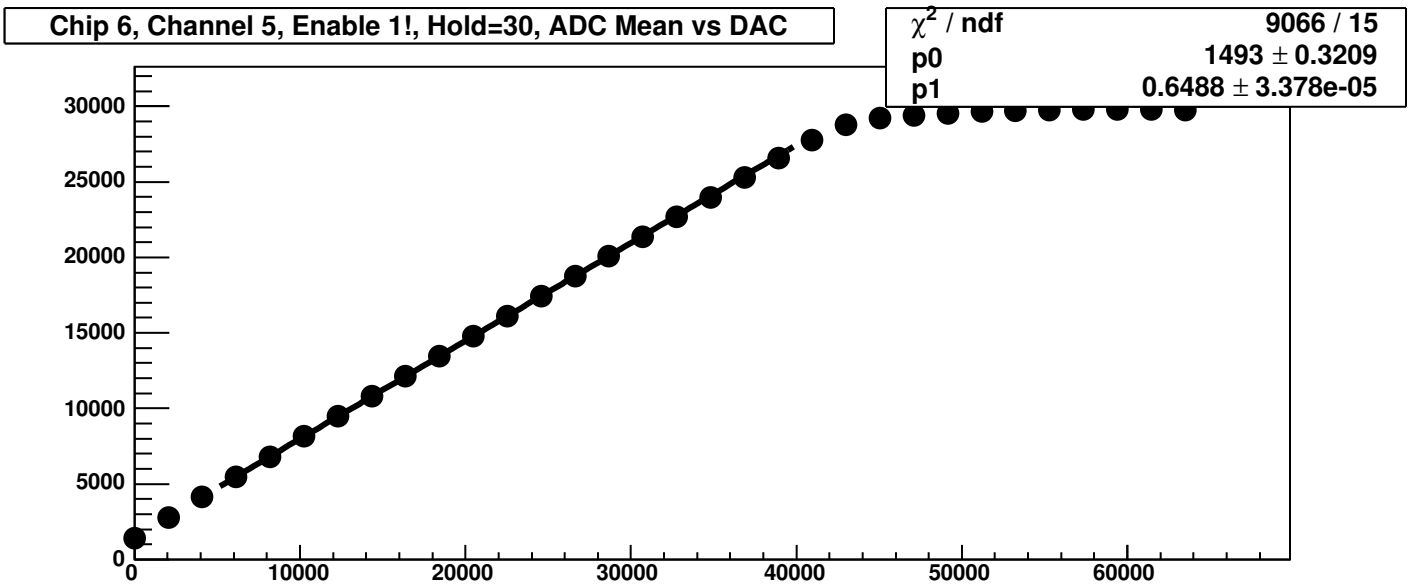

Chip 6, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

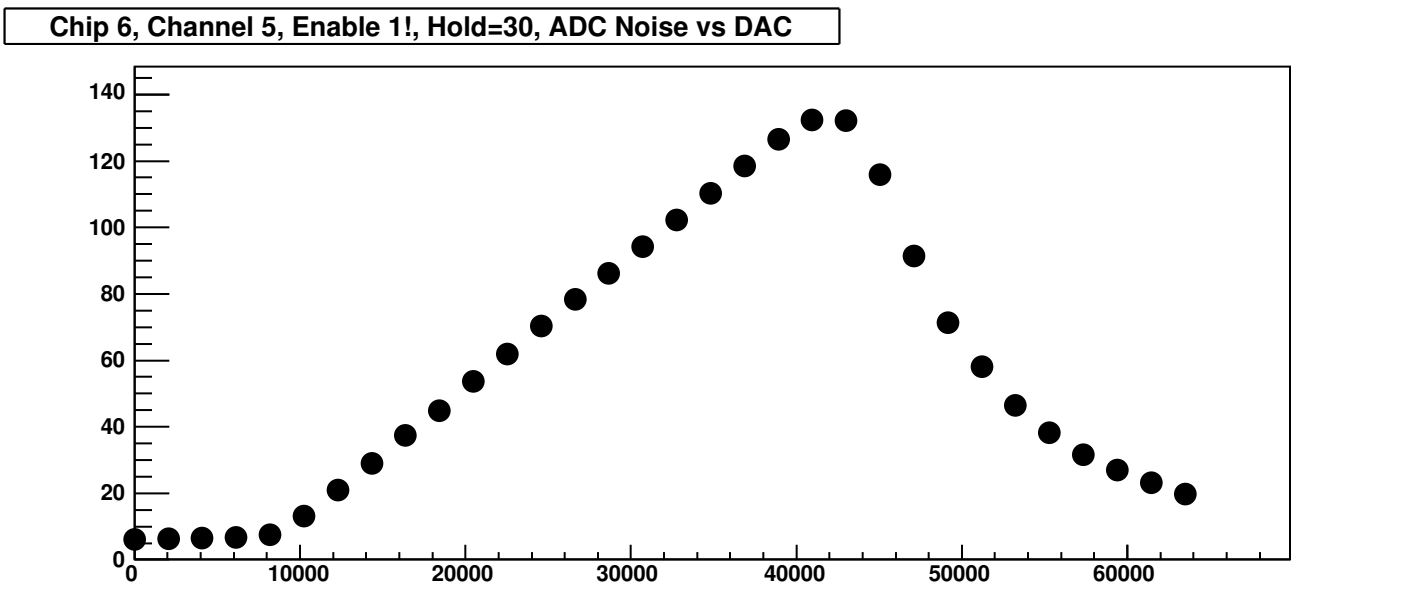

Chip 6, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

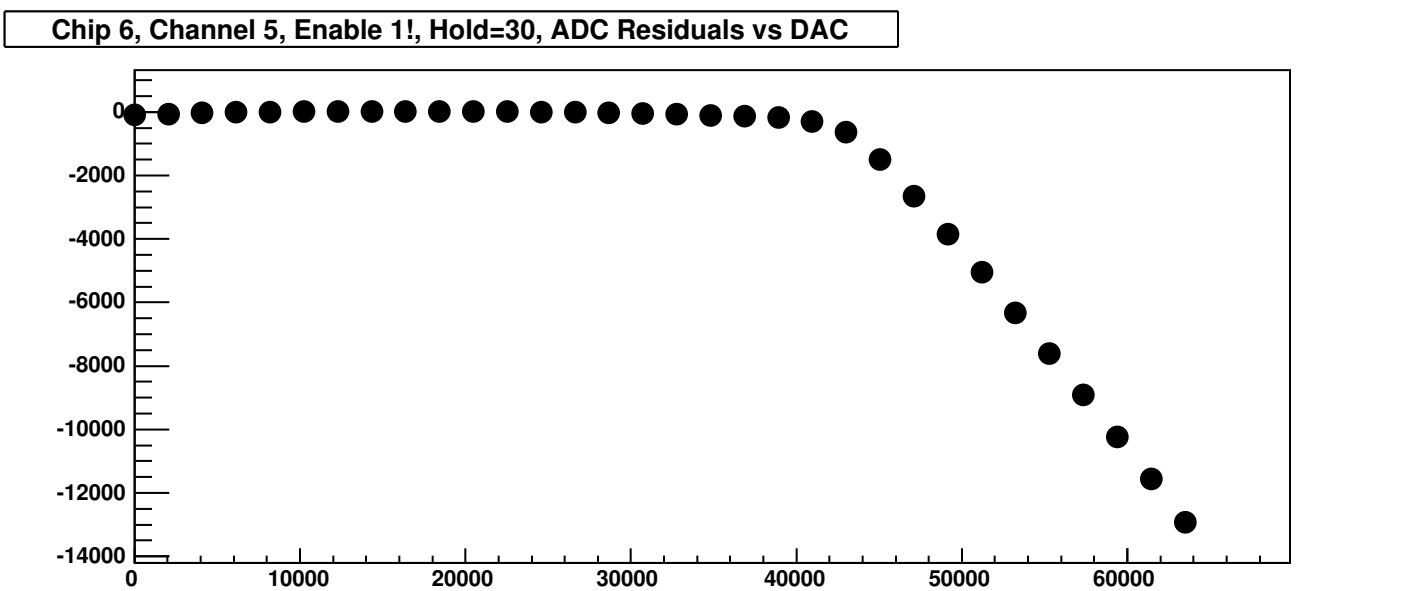

Chip 6, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6296 / 15
p0 1384 ± 0.1172
p1 0.0002699 ± 4.825e-06
1400
1350
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

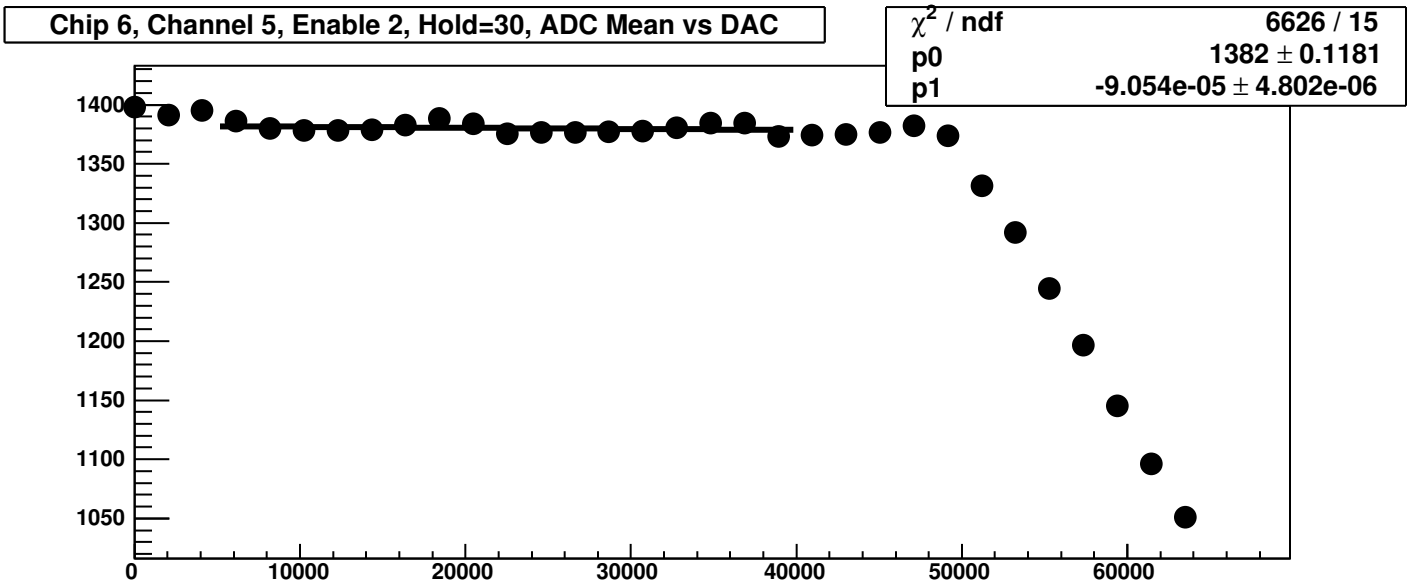

Chip 6, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

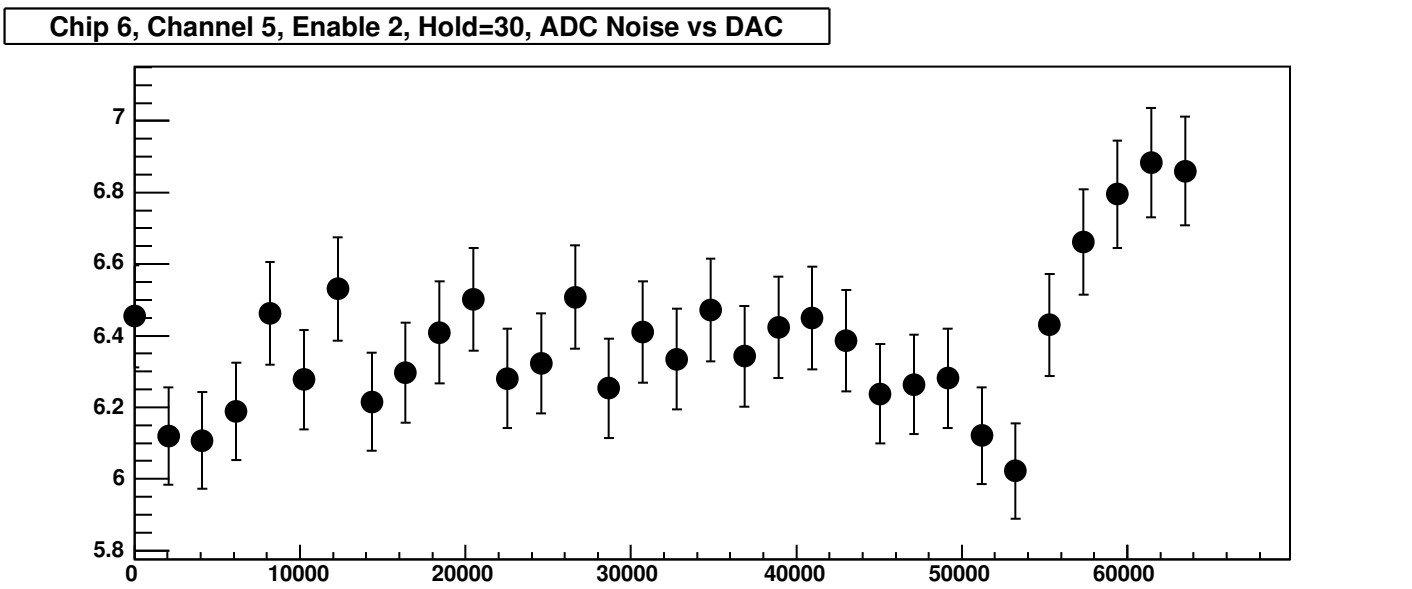

Chip 6, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

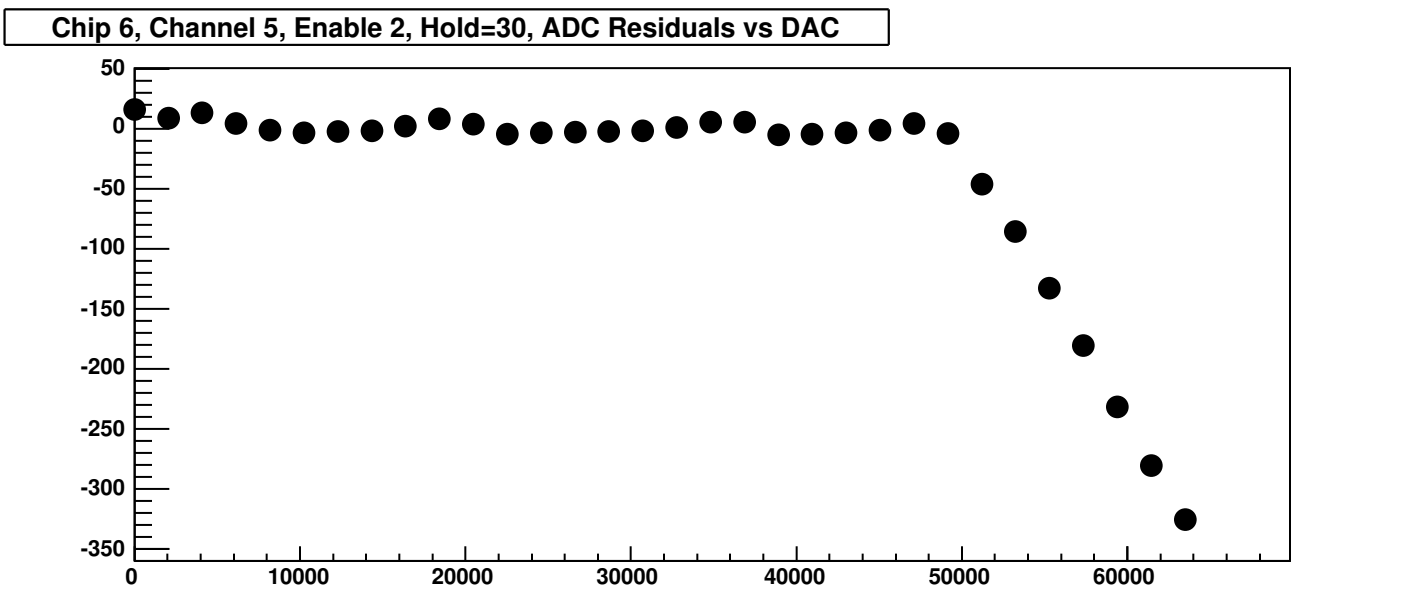

Chip 6, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 9066 / 15
p0 1493 ± 0.3209
p1 0.6488 ± 3.378e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

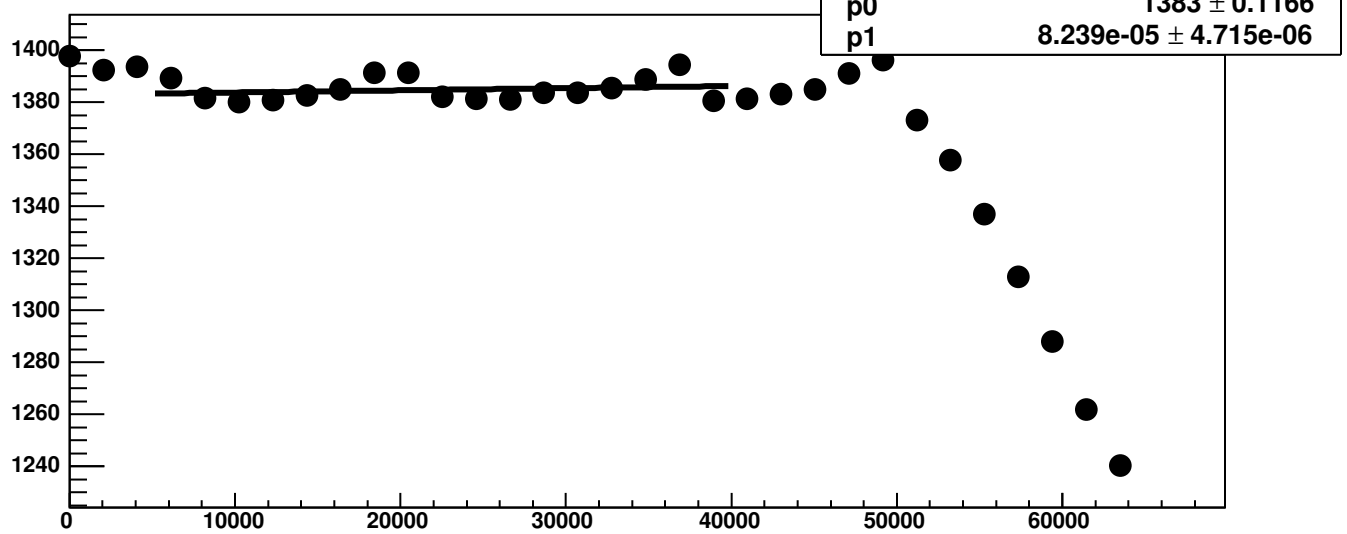

Chip 6, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6626 / 15
p0 1382 ± 0.1181
p1 -9.054e-05 ± 4.802e-06
1400
1350
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

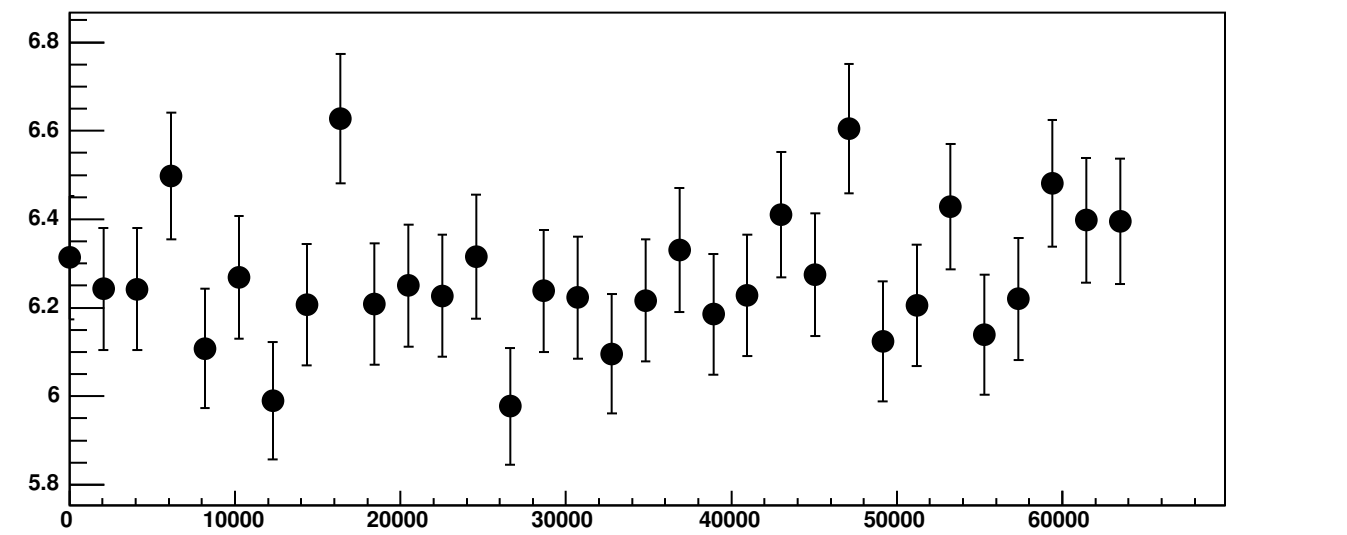

Chip 6, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

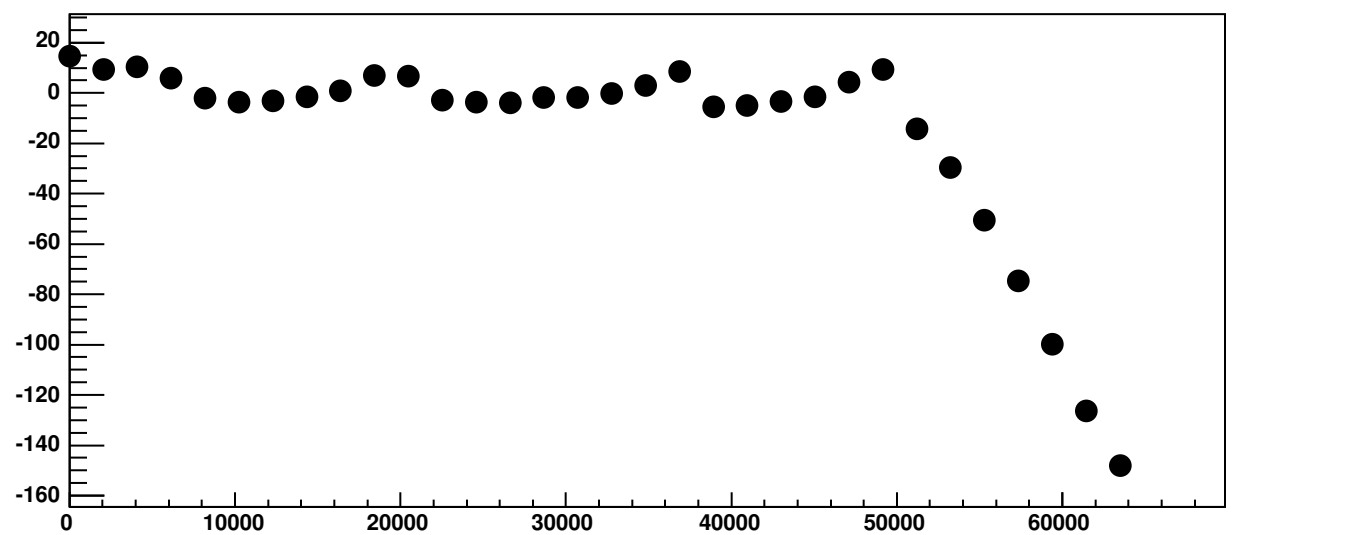
Chip 6, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

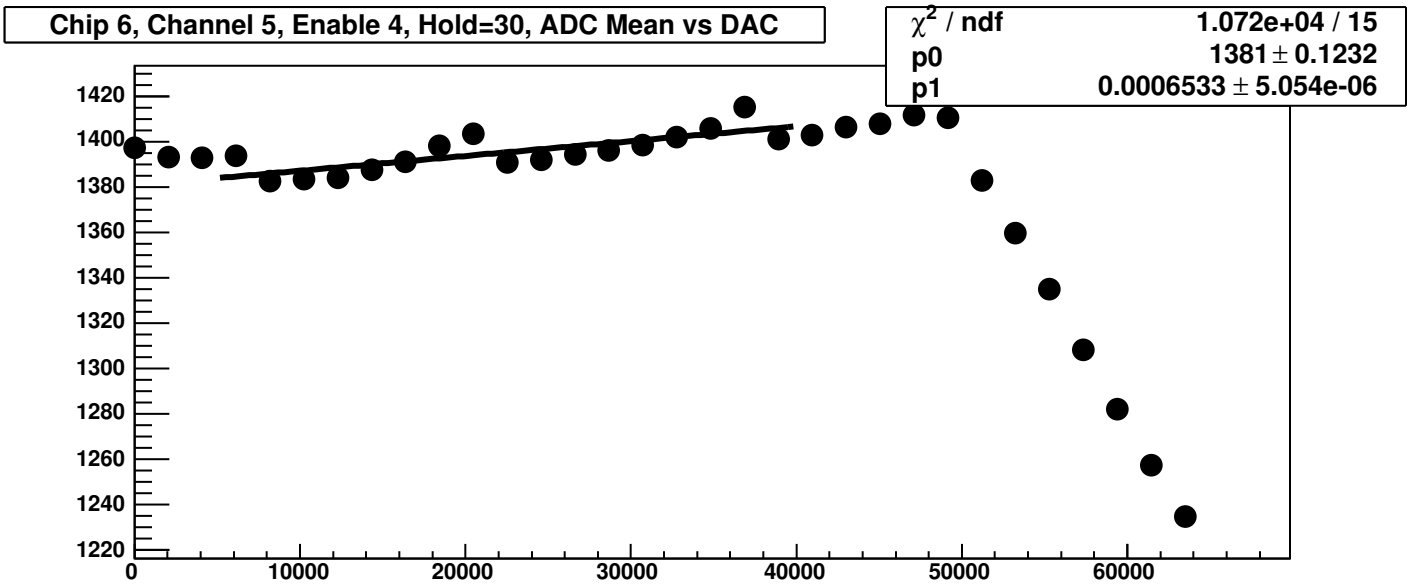


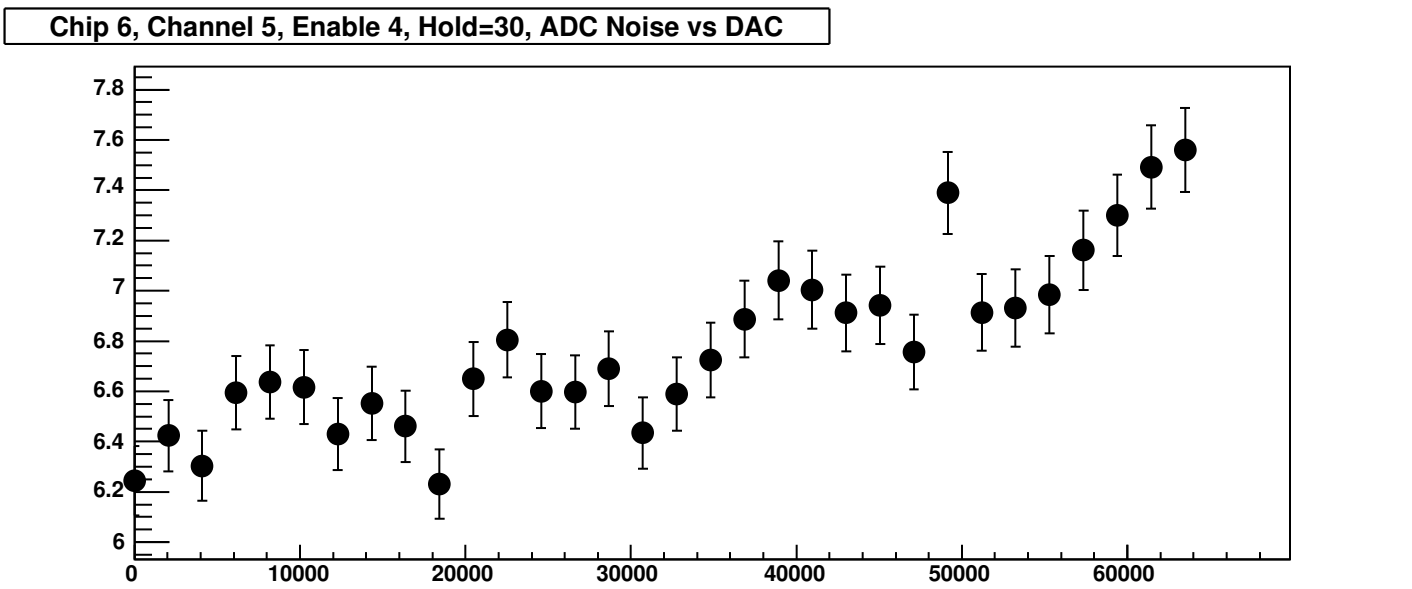
Chip 6, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

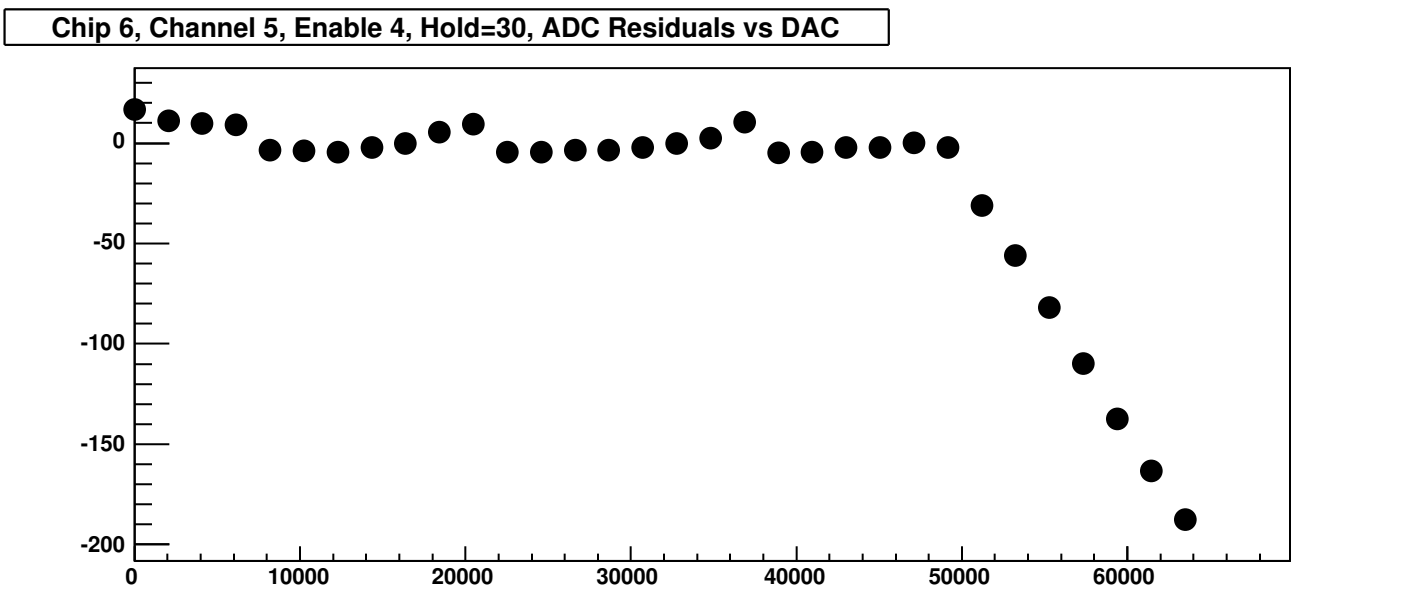


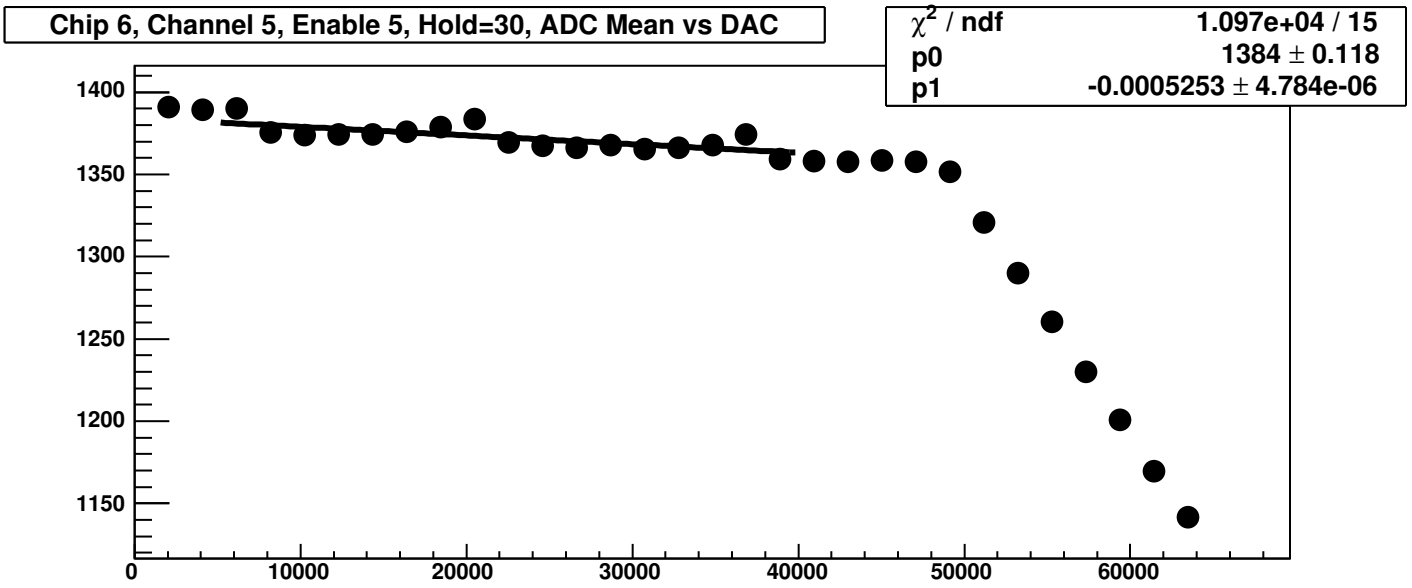
Chip 6, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

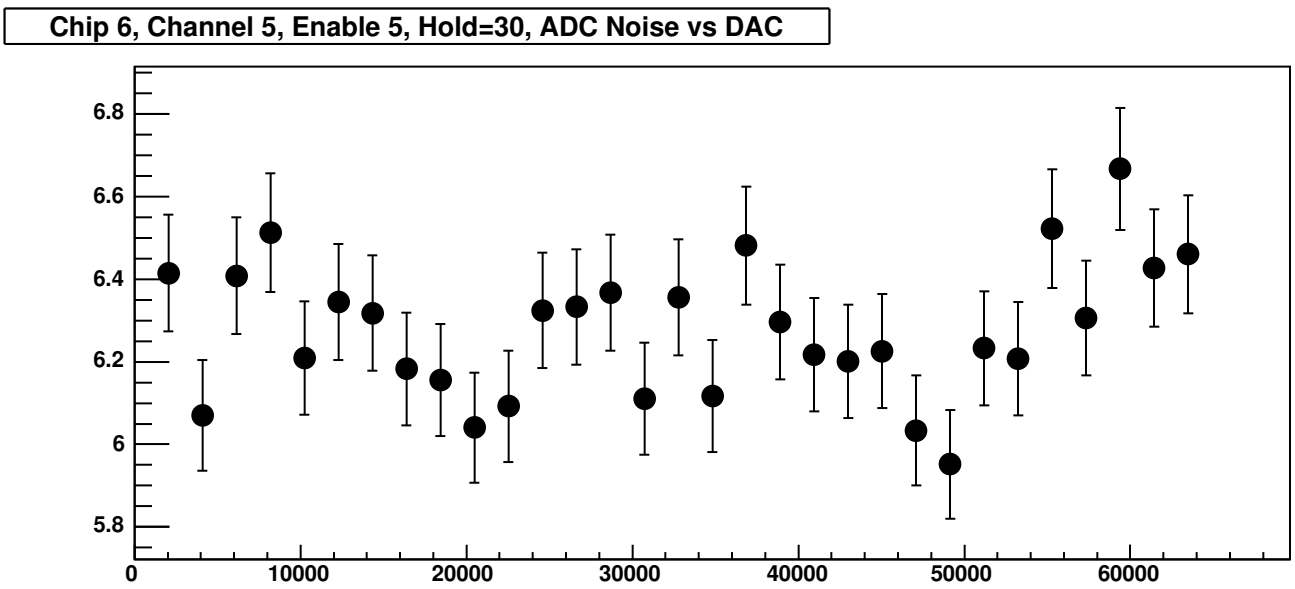


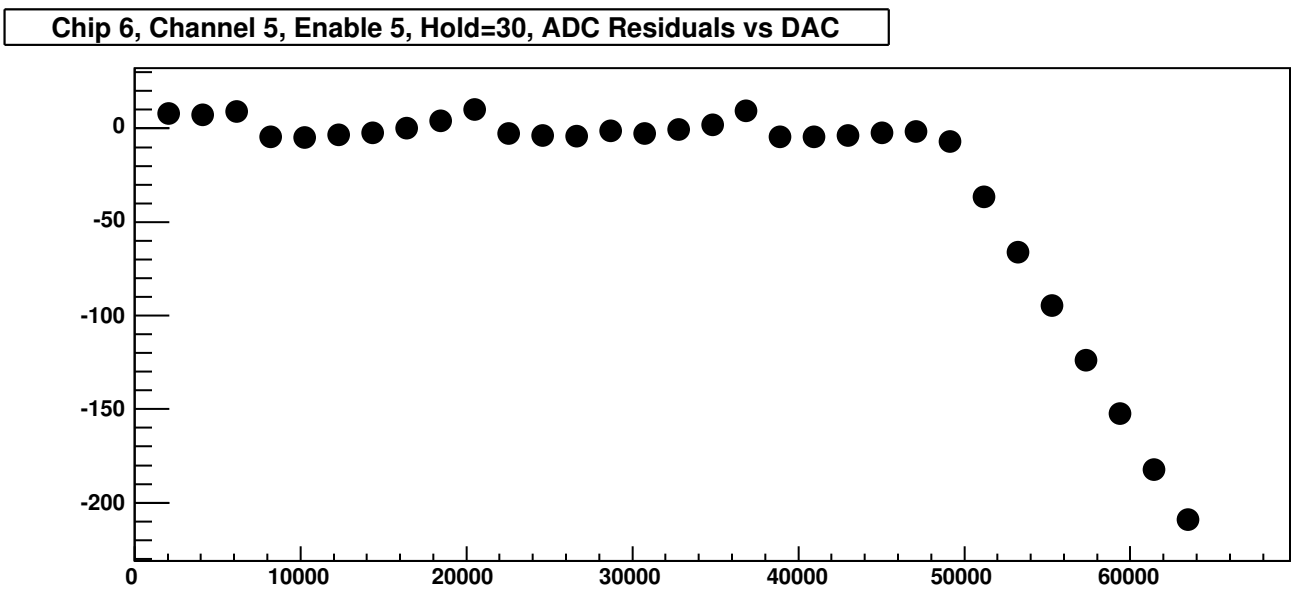

Chip 6, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.072e+04 / 15
p0 1381 ± 0.1232
p1 0.0006533 ± 5.054e-06
1420
1400
1380
1360
1340
1320
1300
1280
1260
1240
1220
0
10000
20000
30000
40000
50000
60000

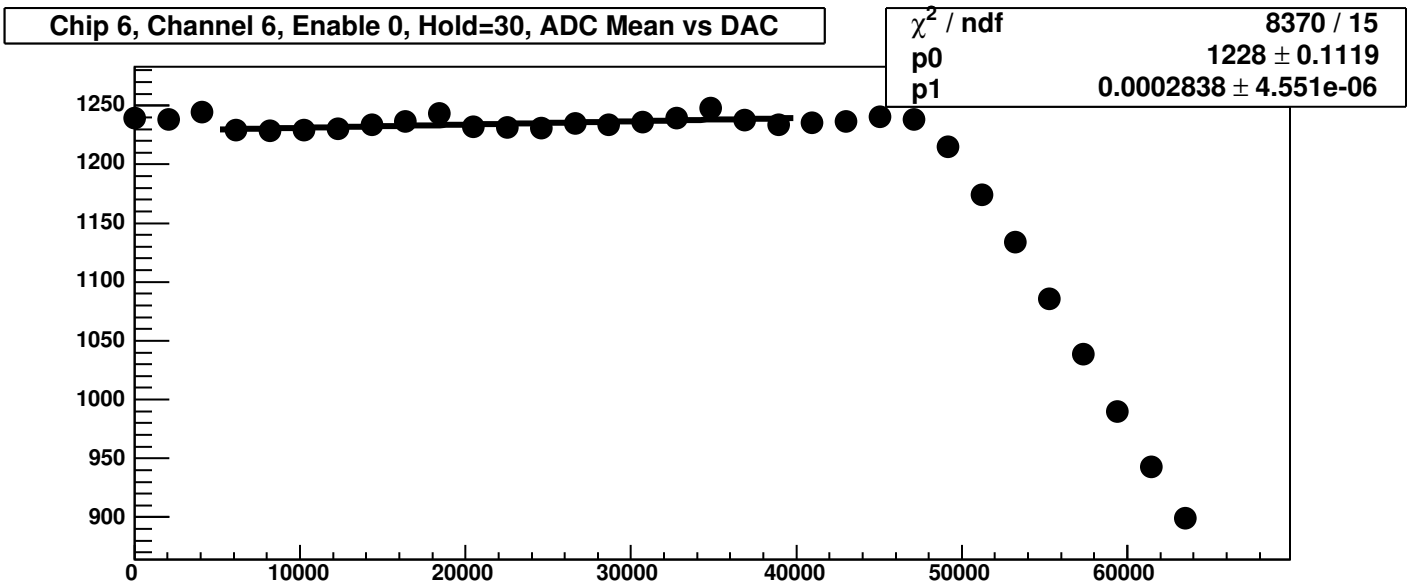

Chip 6, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

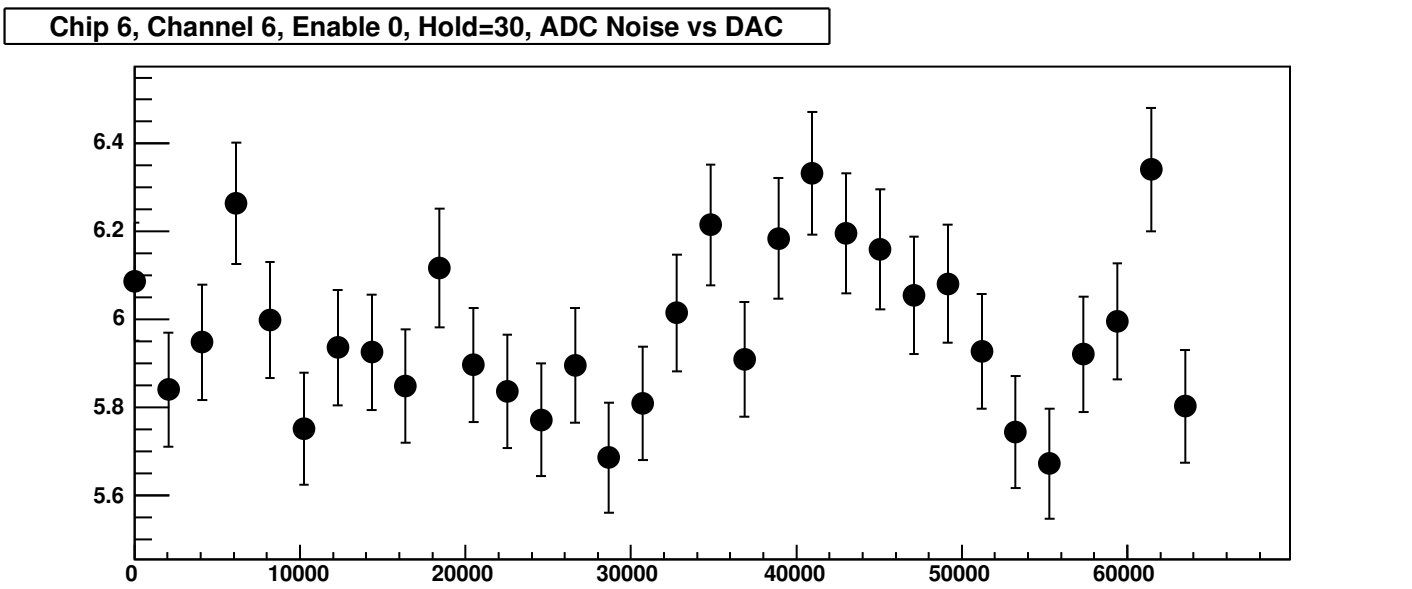

Chip 6, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

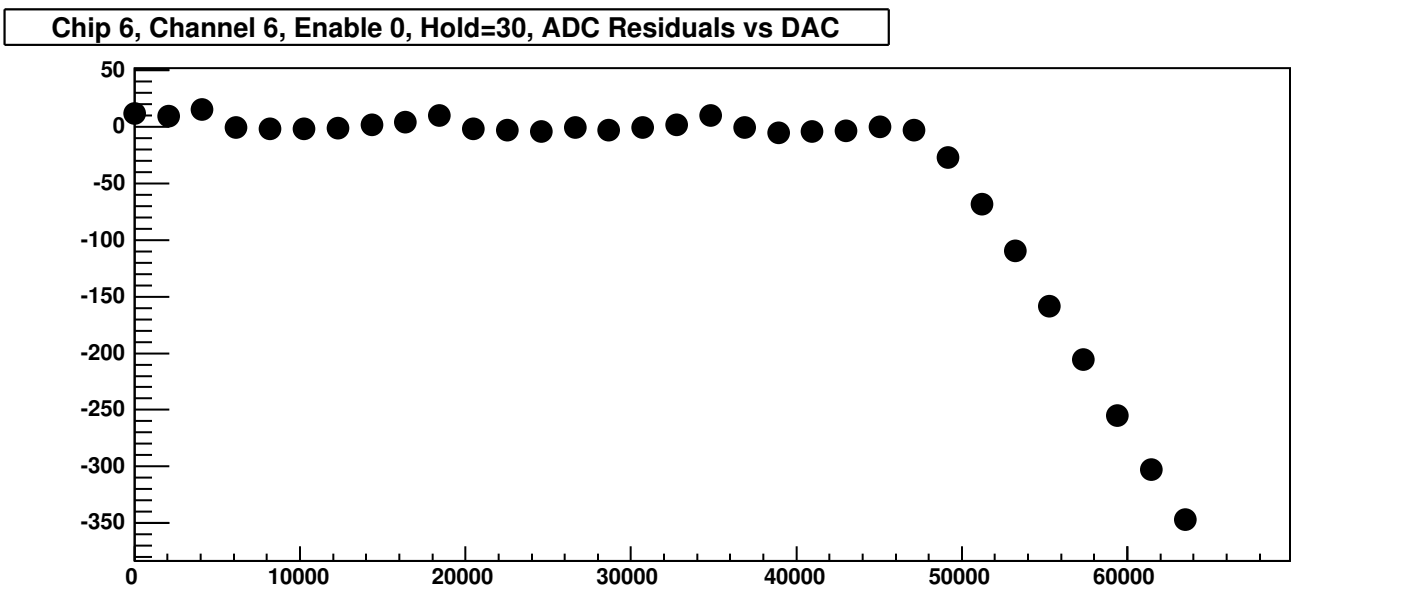

Chip 6, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.097e+04 / 15
p0 1384 ± 0.118
p1 -0.0005253 ± 4.784e-06
1400
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

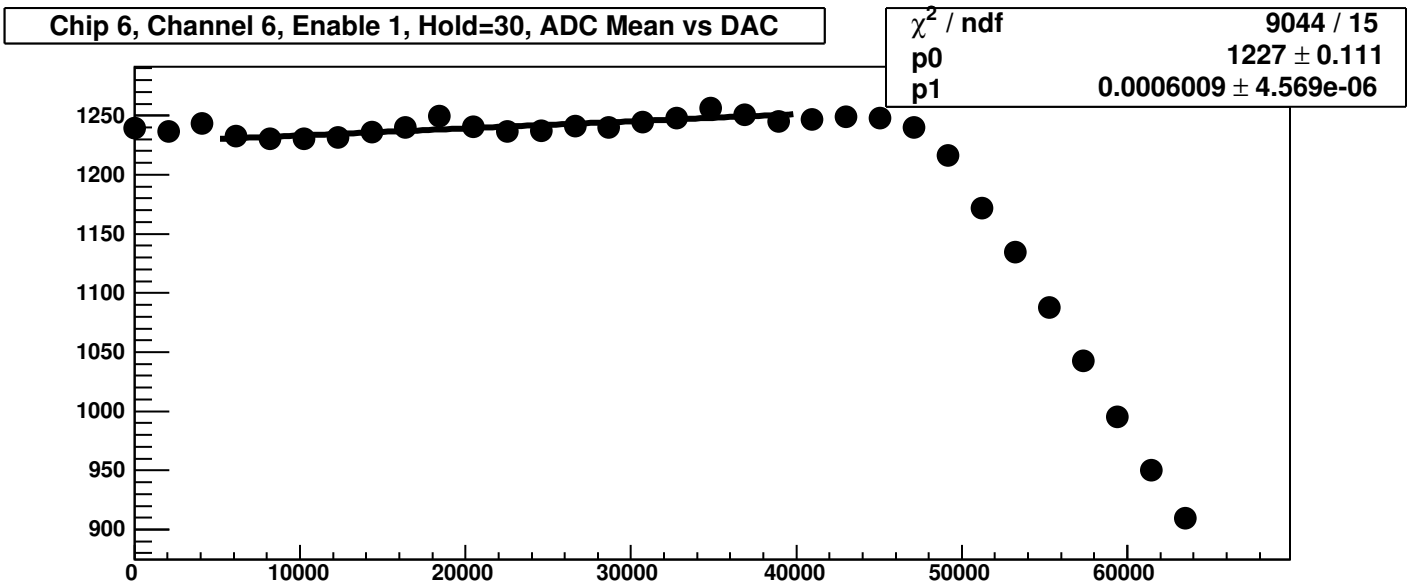

Chip 6, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

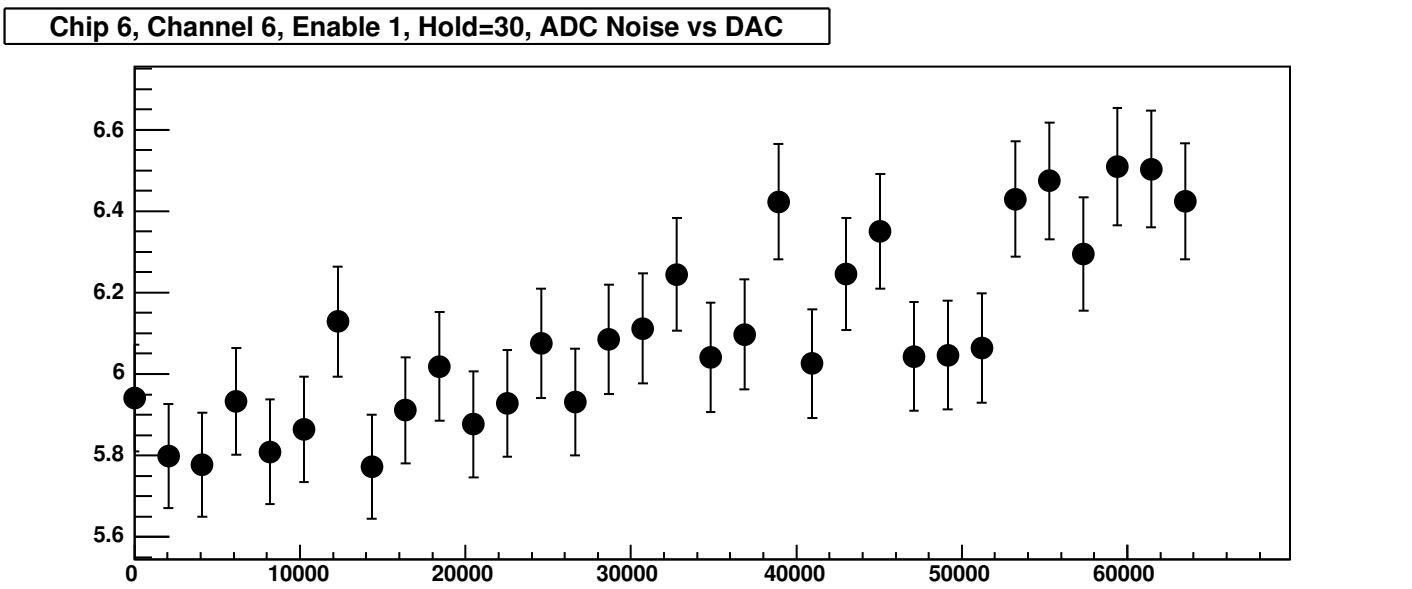

Chip 6, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

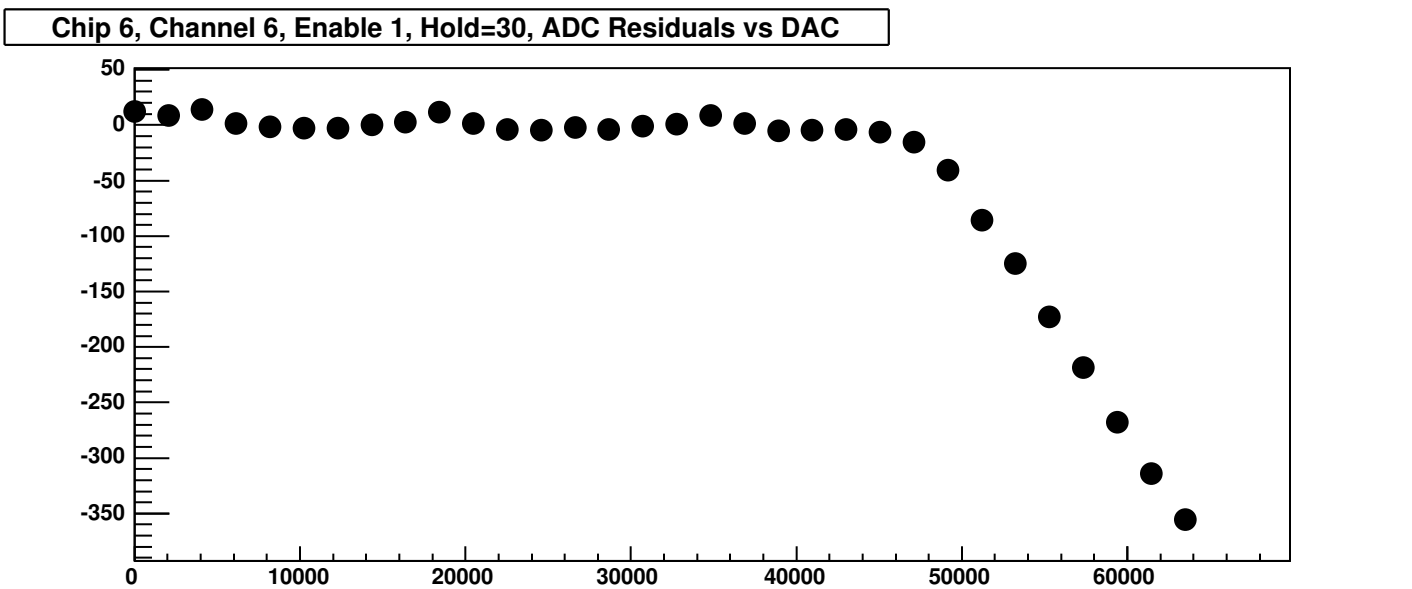

Chip 6, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8370 / 15
p0 1228 ± 0.1119
p1 0.0002838 ± 4.551e-06
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

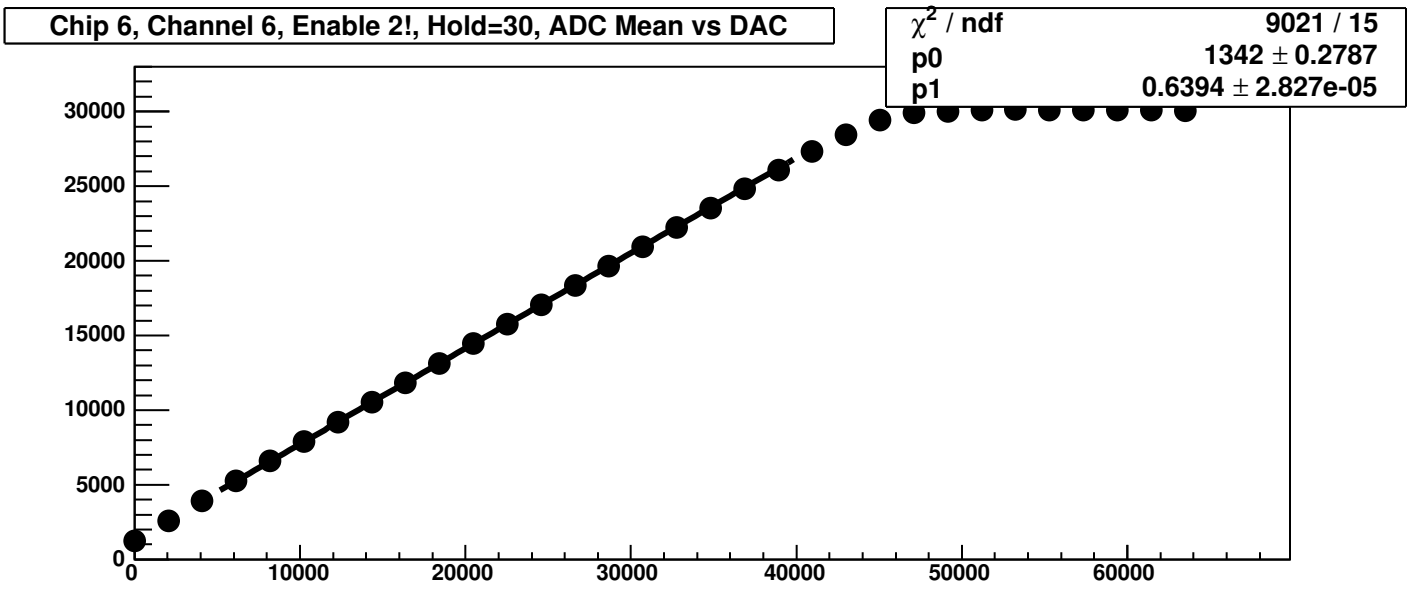

Chip 6, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

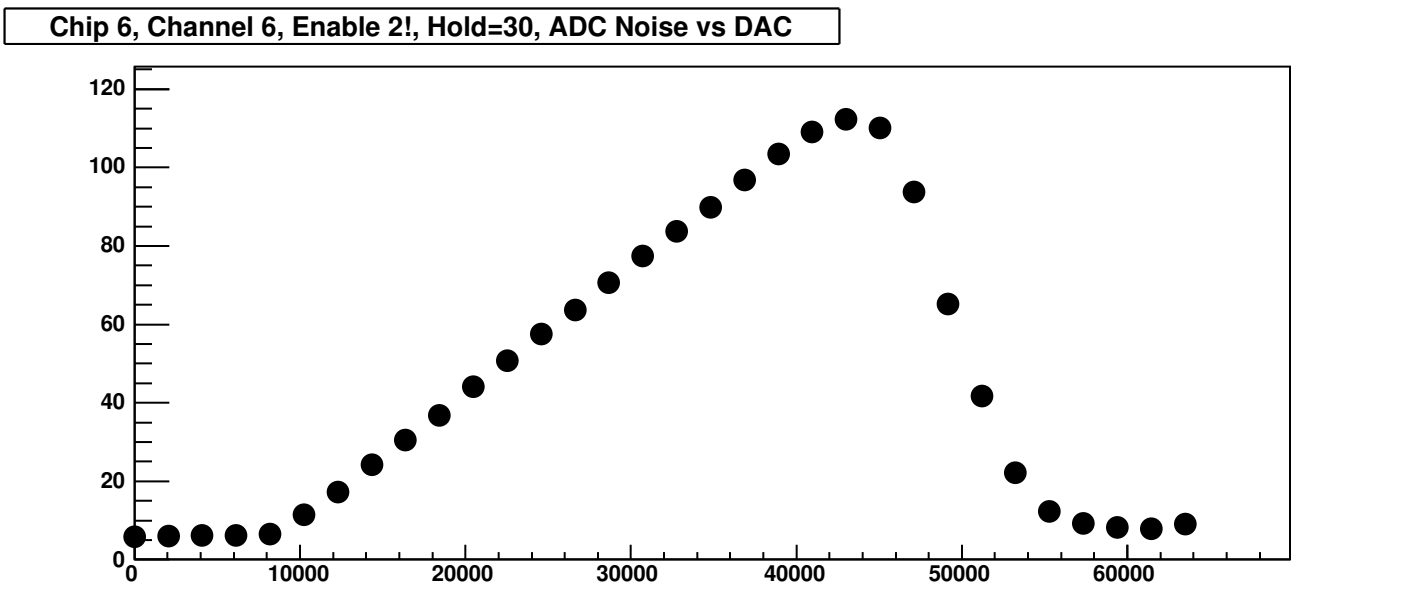

Chip 6, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

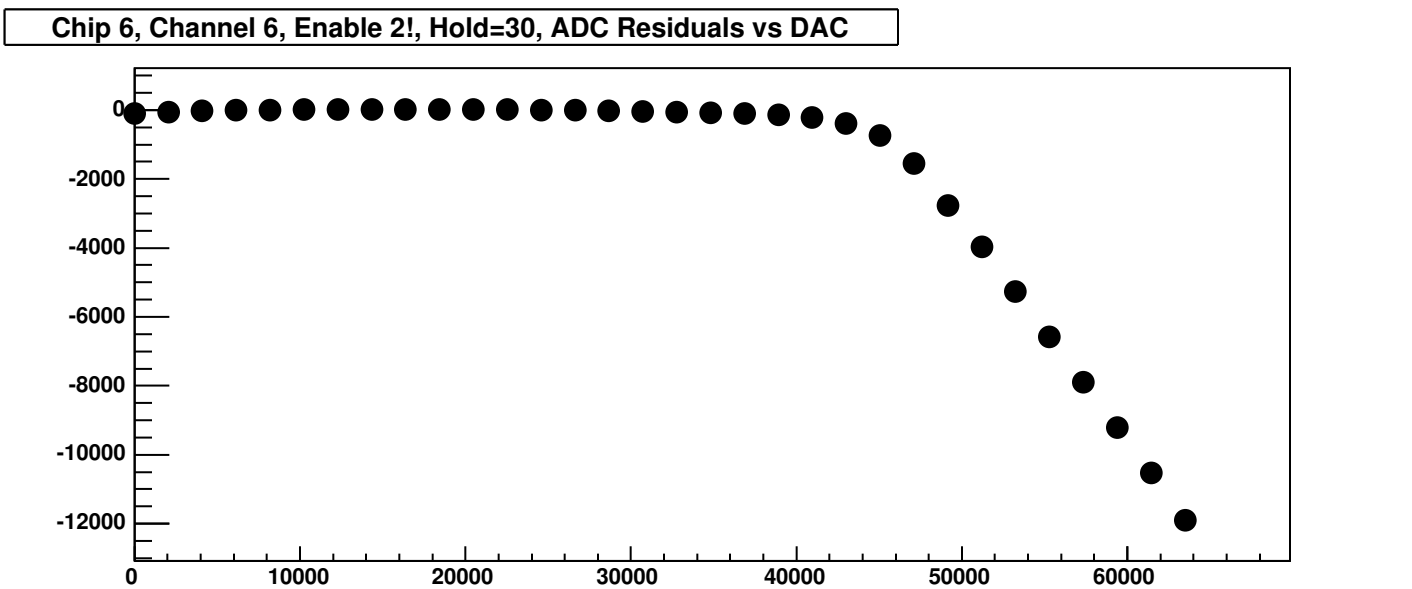

Chip 6, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9044 / 15
p0 1227 ± 0.111
p1 0.0006009 ± 4.569e-06
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

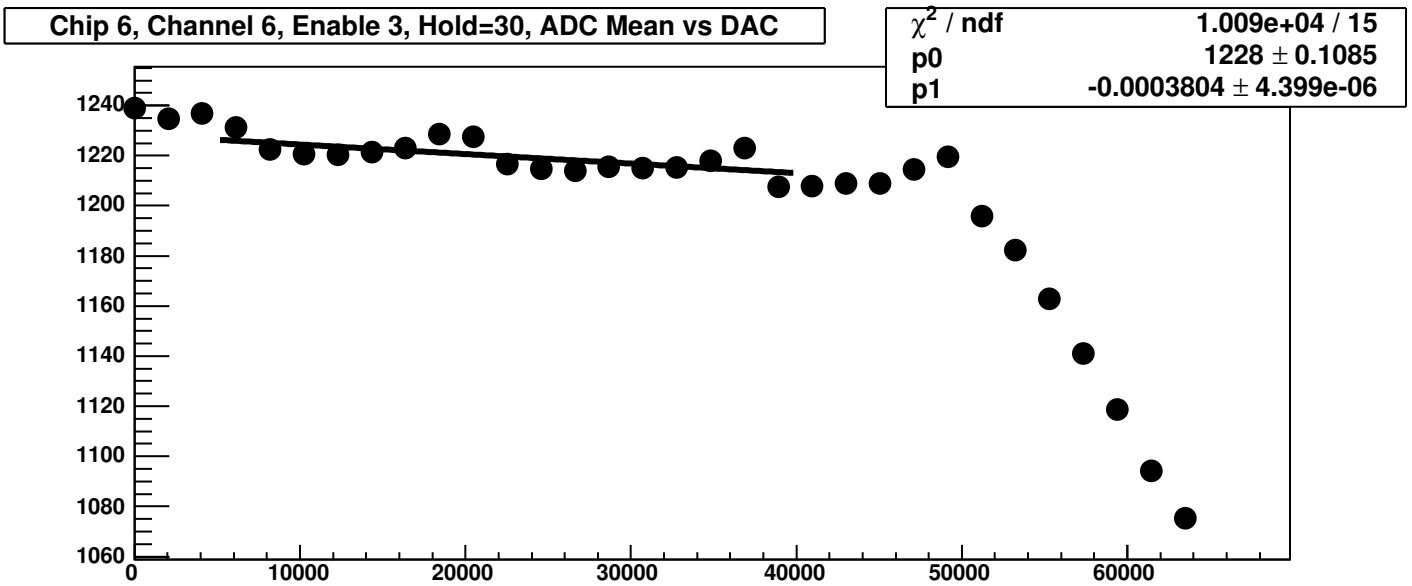

Chip 6, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

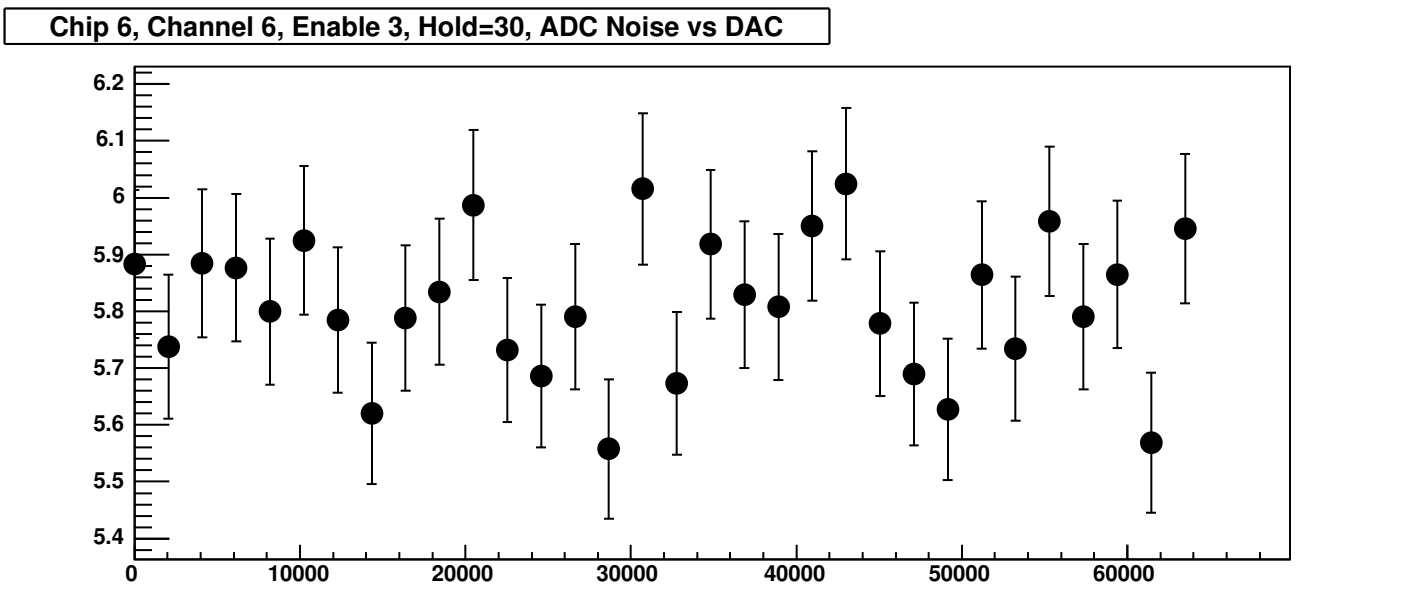

Chip 6, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

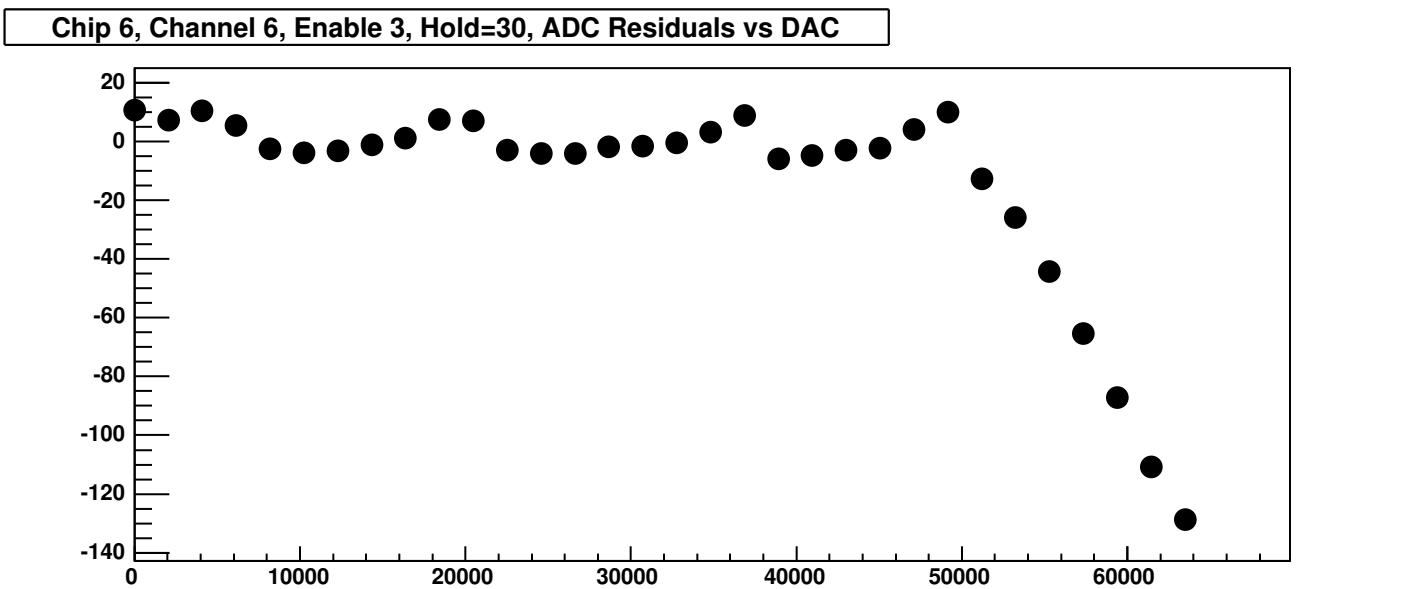

Chip 6, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 9021 / 15
p0 1342 ± 0.2787
p1 0.6394 ± 2.827e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

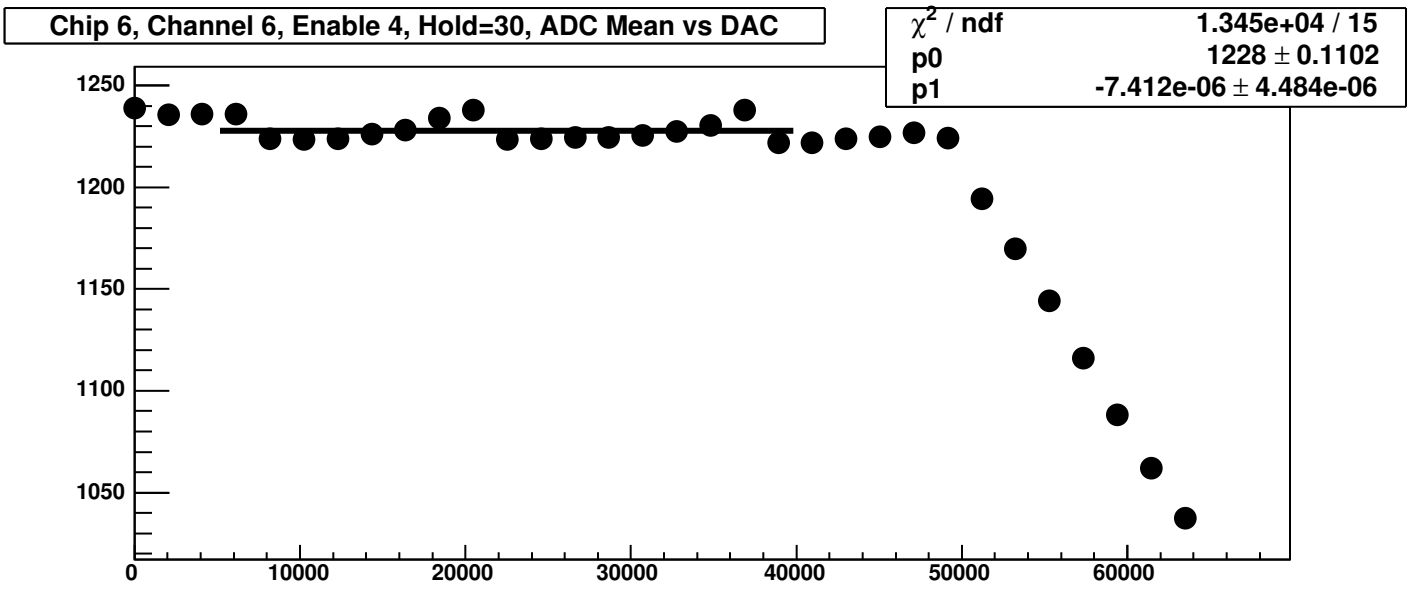

Chip 6, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

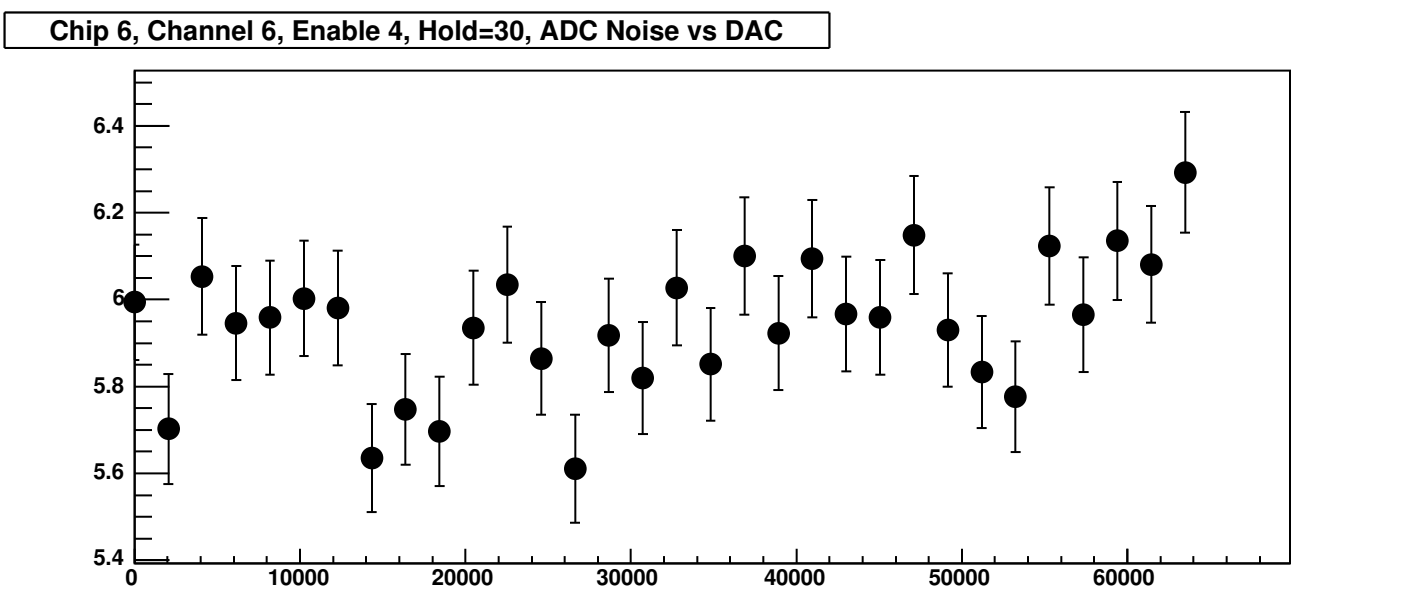

Chip 6, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

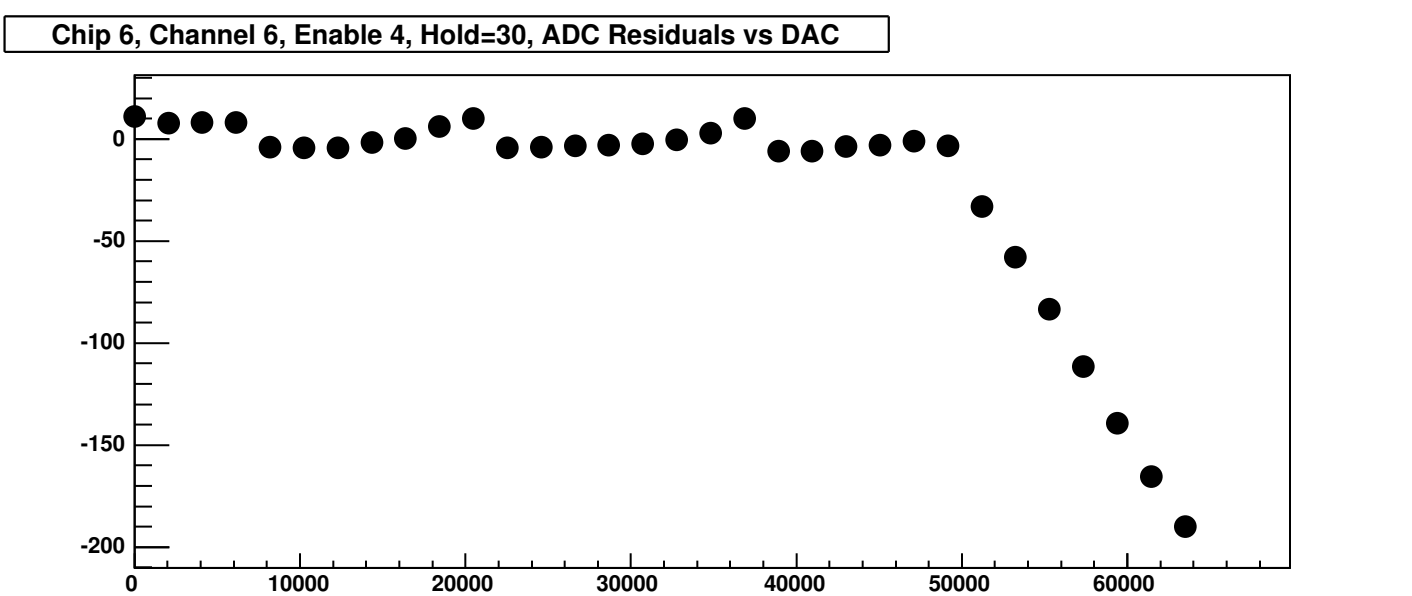

Chip 6, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 1.009e+04 / 15
p0 1228 ± 0.1085
p1 -0.0003804 ± 4.399e-06
1240
1220
1200
1180
1160
1140
1120
1100
1080
1060
0
10000
20000
30000
40000
50000
60000

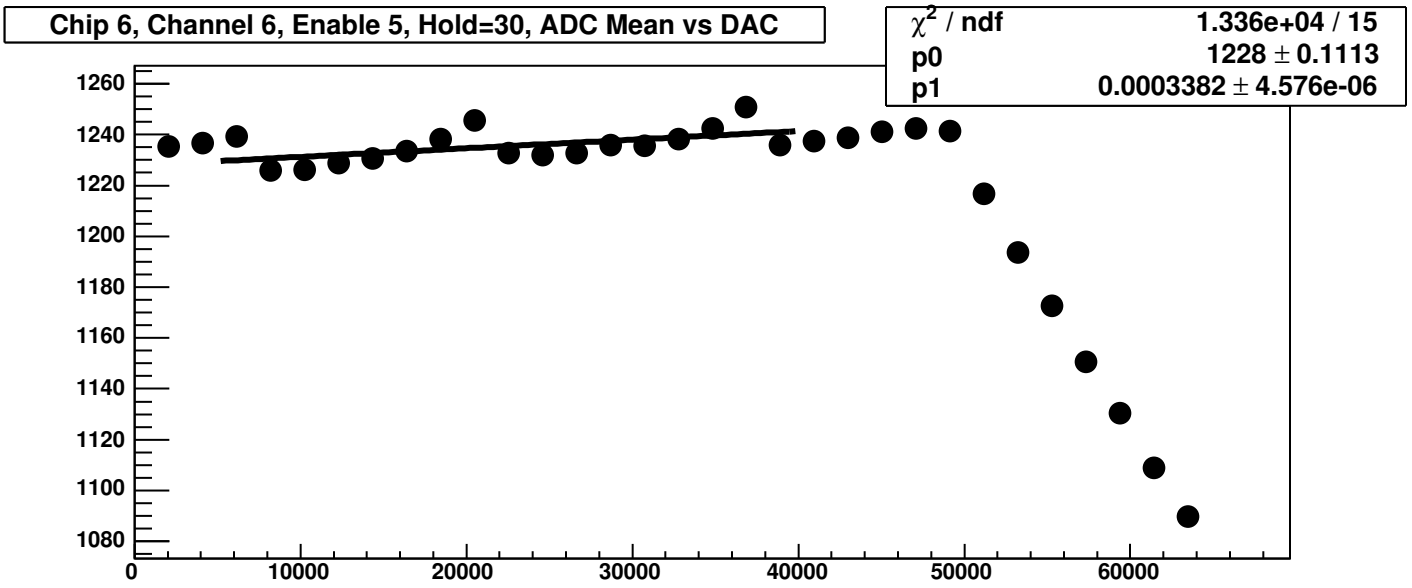

Chip 6, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

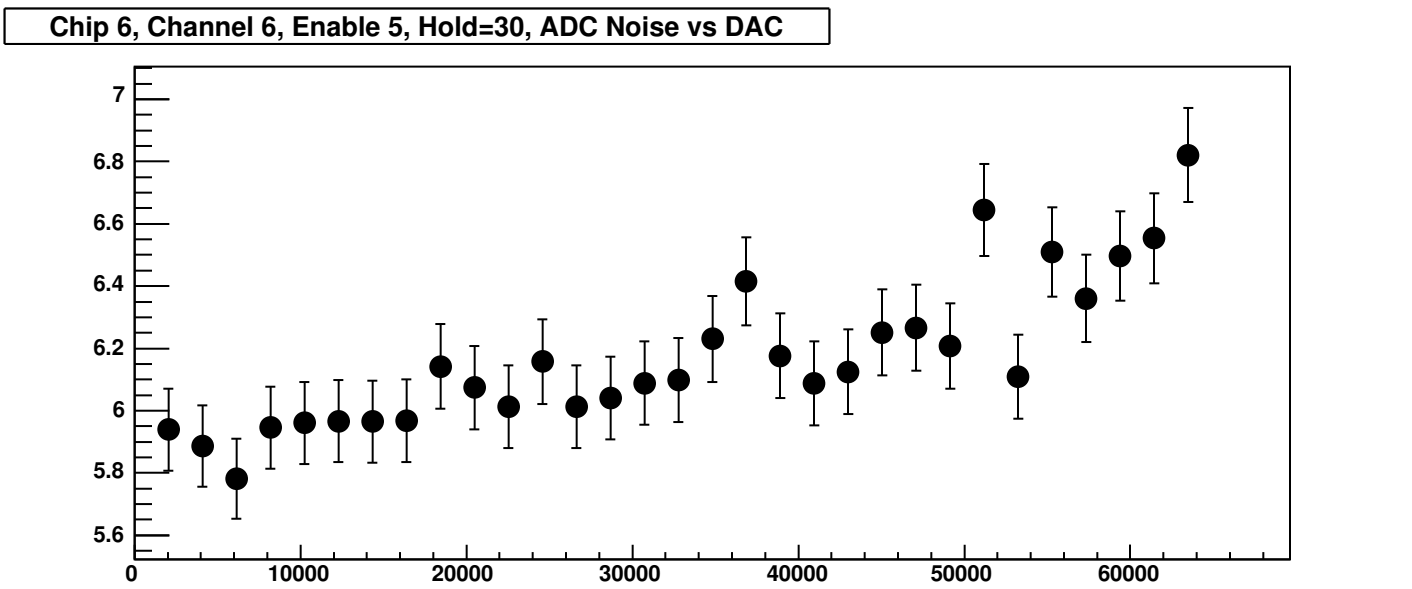

Chip 6, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

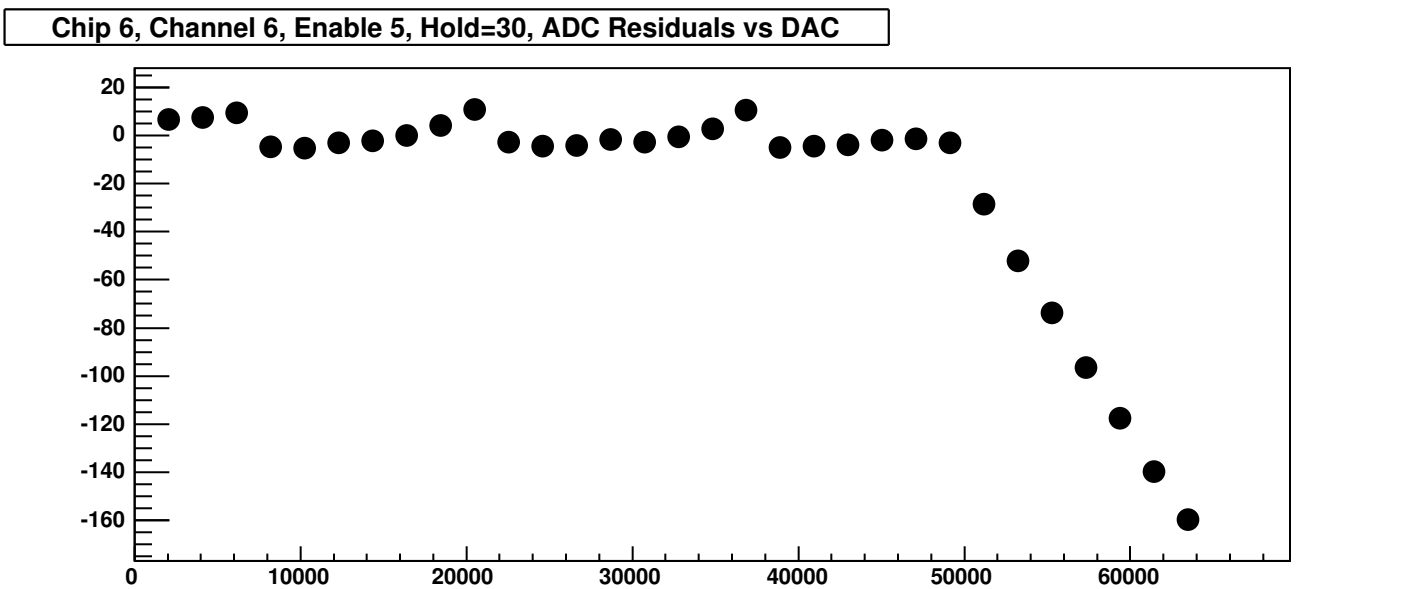

Chip 6, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.345e+04 / 15
p0 1228 ± 0.1102
p1 -7.412e-06 ± 4.484e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

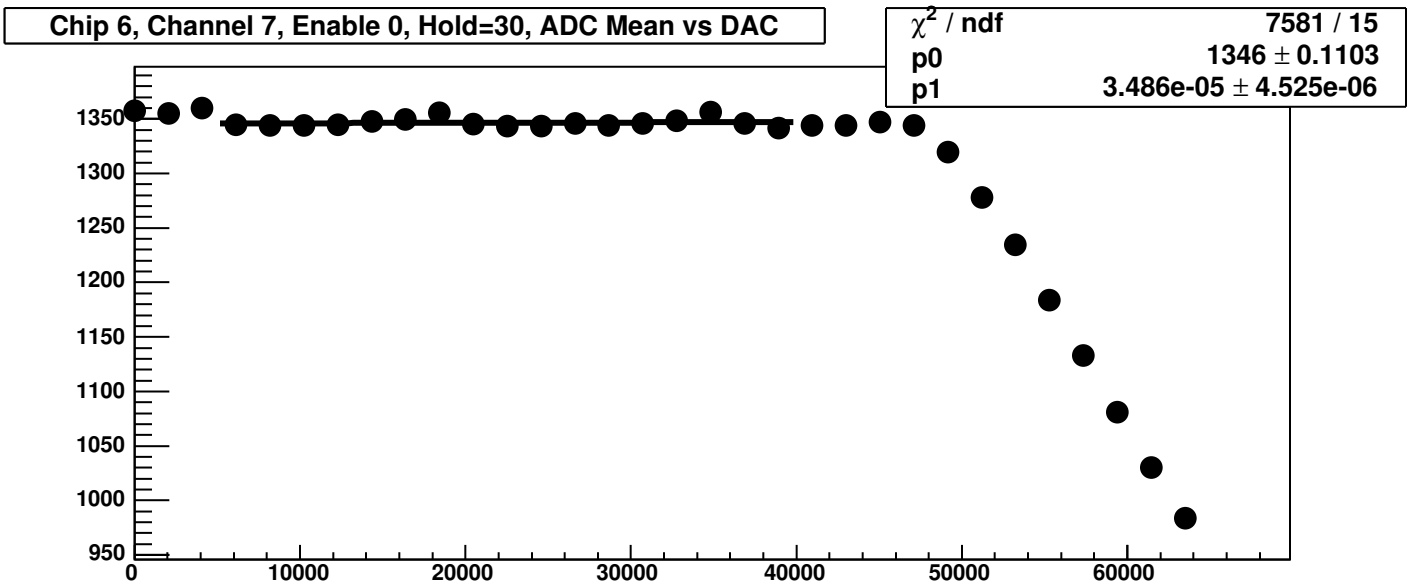

Chip 6, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

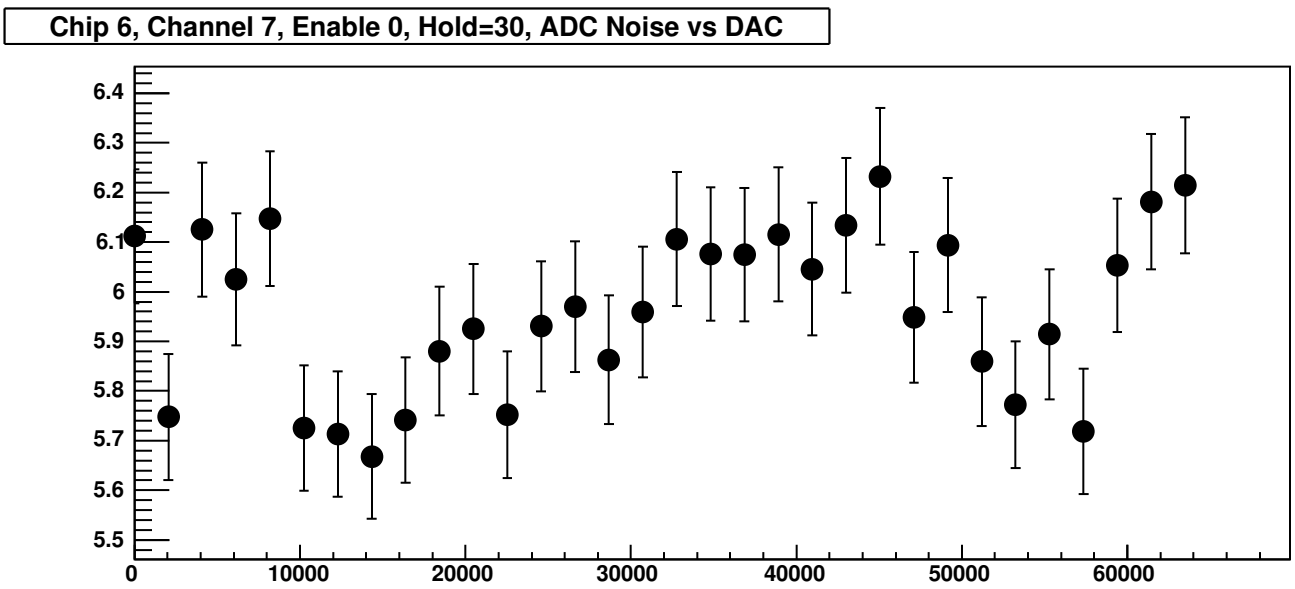

Chip 6, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

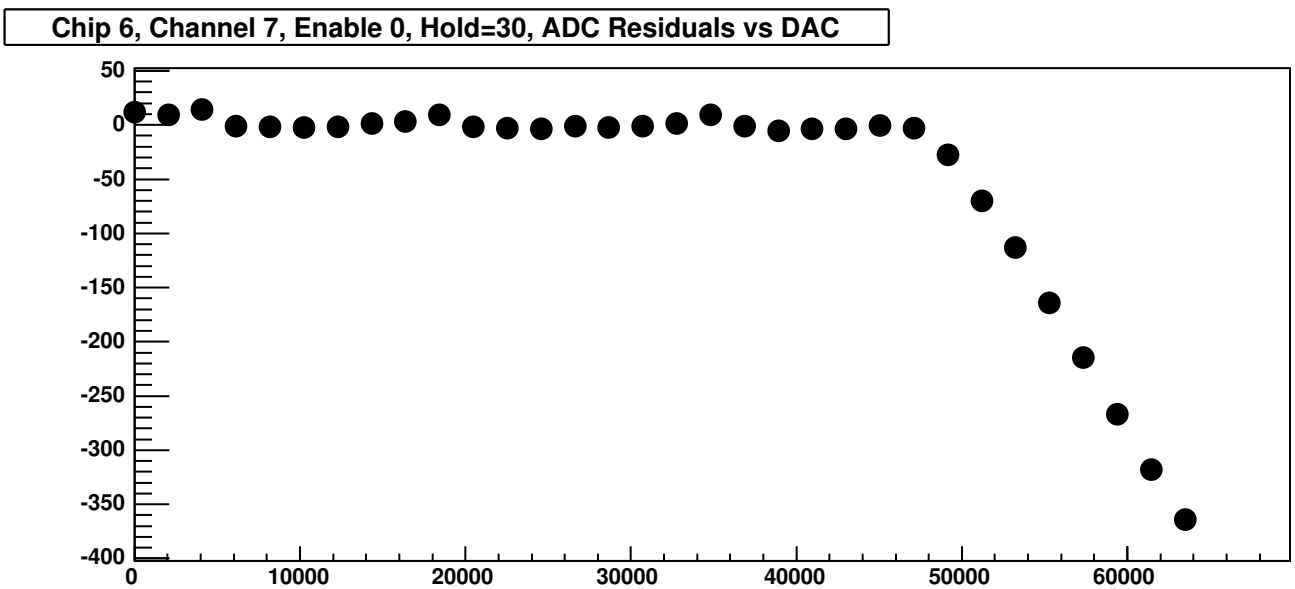

Chip 6, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.336e+04 / 15
p0 1228 ± 0.1113
p1 0.0003382 ± 4.576e-06
1260
1240
1220
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

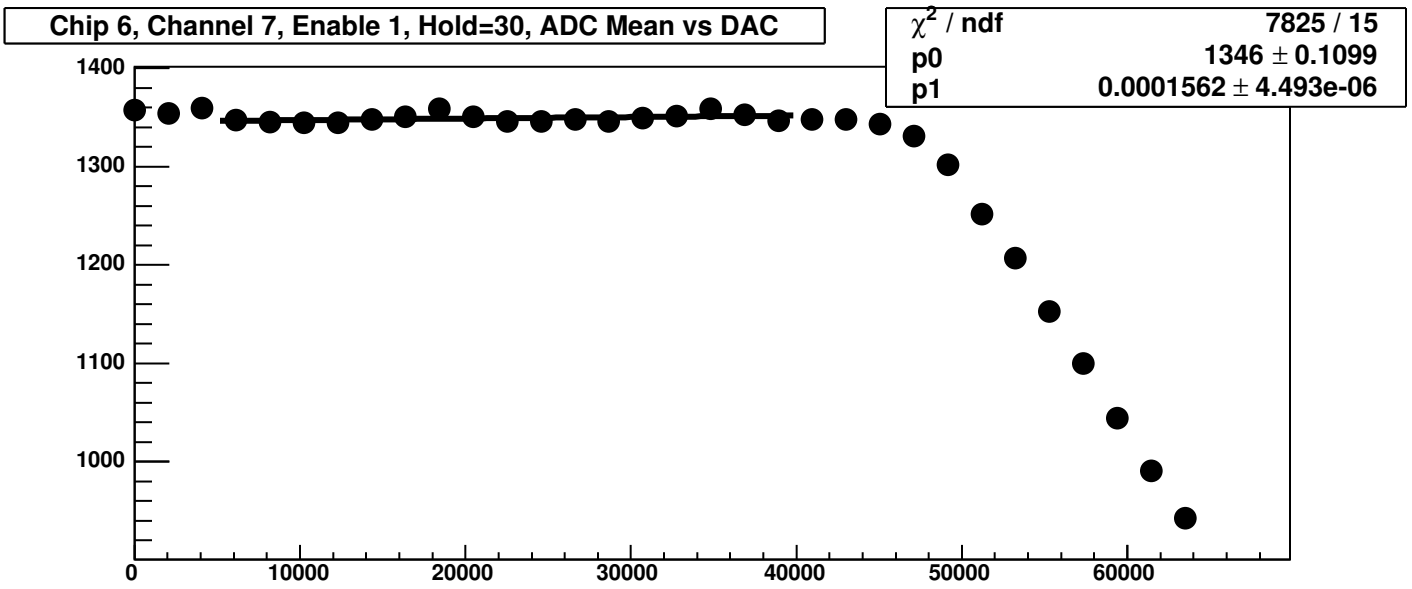

Chip 6, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

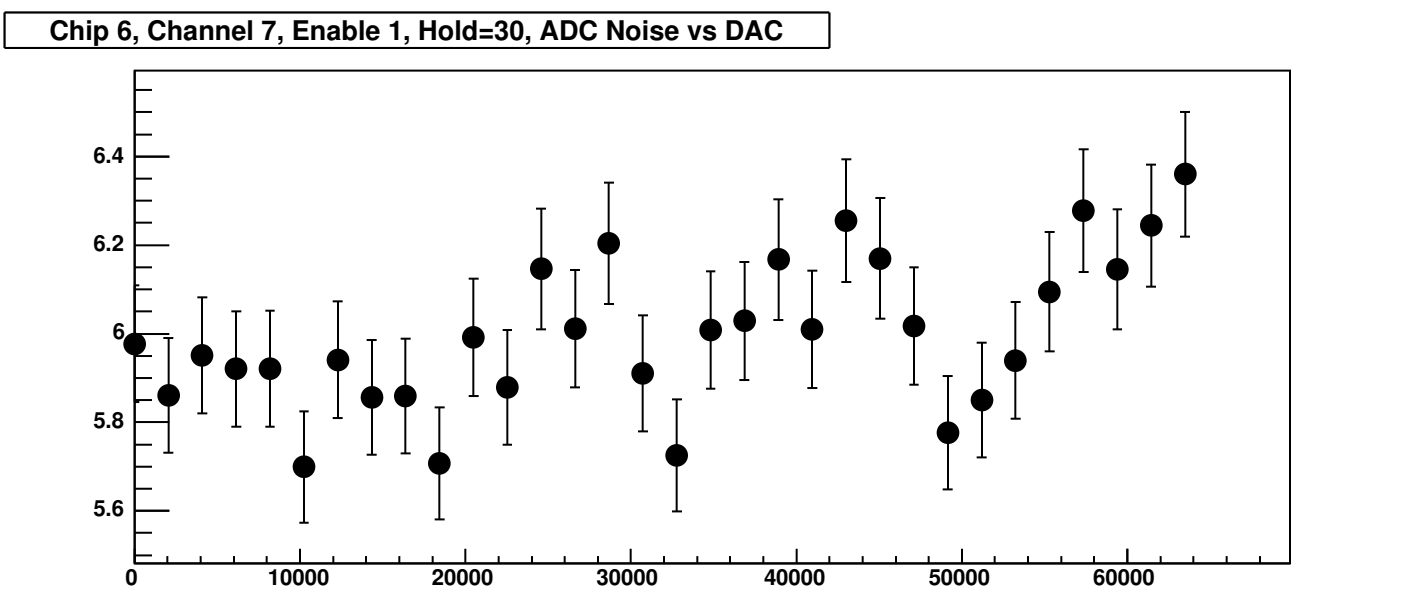

Chip 6, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

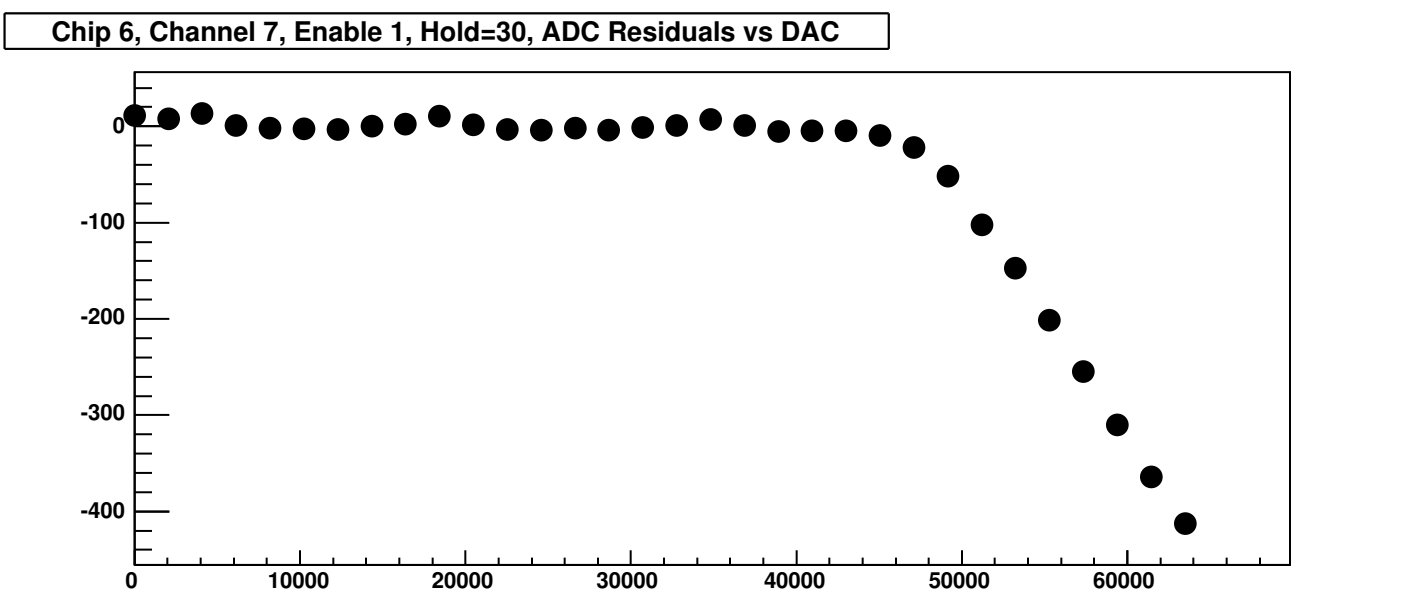

Chip 6, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7581 / 15
p0 1346 ± 0.1103
p1 3.486e-05 ± 4.525e-06
1350
1300
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

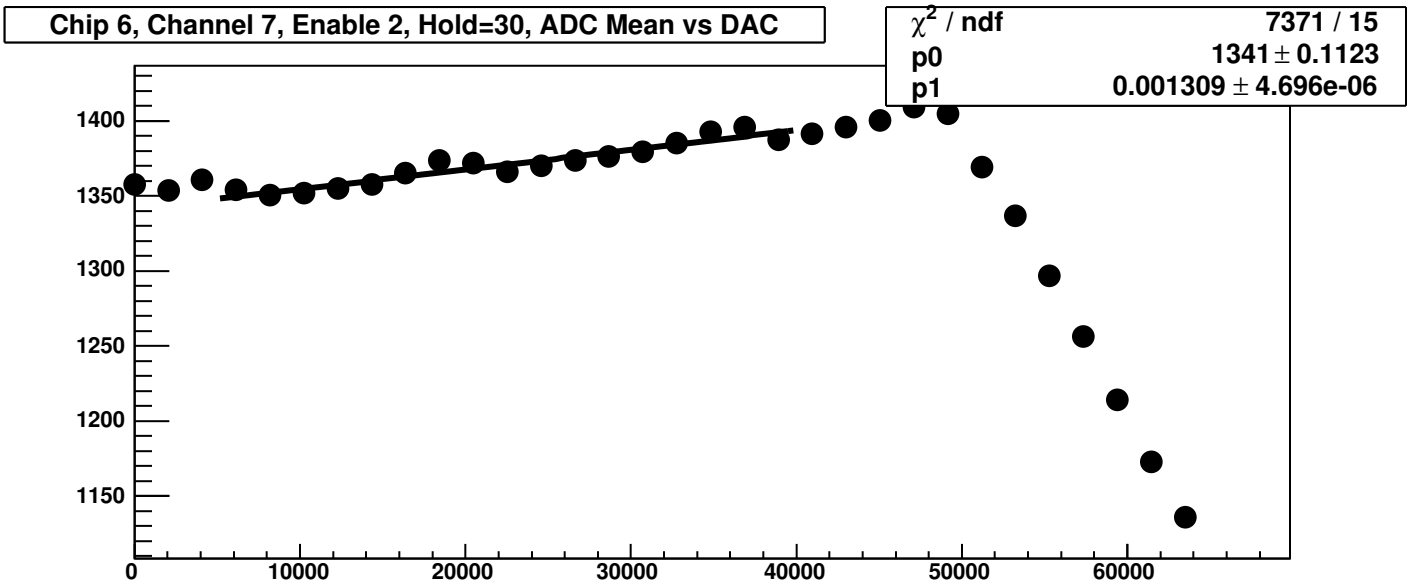

Chip 6, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

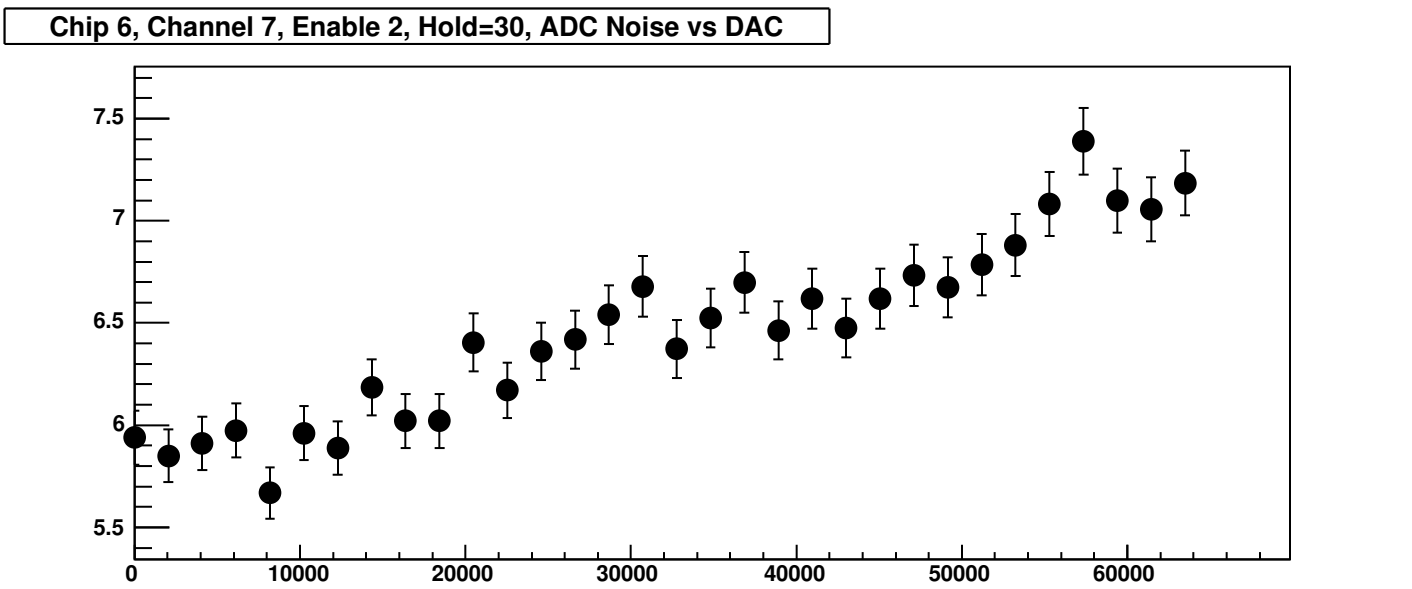

Chip 6, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

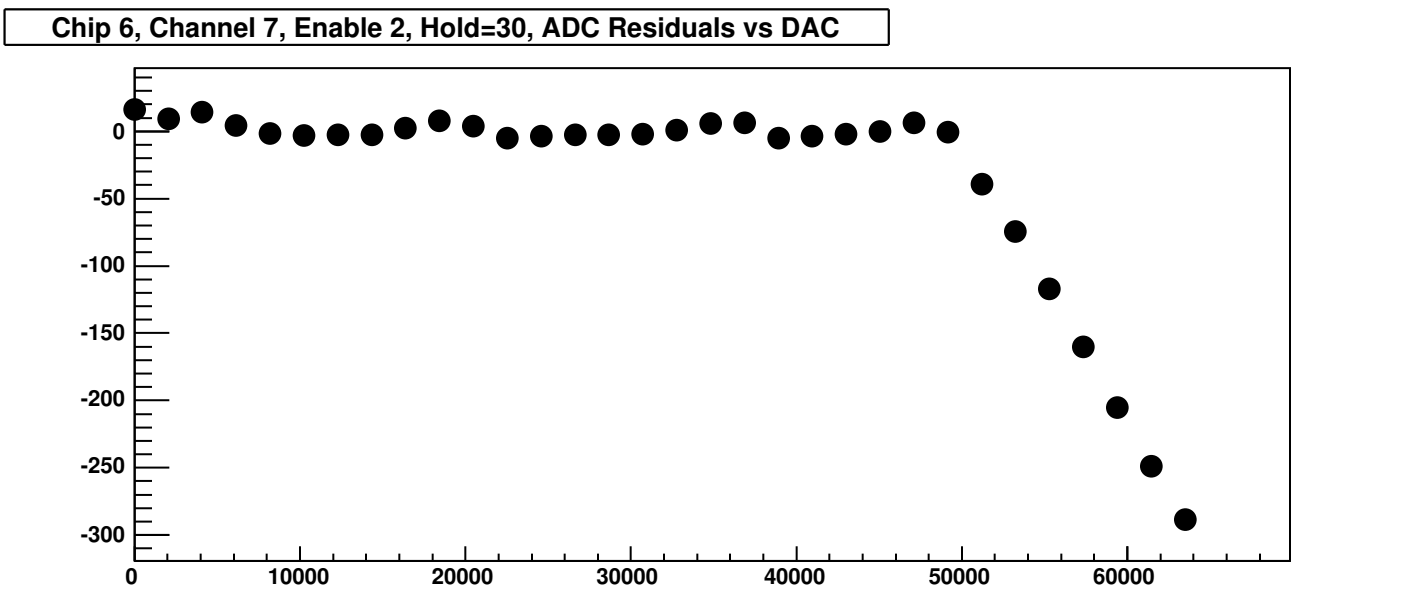

Chip 6, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7825 / 15
p0 1346 ± 0.1099
p1 0.0001562 ± 4.493e-06
1400
1300
1200
1100
1000
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

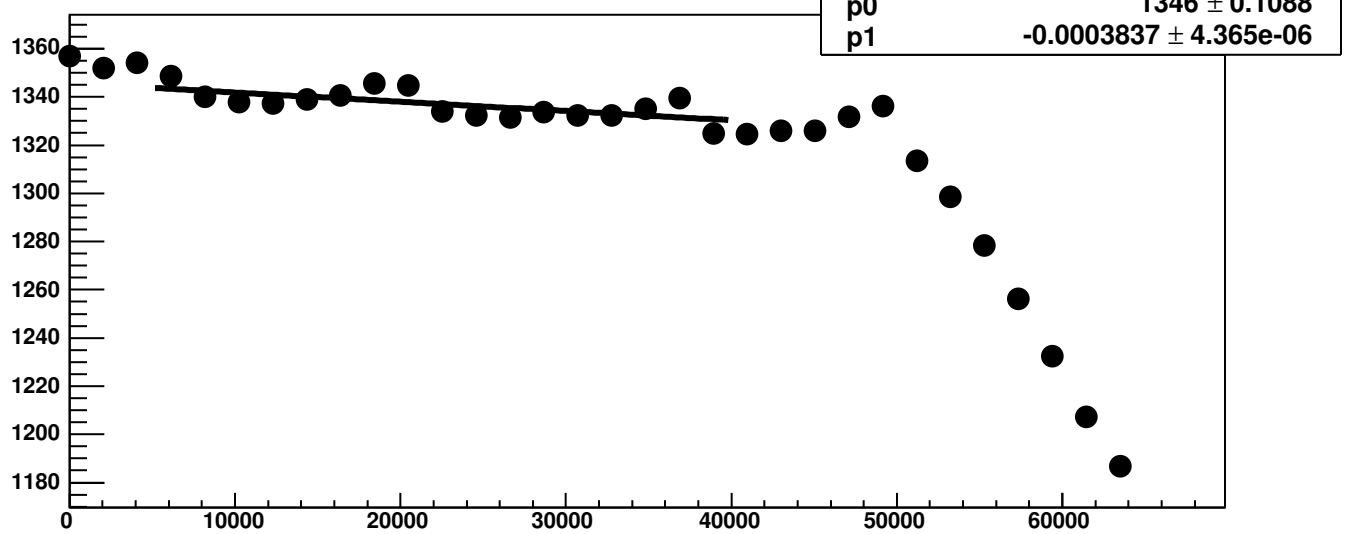

Chip 6, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7371 / 15
p0 1341 ± 0.1123
p1 0.001309 ± 4.696e-06
1400
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

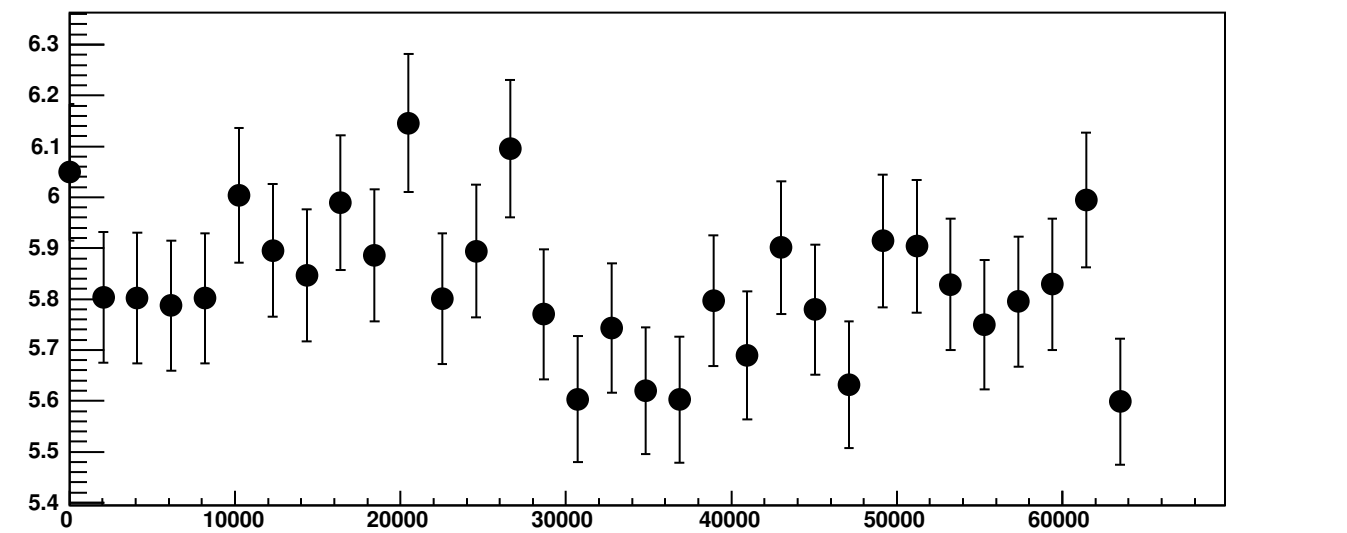

Chip 6, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

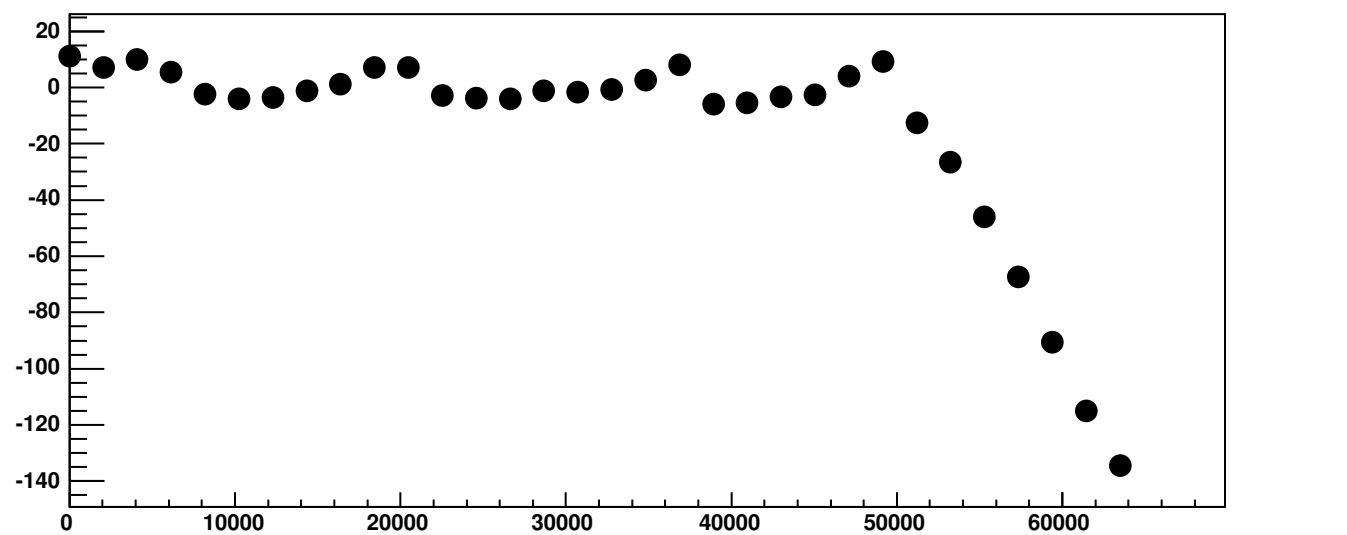
Chip 6, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

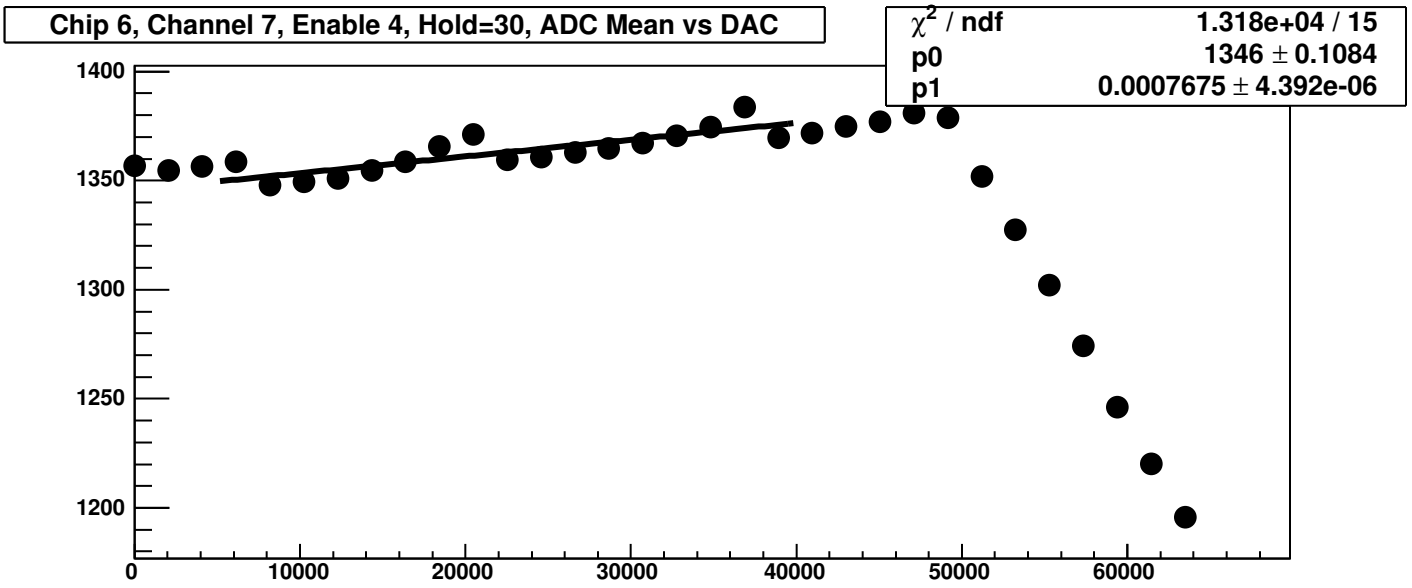


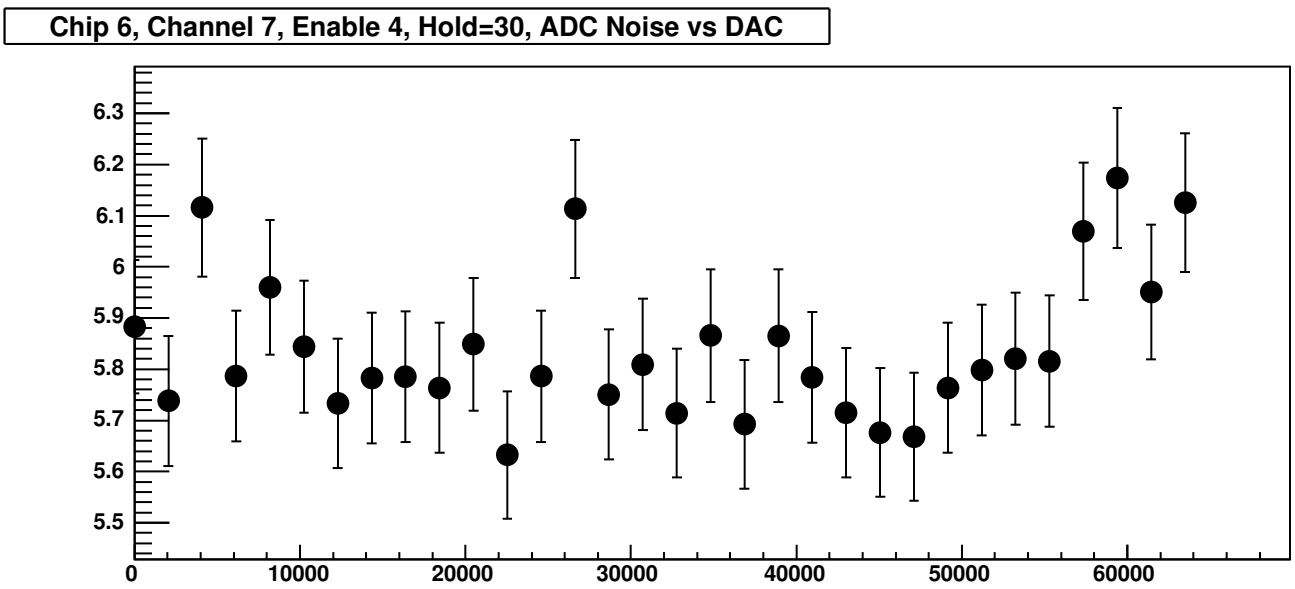
Chip 6, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

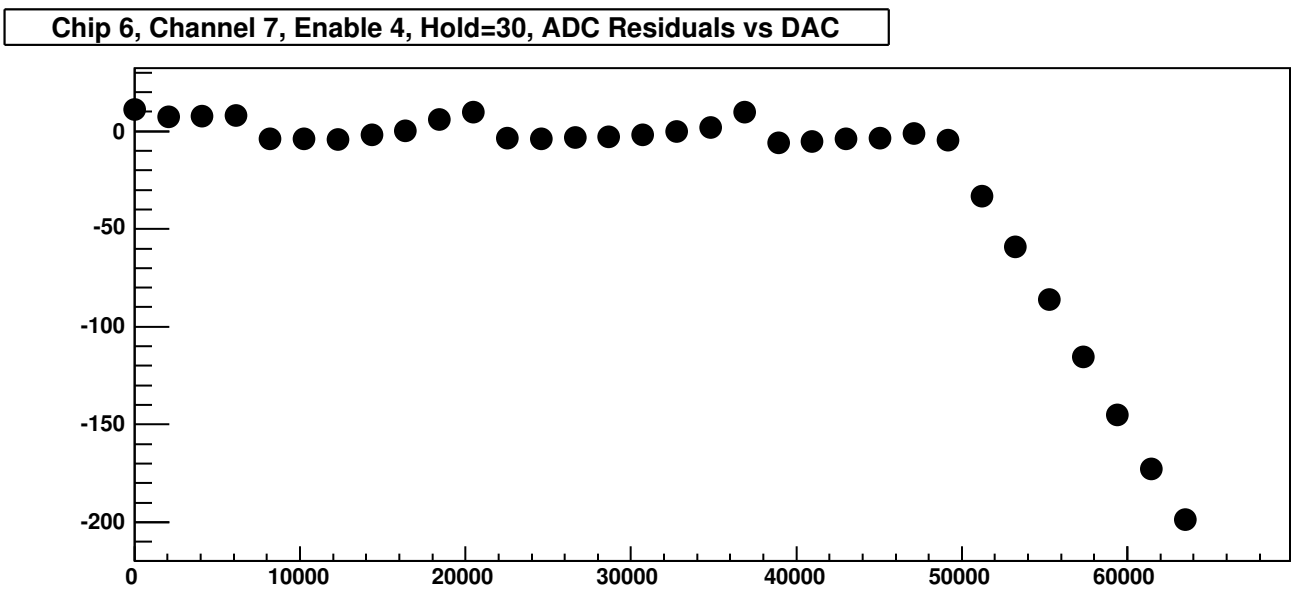


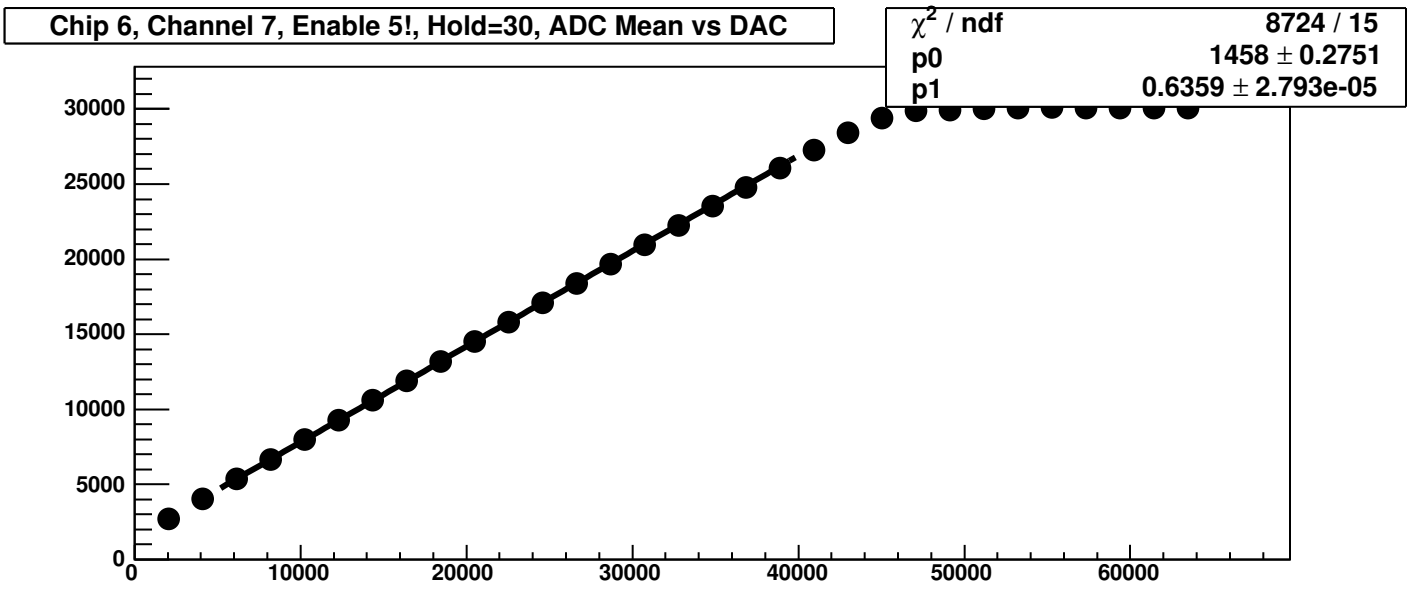
Chip 6, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

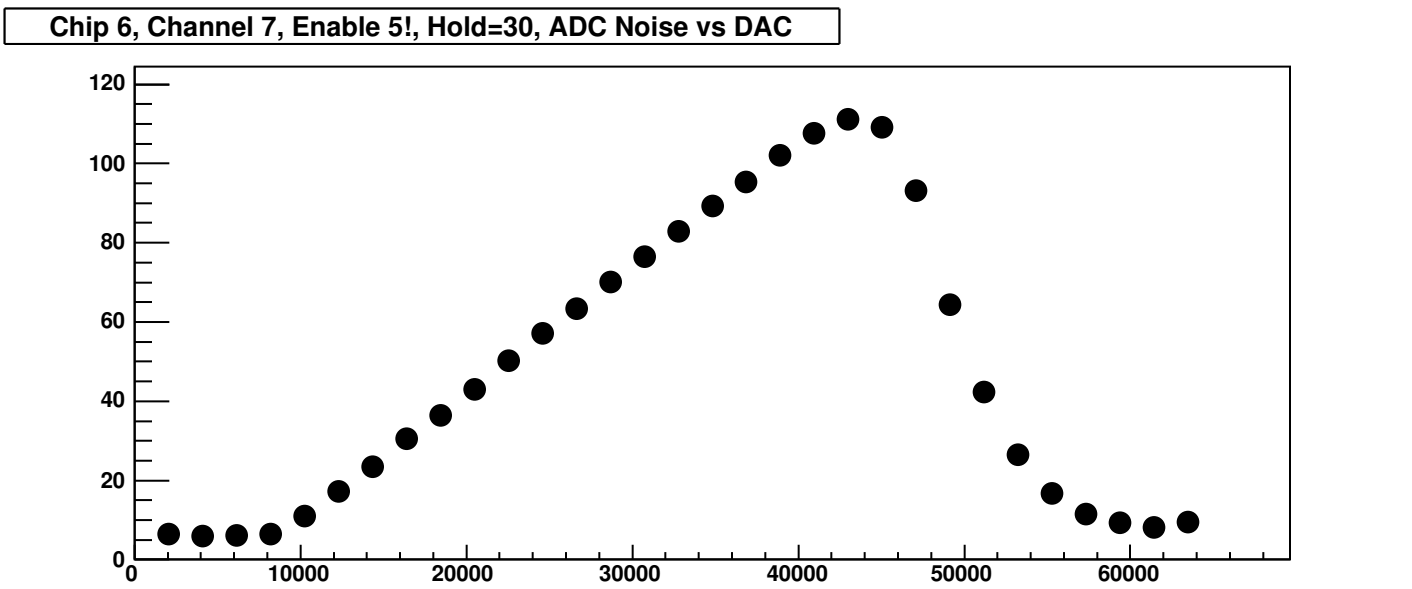


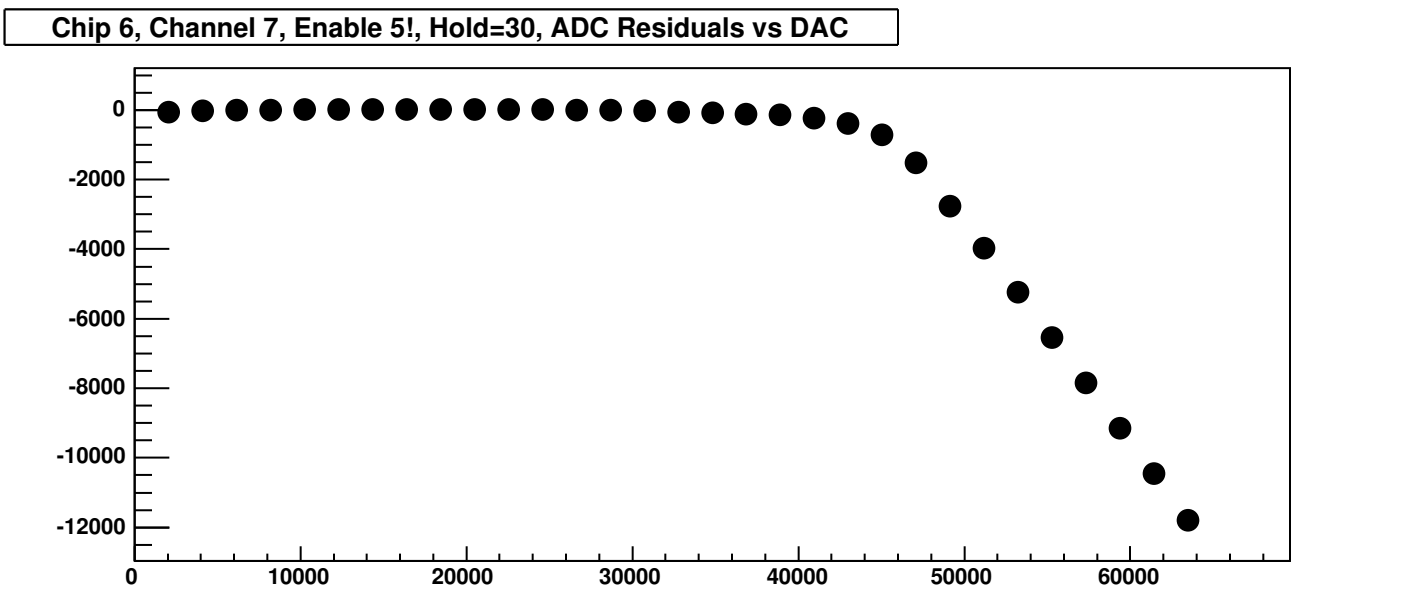

Chip 6, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.318e+04 / 15
p0 1346 ± 0.1084
p1 0.0007675 ± 4.392e-06
1400
1350
1300
1250
1200
0
10000
20000
30000
40000
50000
60000

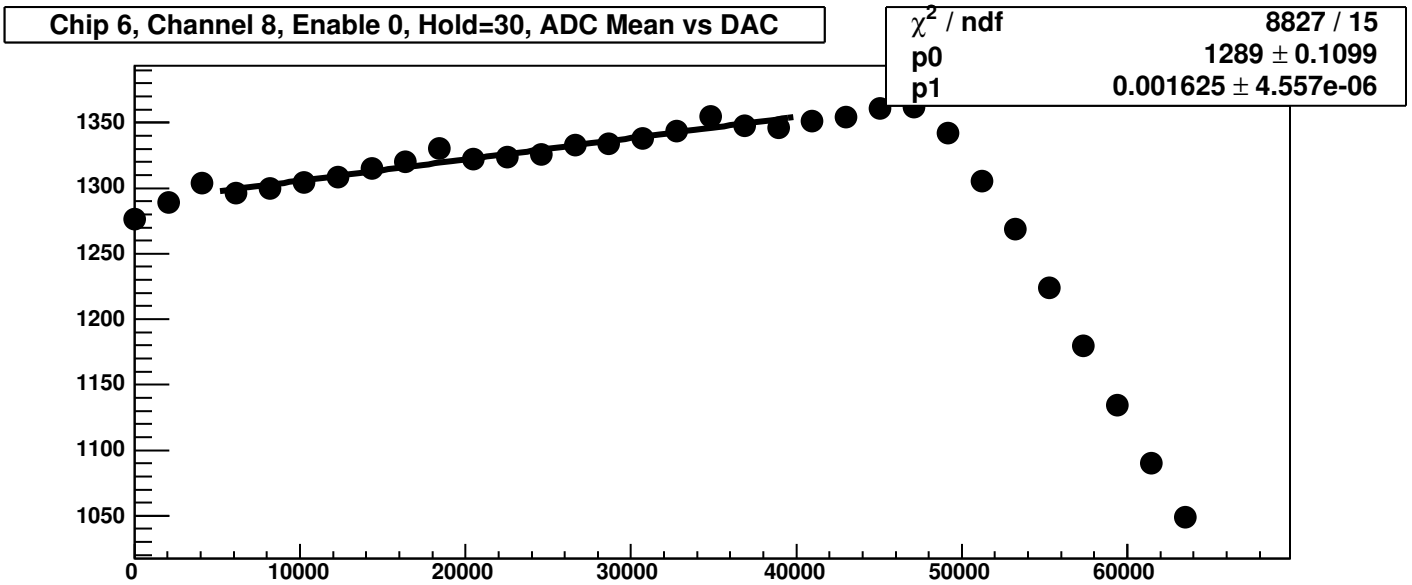

Chip 6, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

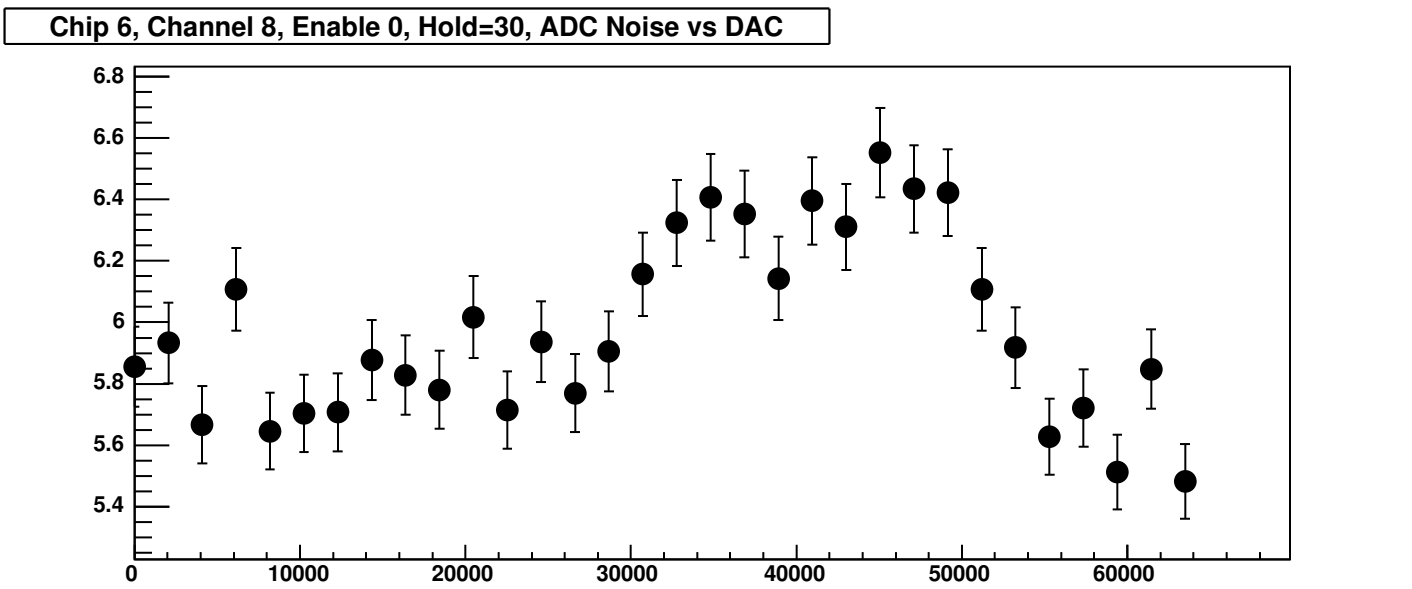

Chip 6, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

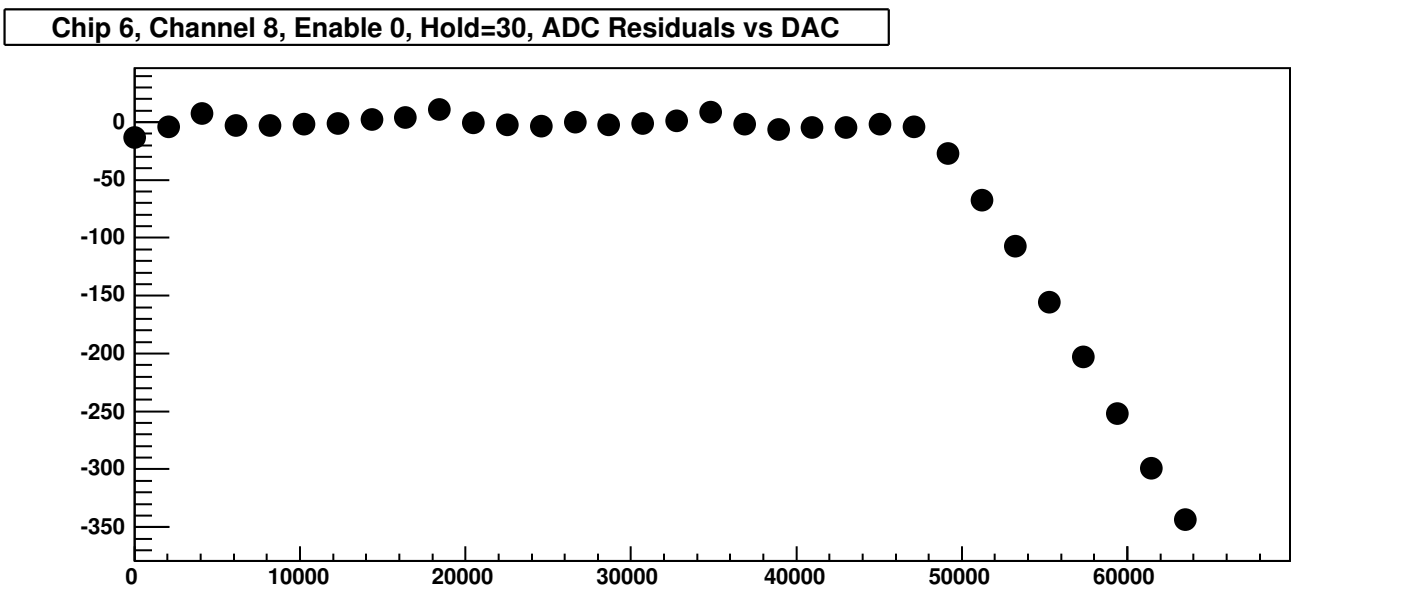

Chip 6, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 8724 / 15
p0 1458 ± 0.2751
p1 0.6359 ± 2.793e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

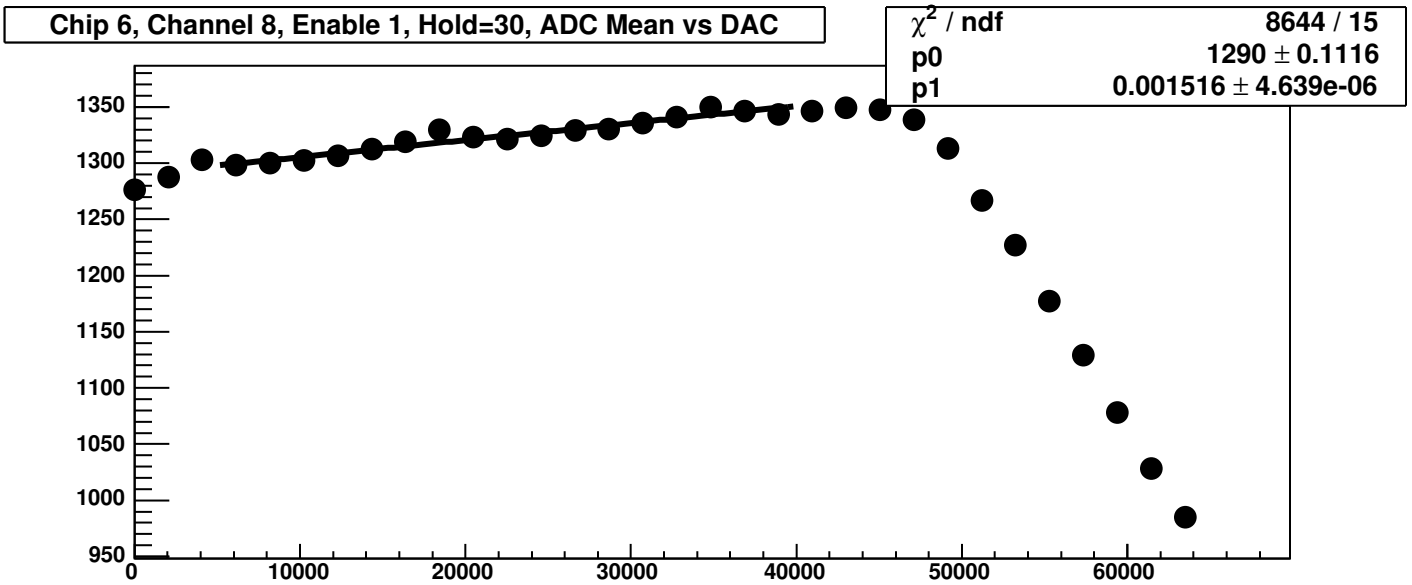

Chip 6, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

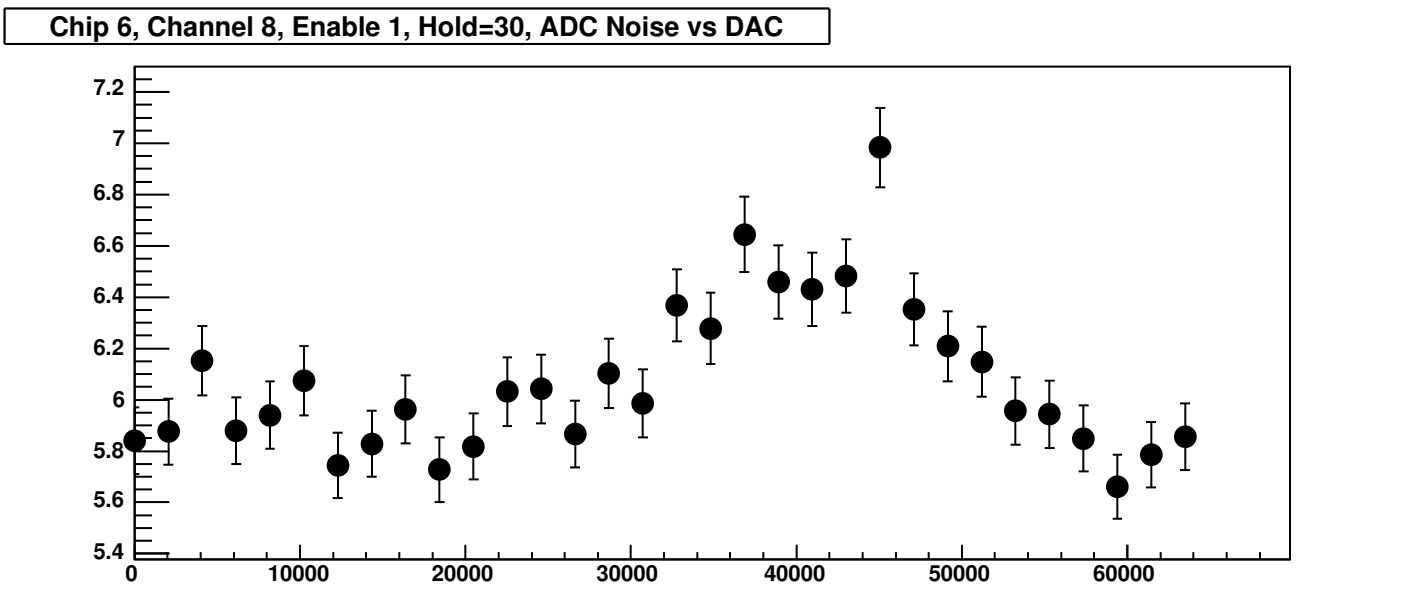

Chip 6, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

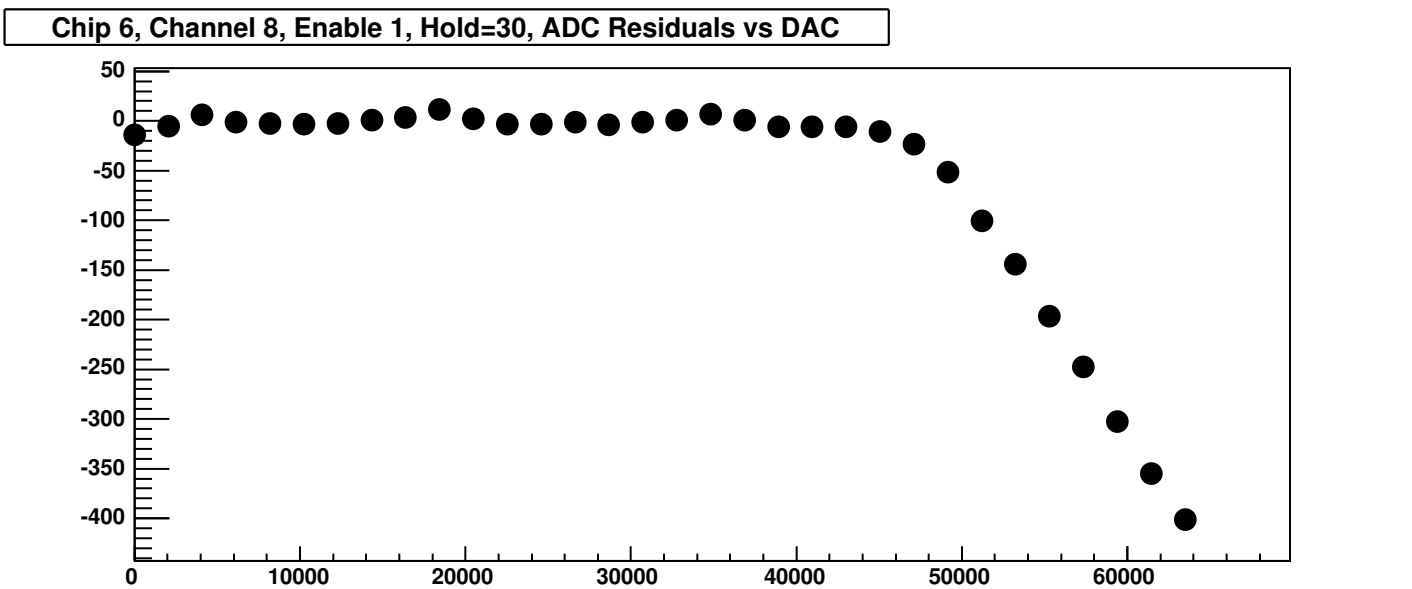

Chip 6, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8827 / 15
p0 1289 ± 0.1099
p1 0.001625 ± 4.557e-06
1350
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

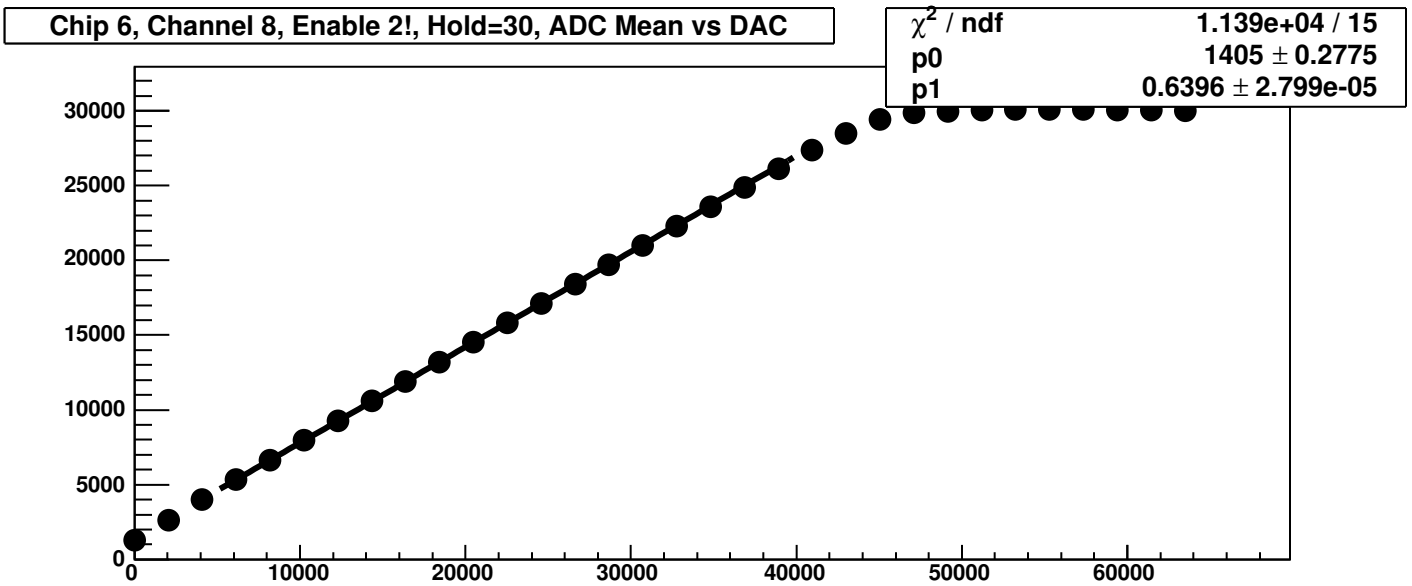

Chip 6, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

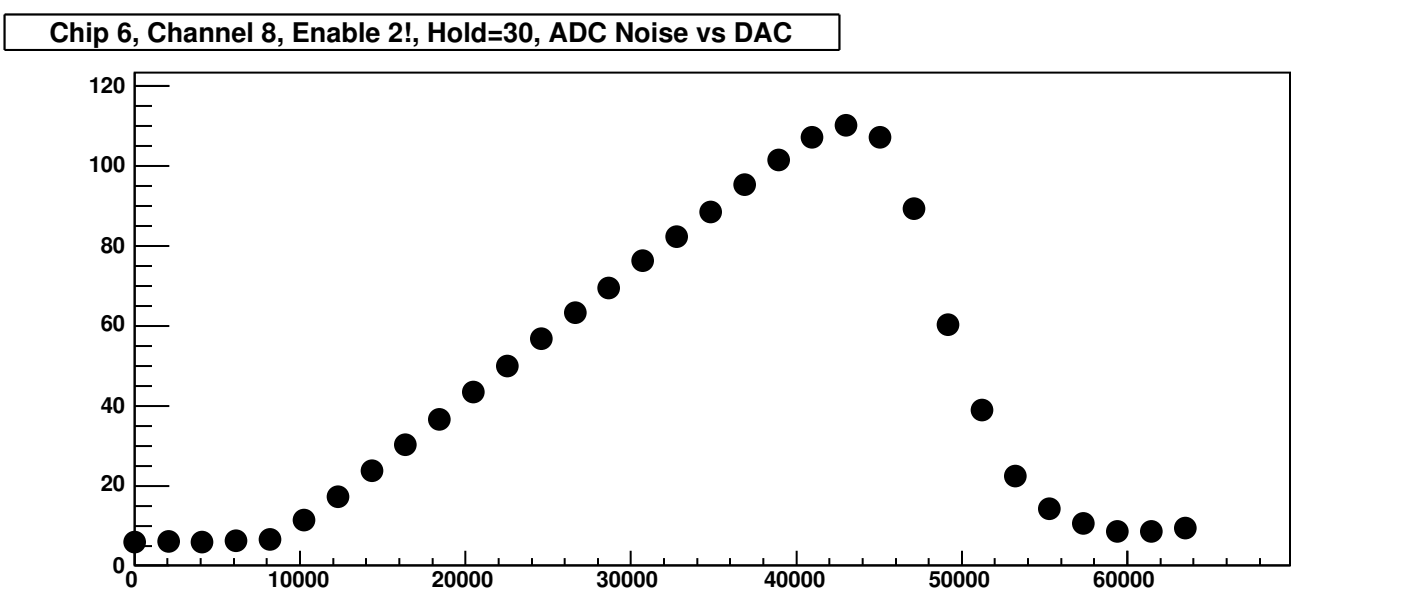

Chip 6, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

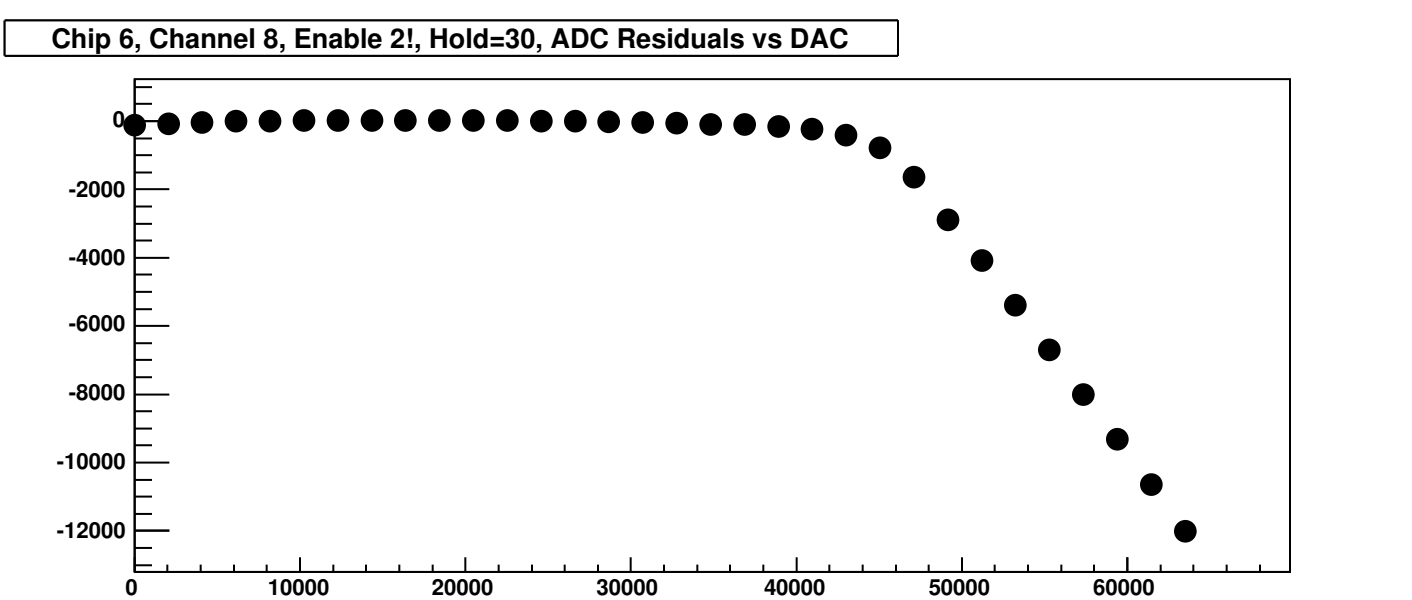

Chip 6, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 8644 / 15
p0 1290 ± 0.1116
p1 0.001516 ± 4.639e-06
1350
1300
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

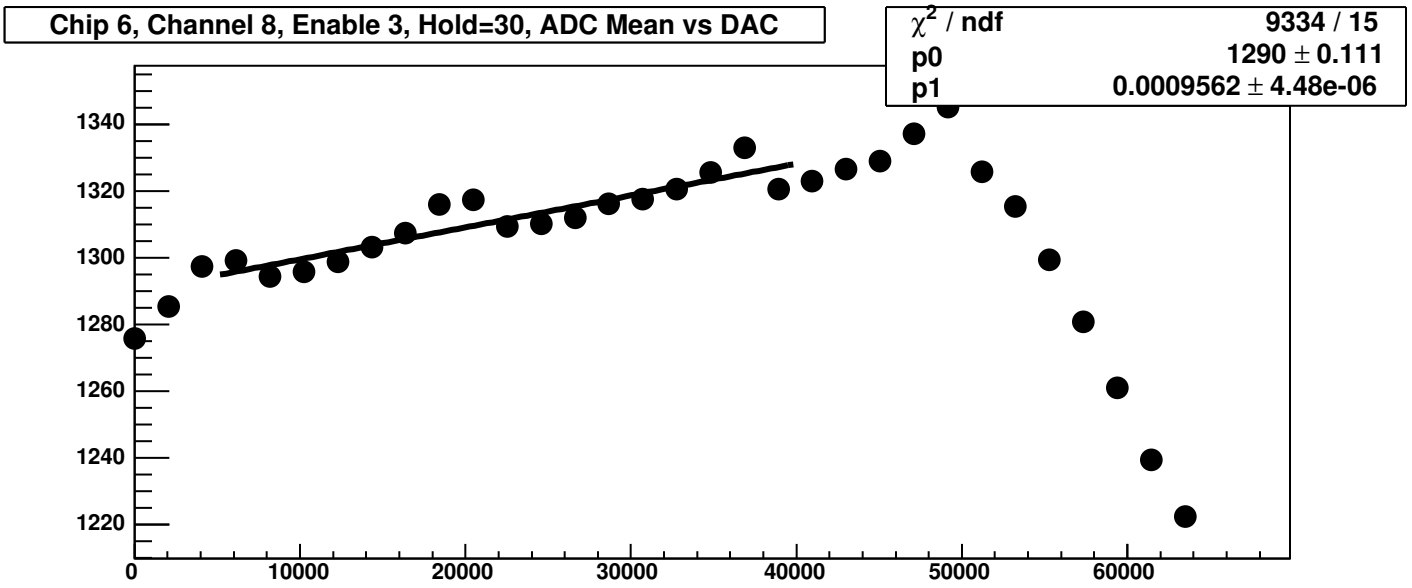

Chip 6, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

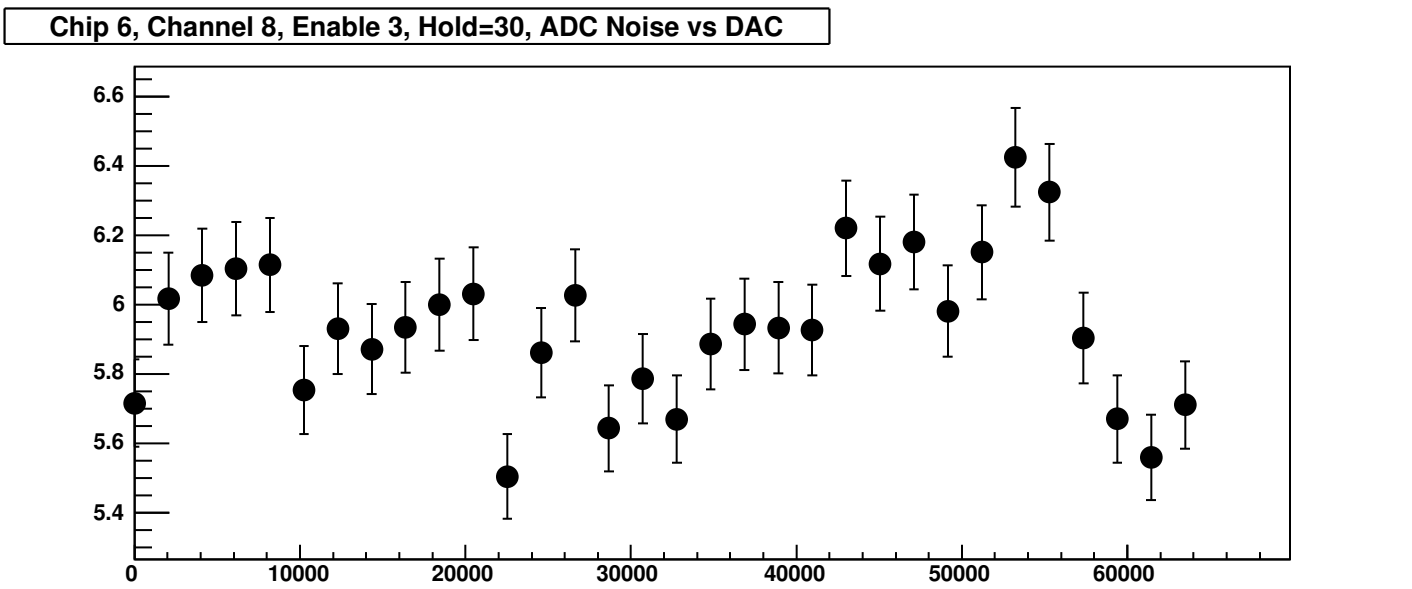

Chip 6, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

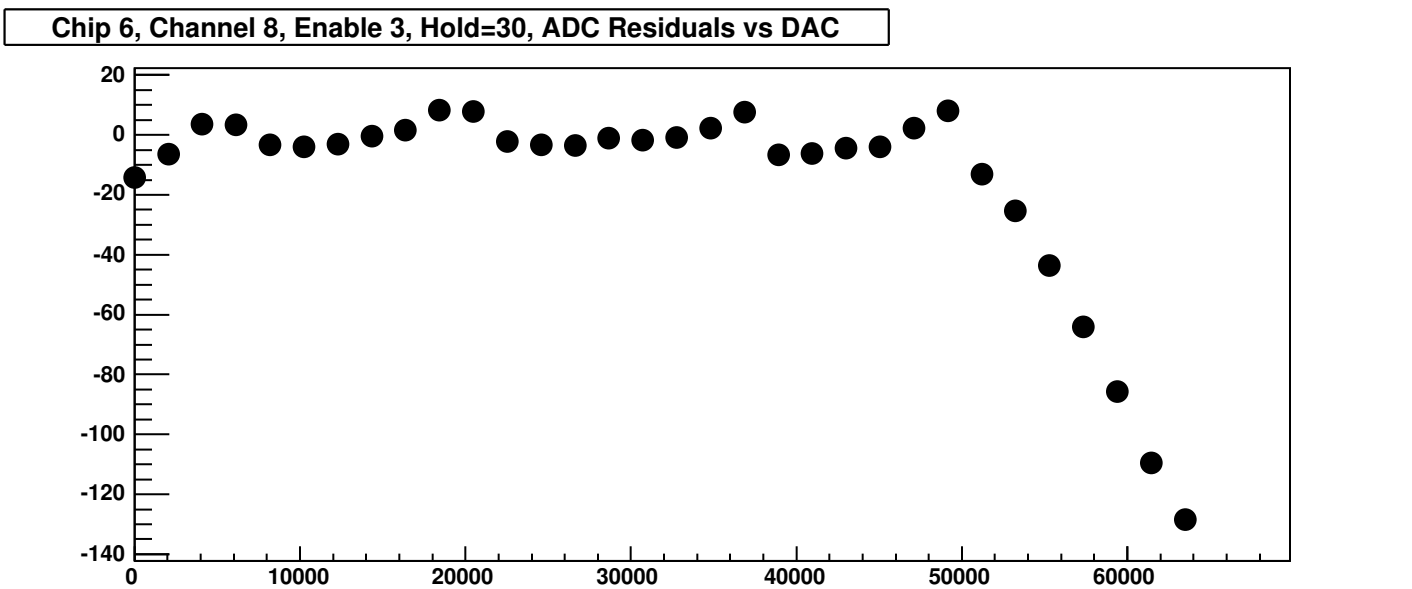

Chip 6, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.139e+04 / 15
p0 1405 ± 0.2775
p1 0.6396 ± 2.799e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

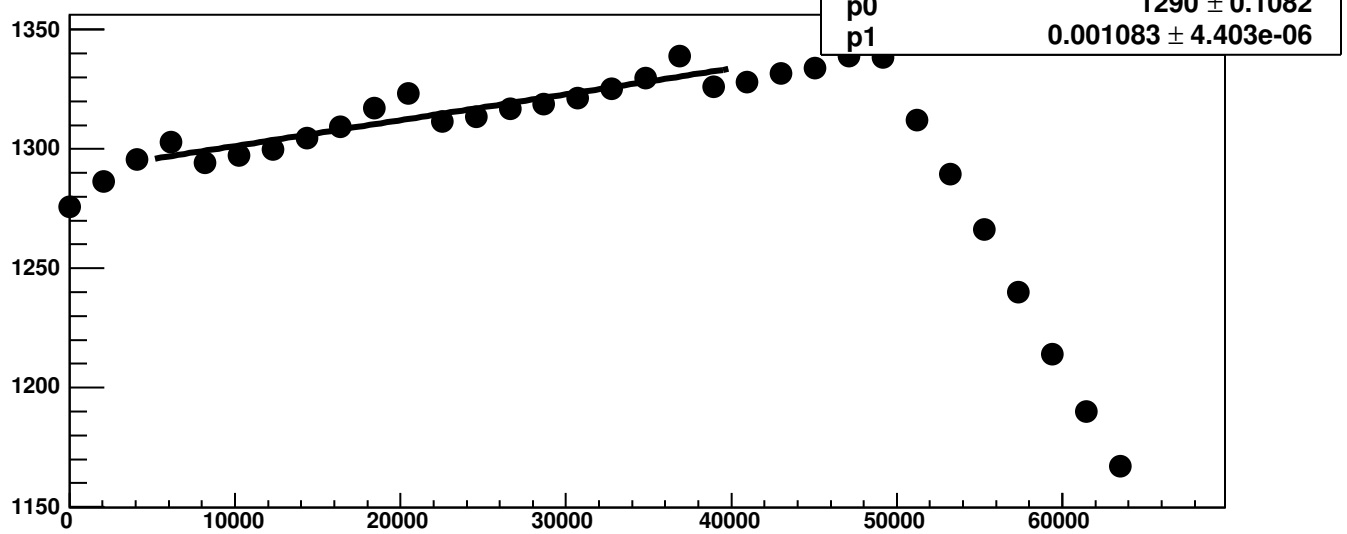

Chip 6, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9334 / 15
p0 1290 ± 0.111
p1 0.0009562 ± 4.48e-06
1340
1320
1300
1280
1260
1240
1220
0
10000
20000
30000
40000
50000
60000

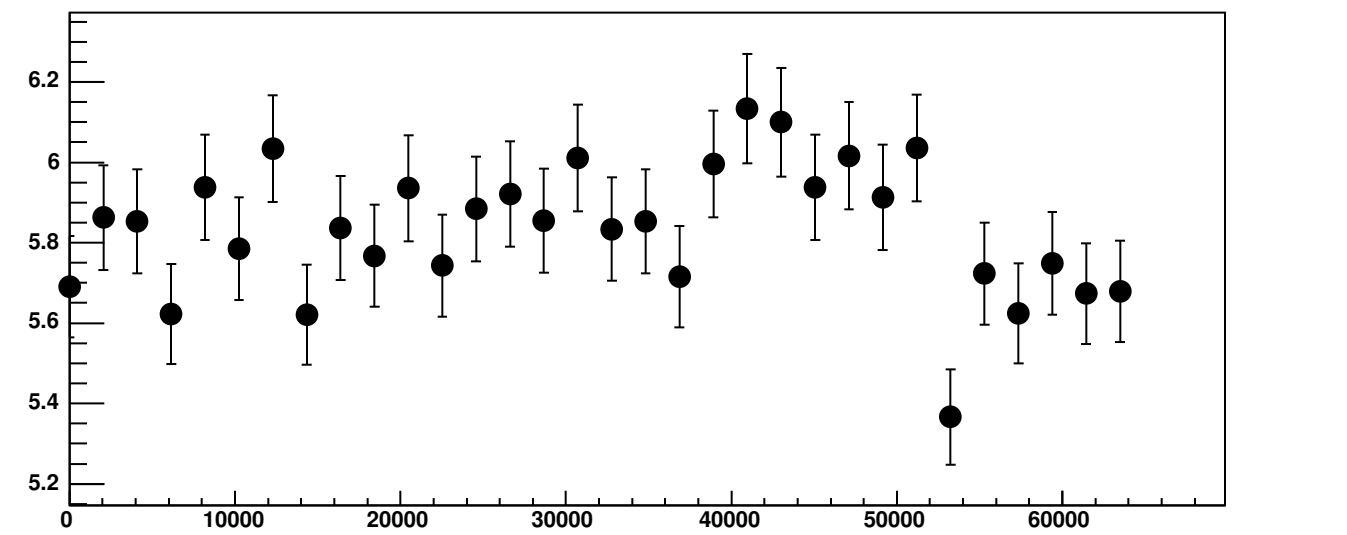

Chip 6, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

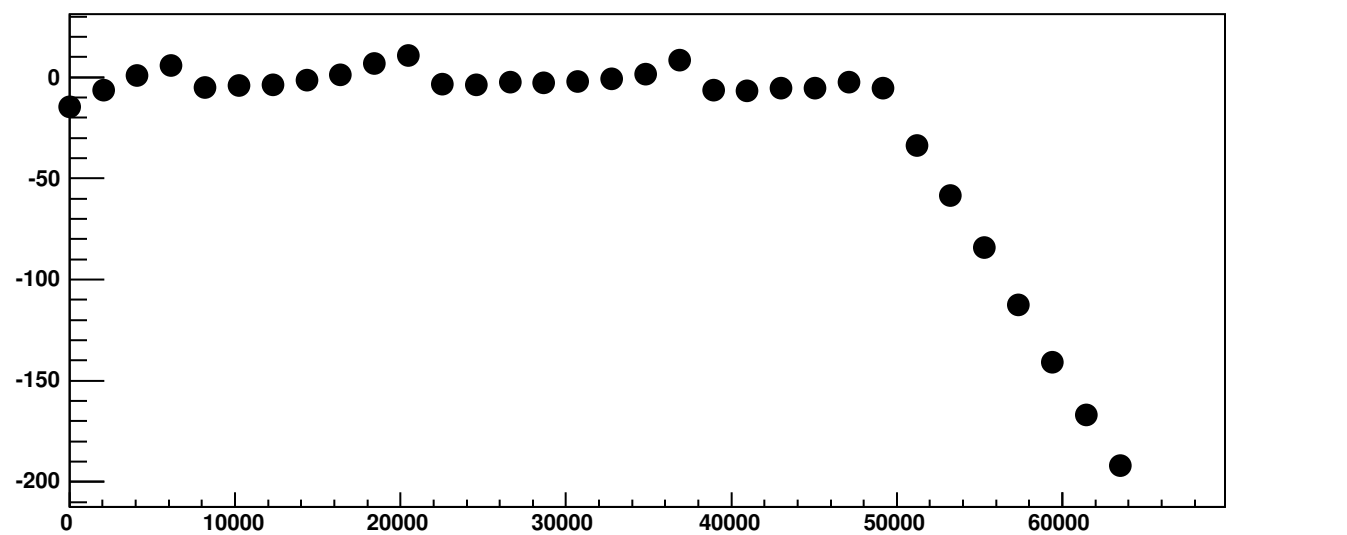
Chip 6, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

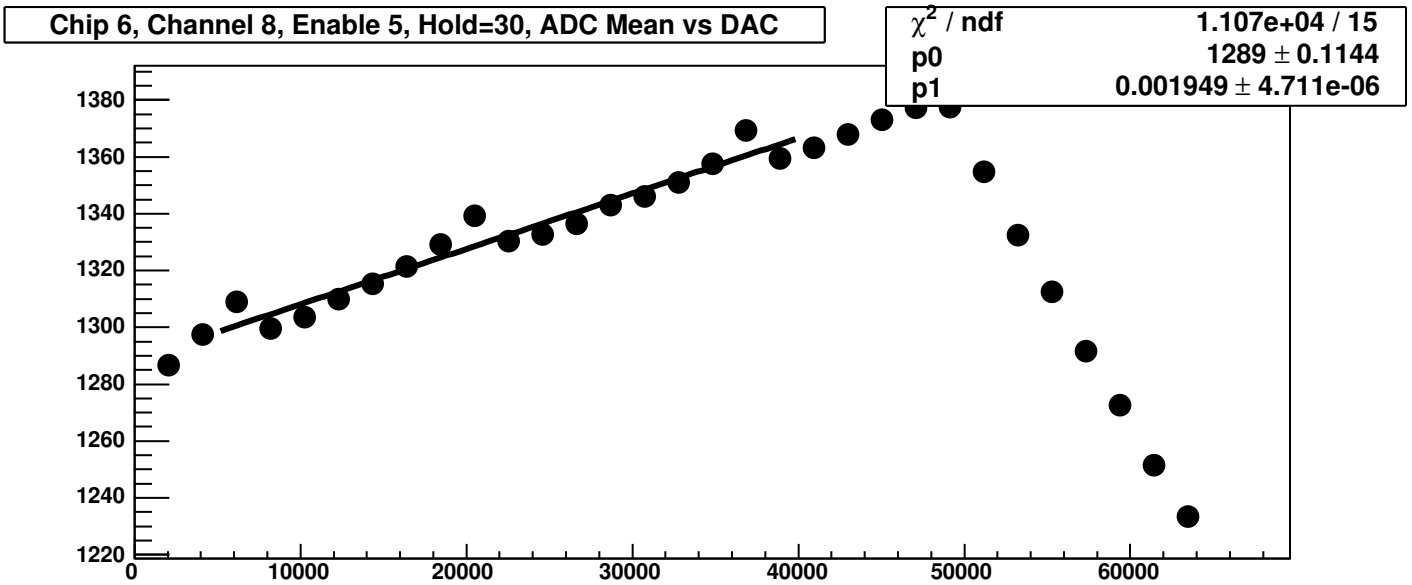


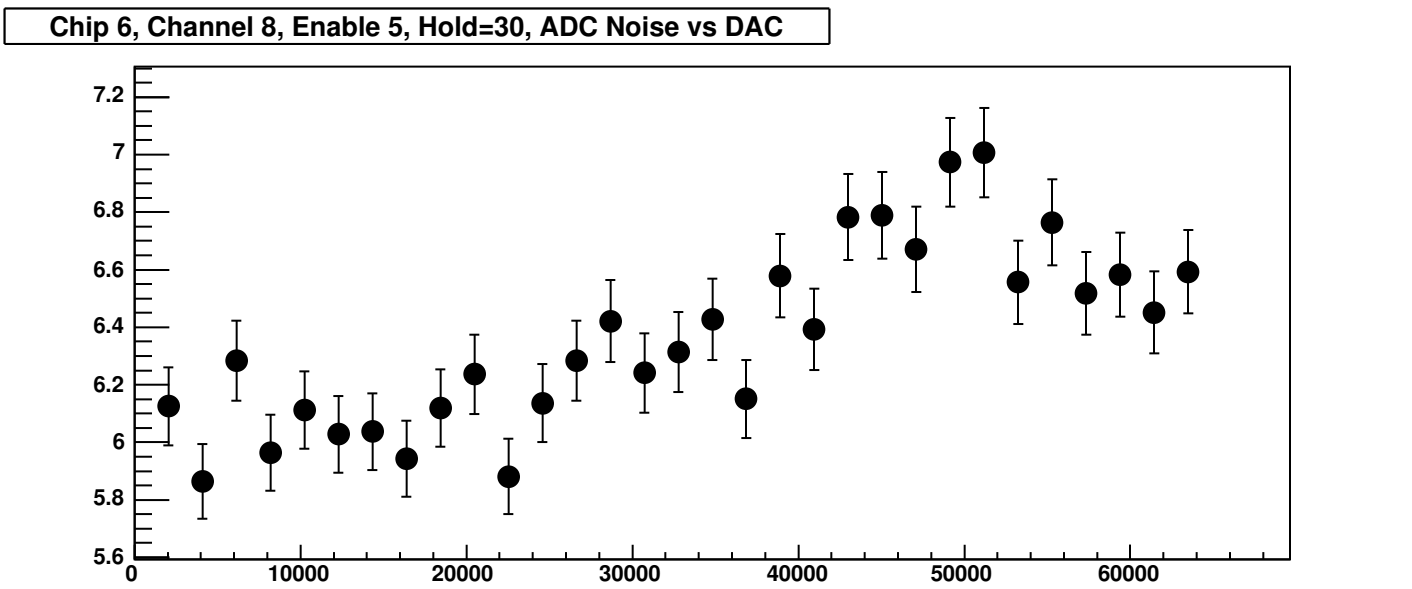
Chip 6, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC

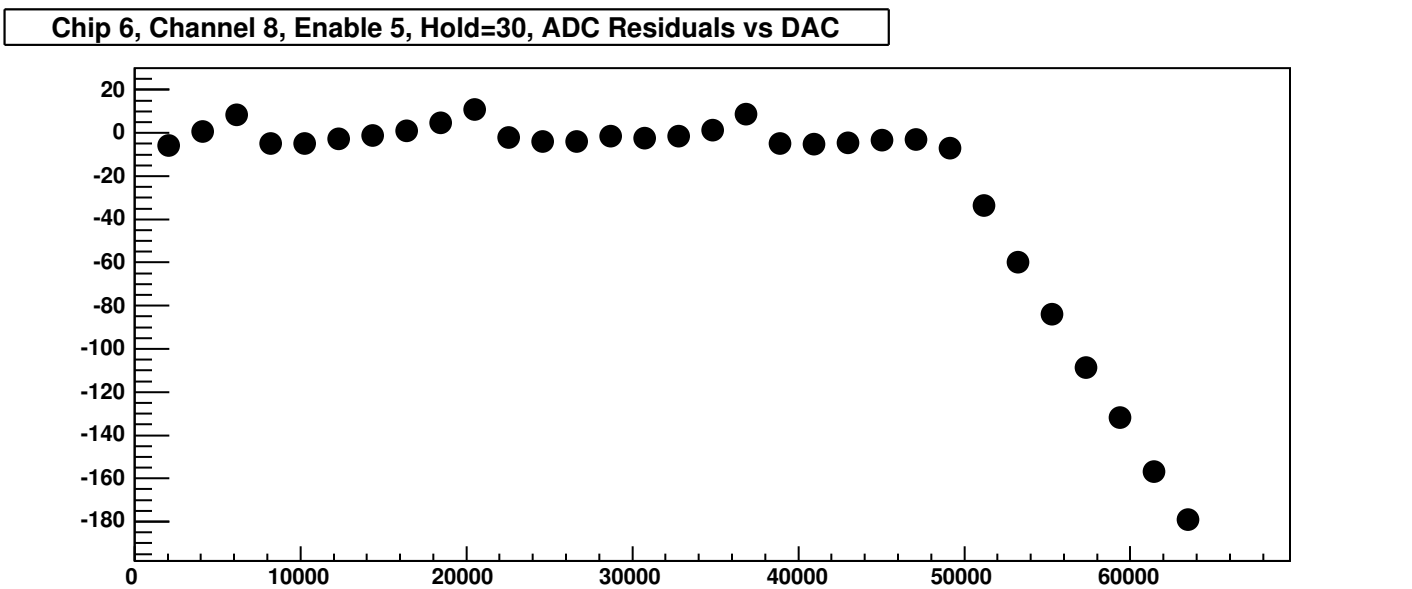


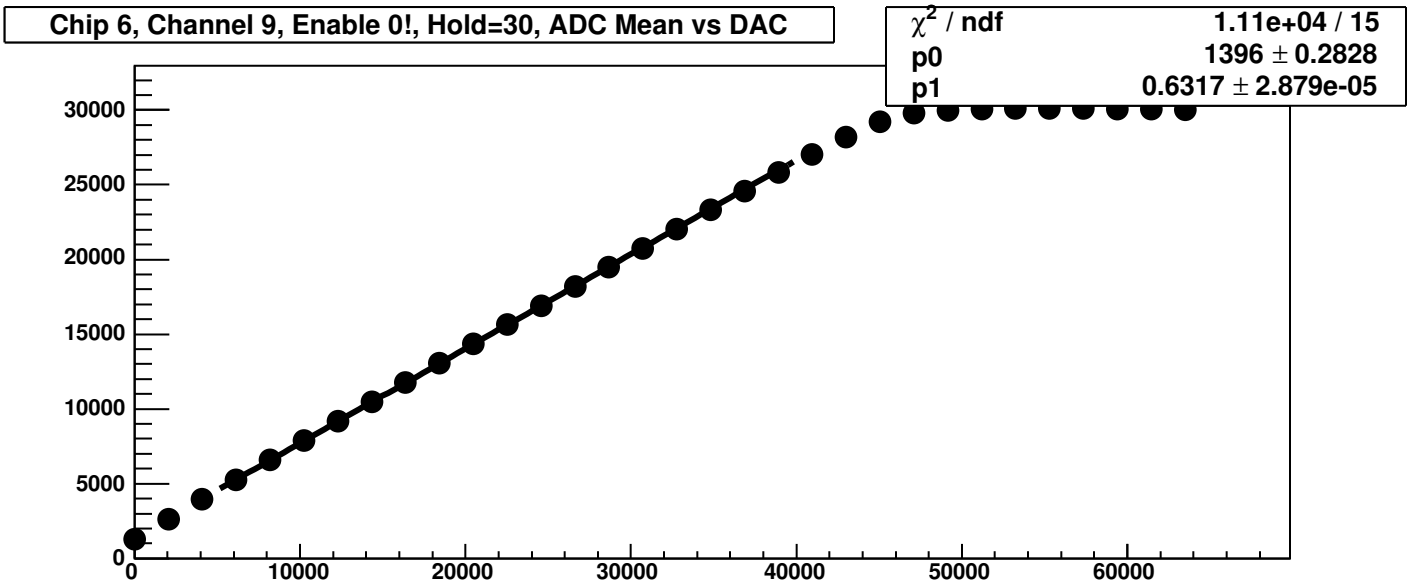
Chip 6, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC

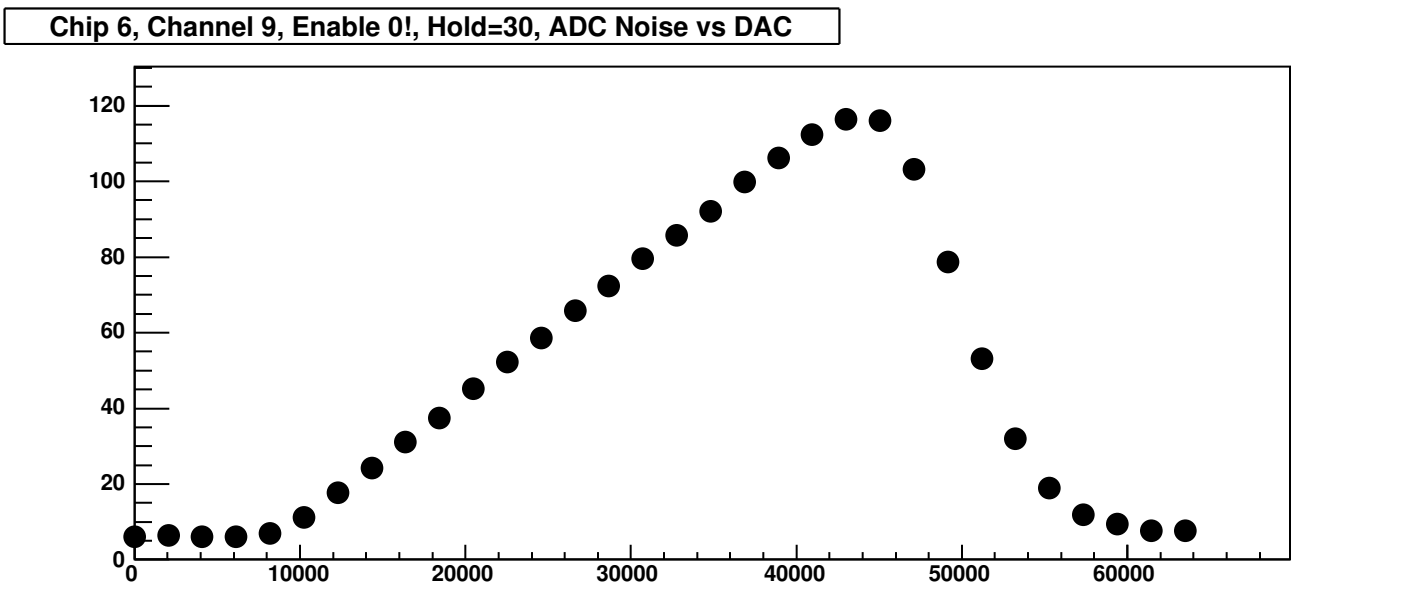


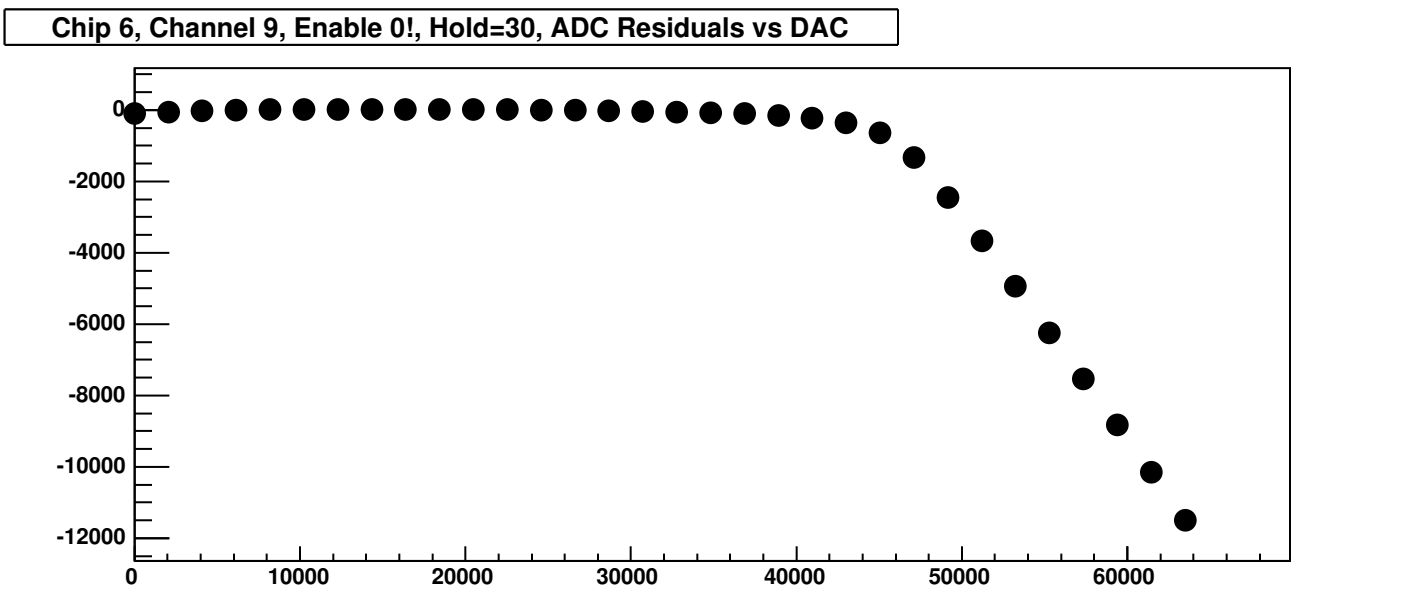

Chip 6, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.107e+04 / 15
p0 1289 ± 0.1144
p1 0.001949 ± 4.711e-06
1380
1360
1340
1320
1300
1280
1260
1240
1220
0
10000
20000
30000
40000
50000
60000

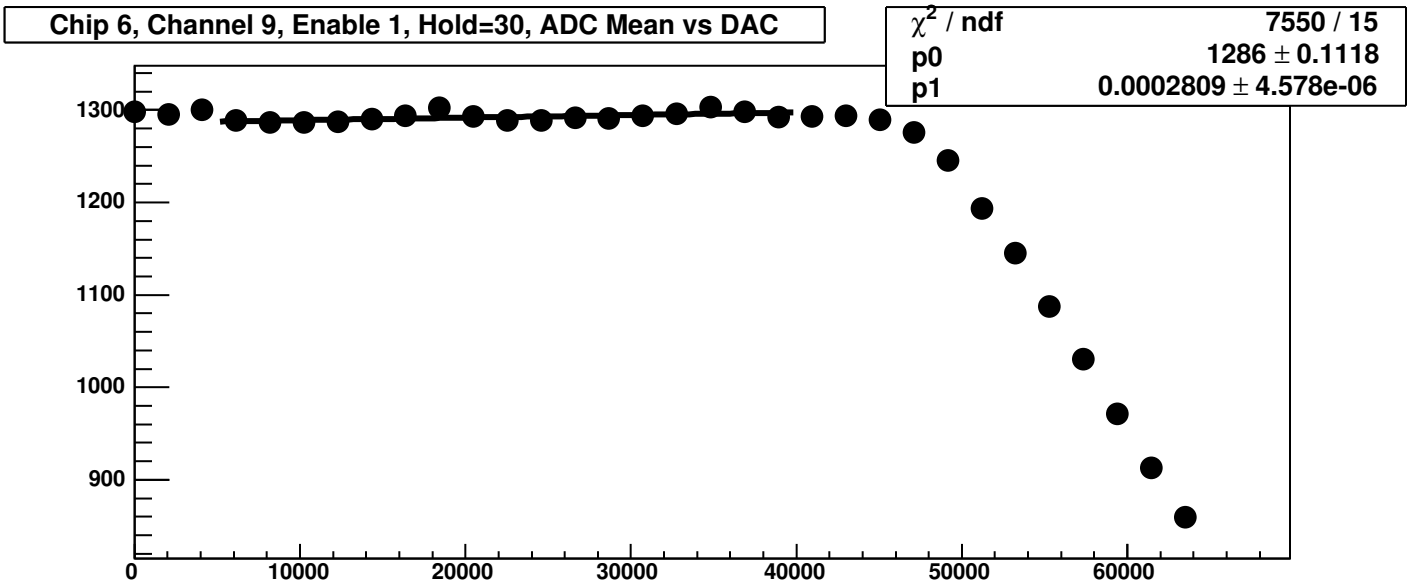

Chip 6, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

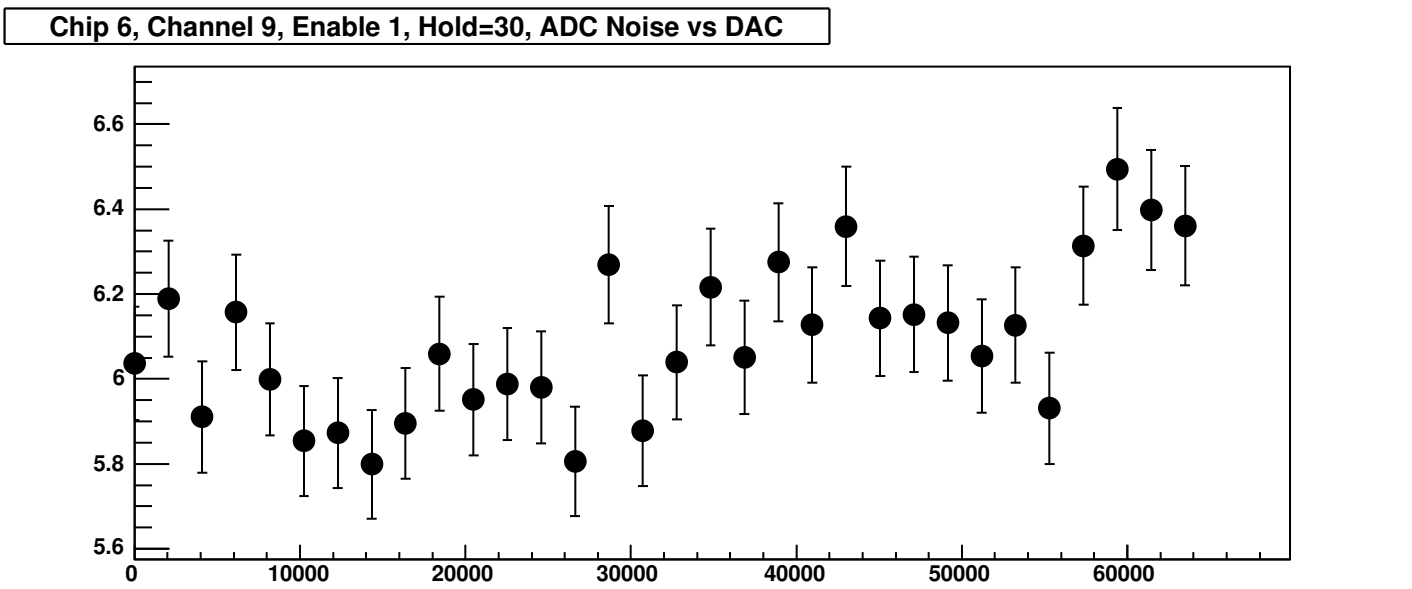

Chip 6, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

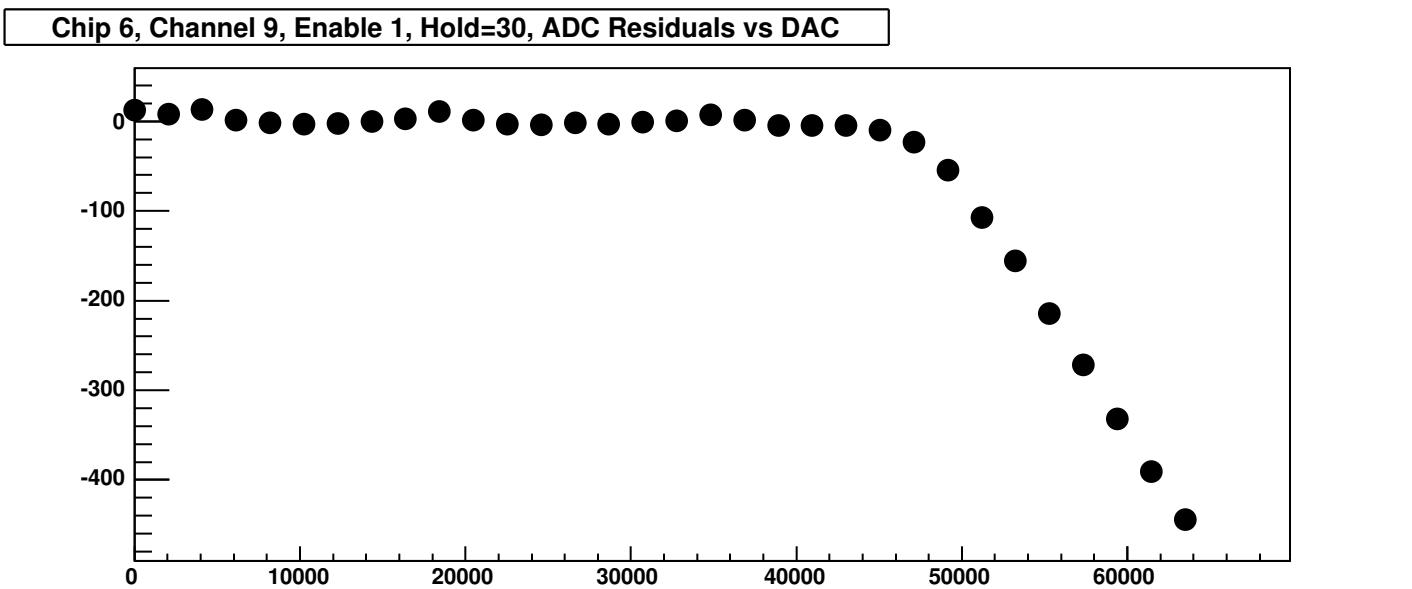

Chip 6, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.11e+04 / 15
p0 1396 ± 0.2828
p1 0.6317 ± 2.879e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

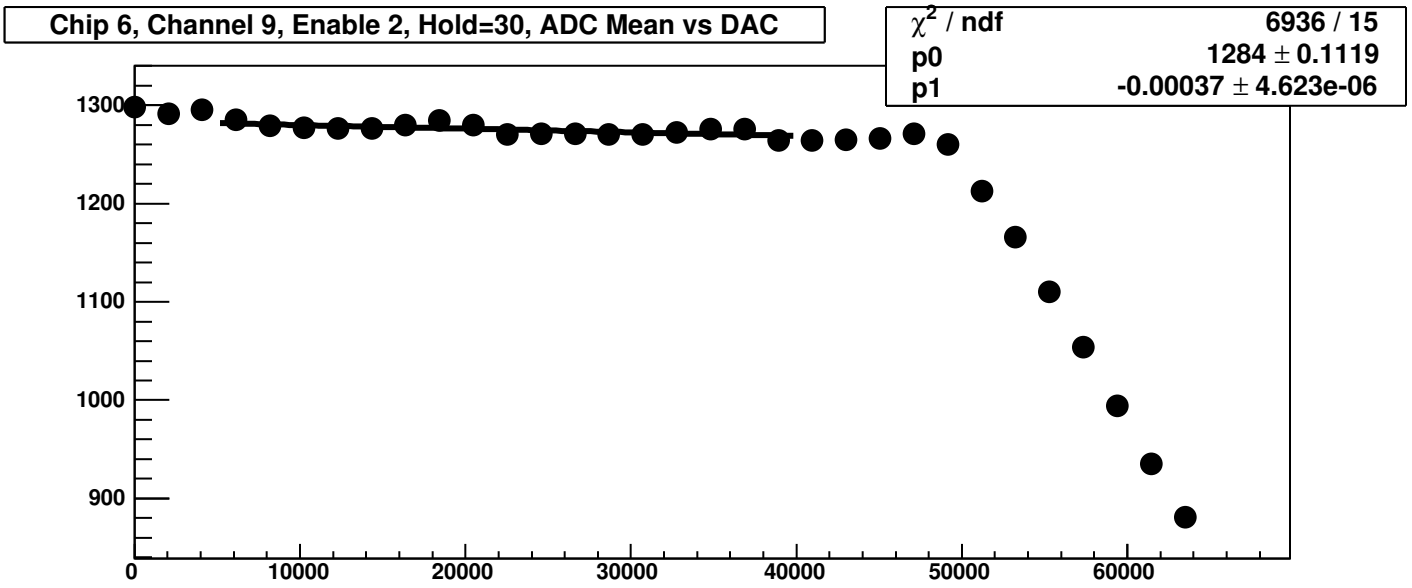

Chip 6, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

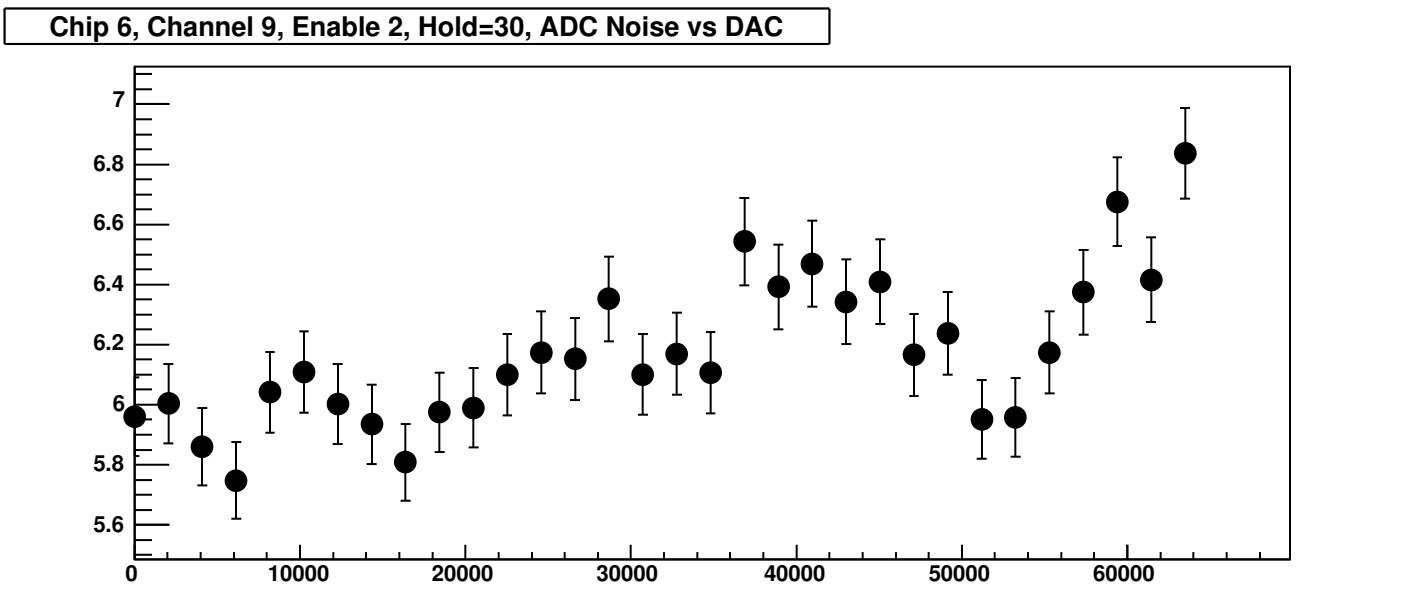

Chip 6, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

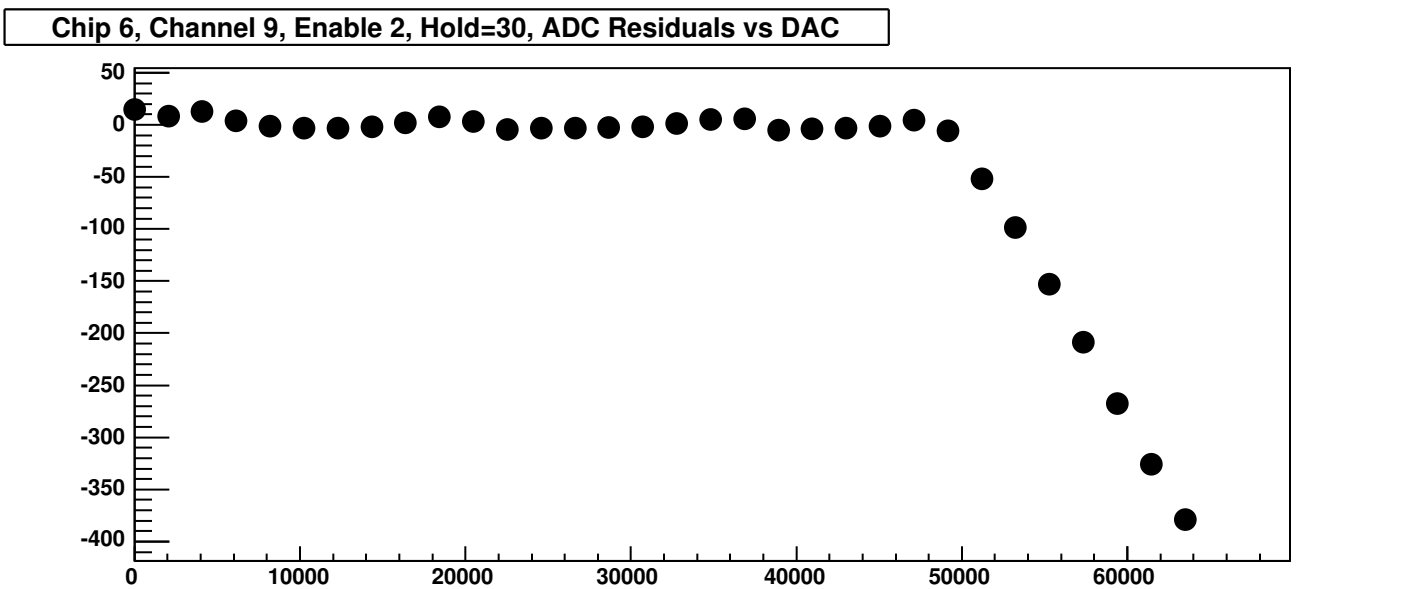

Chip 6, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7550 / 15
p0 1286 ± 0.1118
p1 0.0002809 ± 4.578e-06
1300
1200
1100
1000
900
0
10000
20000
30000
40000
50000
60000

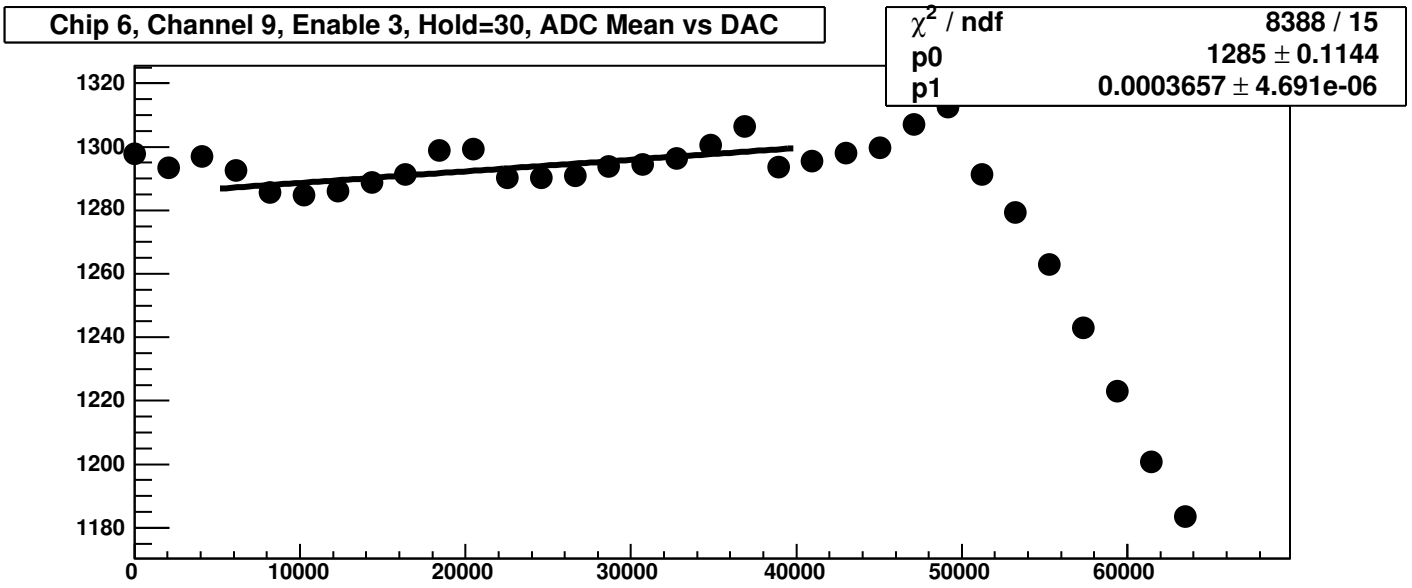

Chip 6, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

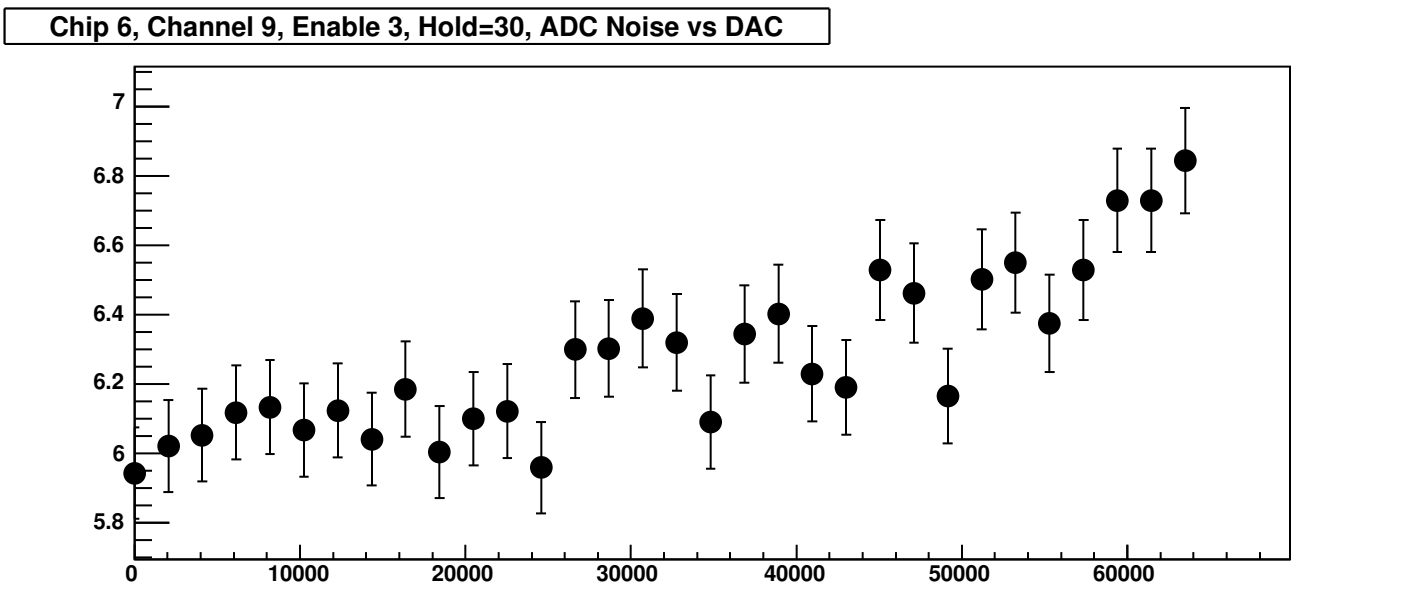

Chip 6, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

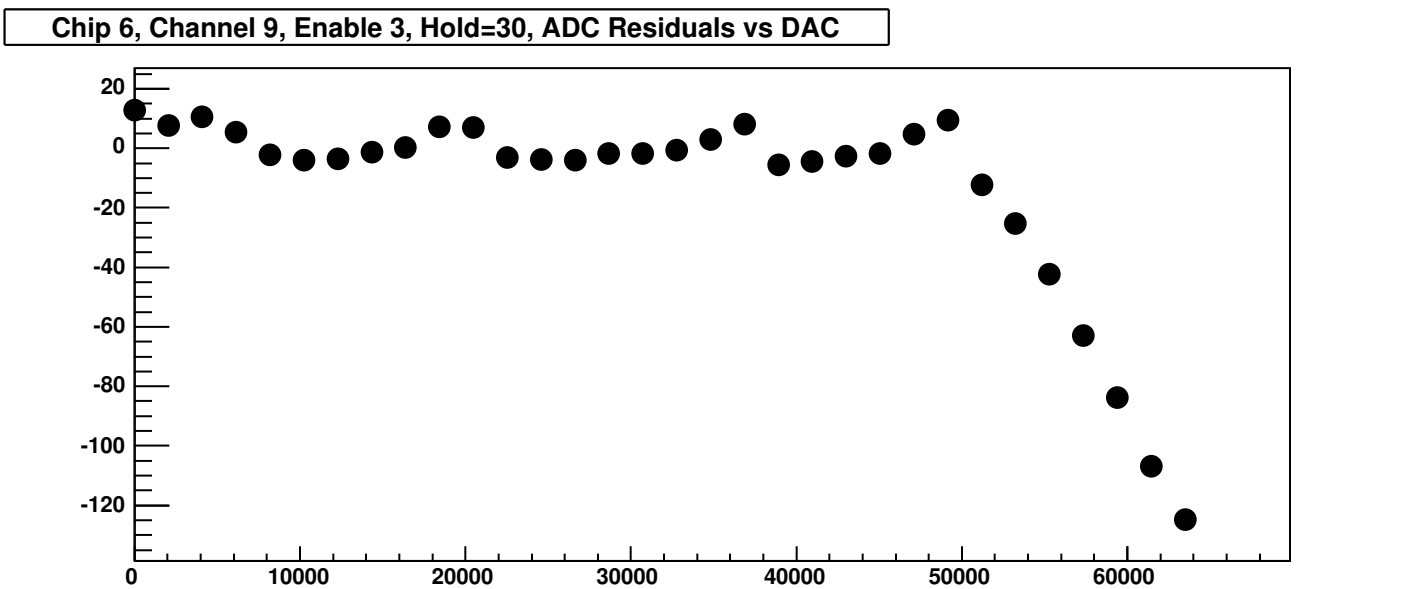

Chip 6, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6936 / 15
p0 1284 ± 0.1119
p1 -0.00037 ± 4.623e-06
1300
1200
1100
1000
900
0
10000
20000
30000
40000
50000
60000

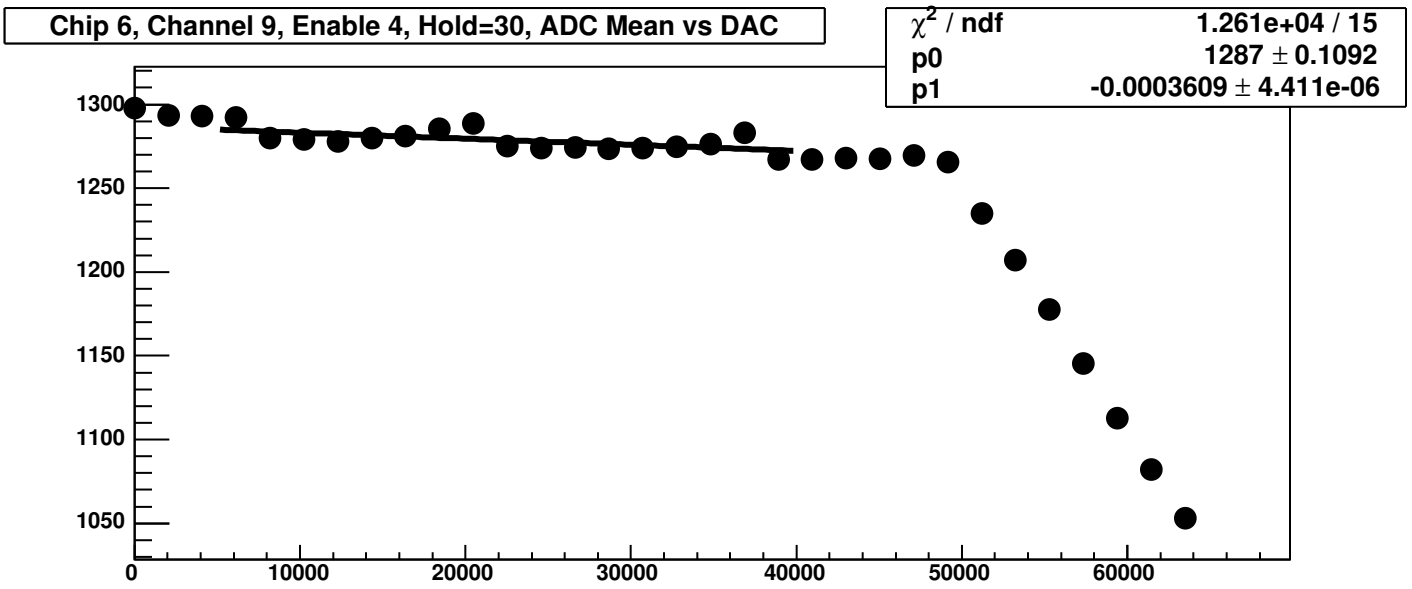

Chip 6, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

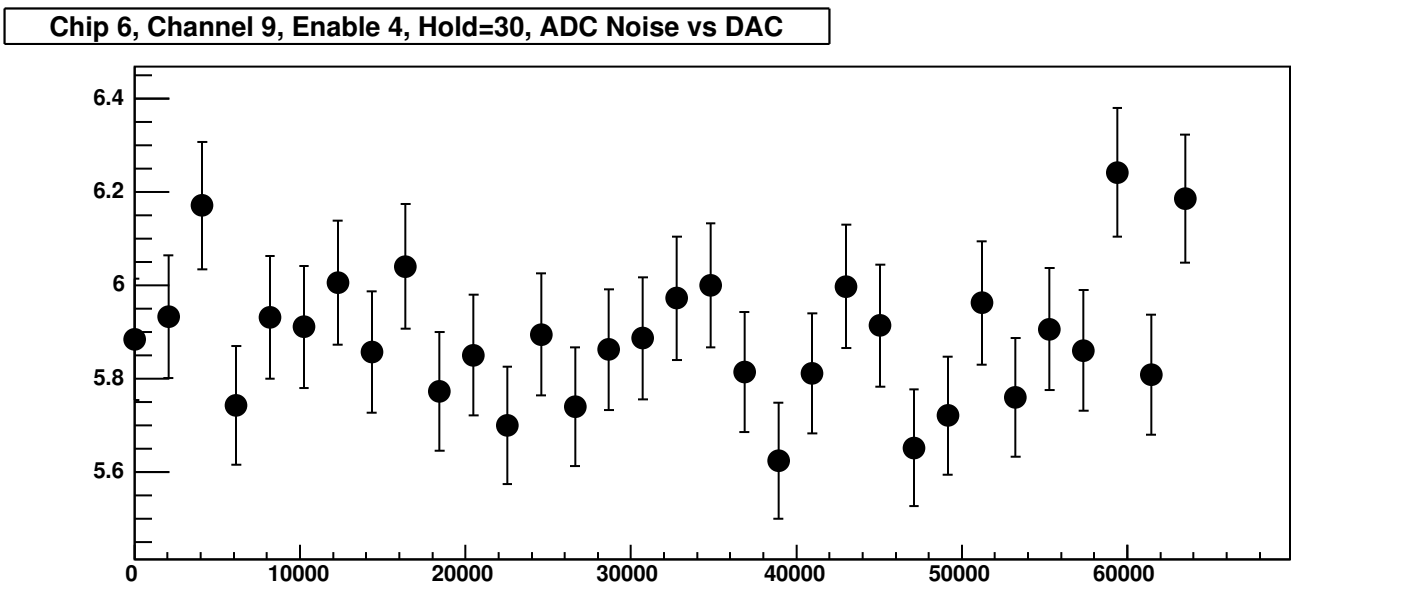

Chip 6, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

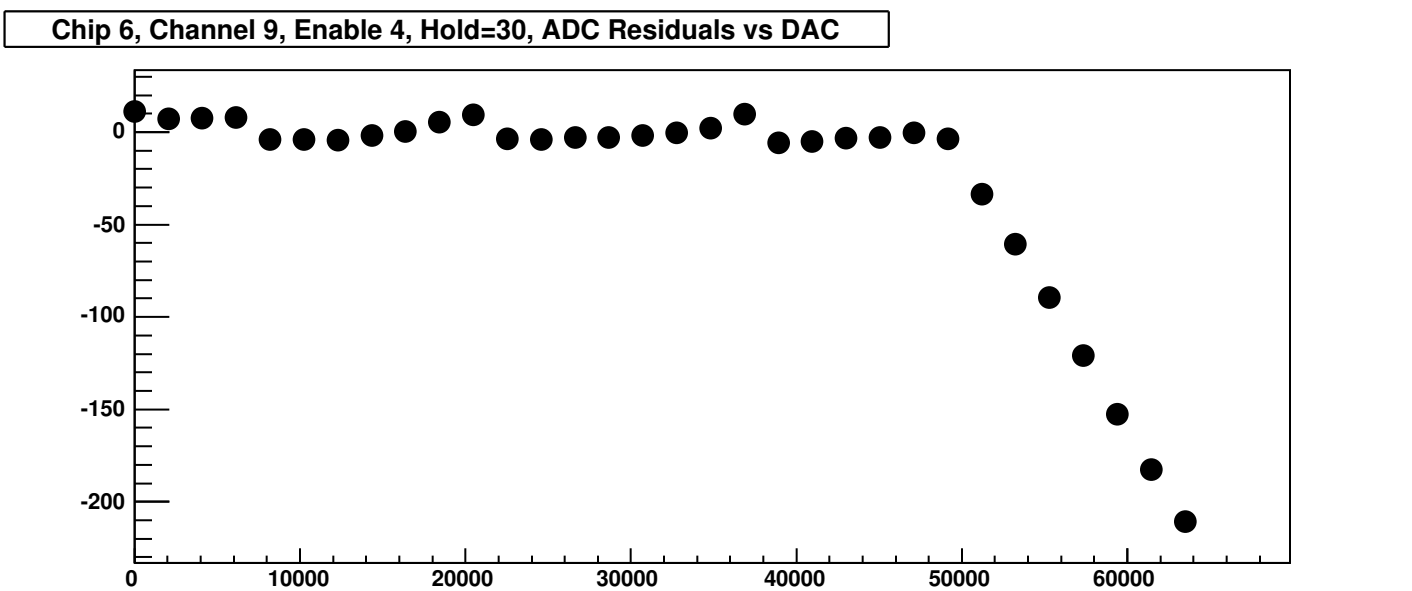

Chip 6, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8388 / 15
p0 1285 ± 0.1144
p1 0.0003657 ± 4.691e-06
1320
1300
1280
1260
1240
1220
1200
1180
0
10000
20000
30000
40000
50000
60000

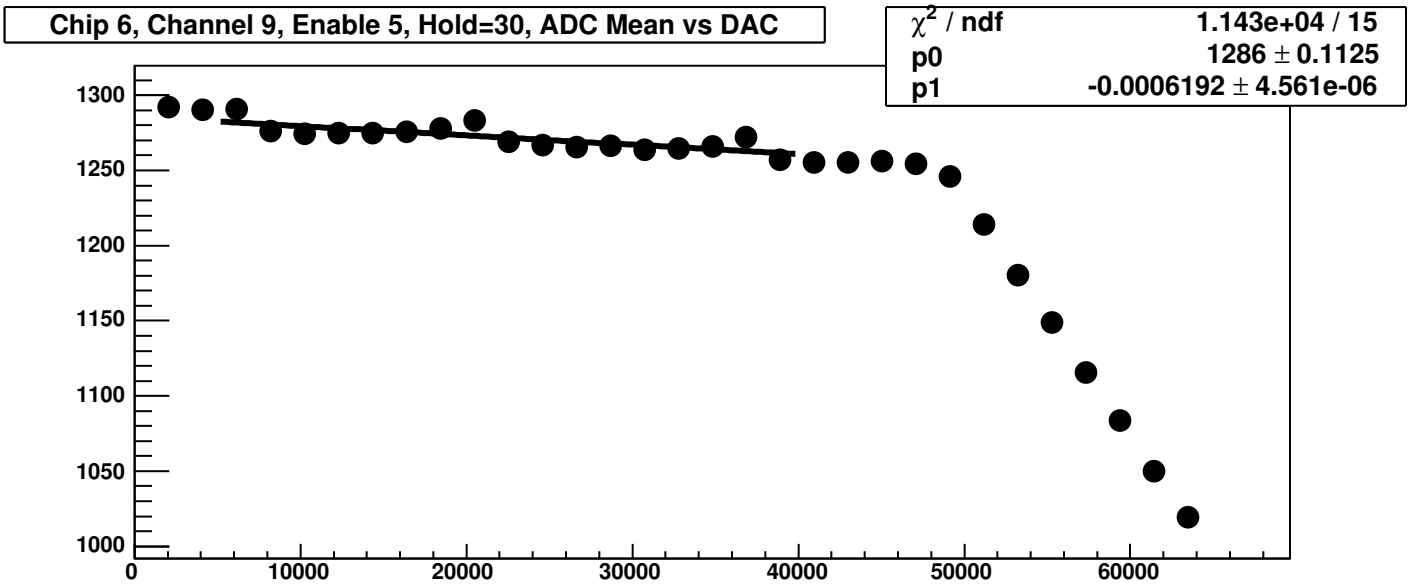

Chip 6, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

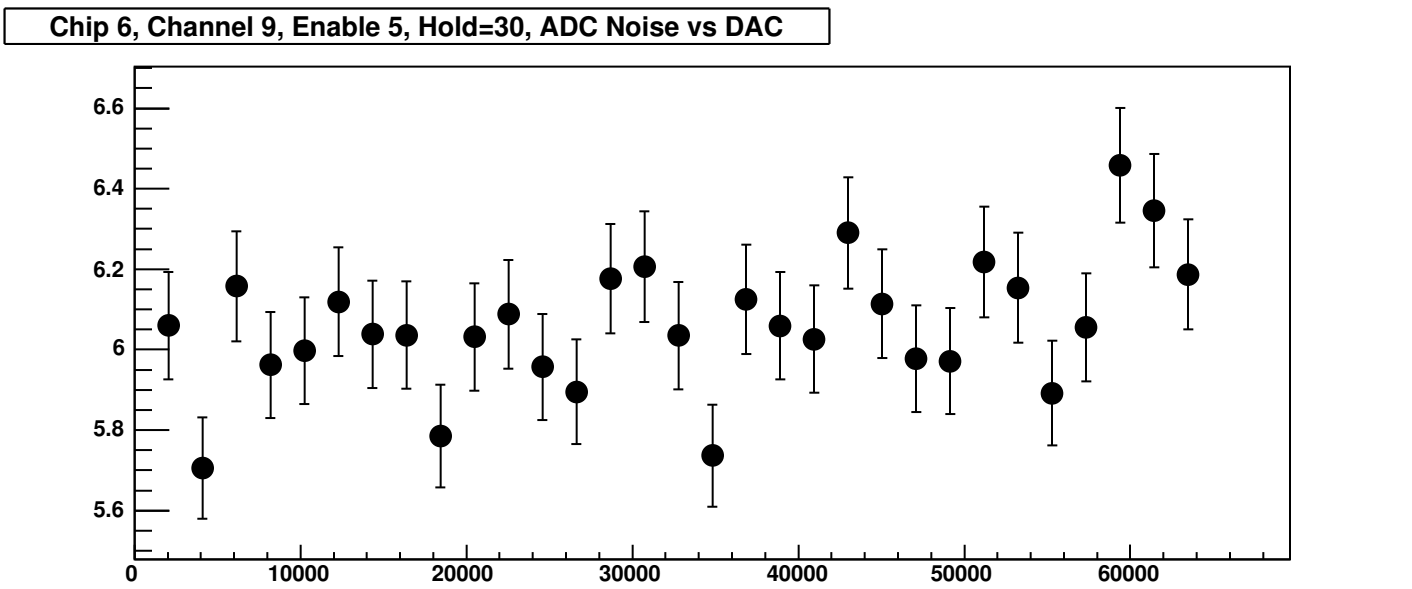

Chip 6, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

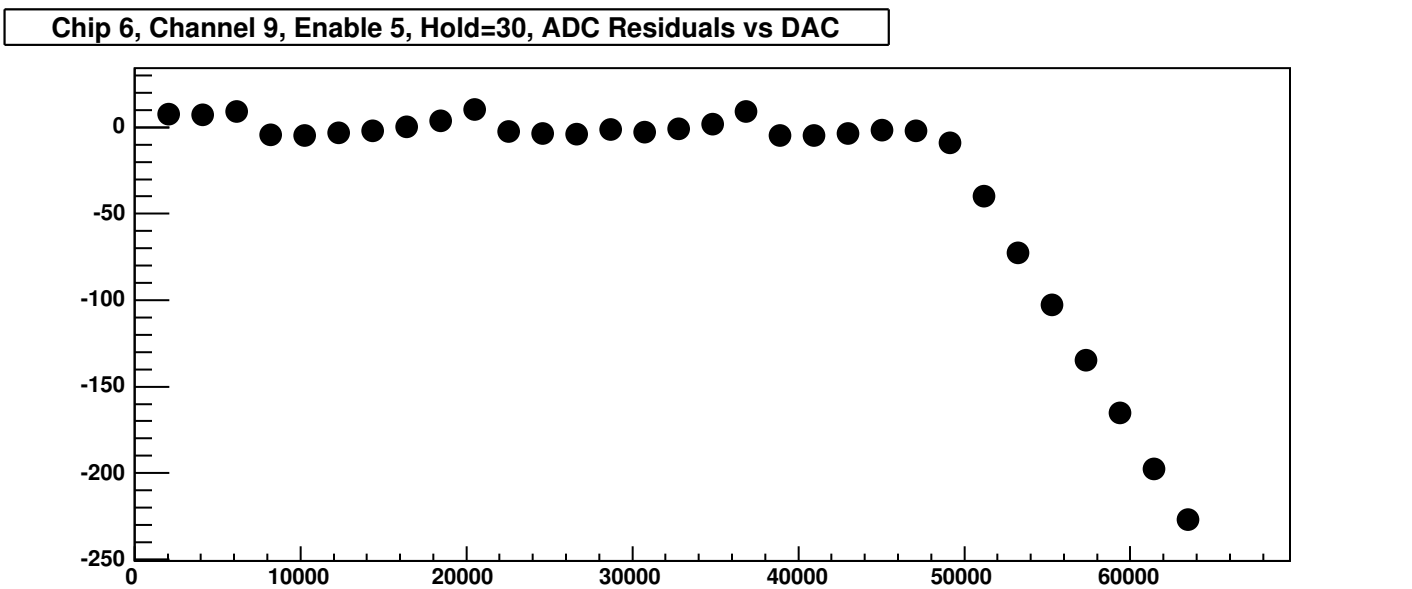

Chip 6, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.261e+04 / 15
p0 1287 ± 0.1092
p1 -0.0003609 ± 4.411e-06
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

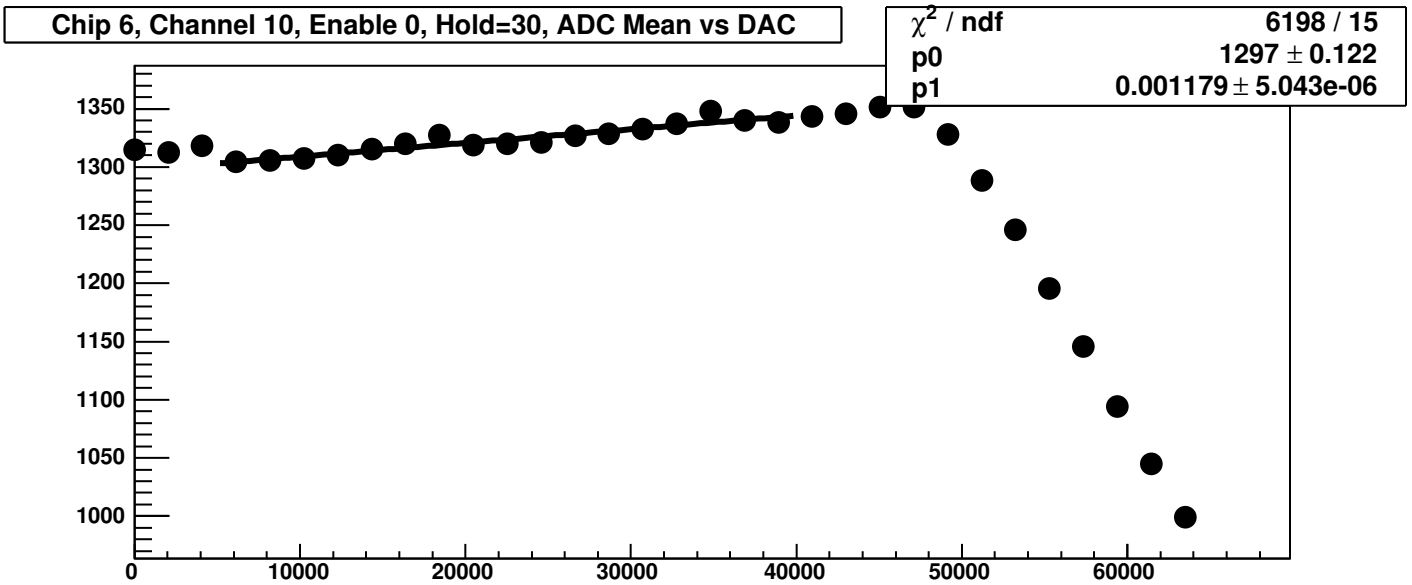

Chip 6, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

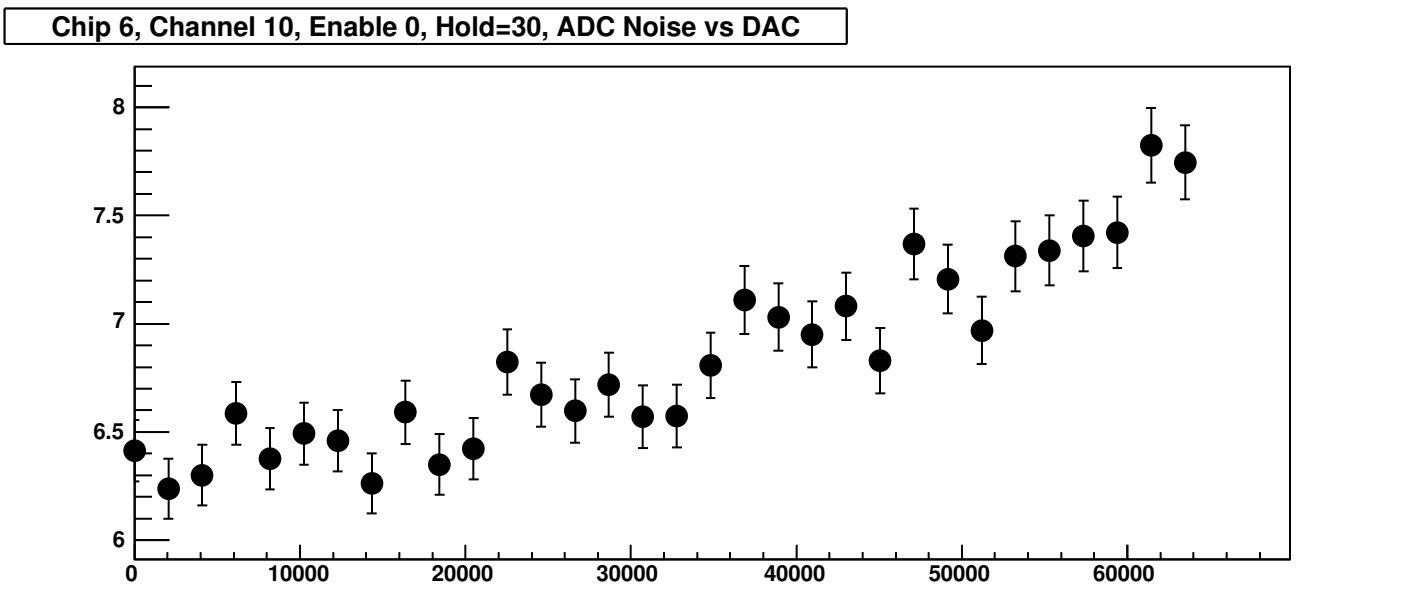

Chip 6, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

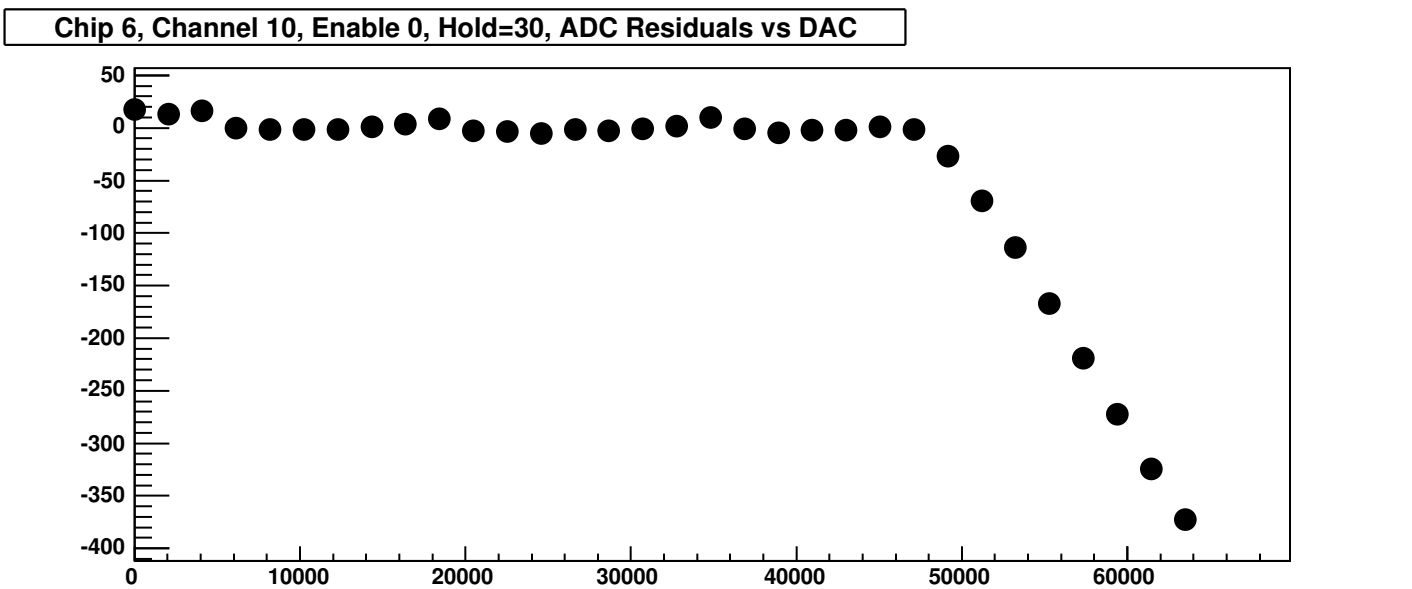

Chip 6, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.143e+04 / 15
p0 1286 ± 0.1125
p1 -0.0006192 ± 4.561e-06
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

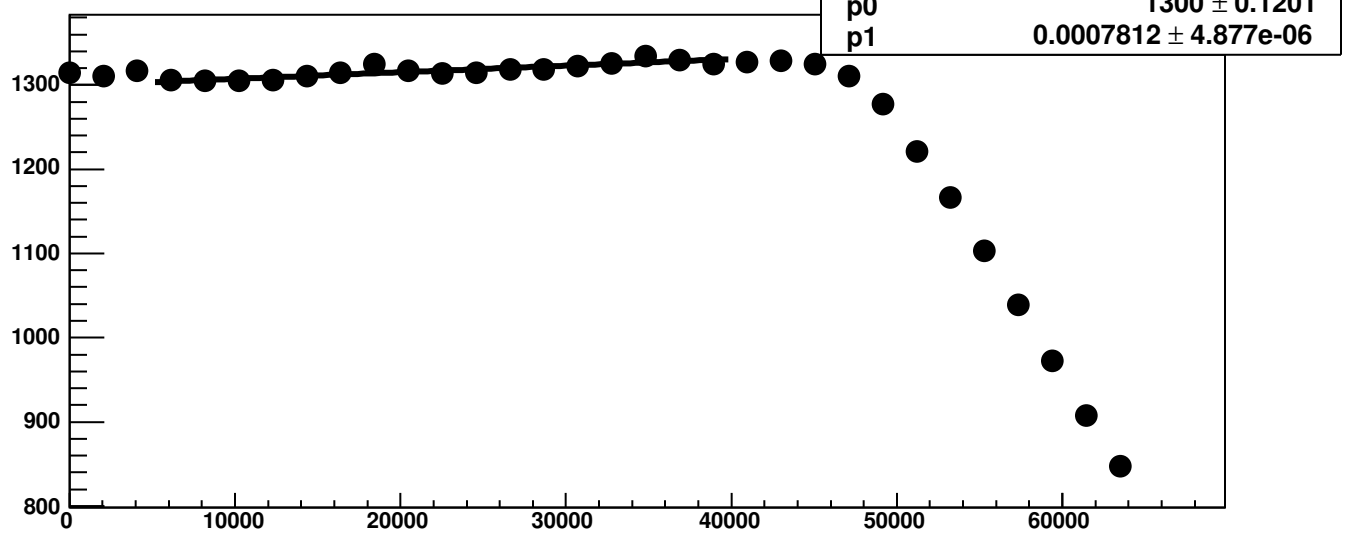

Chip 6, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6198 / 15
p0 1297 ± 0.122
p1 0.001179 ± 5.043e-06
1350
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

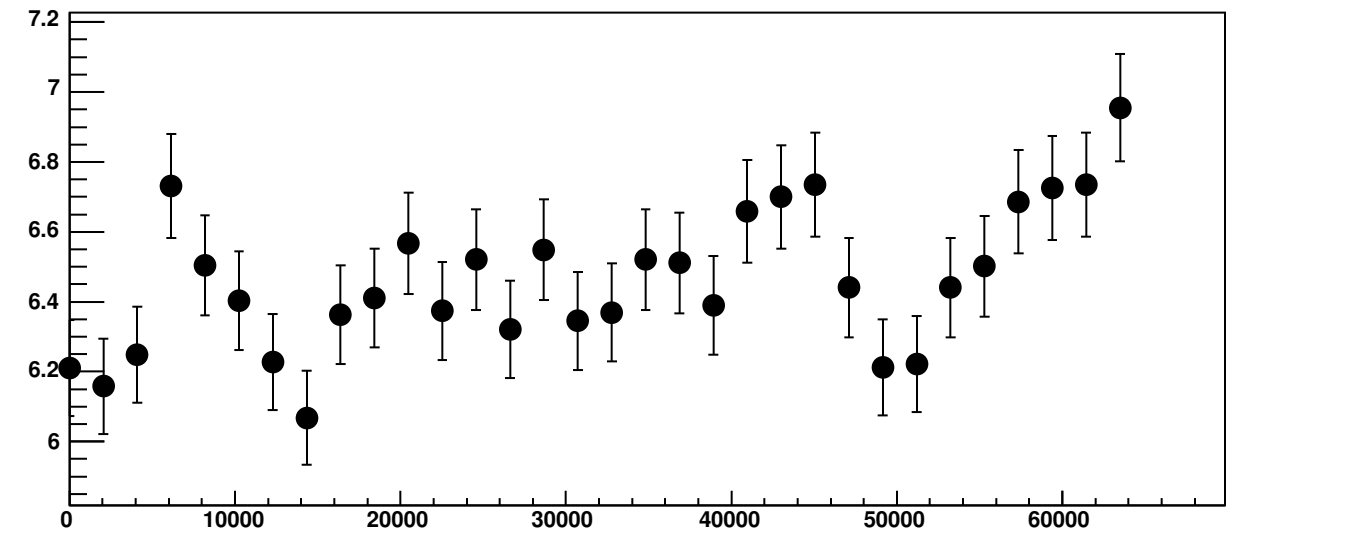

Chip 6, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
8
7.5
7
6.5
6
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

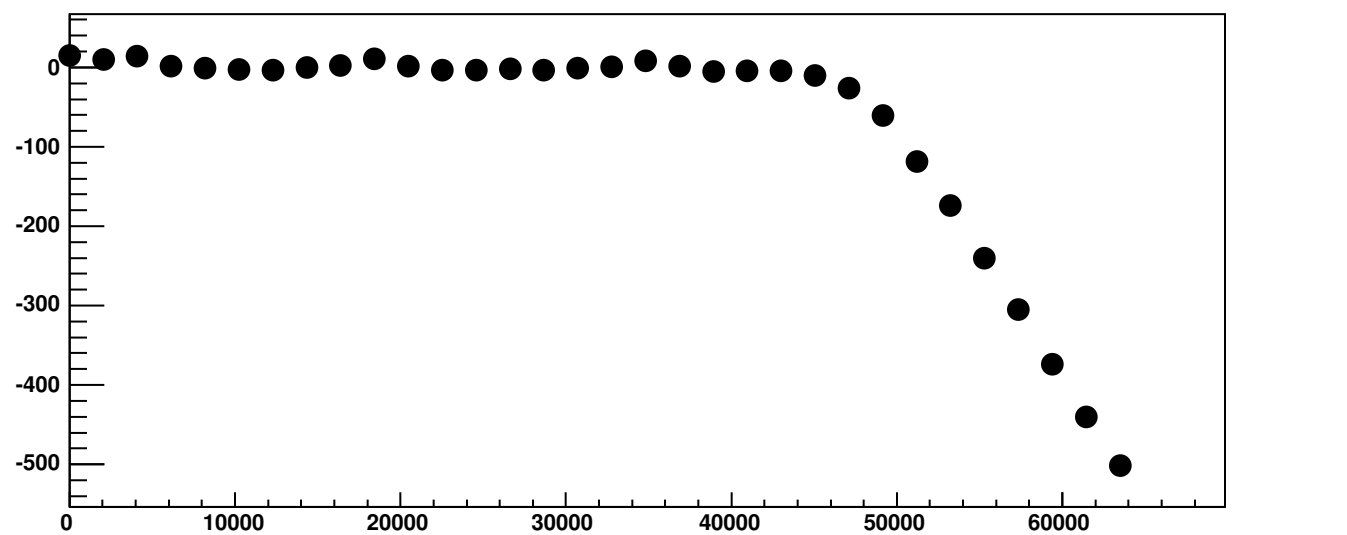
Chip 6, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC



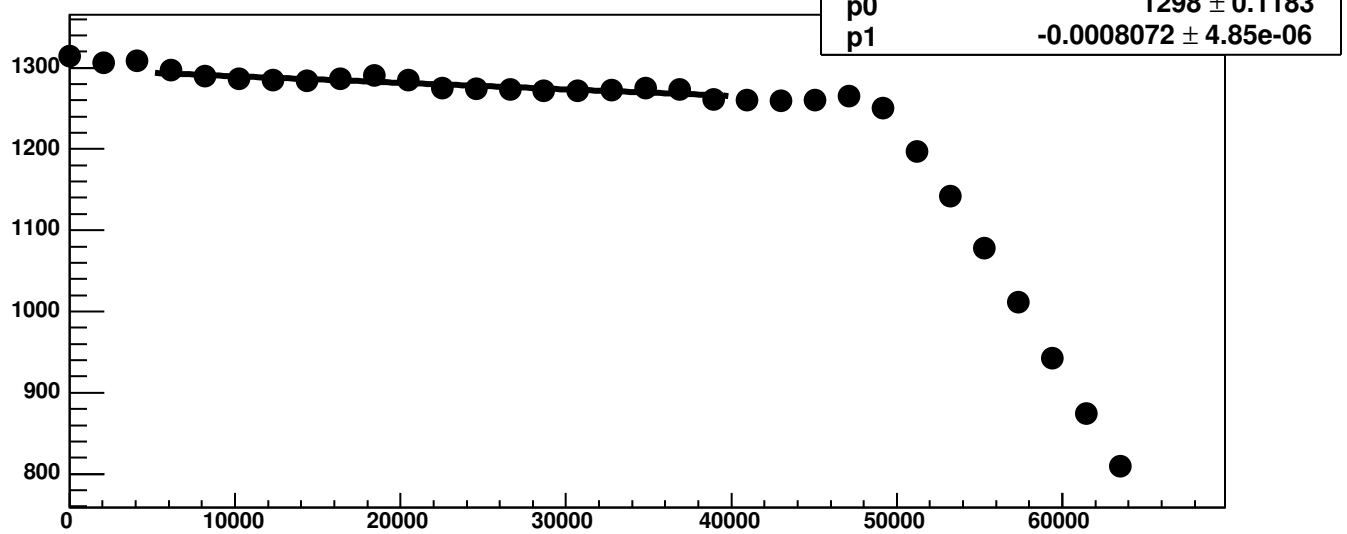
Chip 6, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC



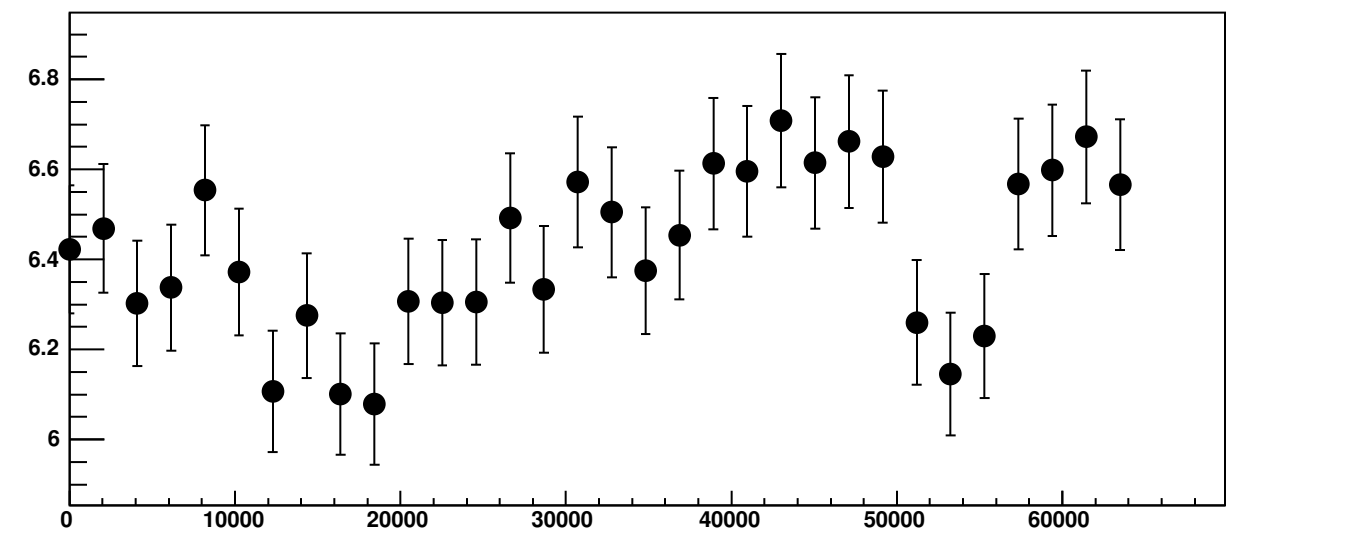
Chip 6, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC



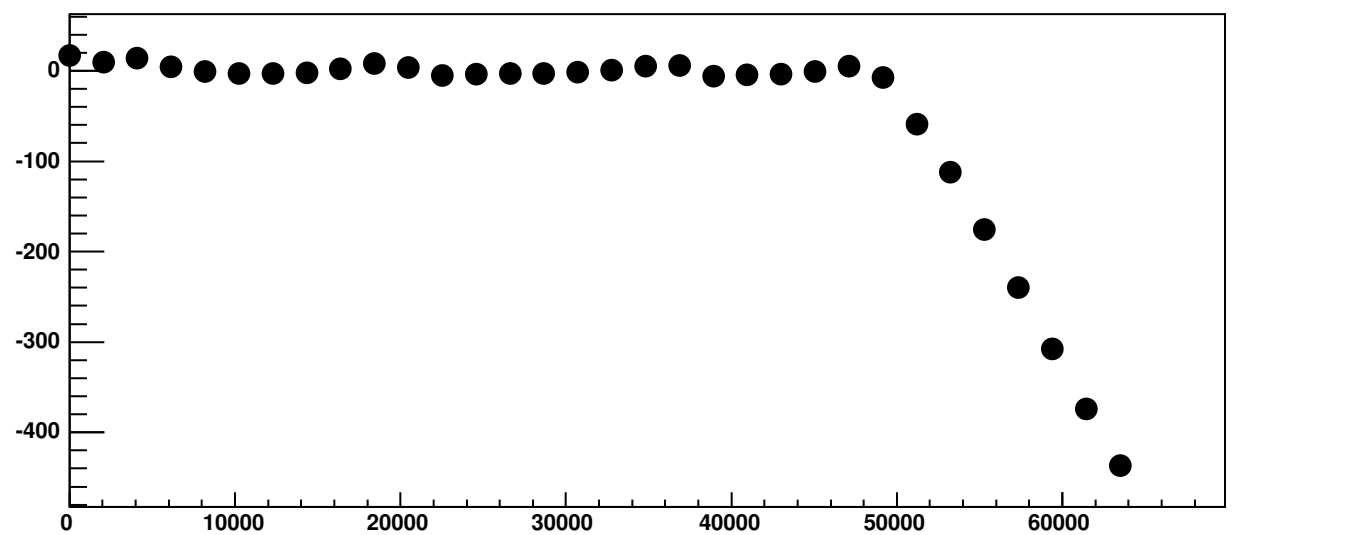
Chip 6, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC

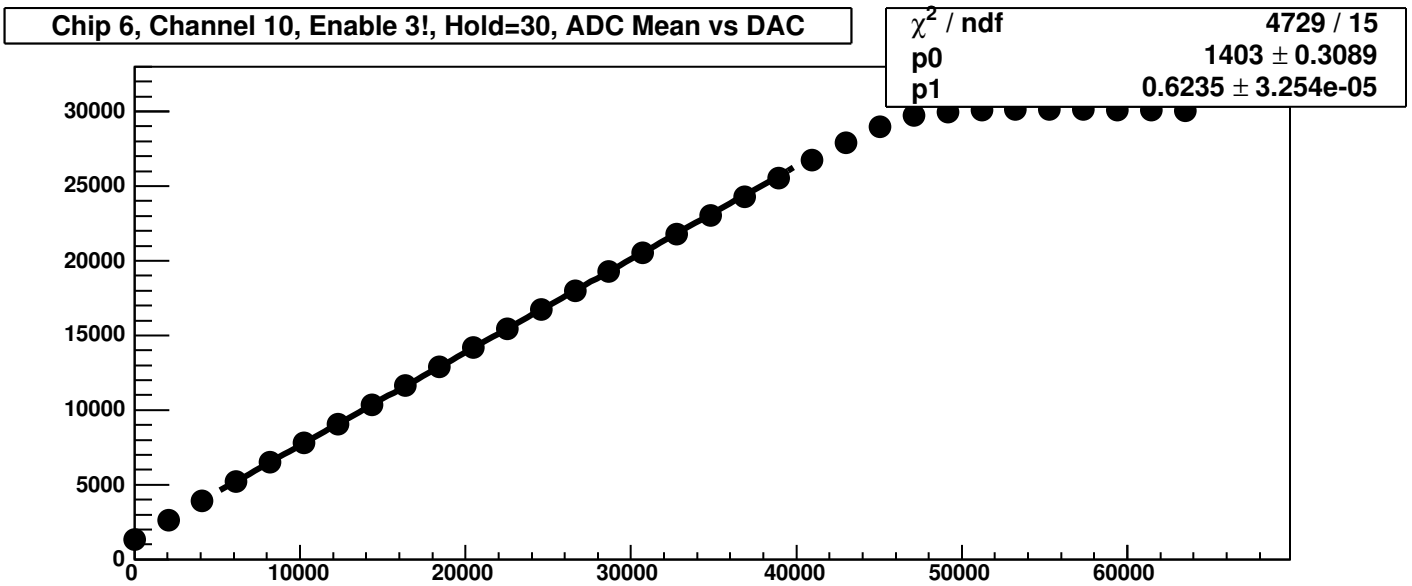


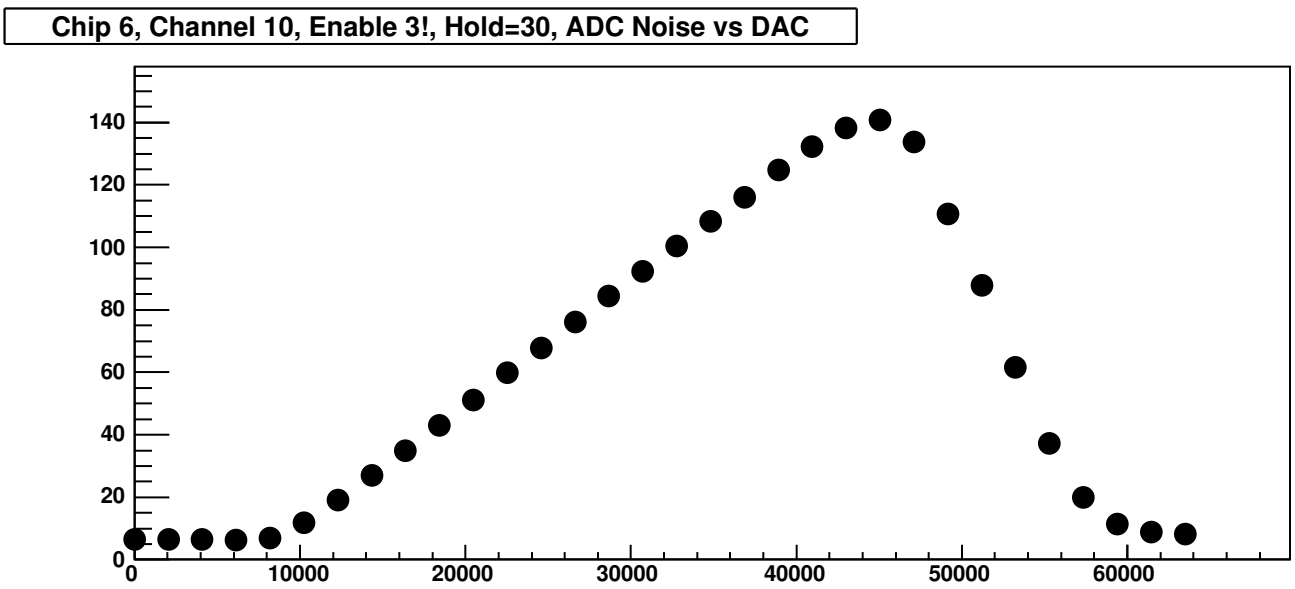
Chip 6, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC

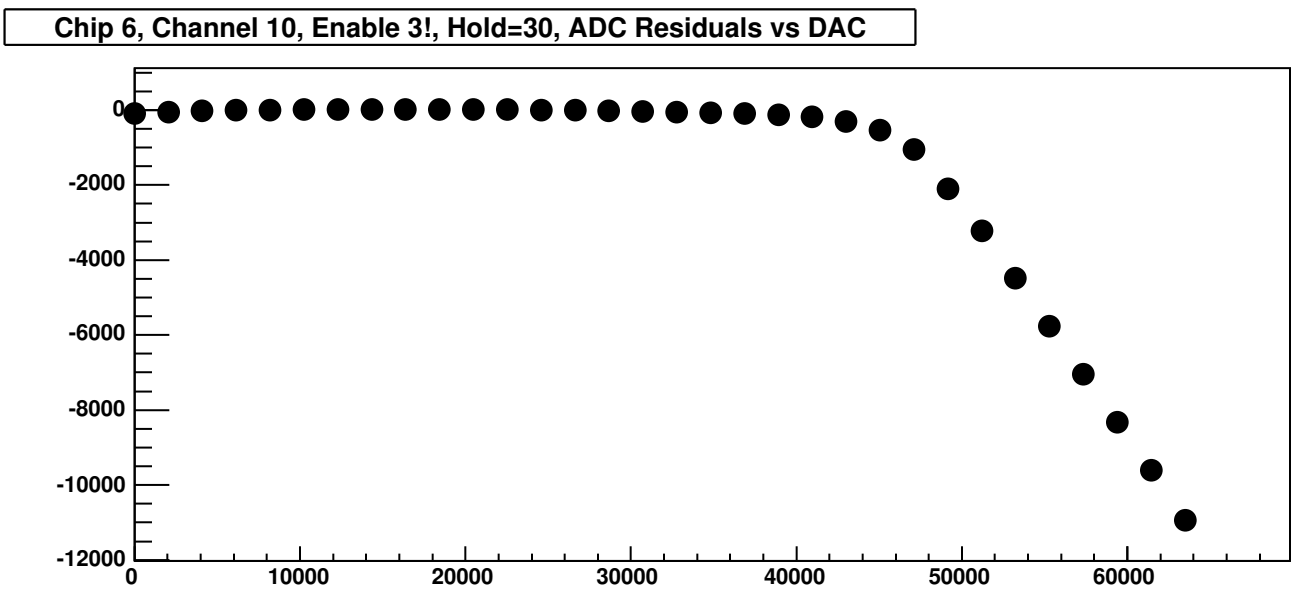


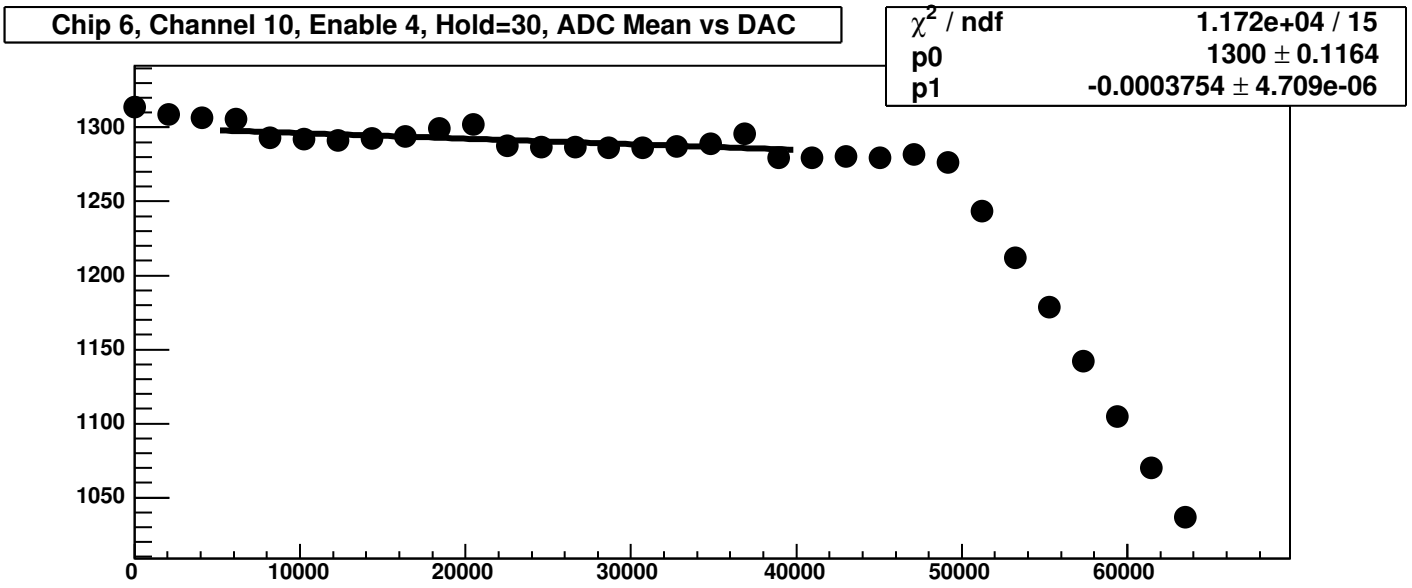
Chip 6, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

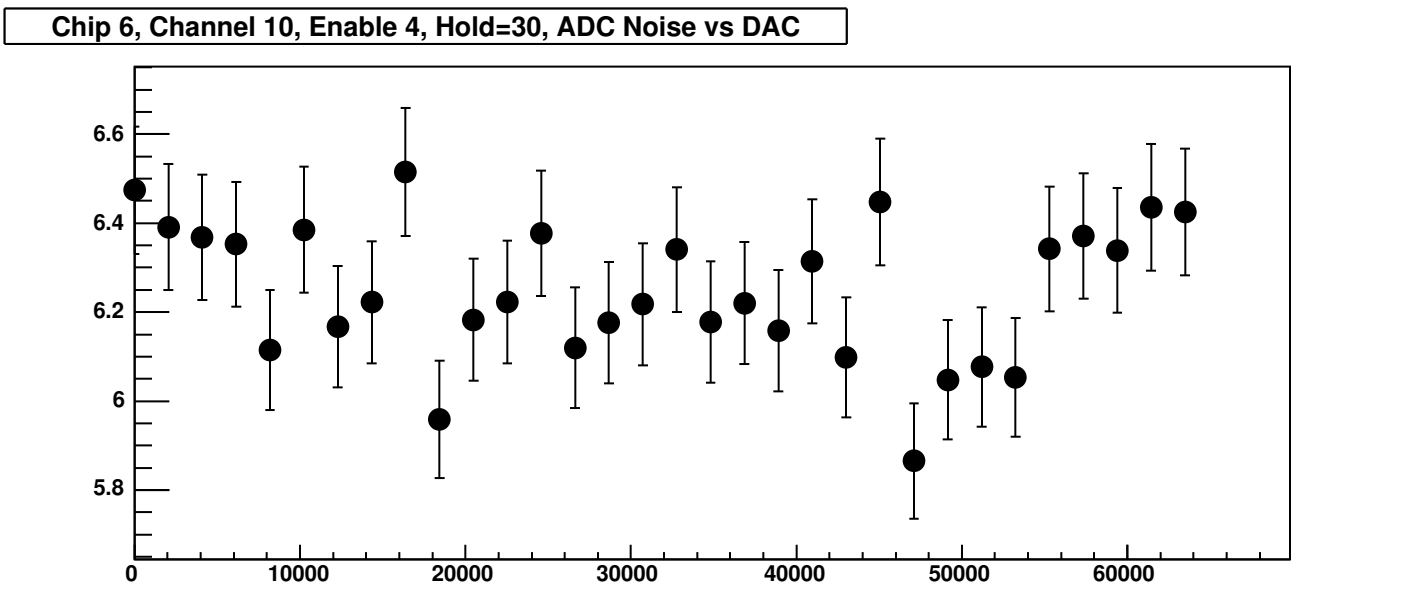


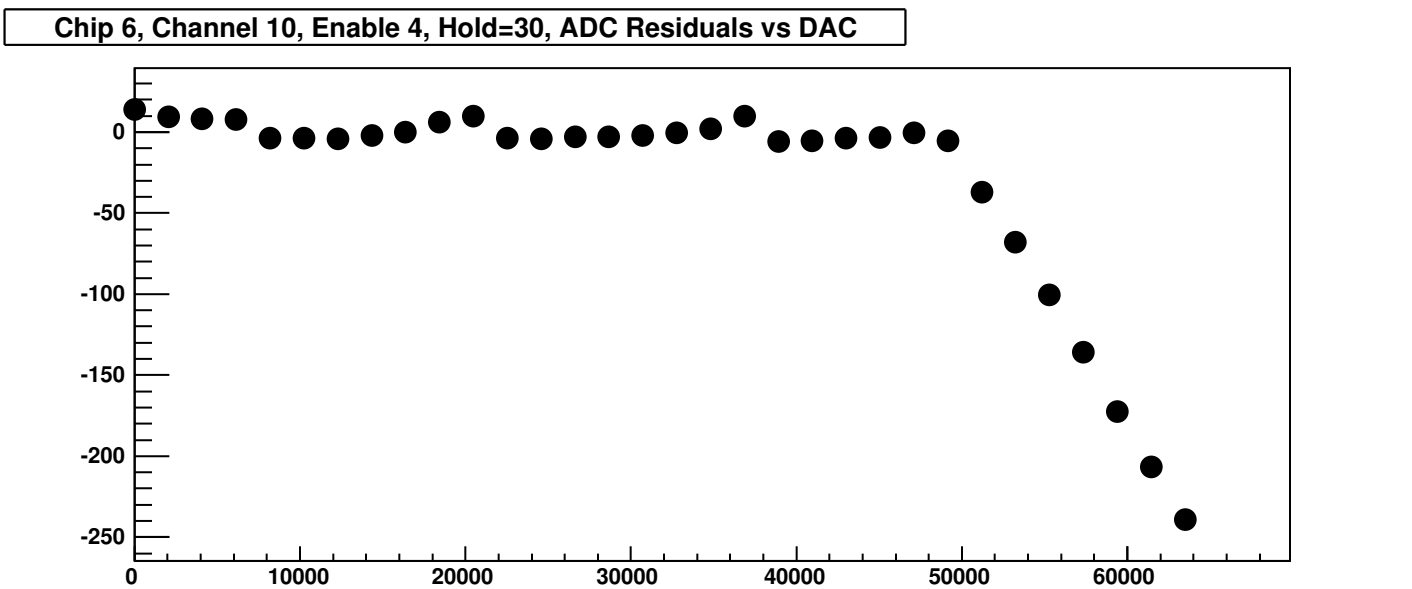

Chip 6, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 4729 / 15
p0 1403 ± 0.3089
p1 0.6235 ± 3.254e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

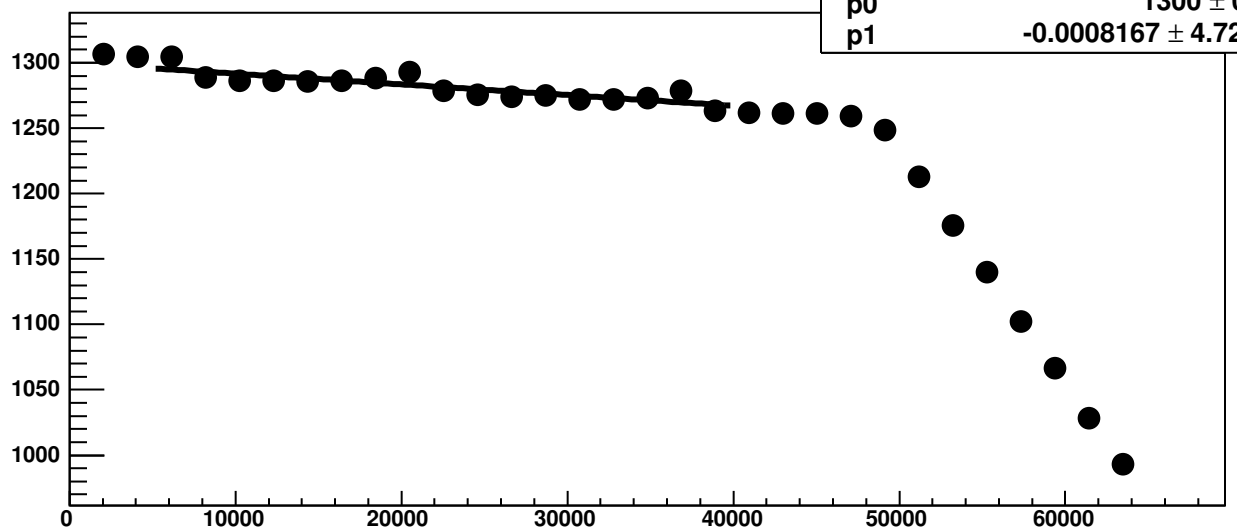

Chip 6, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.172e+04 / 15
p0 1300 ± 0.1164
p1 -0.0003754 ± 4.709e-06
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

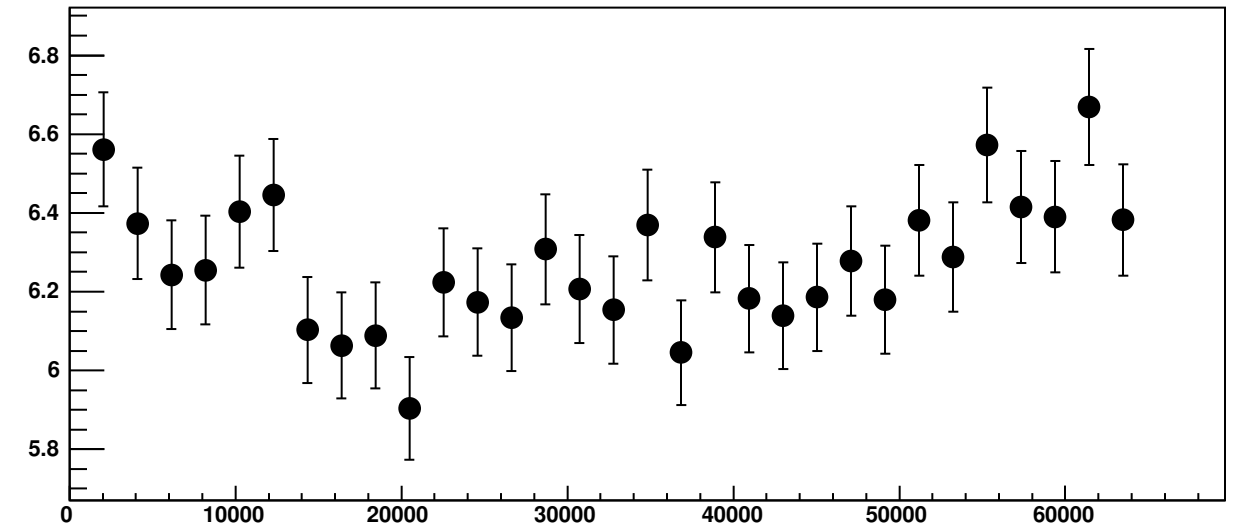

Chip 6, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

Chip 6, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC

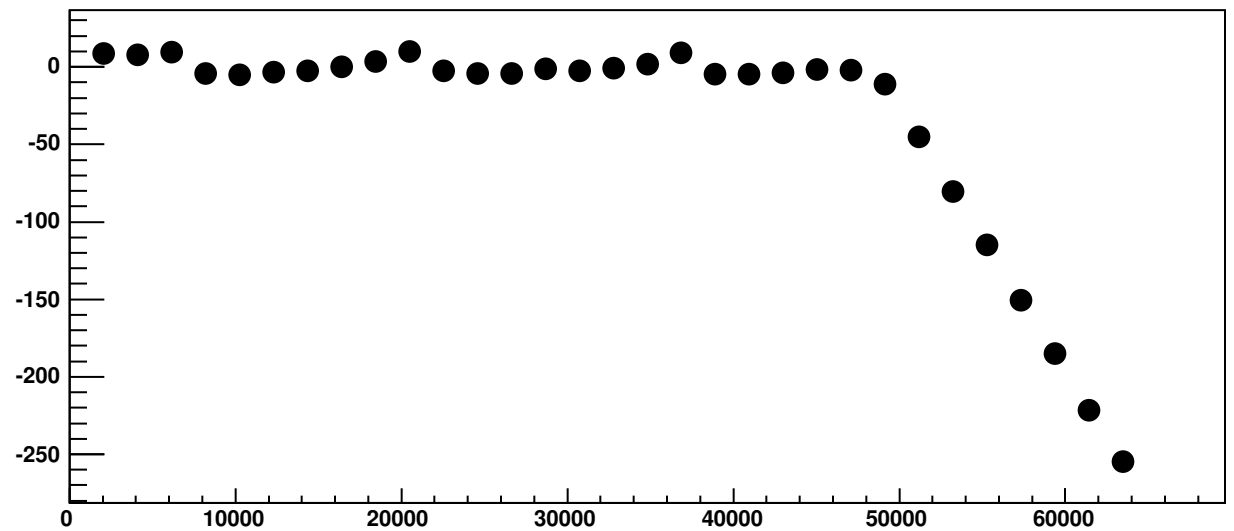
χ^2 / ndf	1.17e+04 / 15
p0	1300 ± 0.1164
p1	-0.0008167 ± 4.722e-06

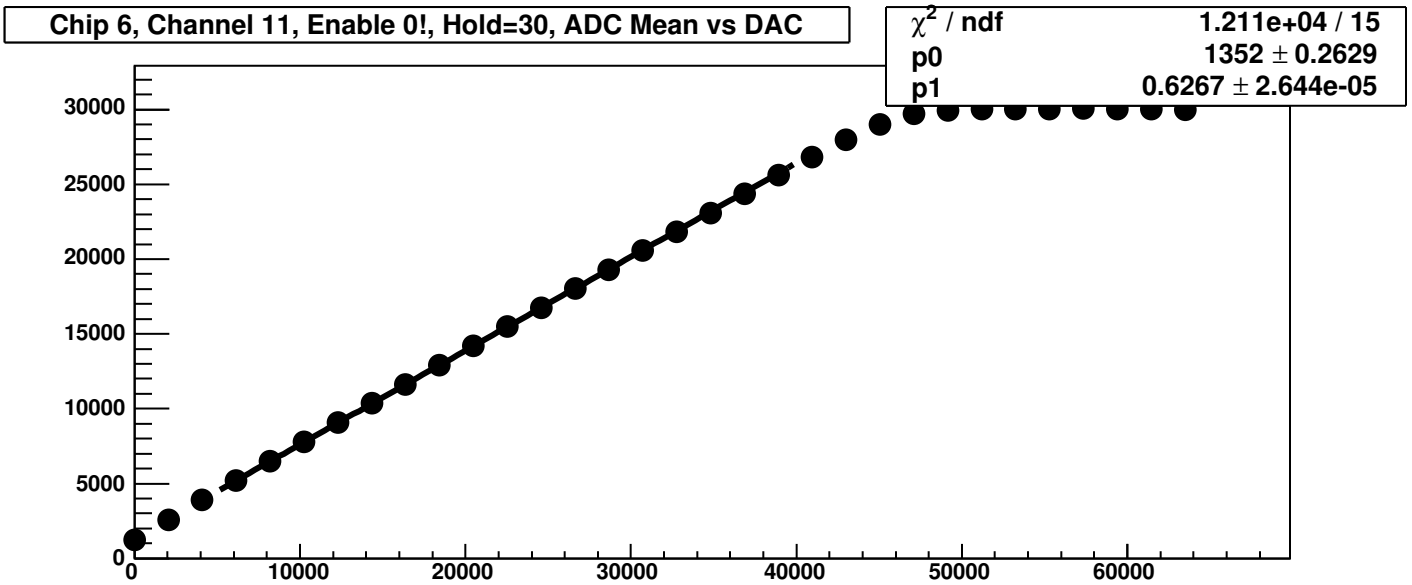


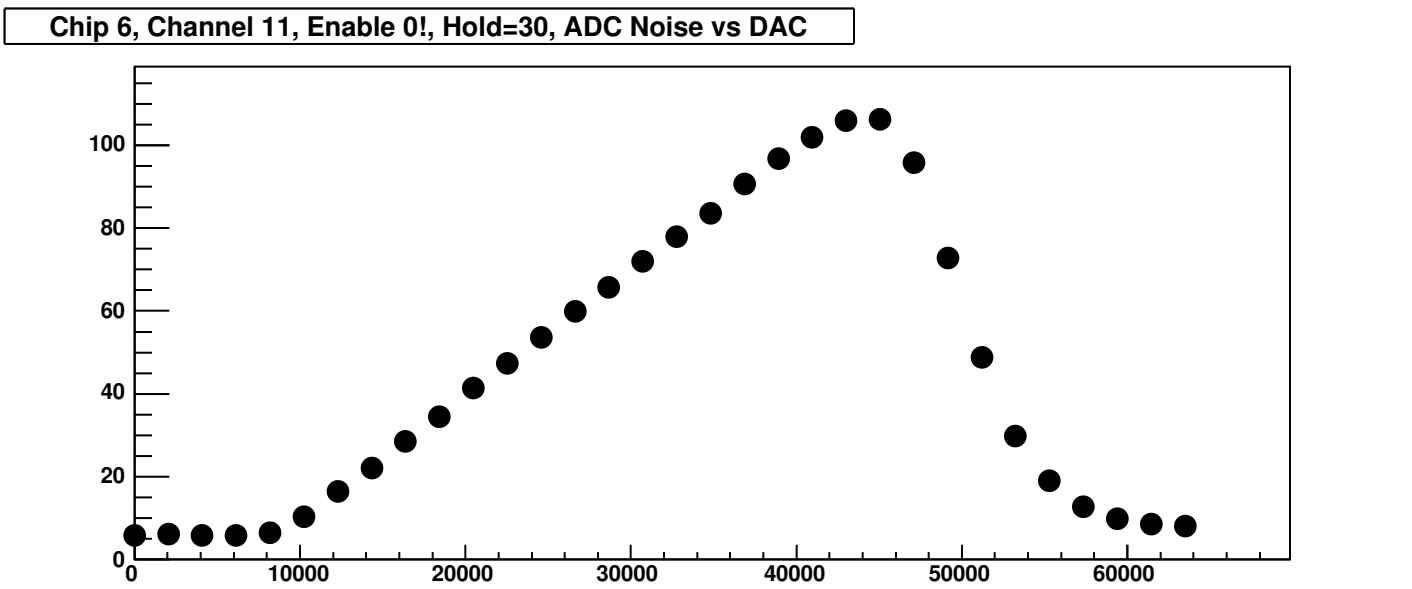
Chip 6, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC

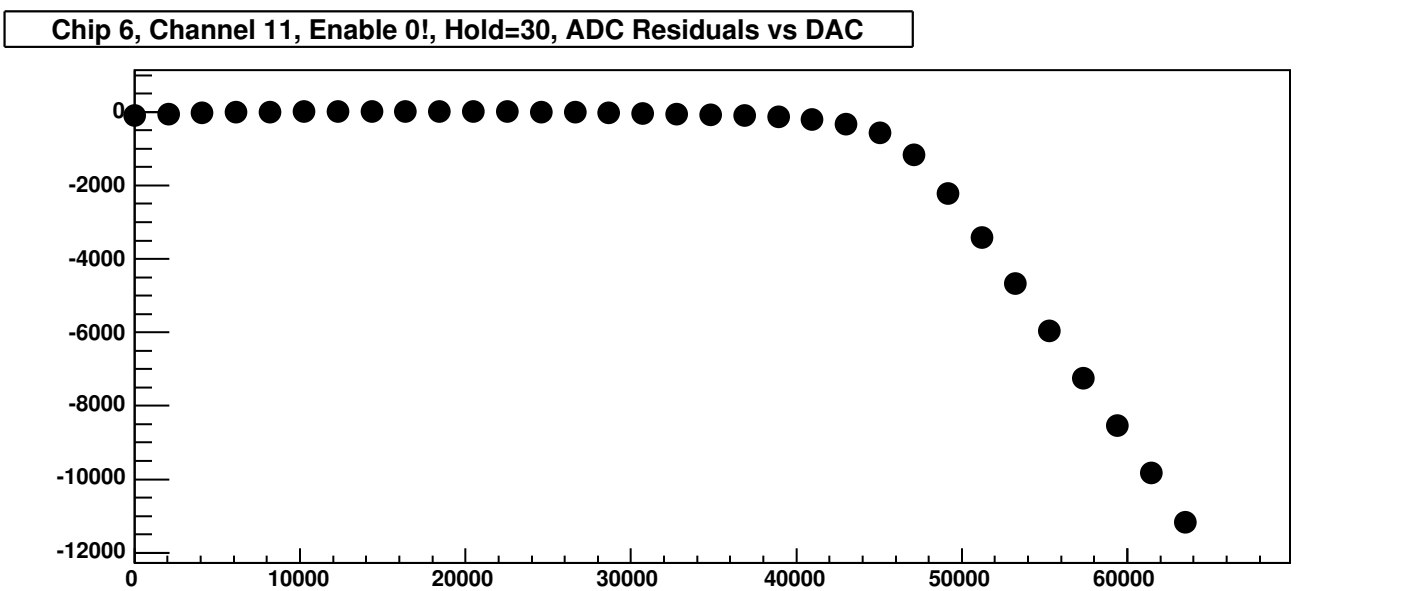


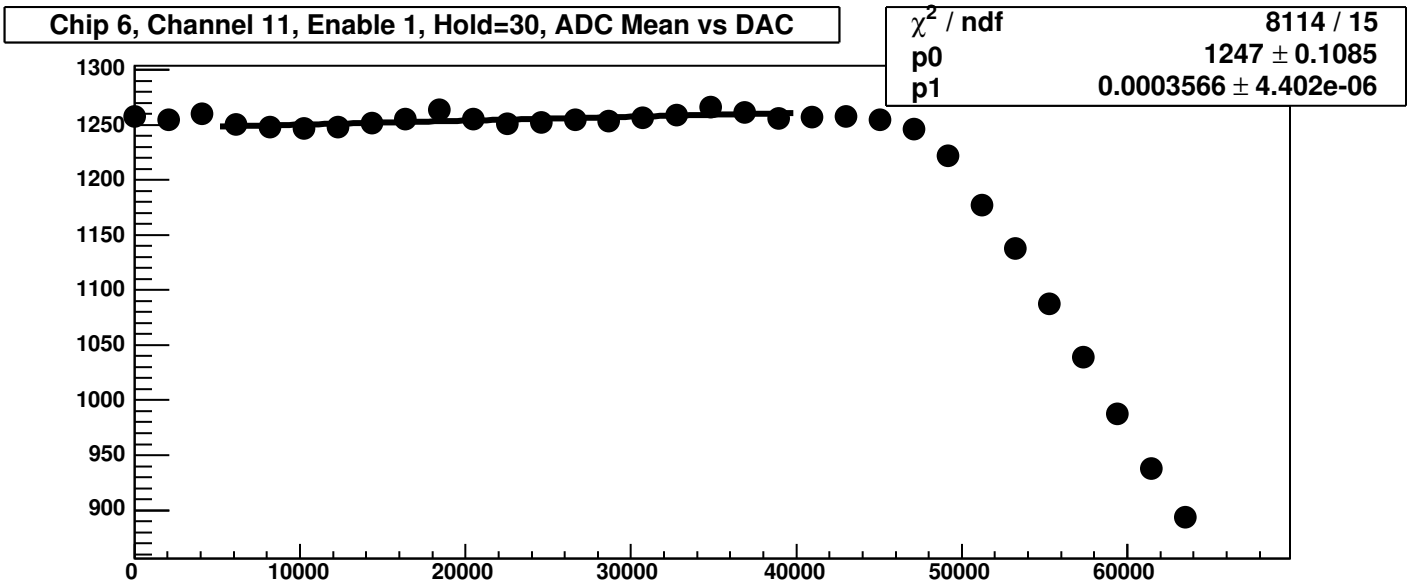
Chip 6, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC

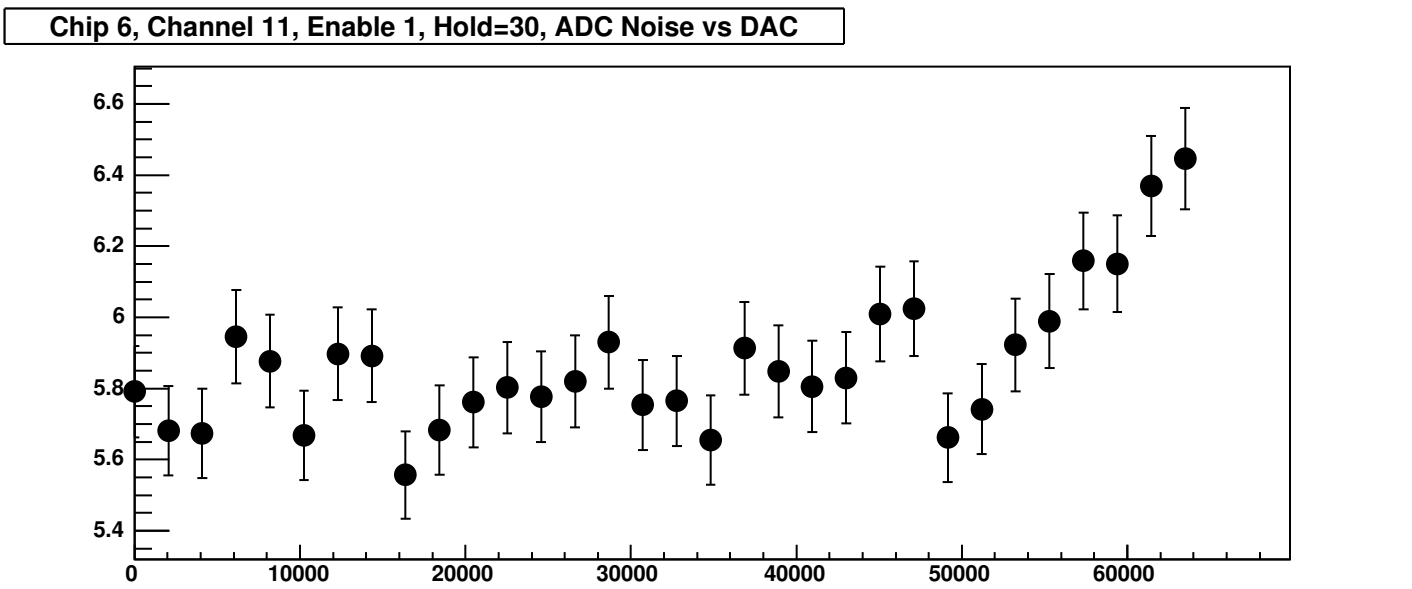


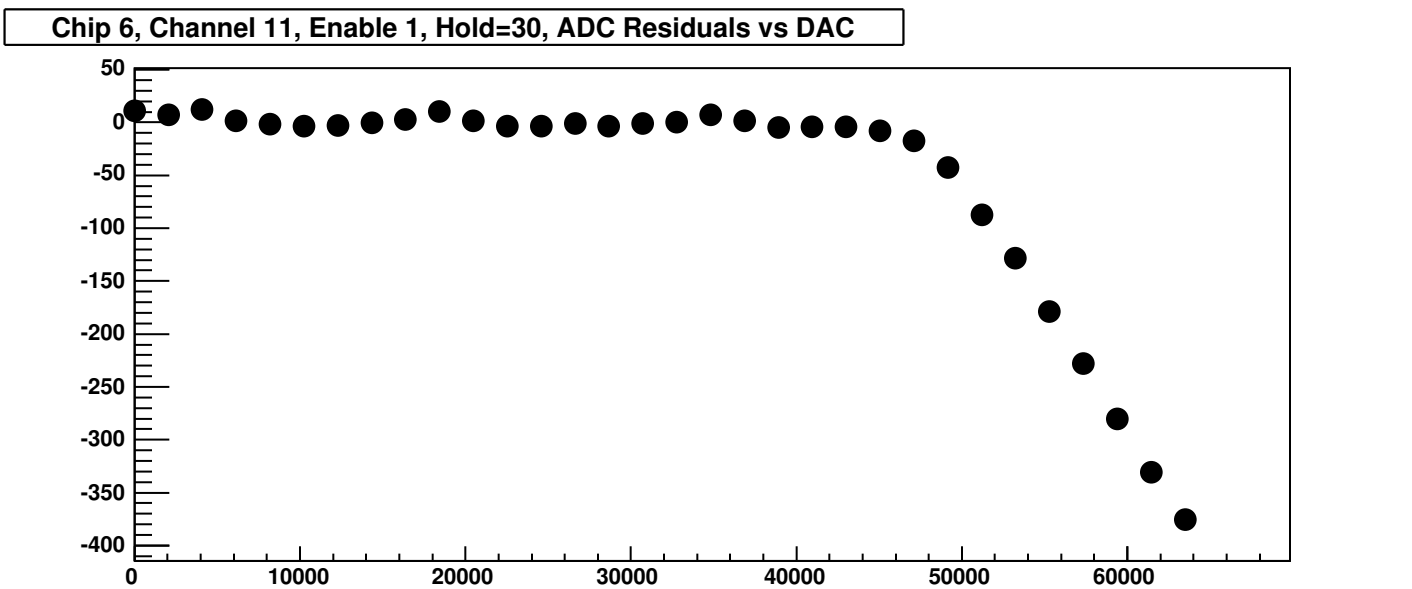

Chip 6, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.211e+04 / 15
p0 1352 ± 0.2629
p1 0.6267 ± 2.644e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

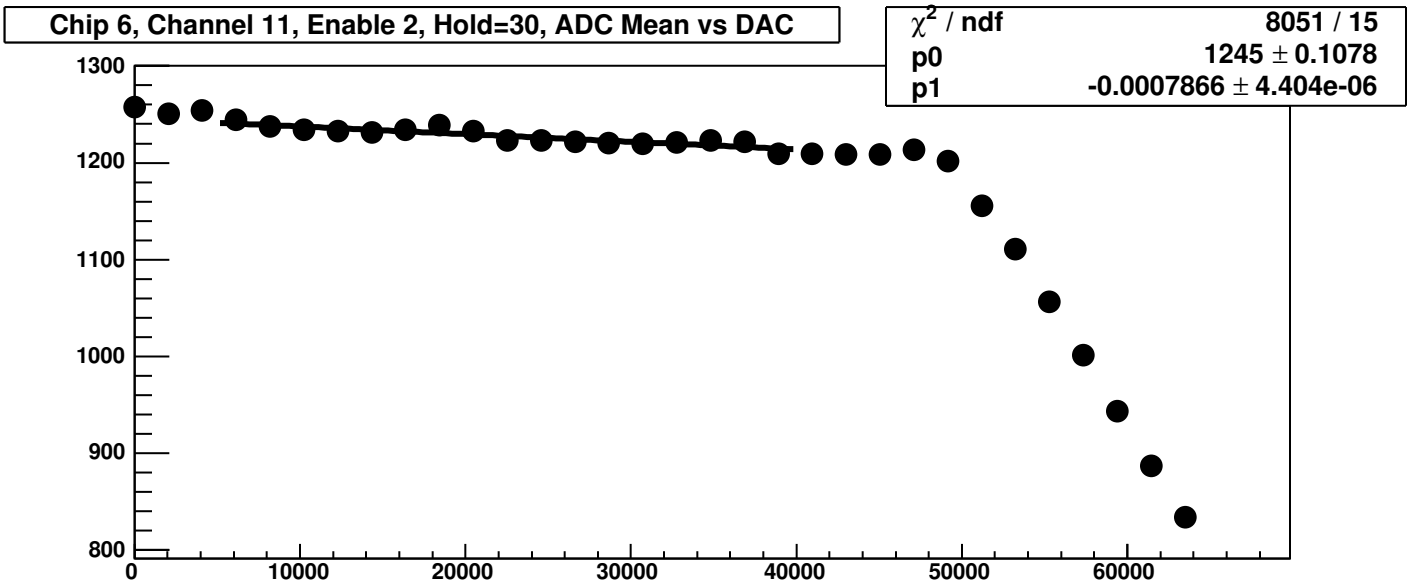

Chip 6, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

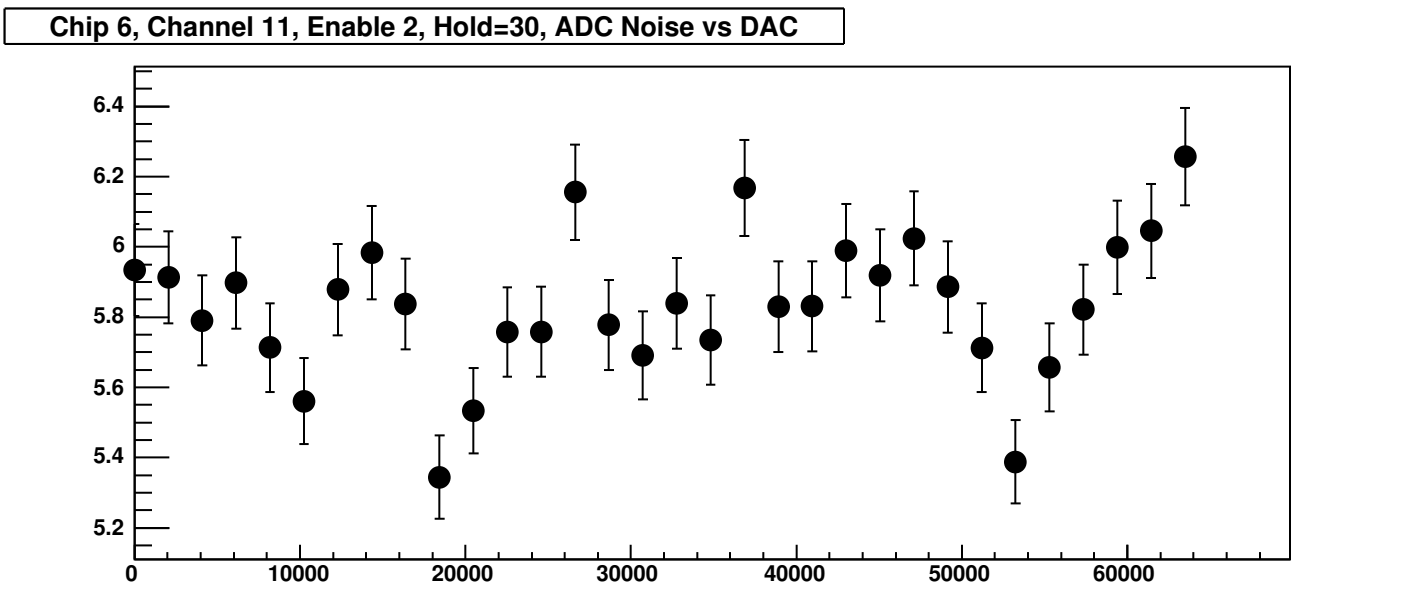

Chip 6, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

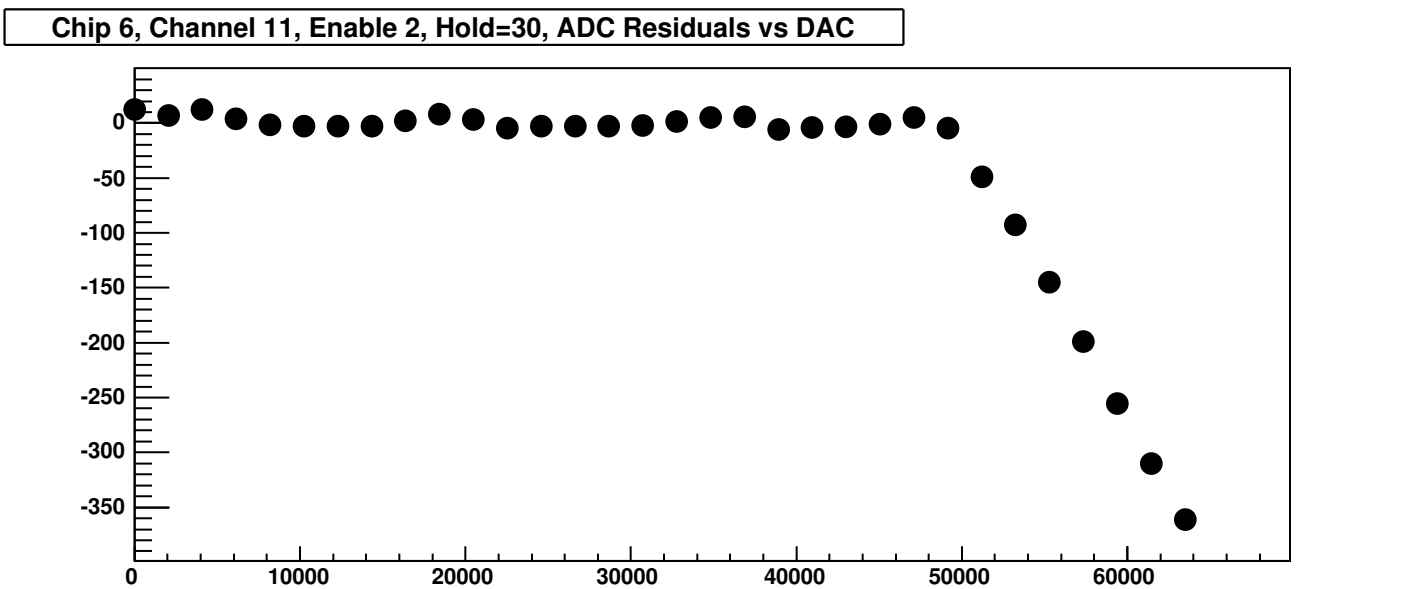

Chip 6, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 8114 / 15
p0 1247 ± 0.1085
p1 0.0003566 ± 4.402e-06
1300
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

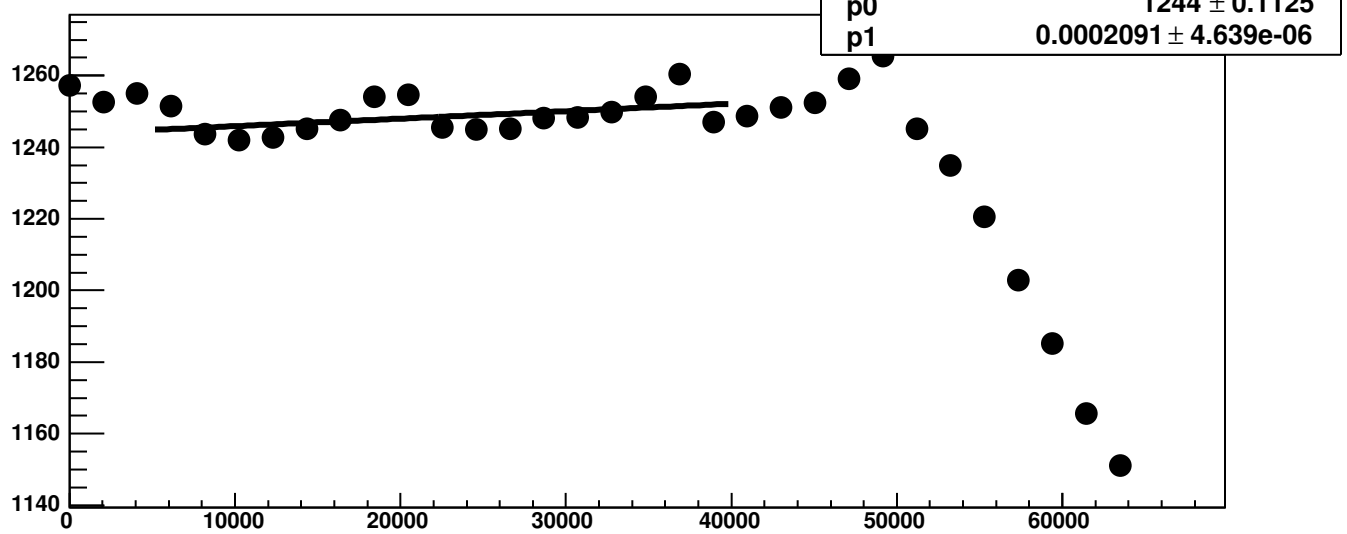

Chip 6, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8051 / 15
p0 1245 ± 0.1078
p1 -0.0007866 ± 4.404e-06
1300
1200
1100
1000
900
800
0
10000
20000
30000
40000
50000
60000

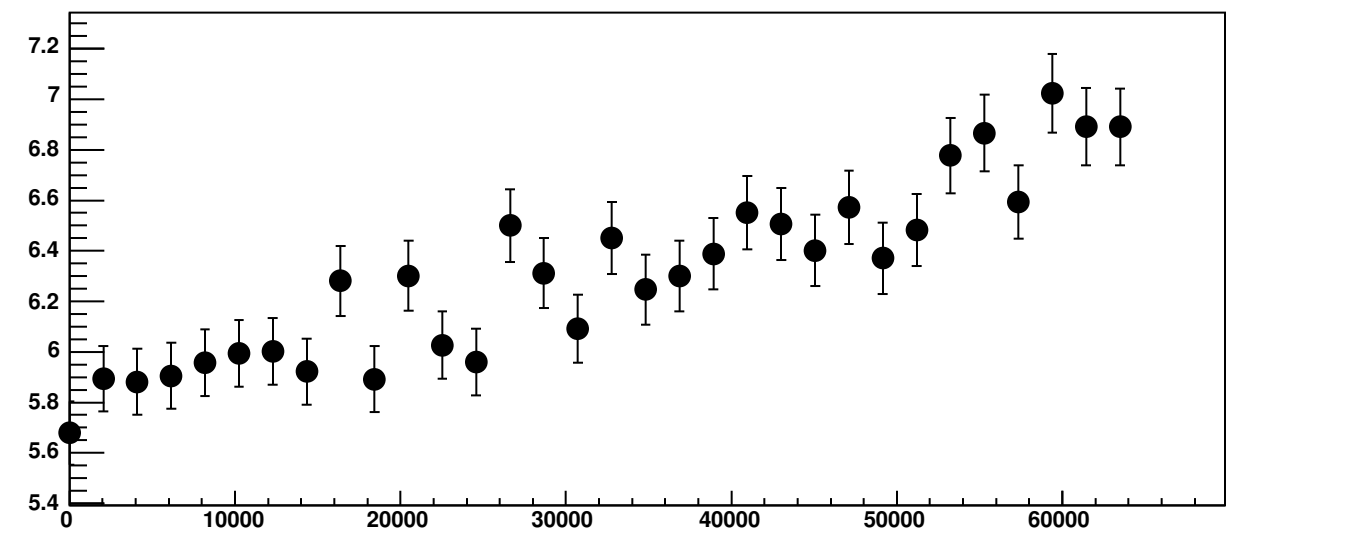

Chip 6, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

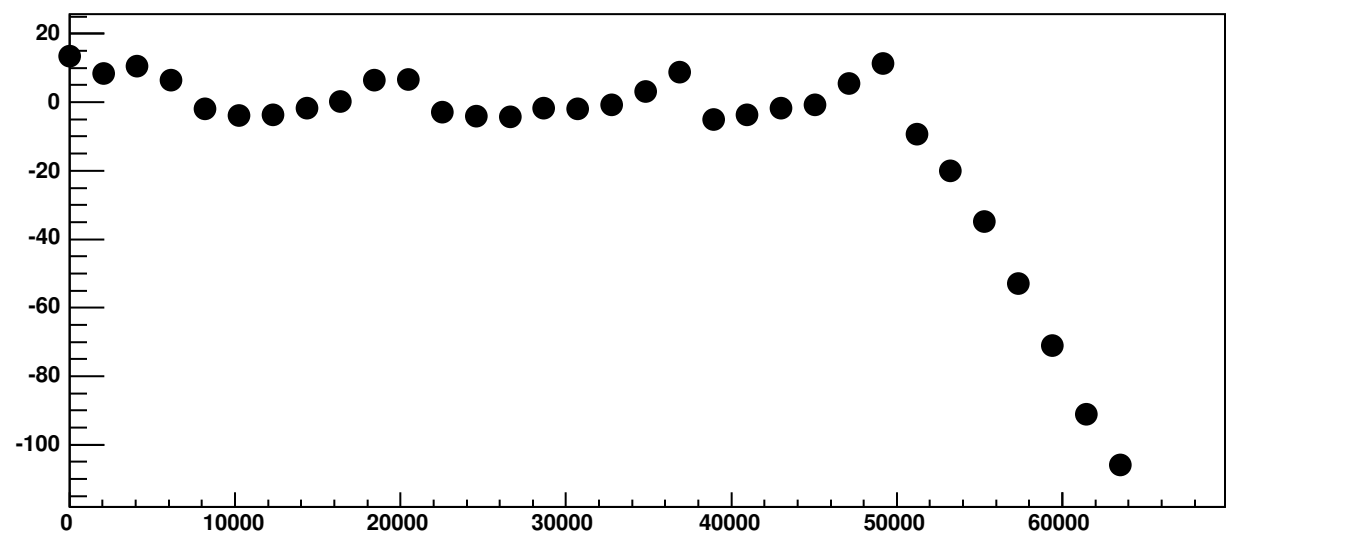
Chip 6, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC

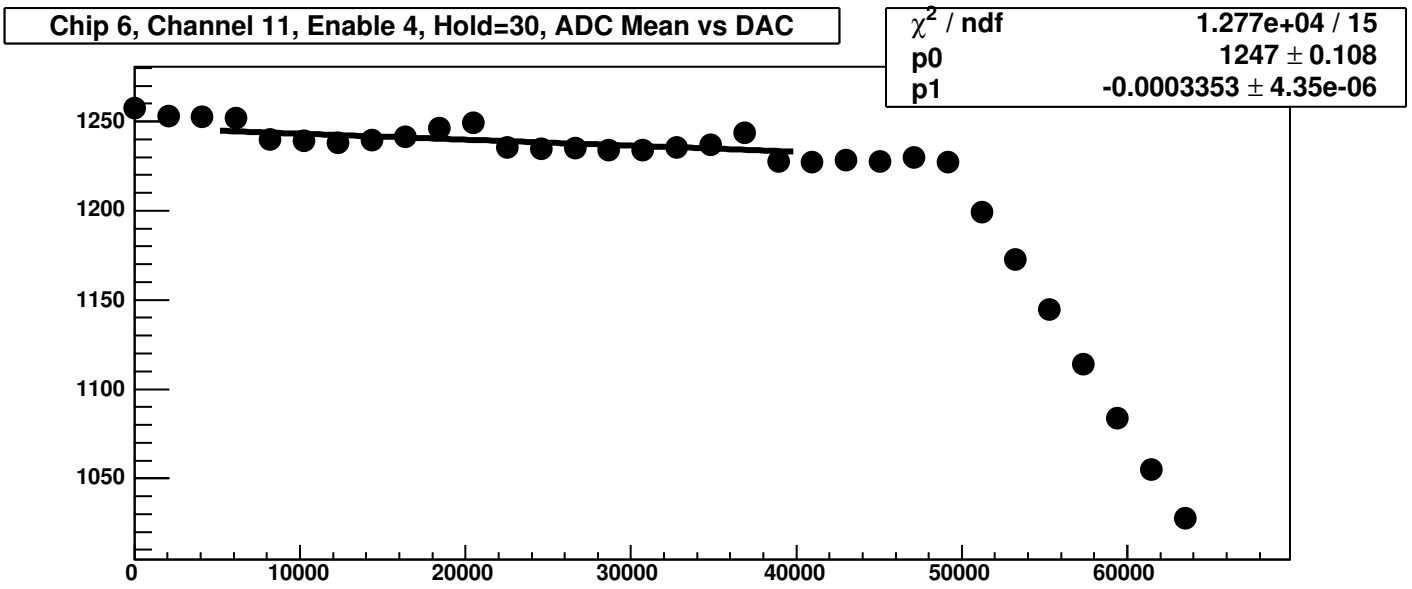


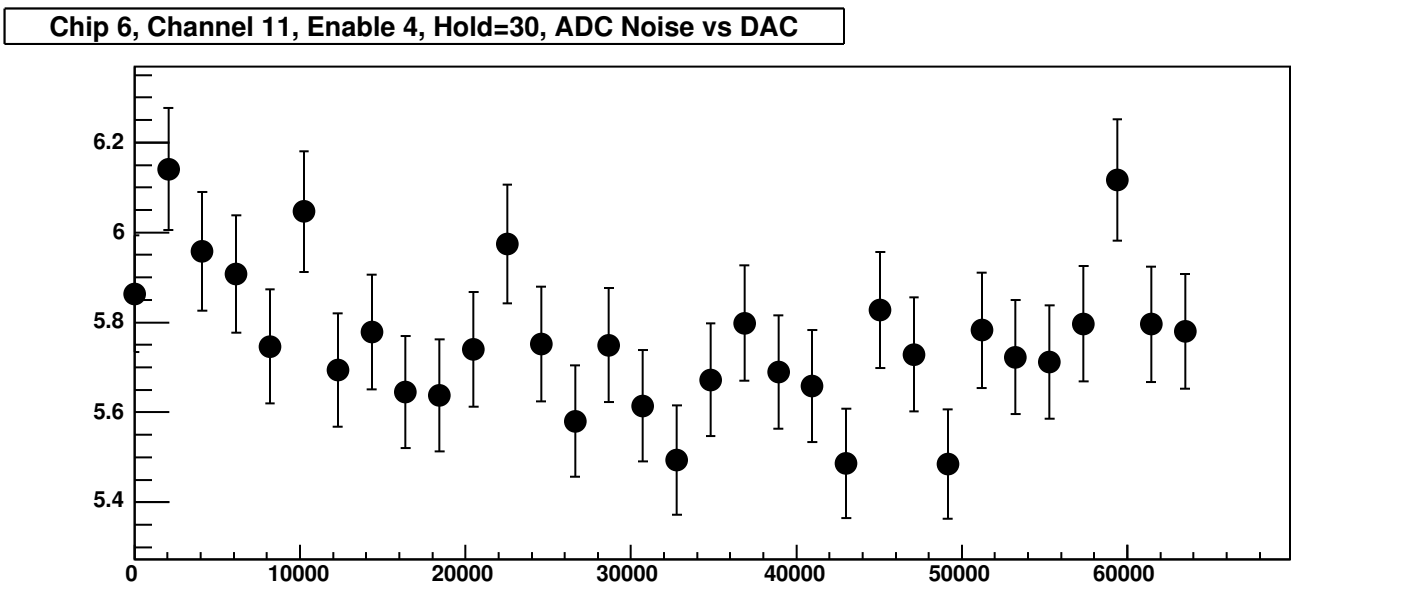
Chip 6, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC

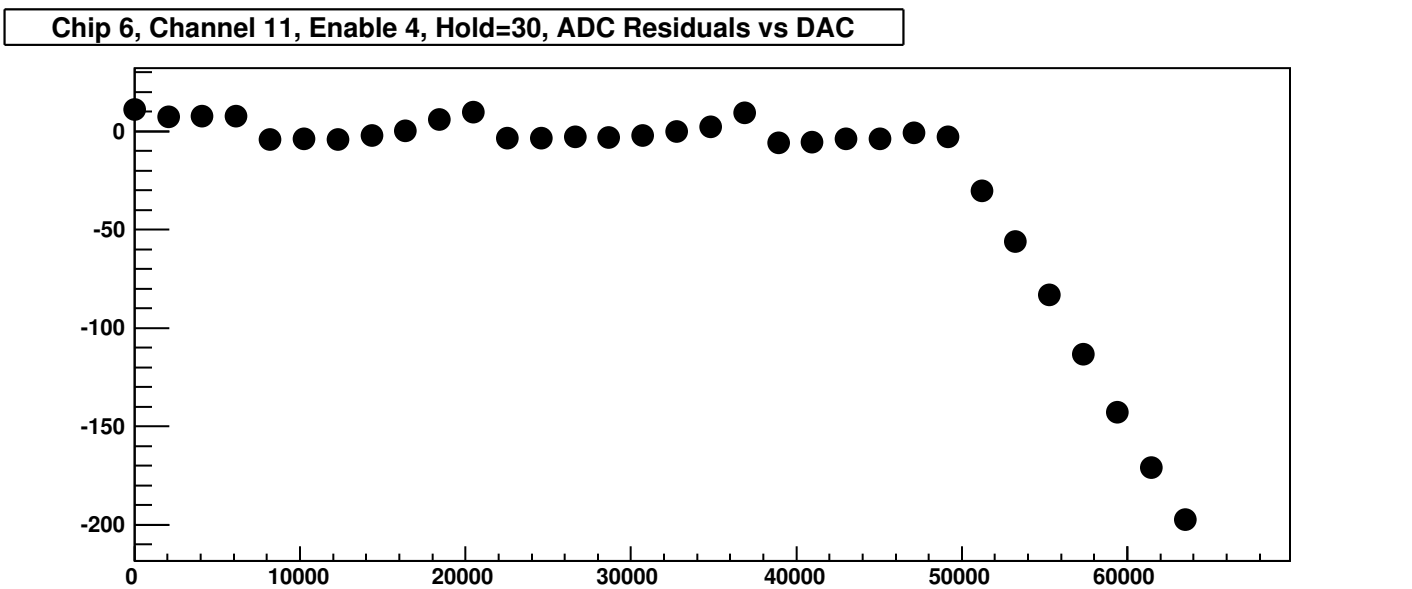


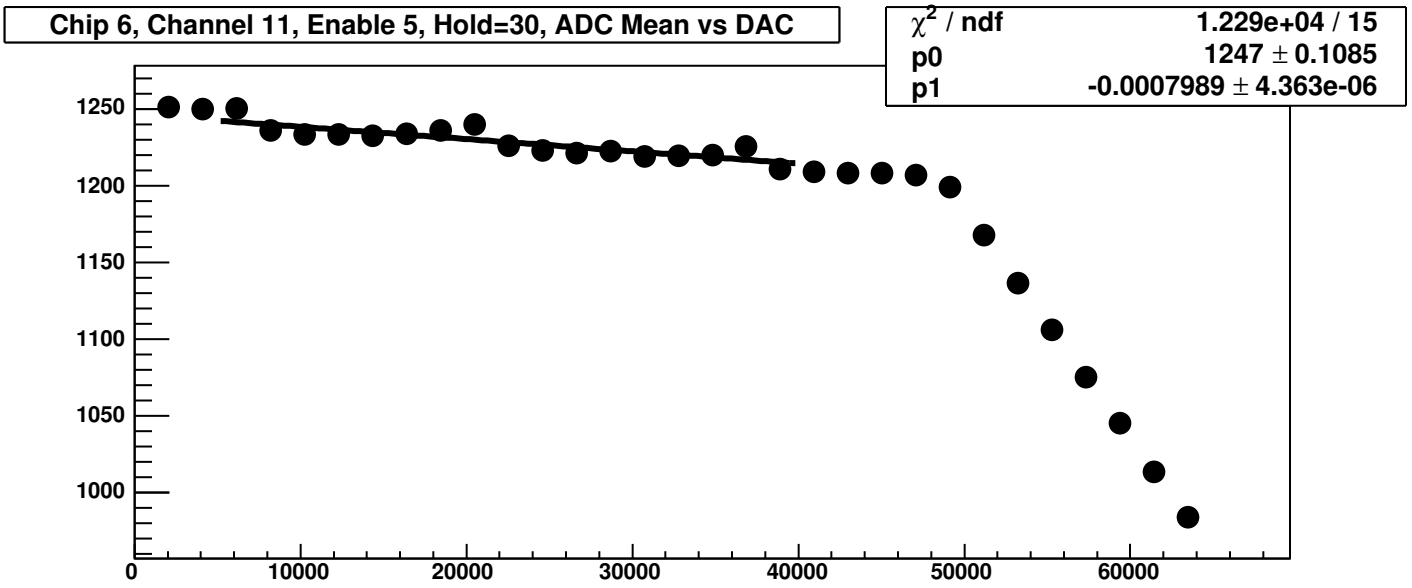
Chip 6, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC

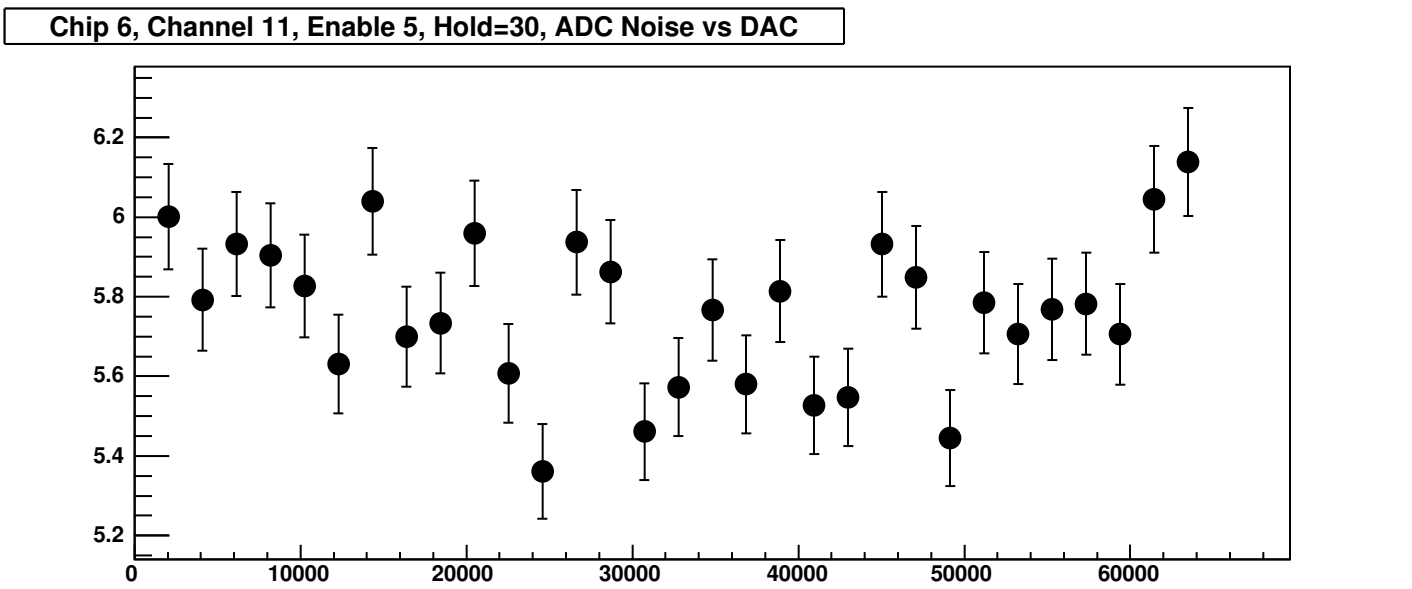


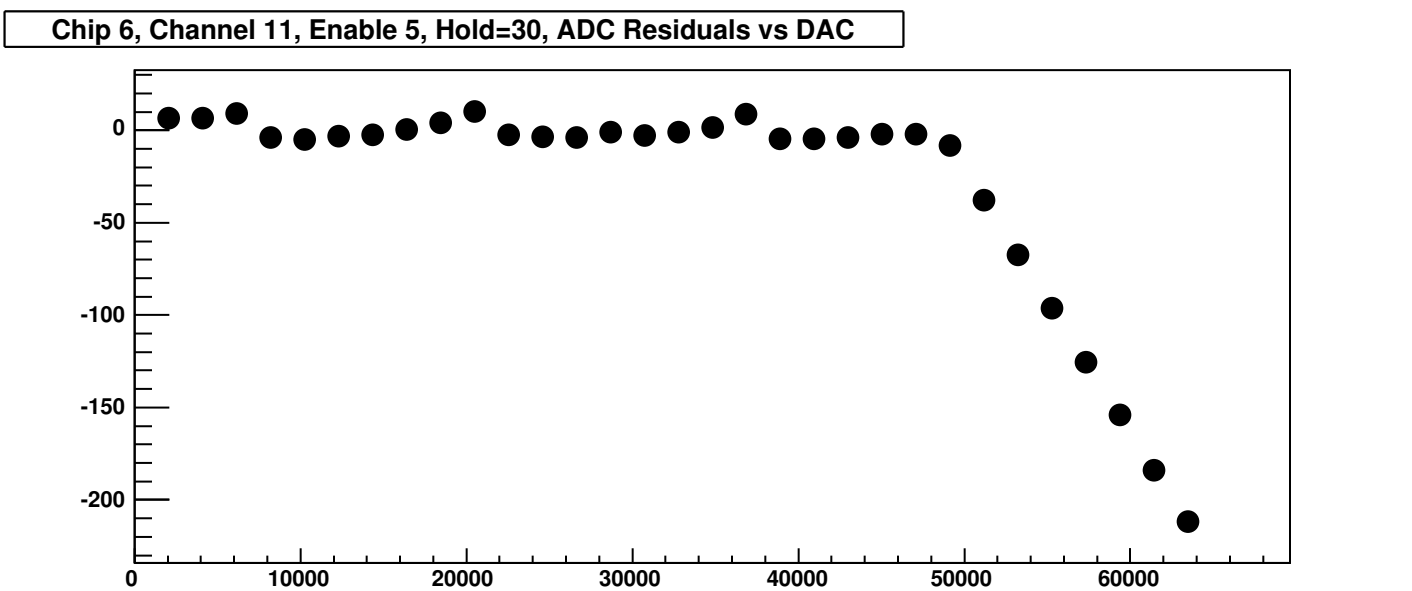

Chip 6, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.277e+04 / 15
p0 1247 ± 0.108
p1 -0.0003353 ± 4.35e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

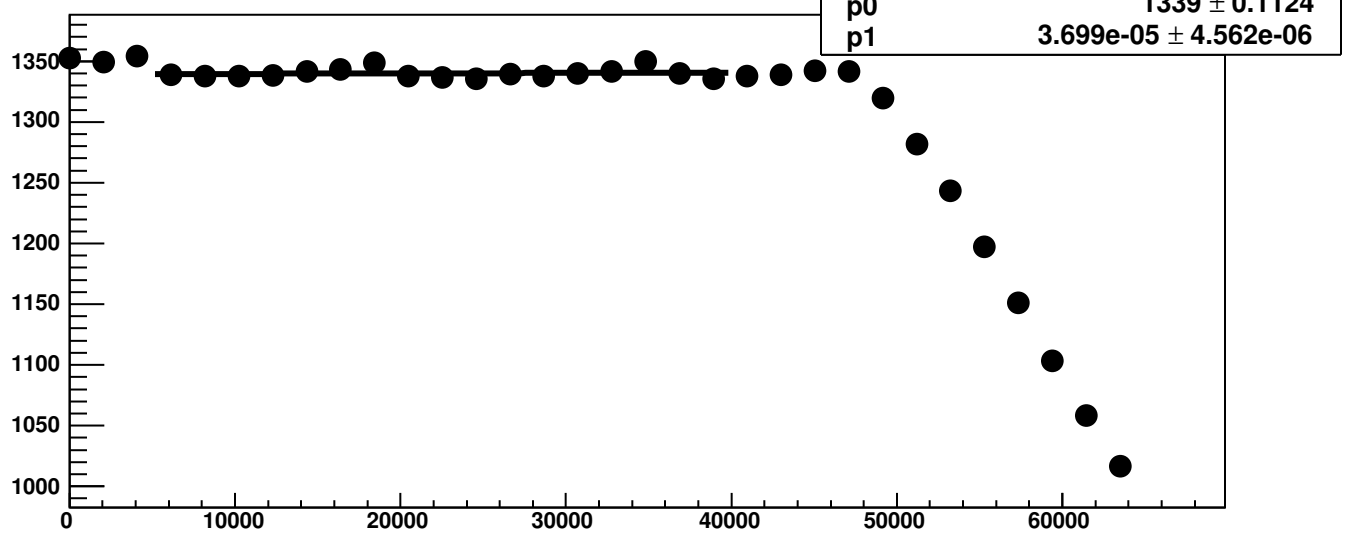

Chip 6, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.229e+04 / 15
p0 1247 ± 0.1085
p1 -0.0007989 ± 4.363e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

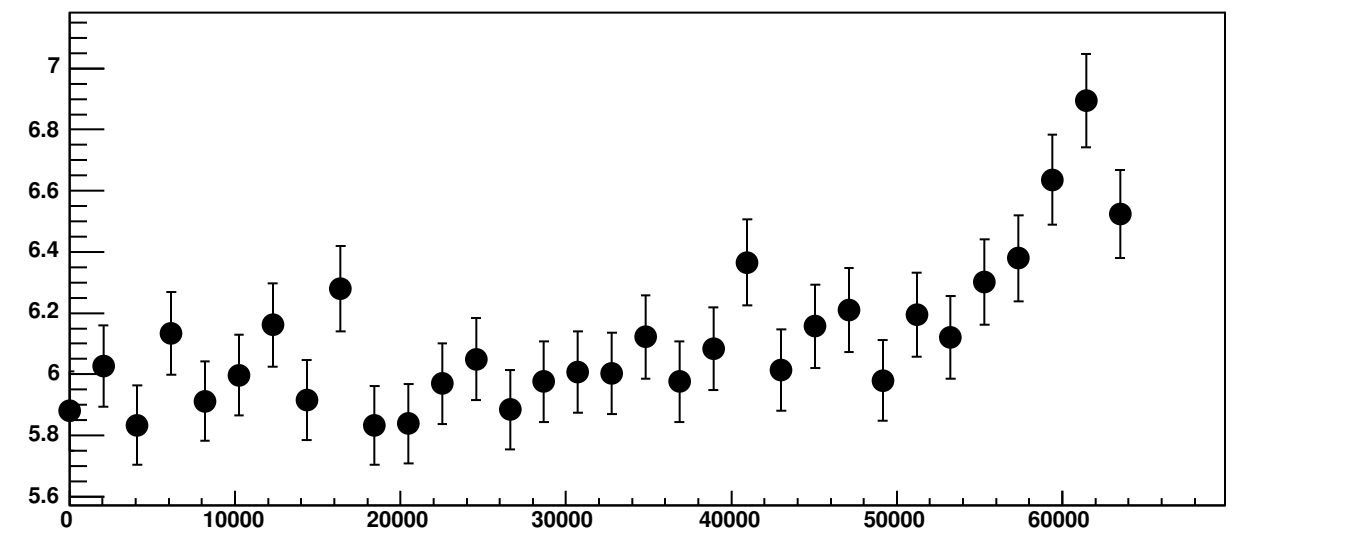

Chip 6, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

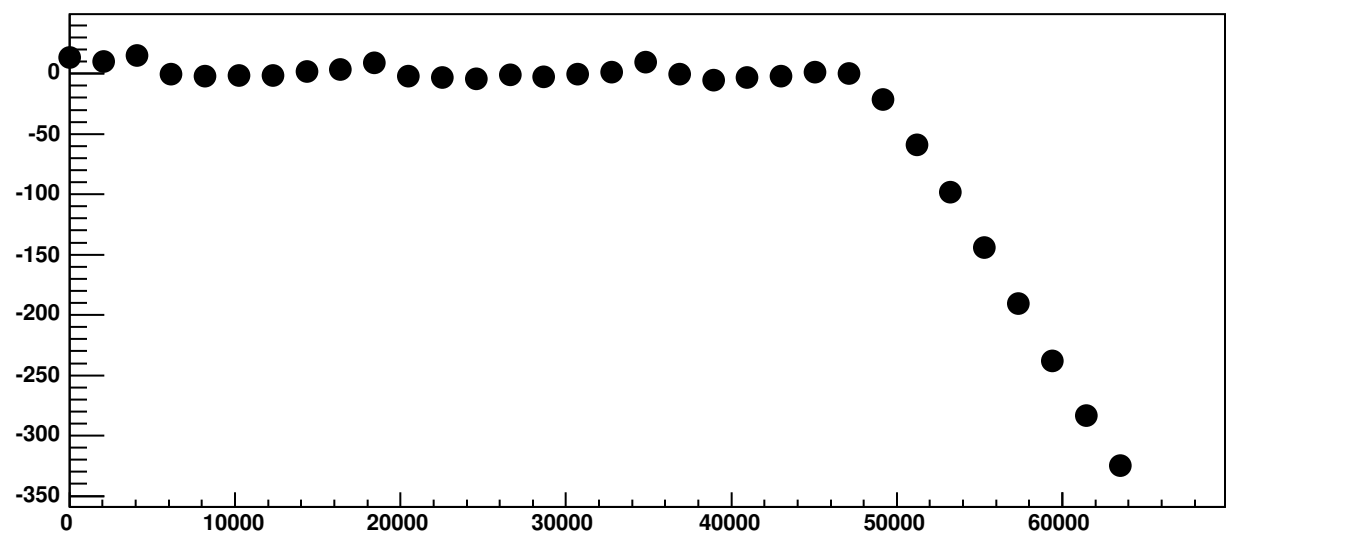
Chip 6, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

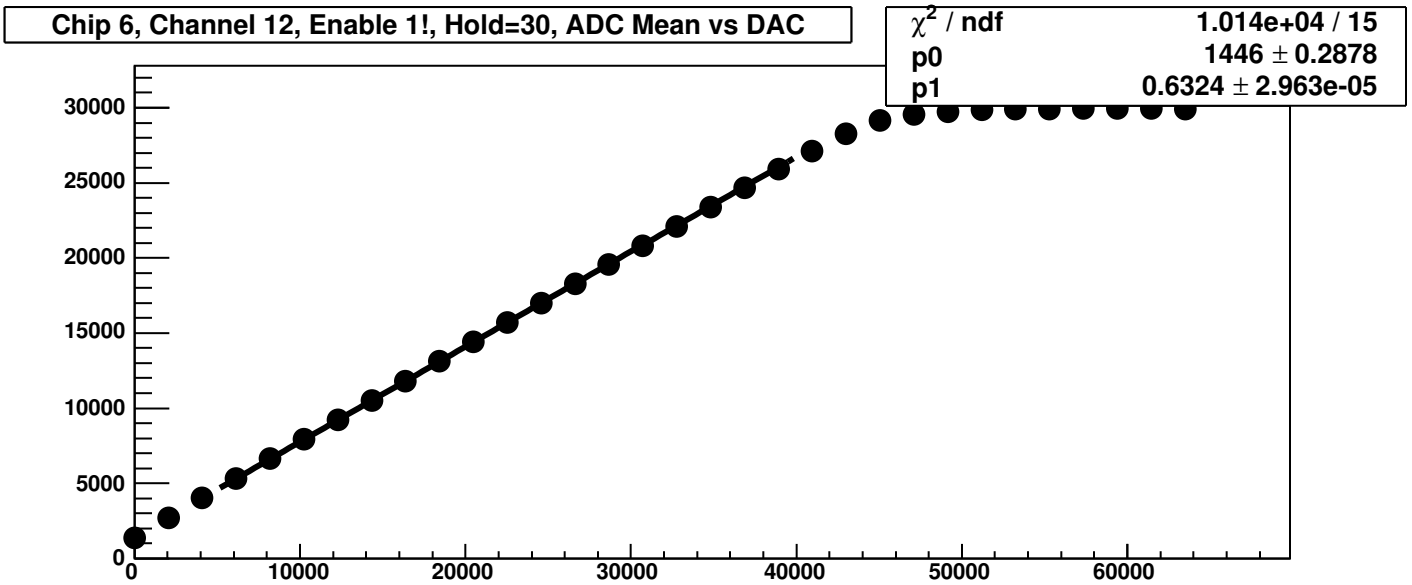


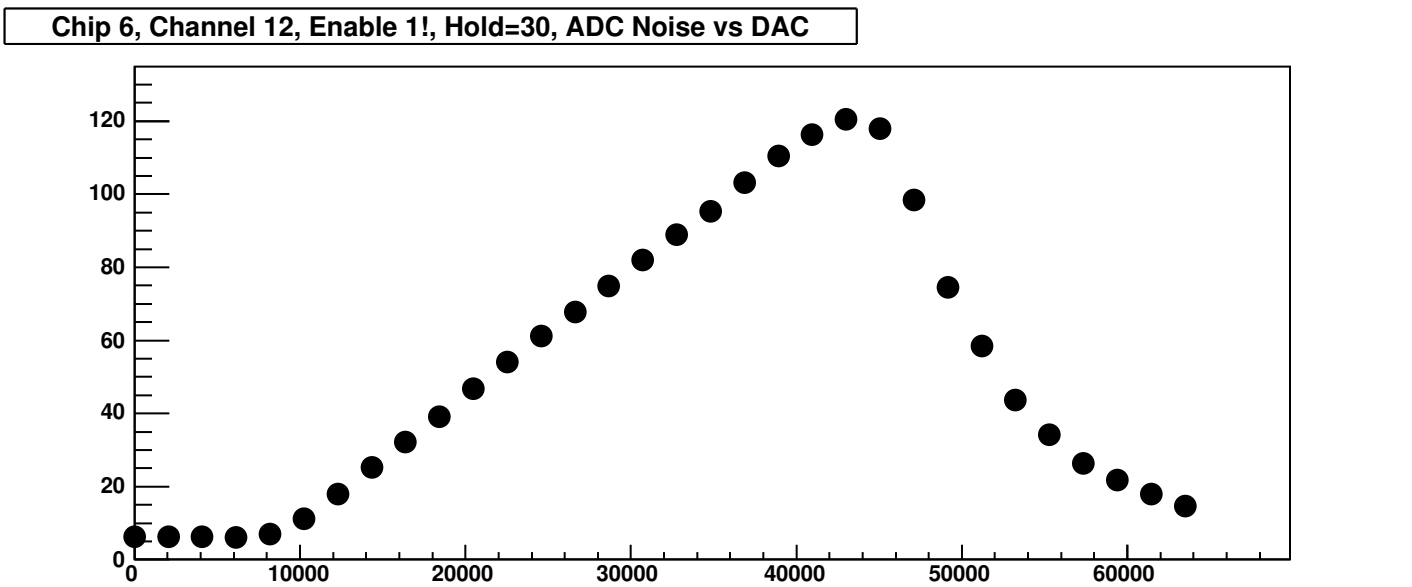
Chip 6, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC

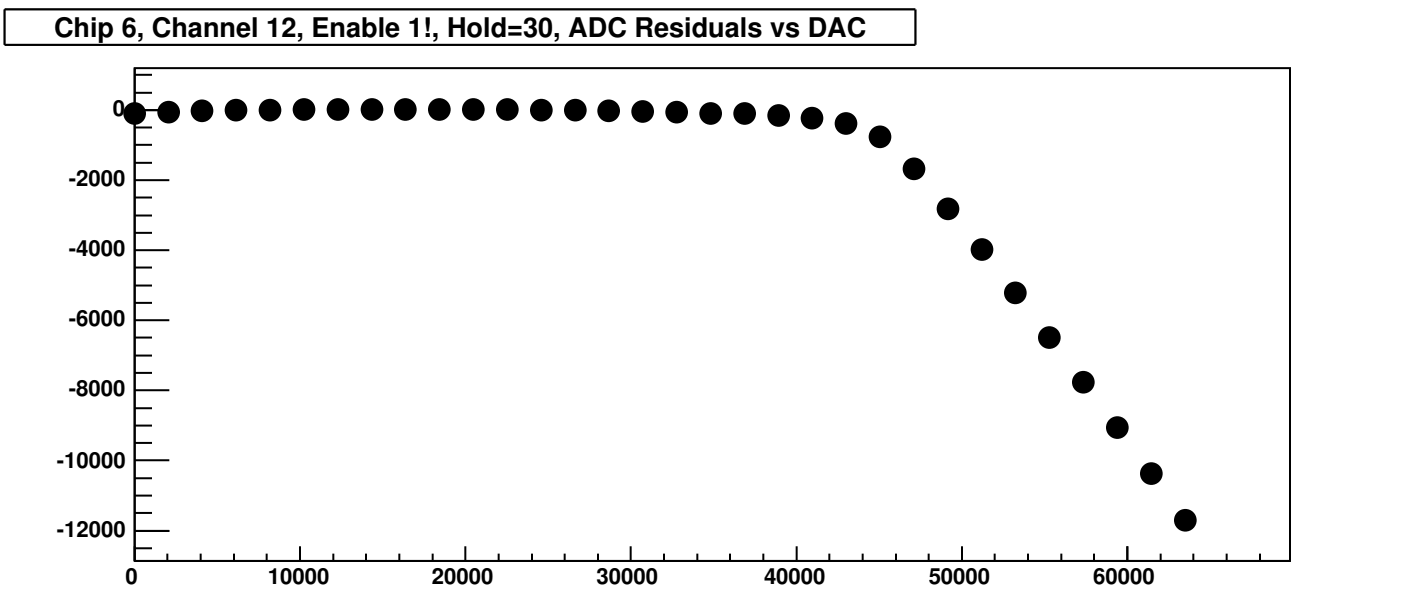


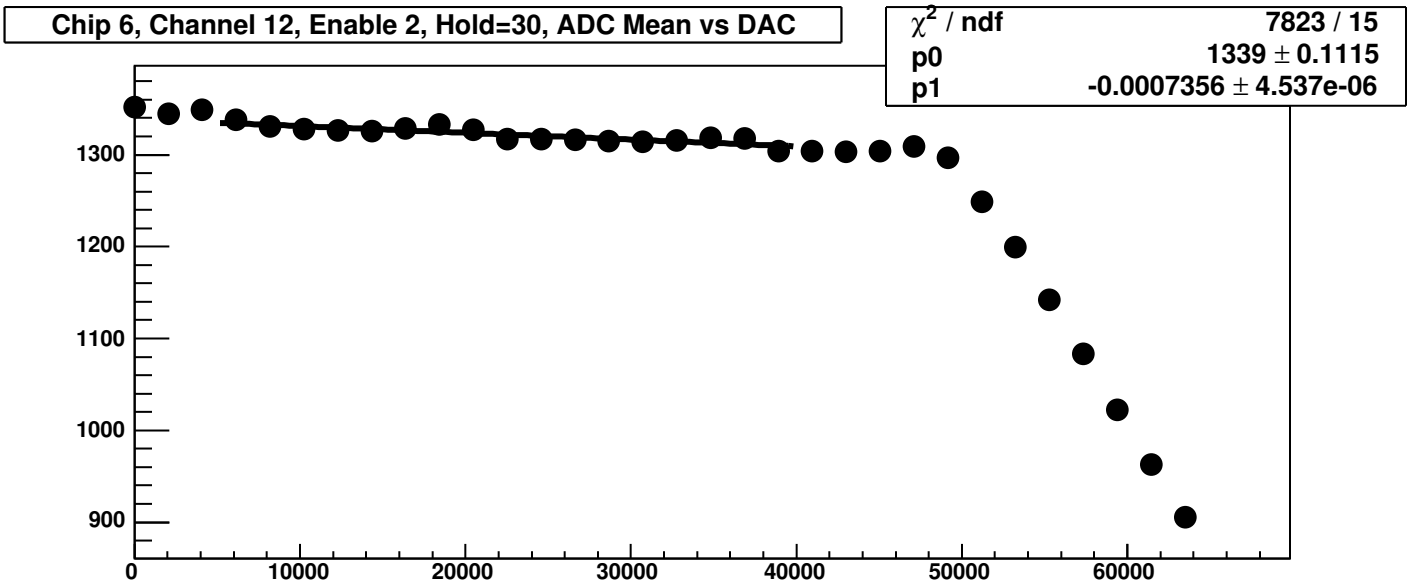
Chip 6, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

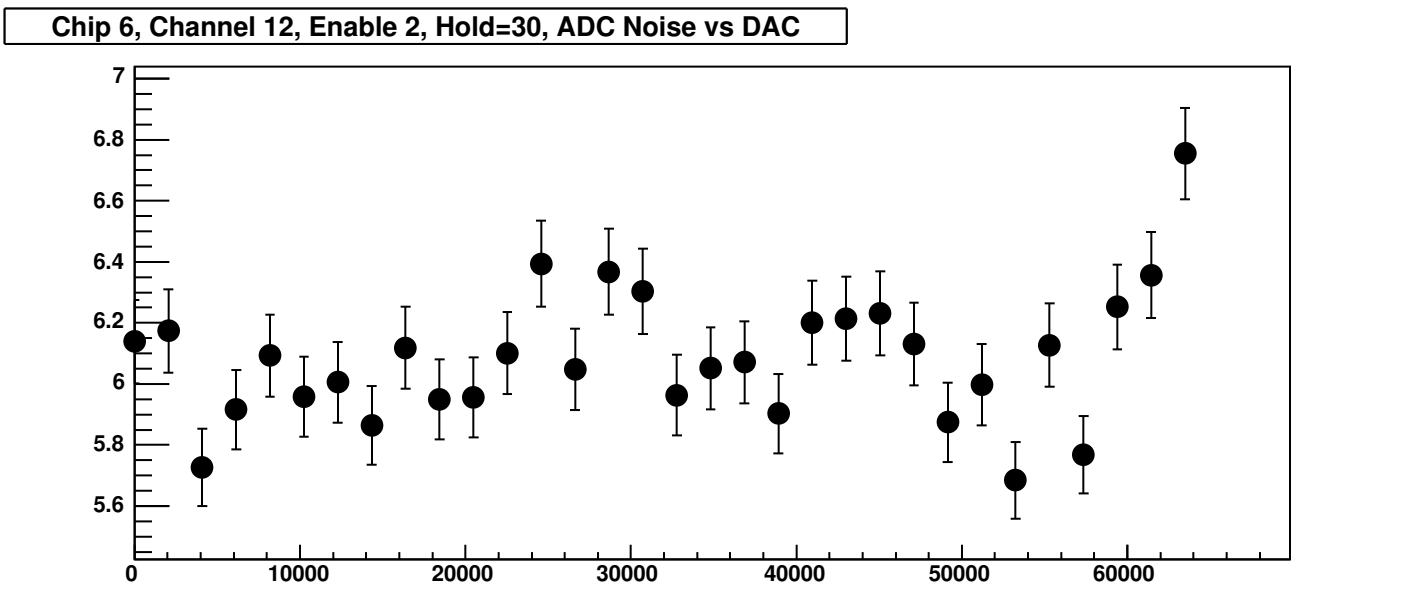


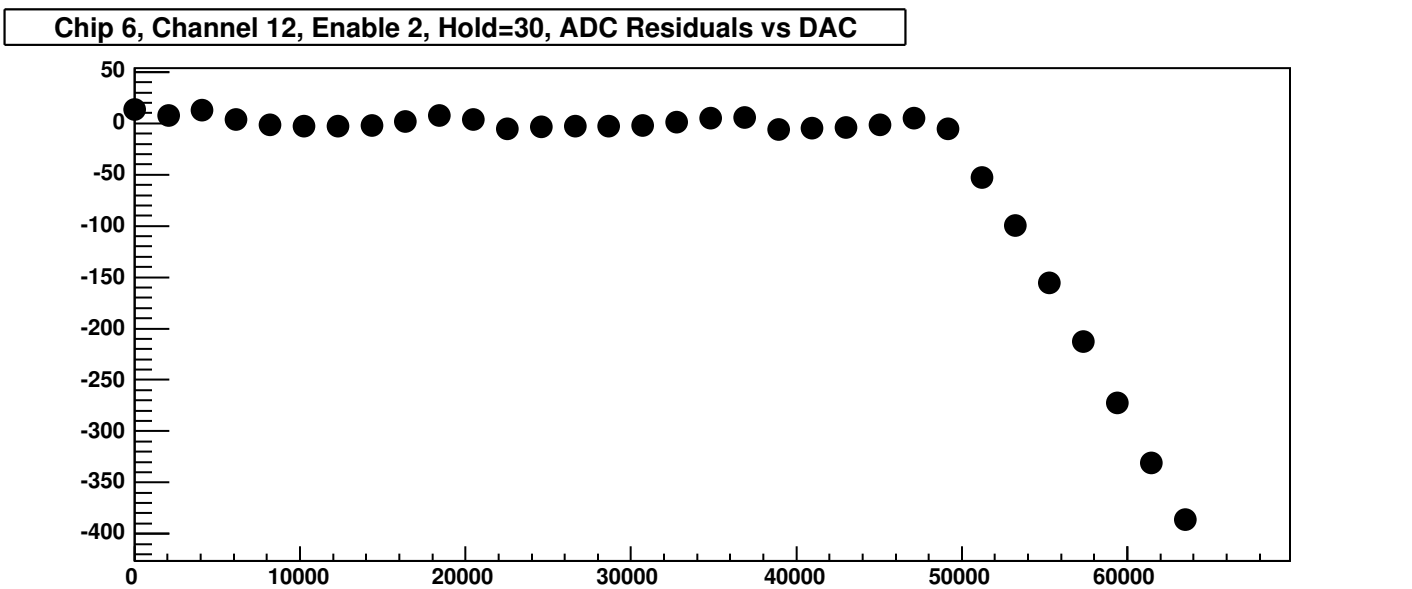

Chip 6, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.014e+04 / 15
p0 1446 ± 0.2878
p1 0.6324 ± 2.963e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

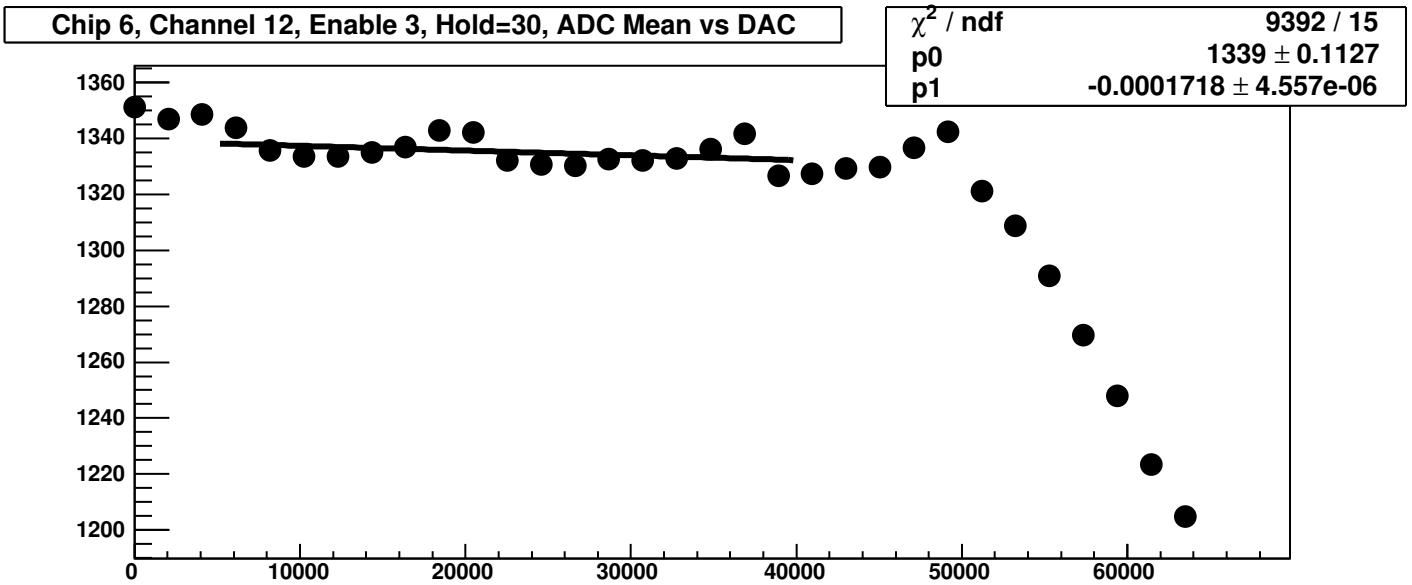

Chip 6, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

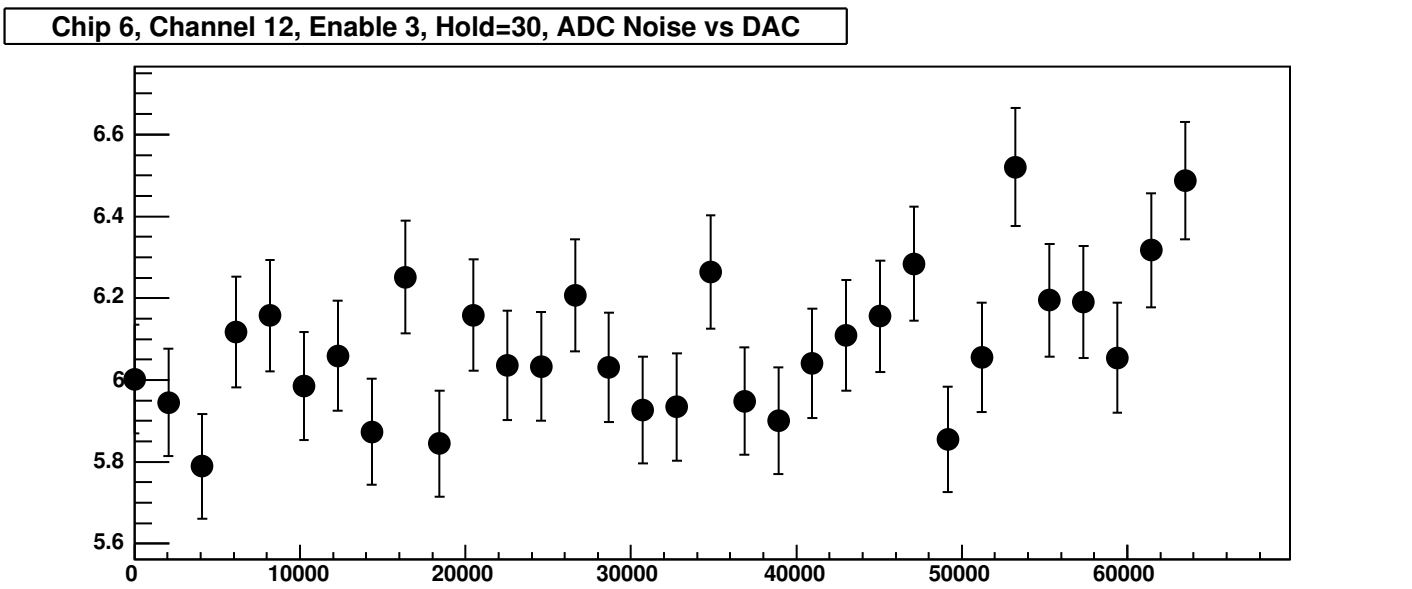

Chip 6, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

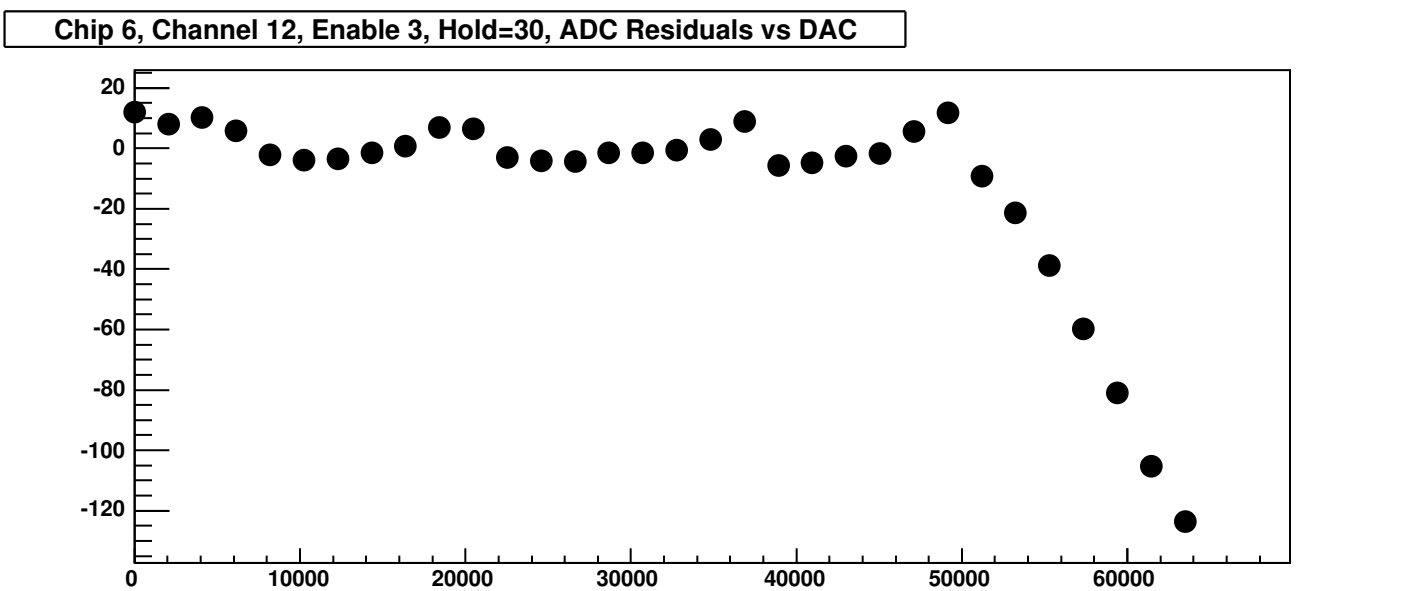

Chip 6, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7823 / 15
p0 1339 ± 0.1115
p1 -0.0007356 ± 4.537e-06
1300
1200
1100
1000
900
0
10000
20000
30000
40000
50000
60000

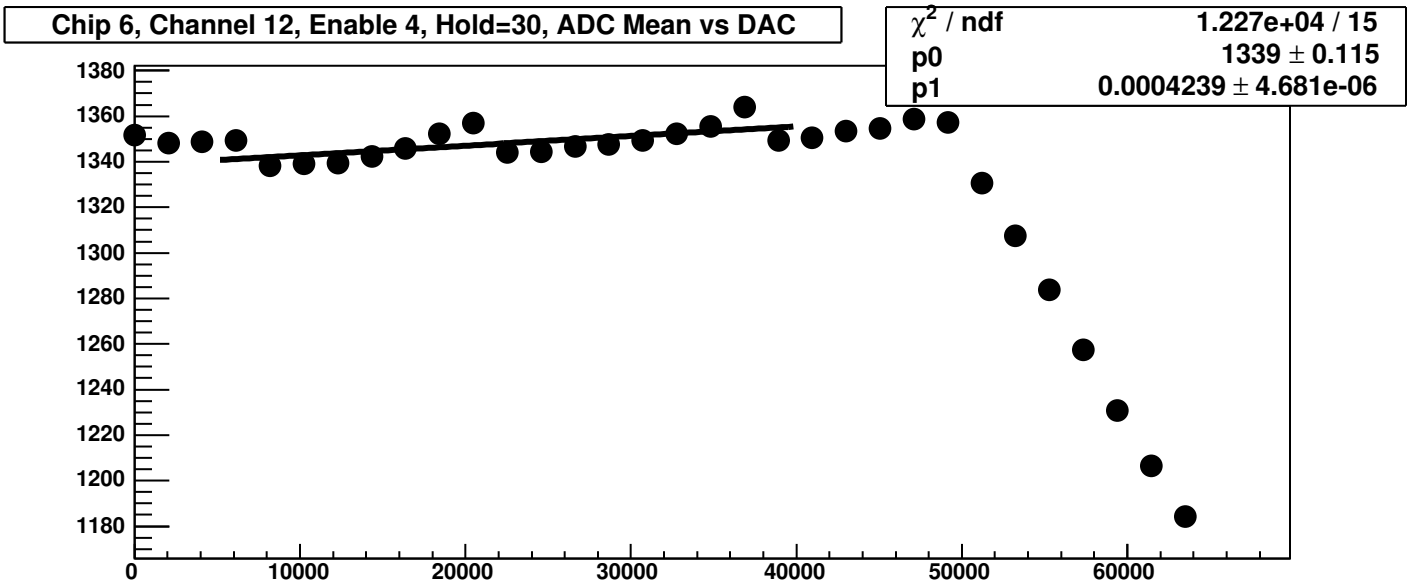

Chip 6, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

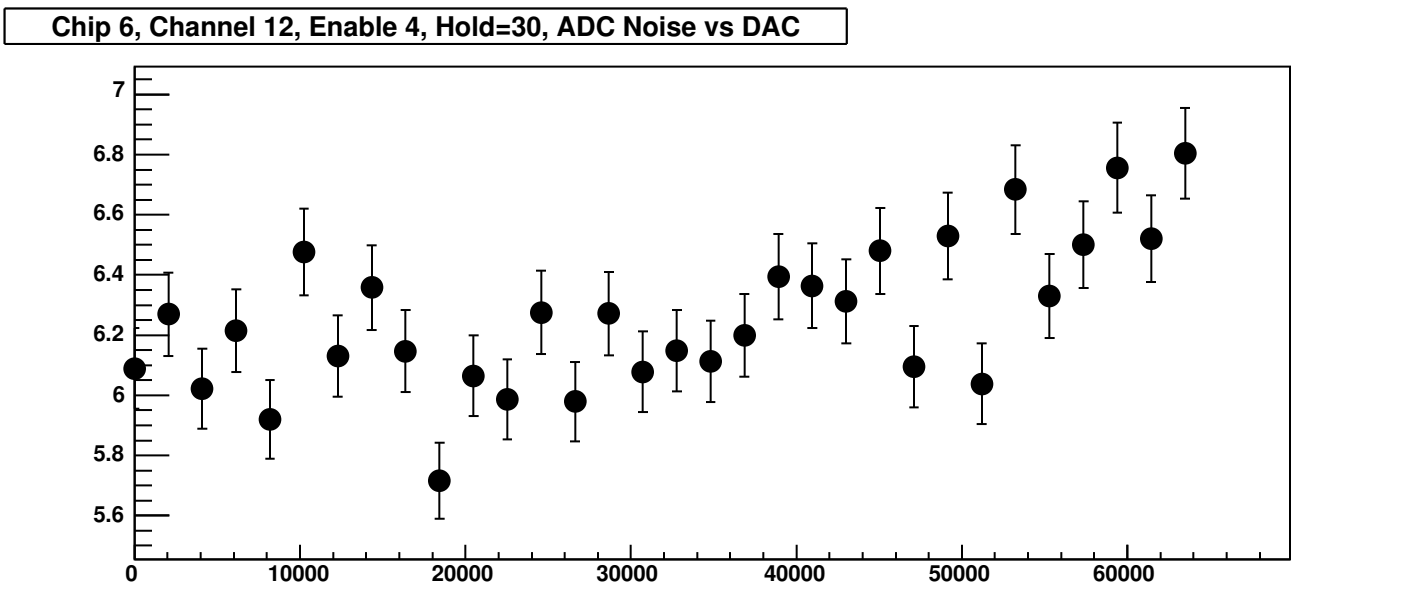

Chip 6, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

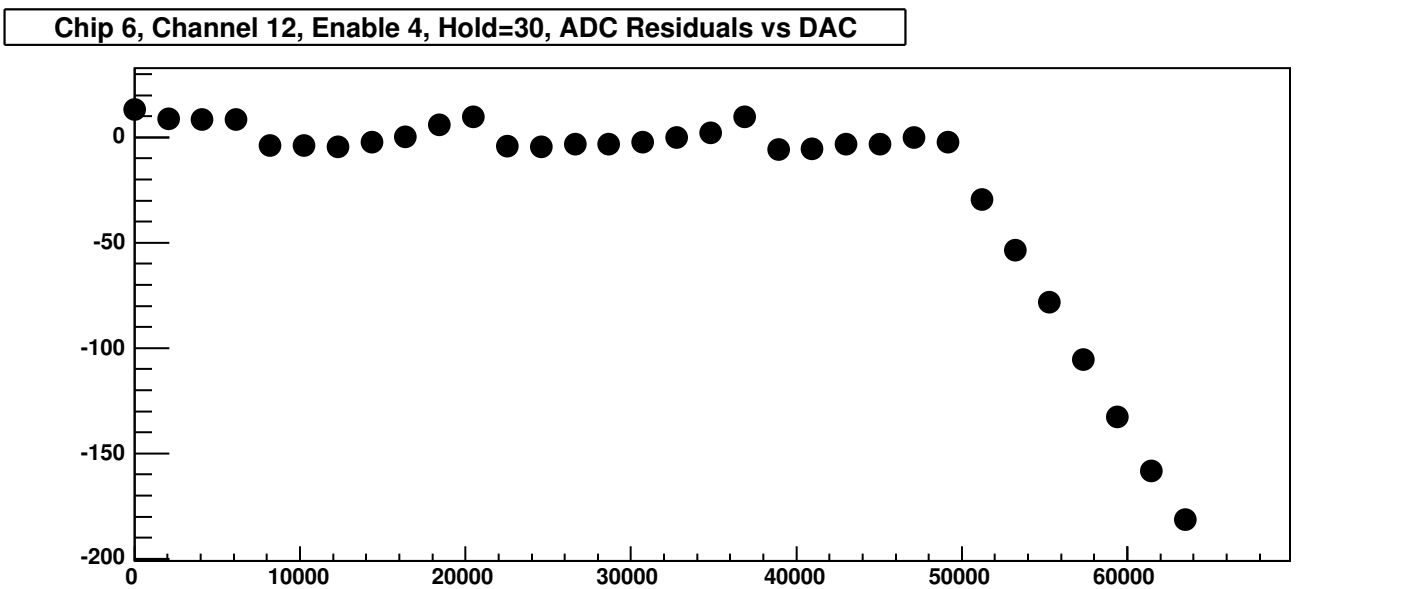

Chip 6, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9392 / 15
p0 1339 ± 0.1127
p1 -0.0001718 ± 4.557e-06
1360
1340
1320
1300
1280
1260
1240
1220
1200
0
10000
20000
30000
40000
50000
60000

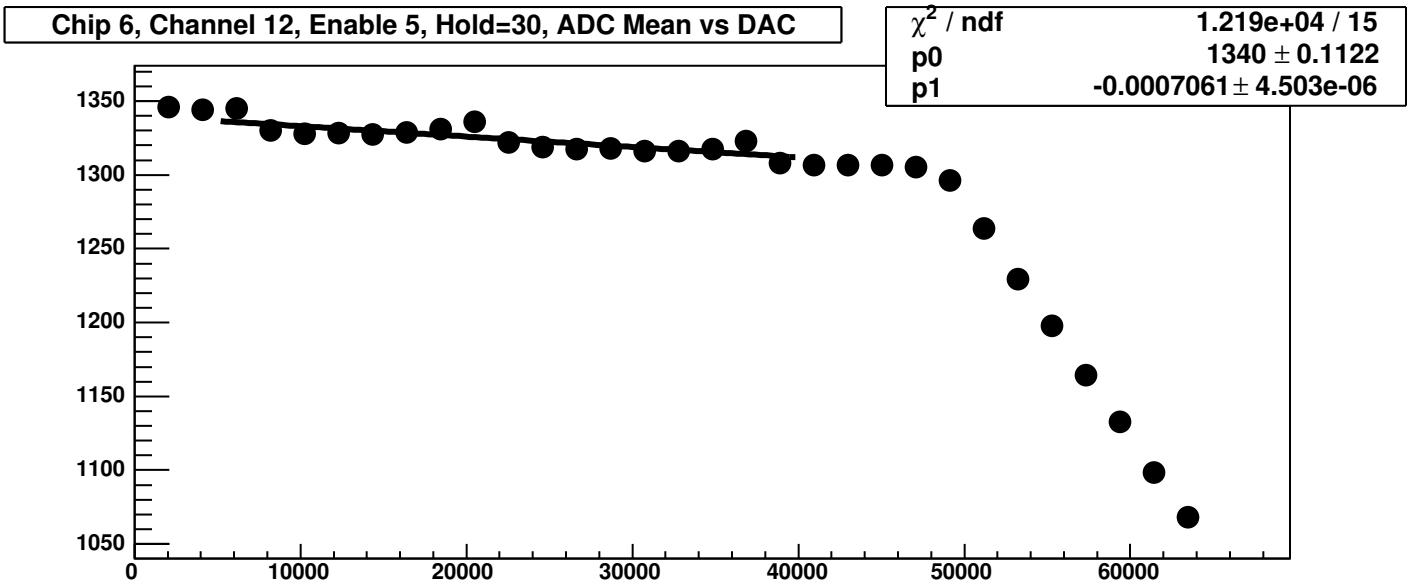

Chip 6, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

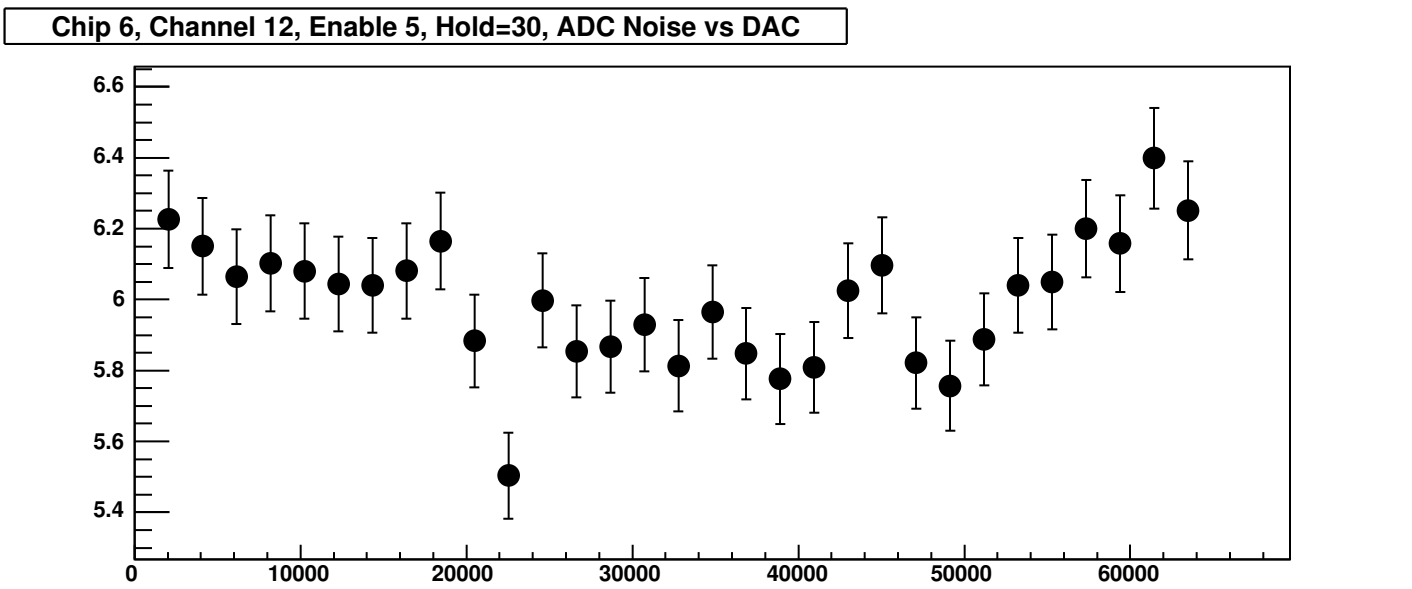

Chip 6, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

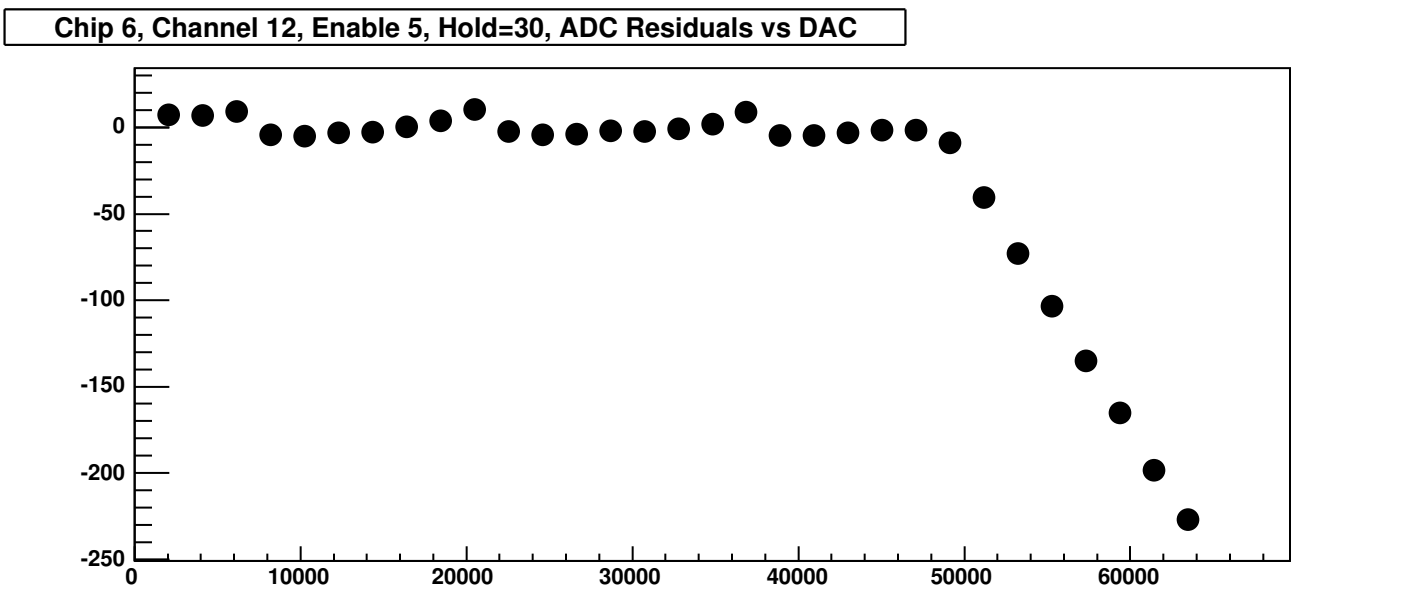

Chip 6, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.227e+04 / 15
p0 1339 ± 0.115
p1 0.0004239 ± 4.681e-06
1380
1360
1340
1320
1300
1280
1260
1240
1220
1200
1180
0
10000
20000
30000
40000
50000
60000

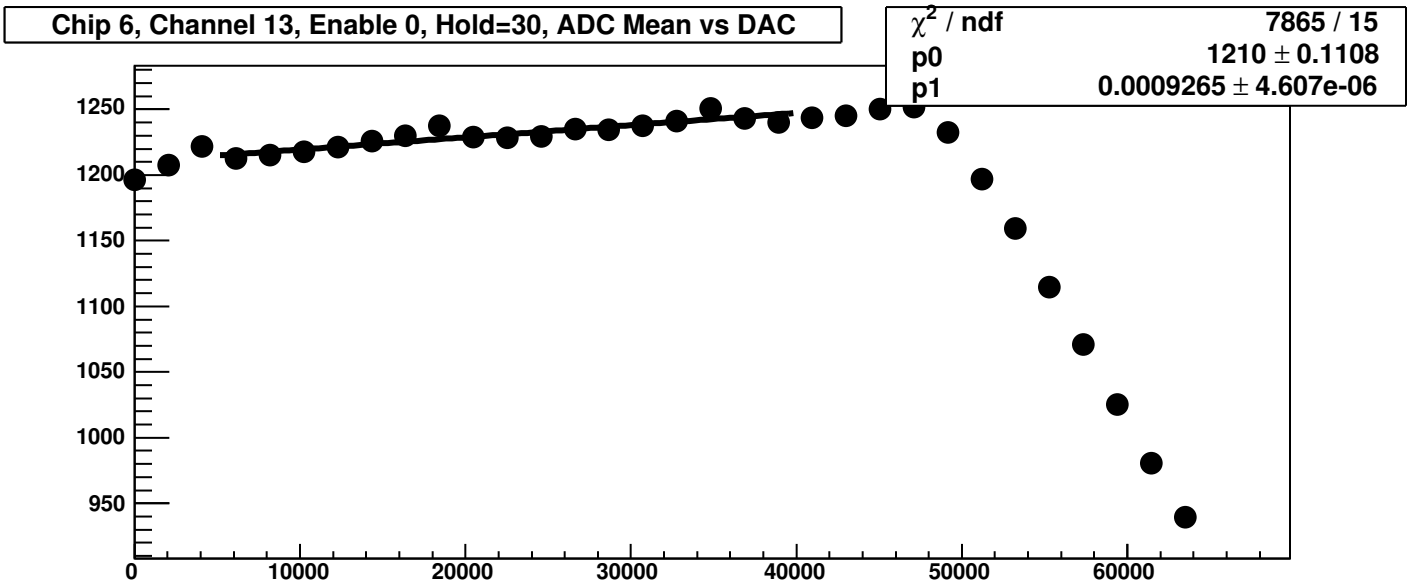

Chip 6, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

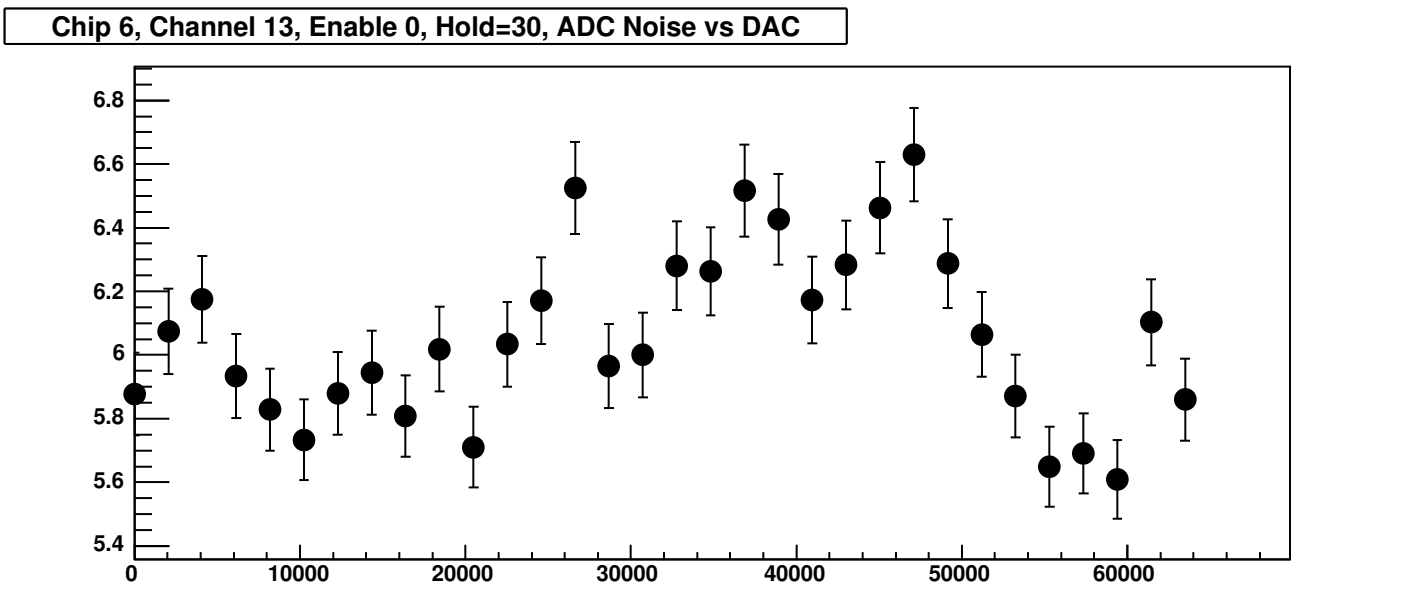

Chip 6, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

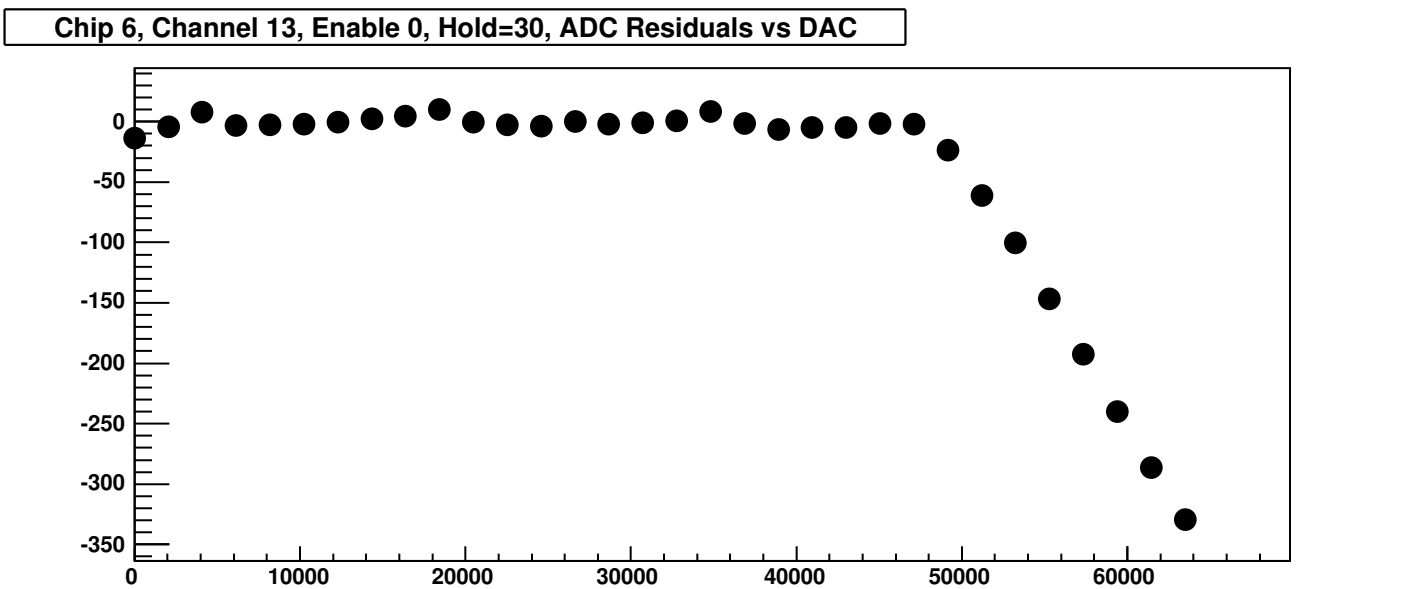

Chip 6, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.219e+04 / 15
p0 1340 ± 0.1122
p1 -0.0007061 ± 4.503e-06
1350
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

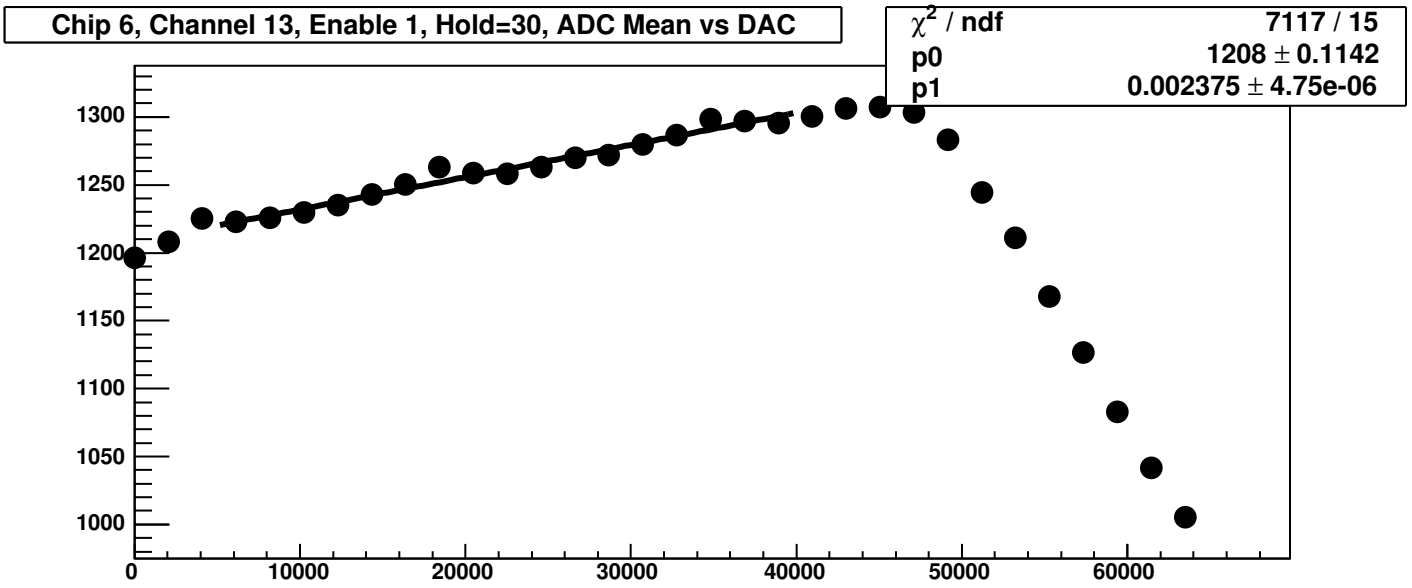

Chip 6, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

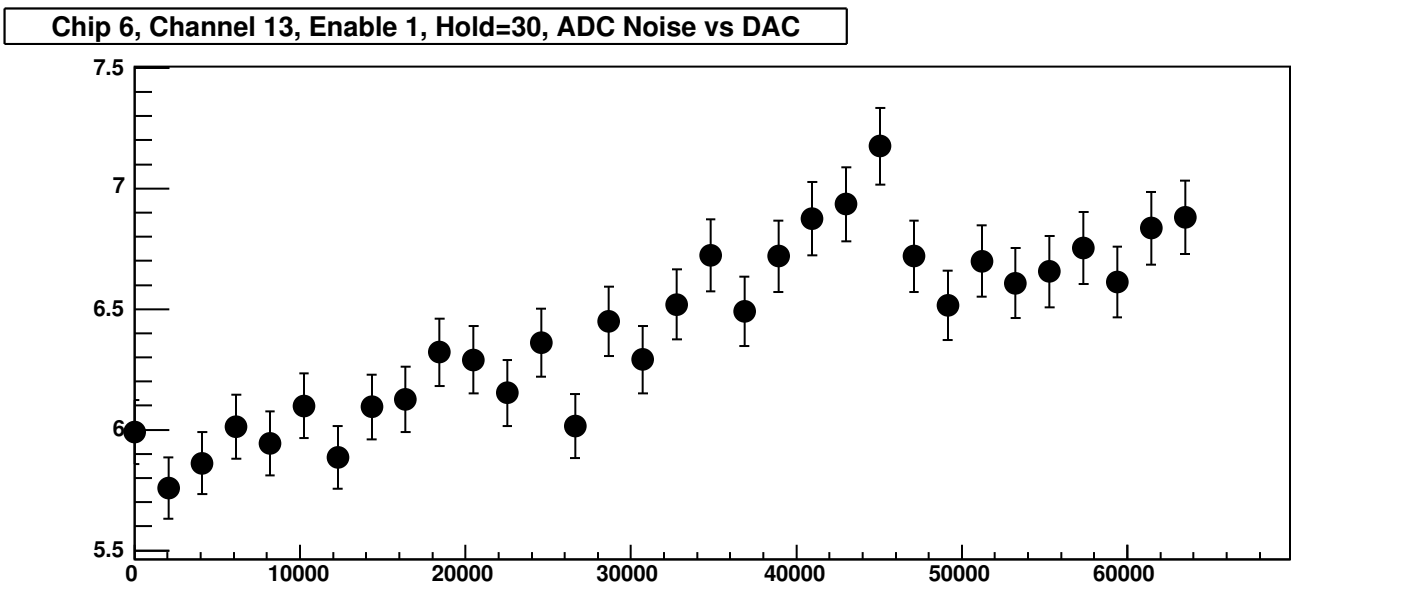

Chip 6, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

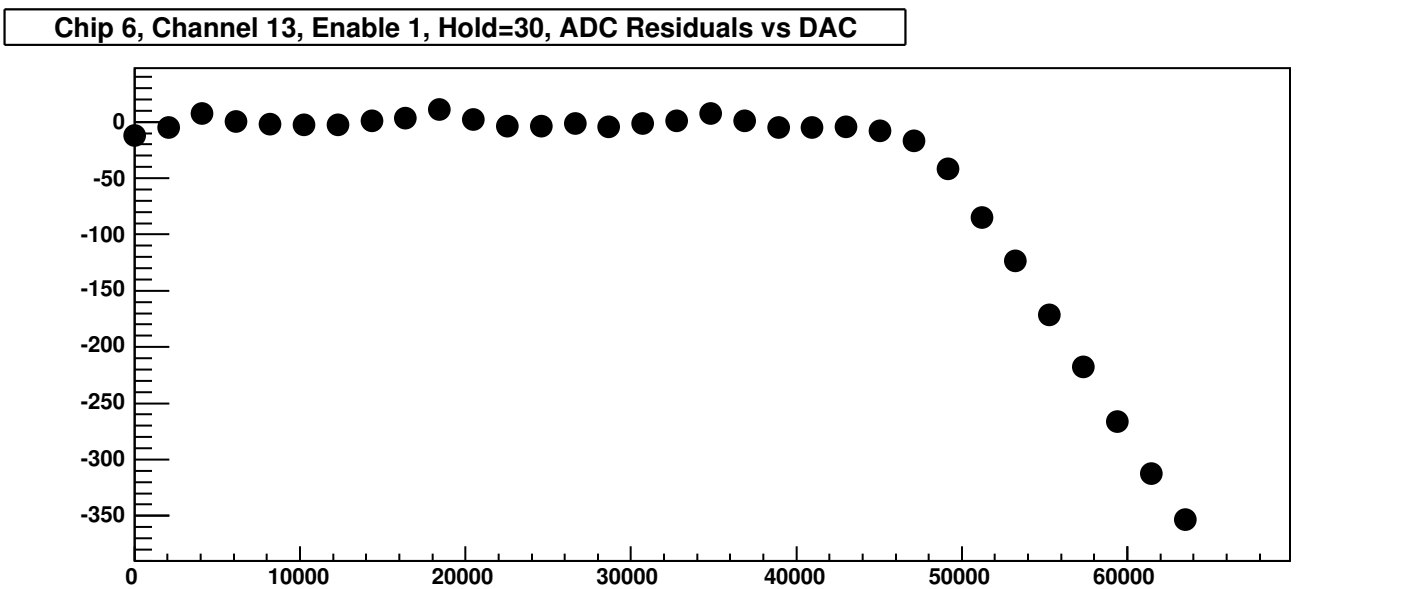

Chip 6, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7865 / 15
p0 1210 ± 0.1108
p1 0.0009265 ± 4.607e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

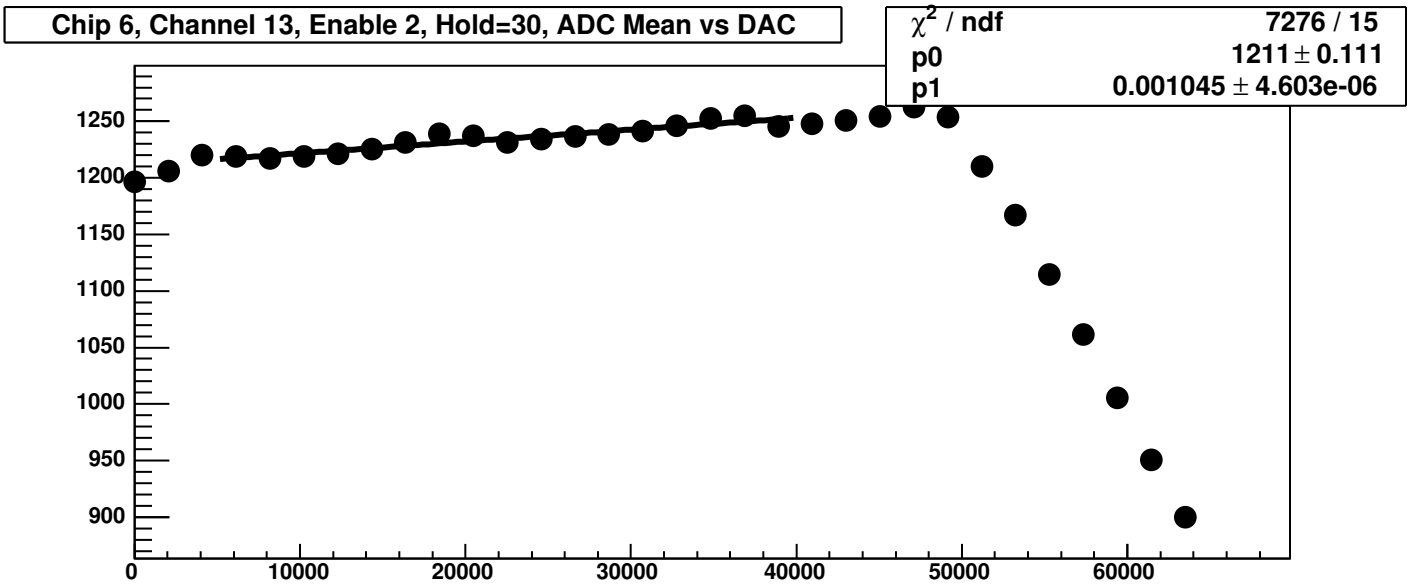

Chip 6, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

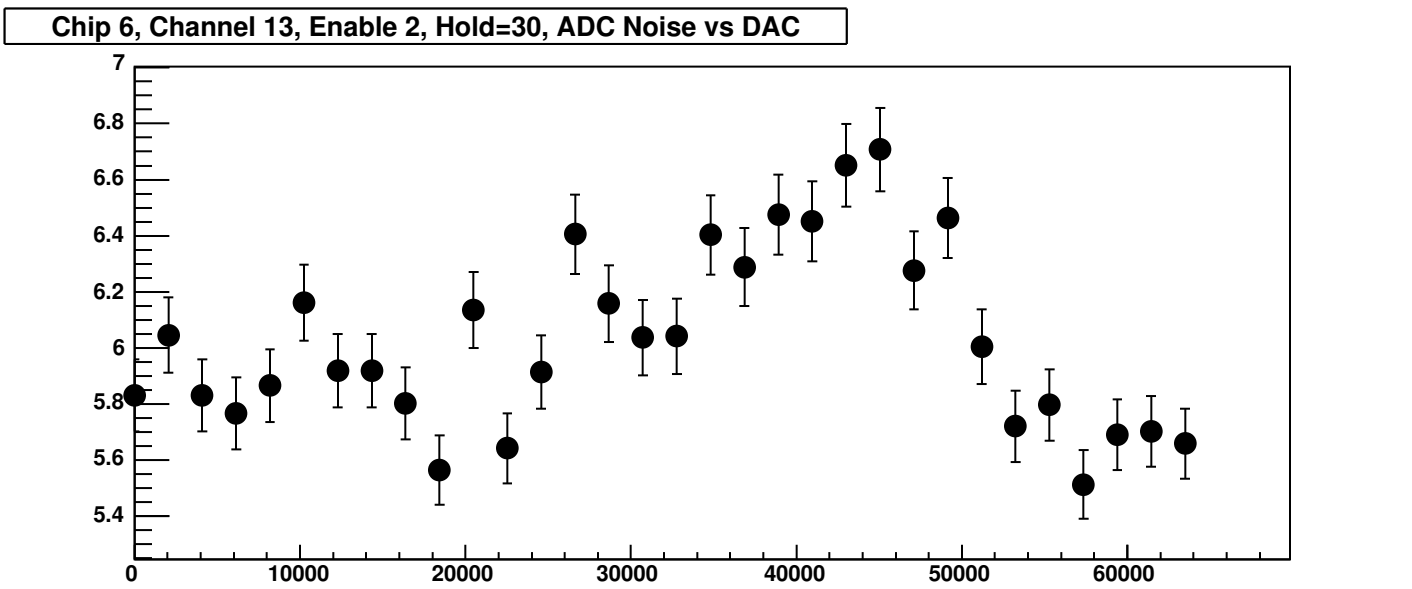

Chip 6, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

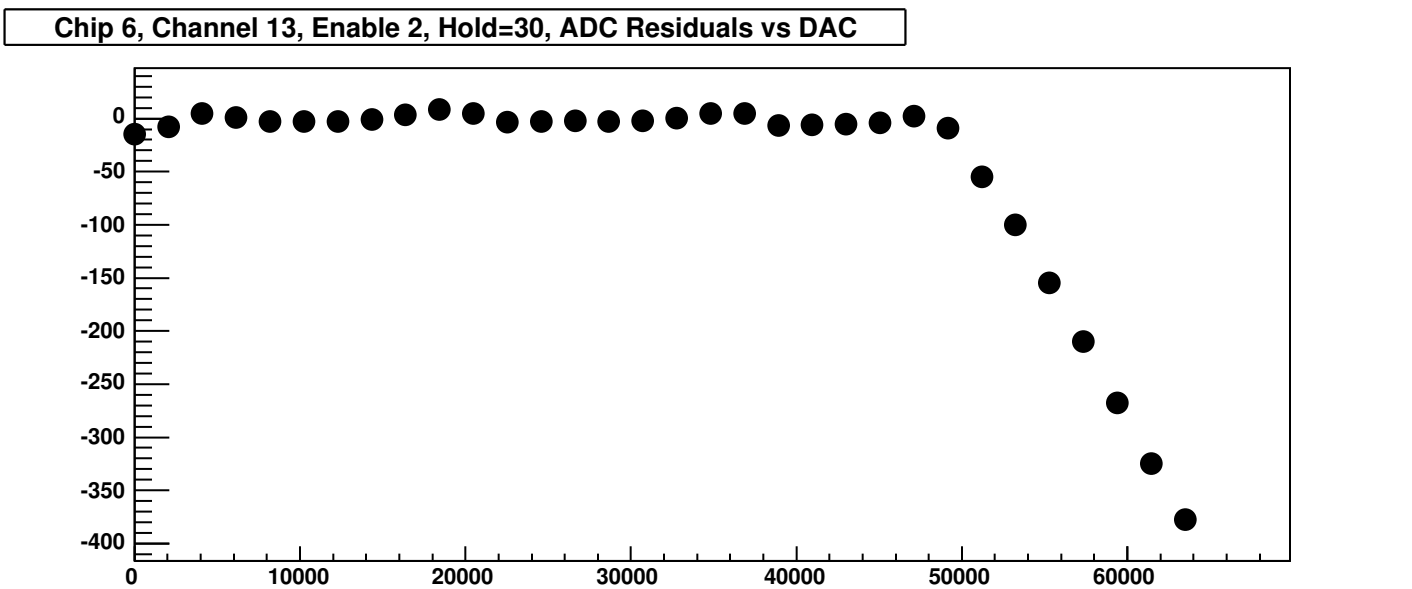

Chip 6, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7117 / 15
p0 1208 ± 0.1142
p1 0.002375 ± 4.75e-06
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

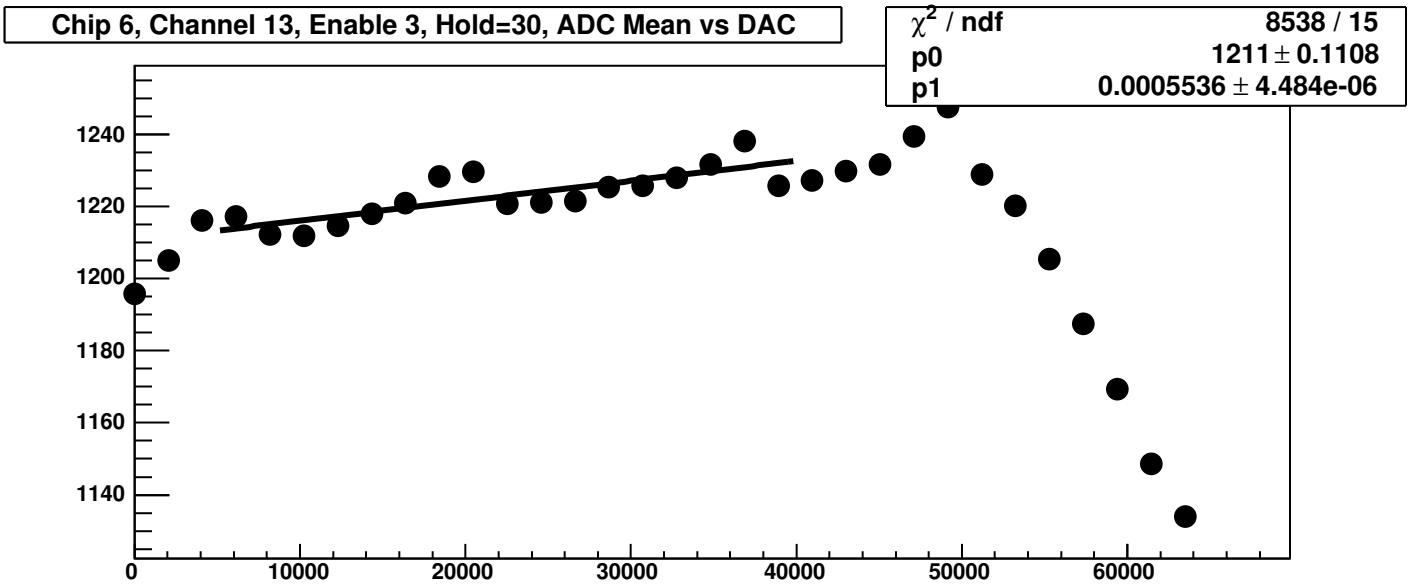

Chip 6, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000

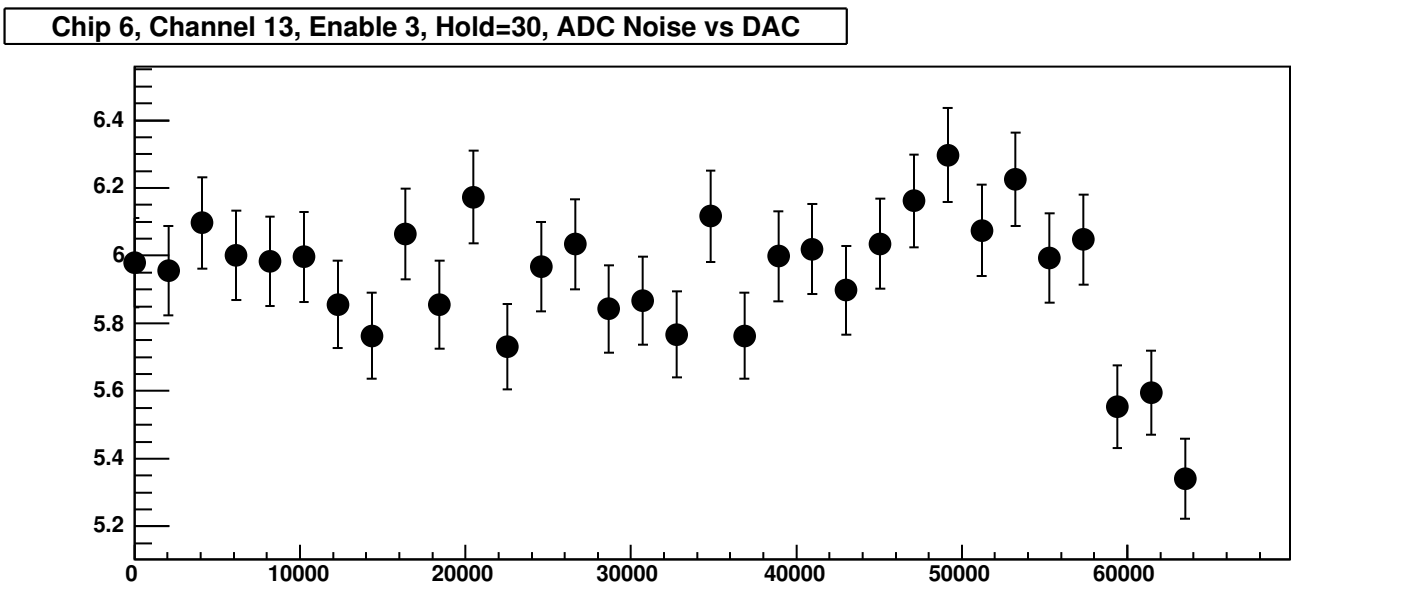

Chip 6, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

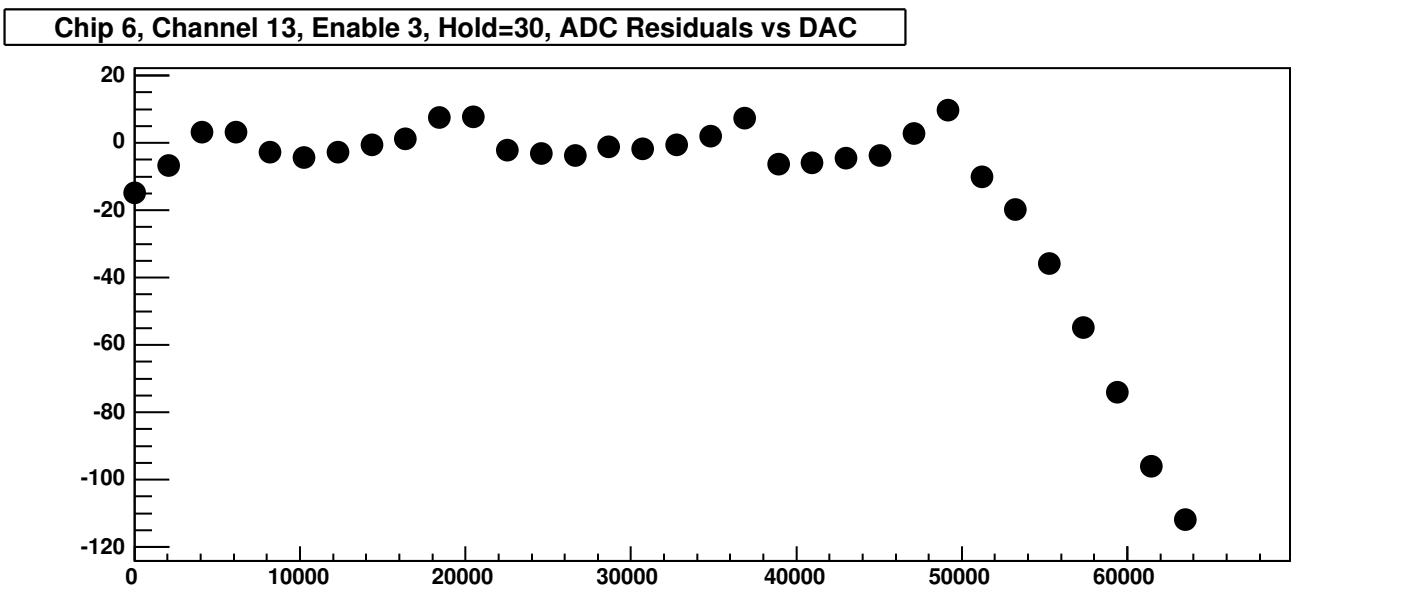

Chip 6, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7276 / 15
p0 1211 ± 0.111
p1 0.001045 ± 4.603e-06
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

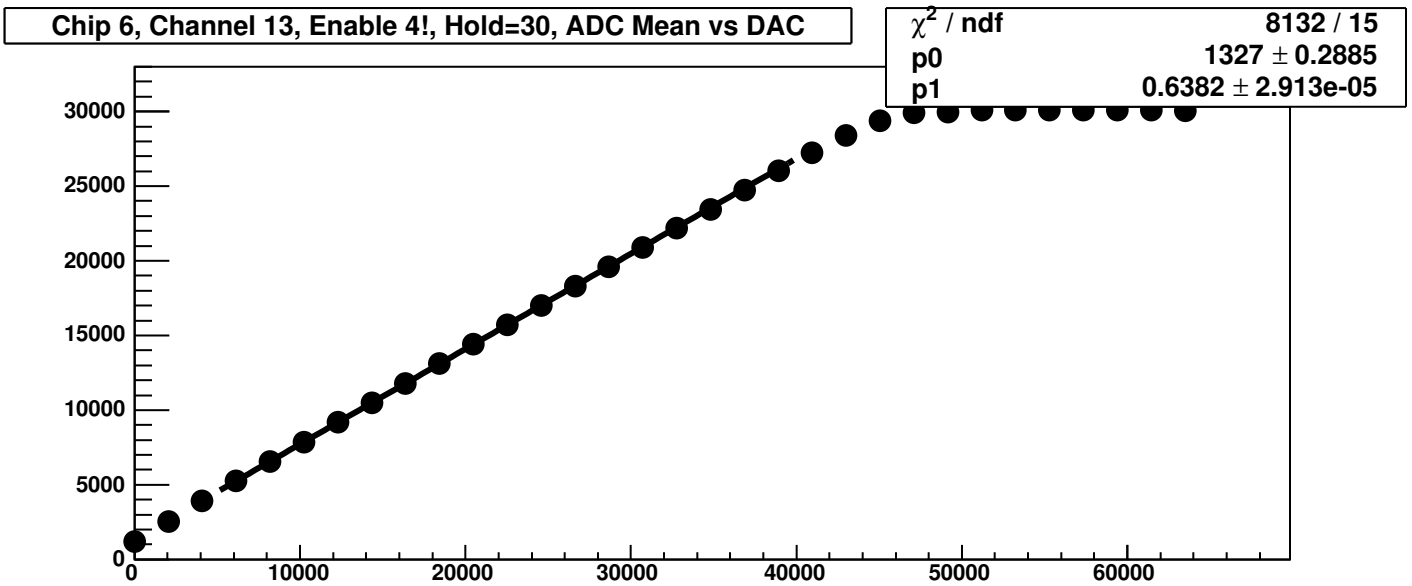

Chip 6, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

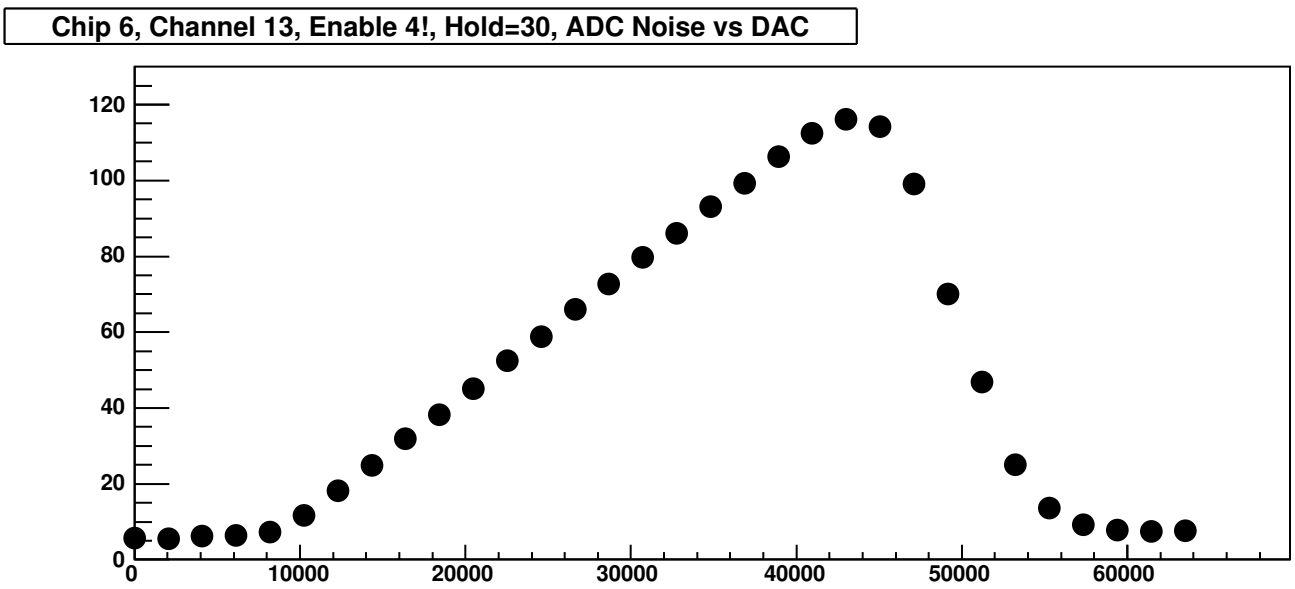

Chip 6, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

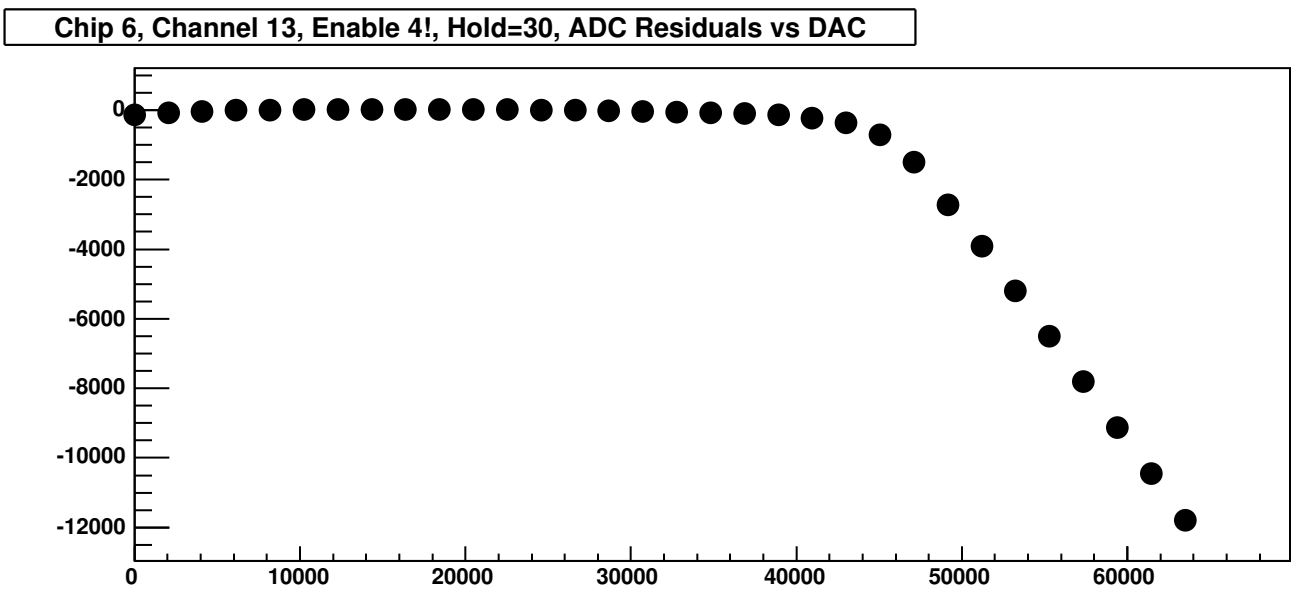

Chip 6, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8538 / 15
p0 1211 ± 0.1108
p1 0.0005536 ± 4.484e-06
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

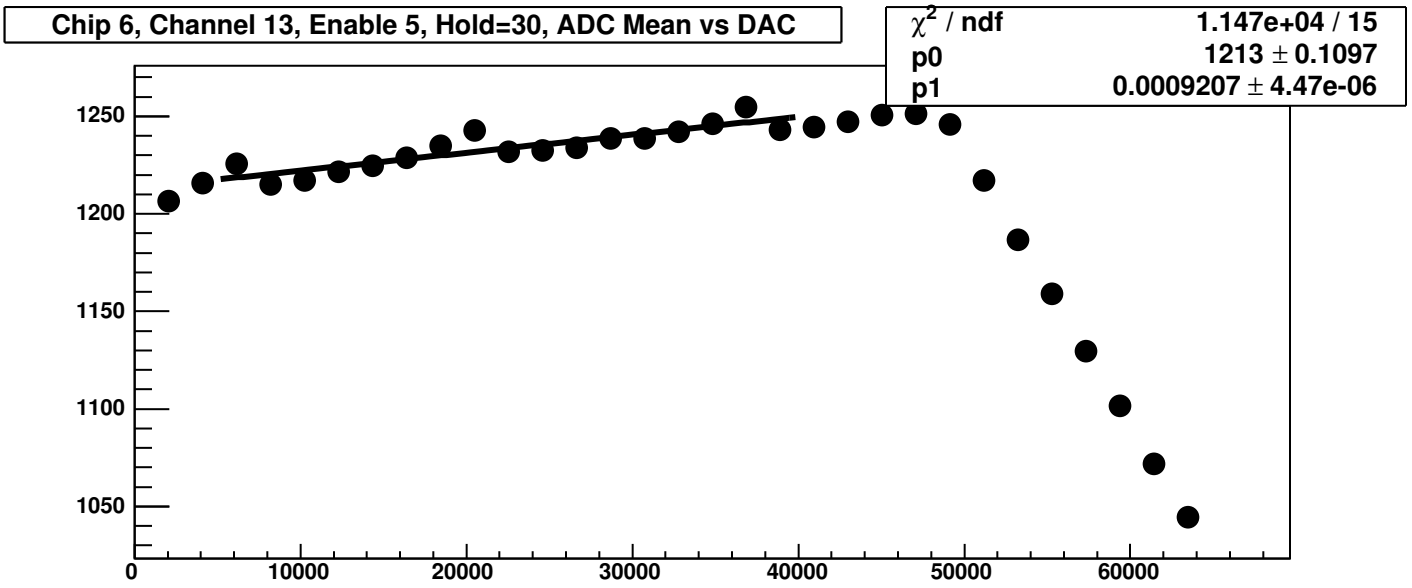

Chip 6, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

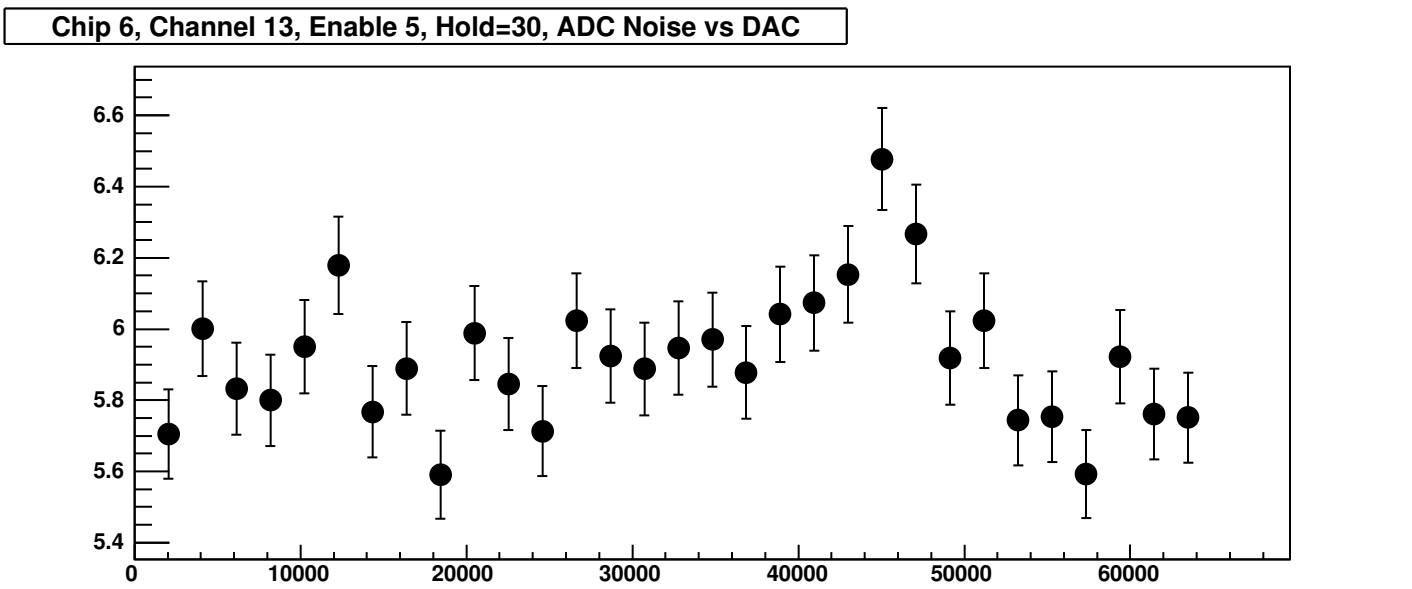

Chip 6, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

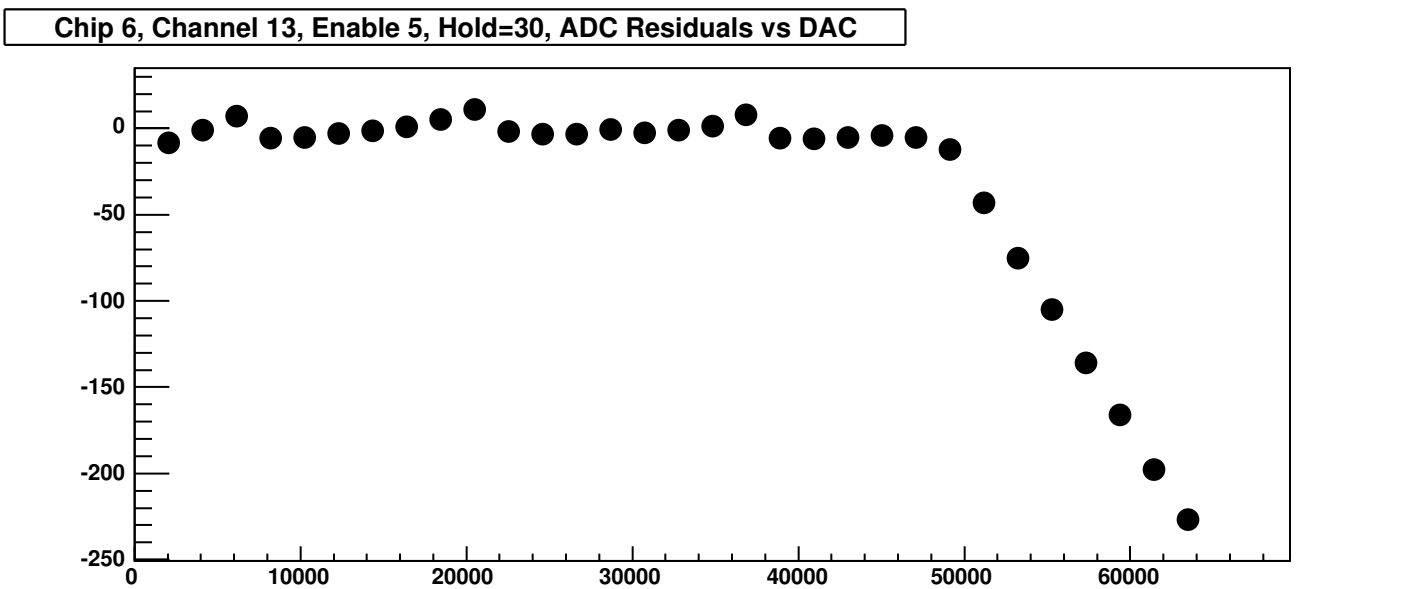

Chip 6, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 8132 / 15
p0 1327 ± 0.2885
p1 0.6382 ± 2.913e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

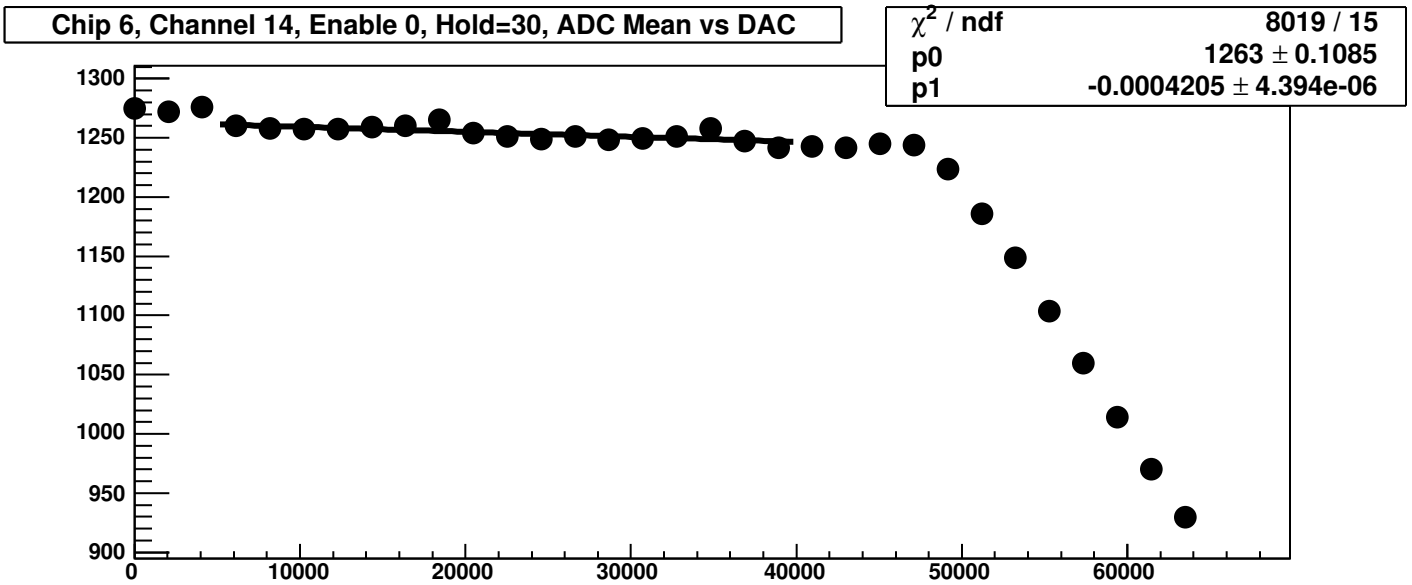

Chip 6, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

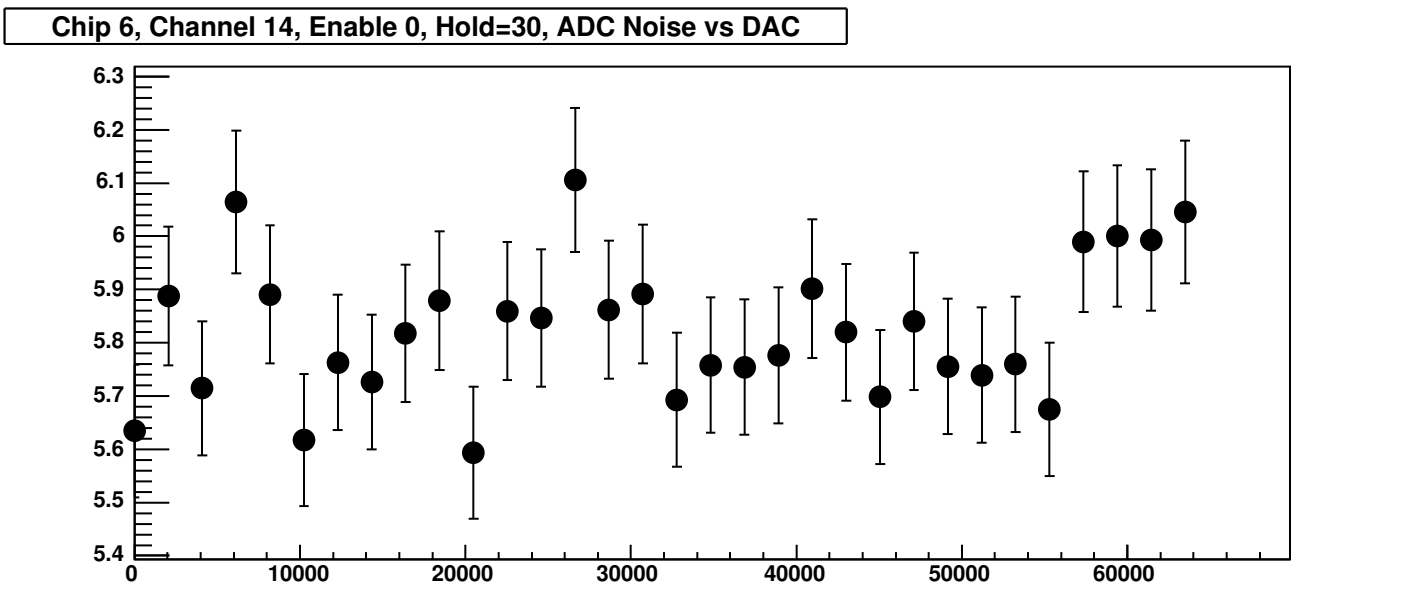

Chip 6, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

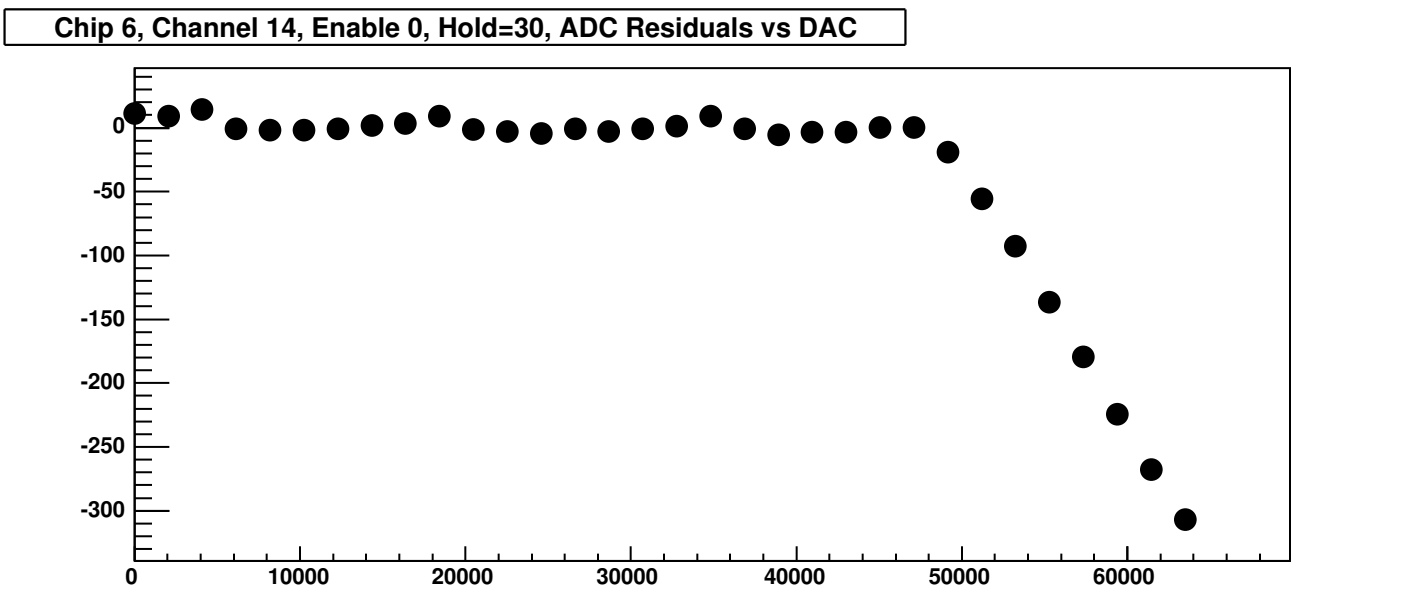

Chip 6, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.147e+04 / 15
p0 1213 ± 0.1097
p1 0.0009207 ± 4.47e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

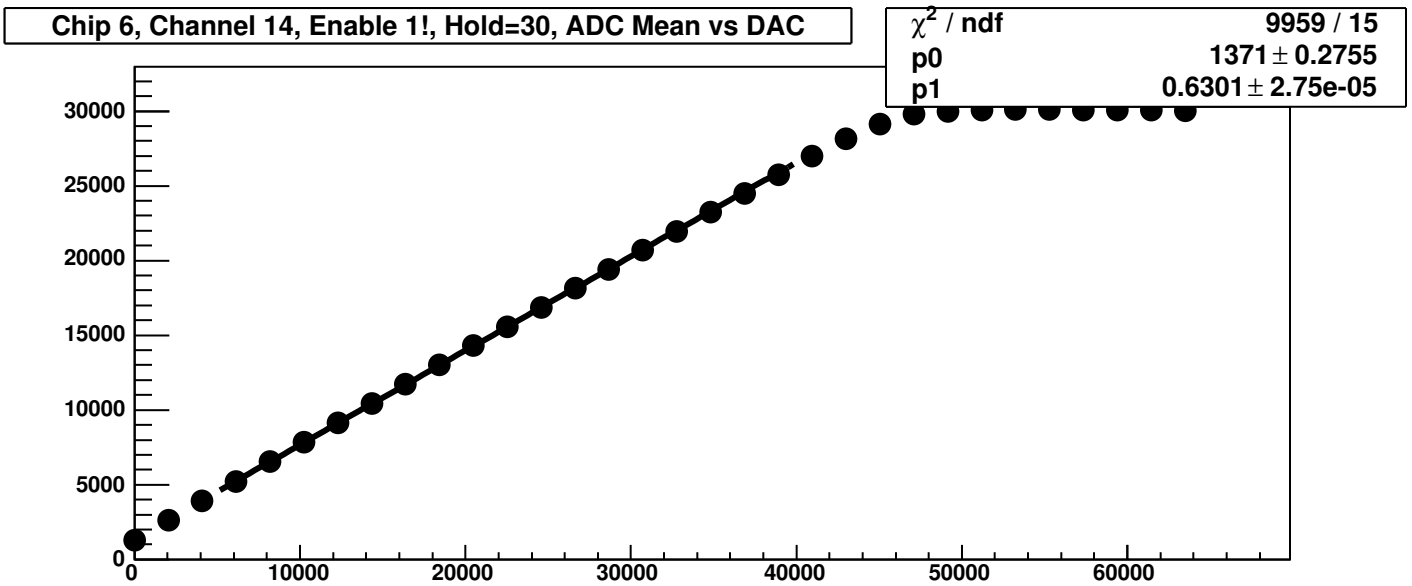

Chip 6, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

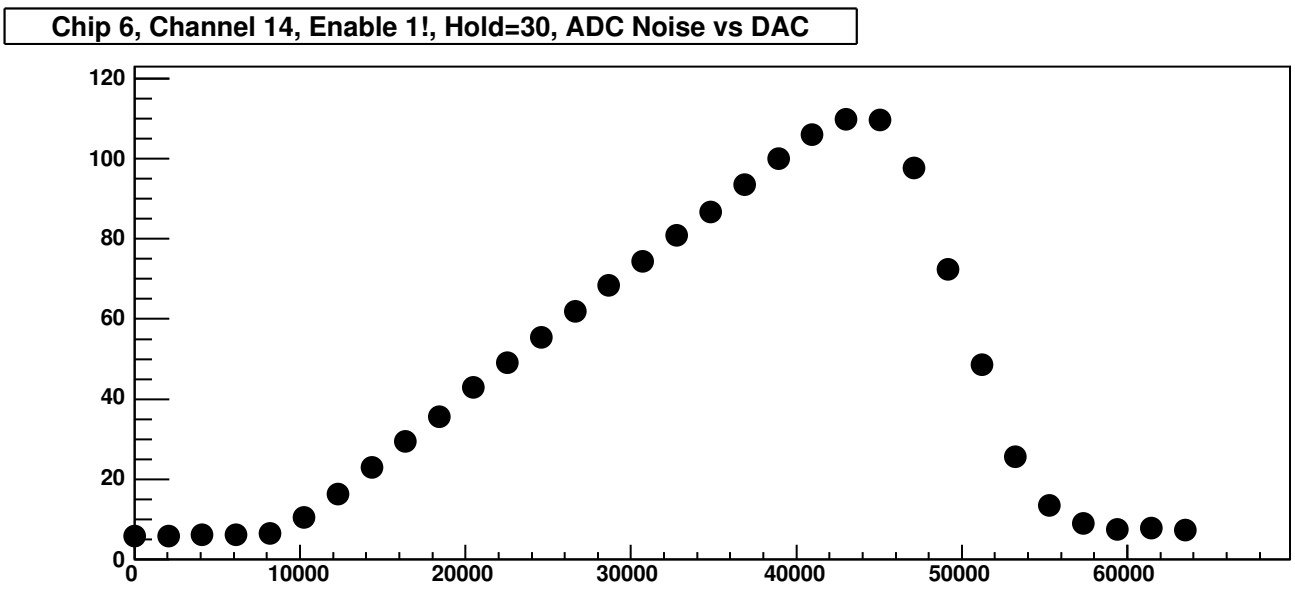

Chip 6, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

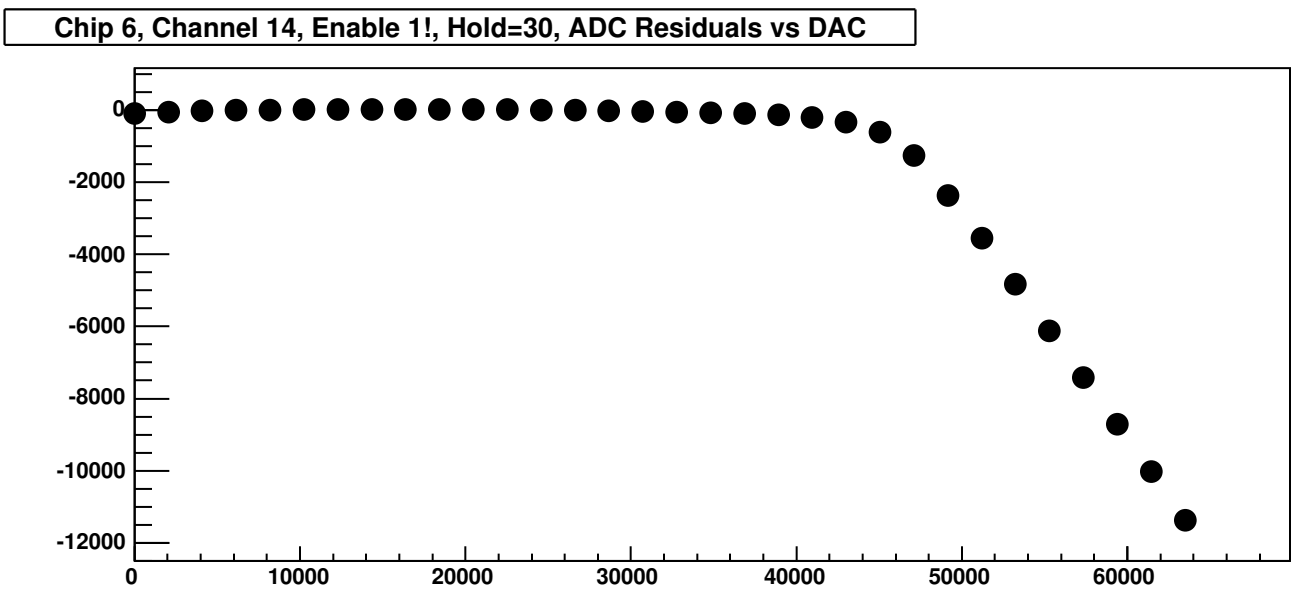

Chip 6, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8019 / 15
p0 1263 ± 0.1085
p1 -0.0004205 ± 4.394e-06
1300
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

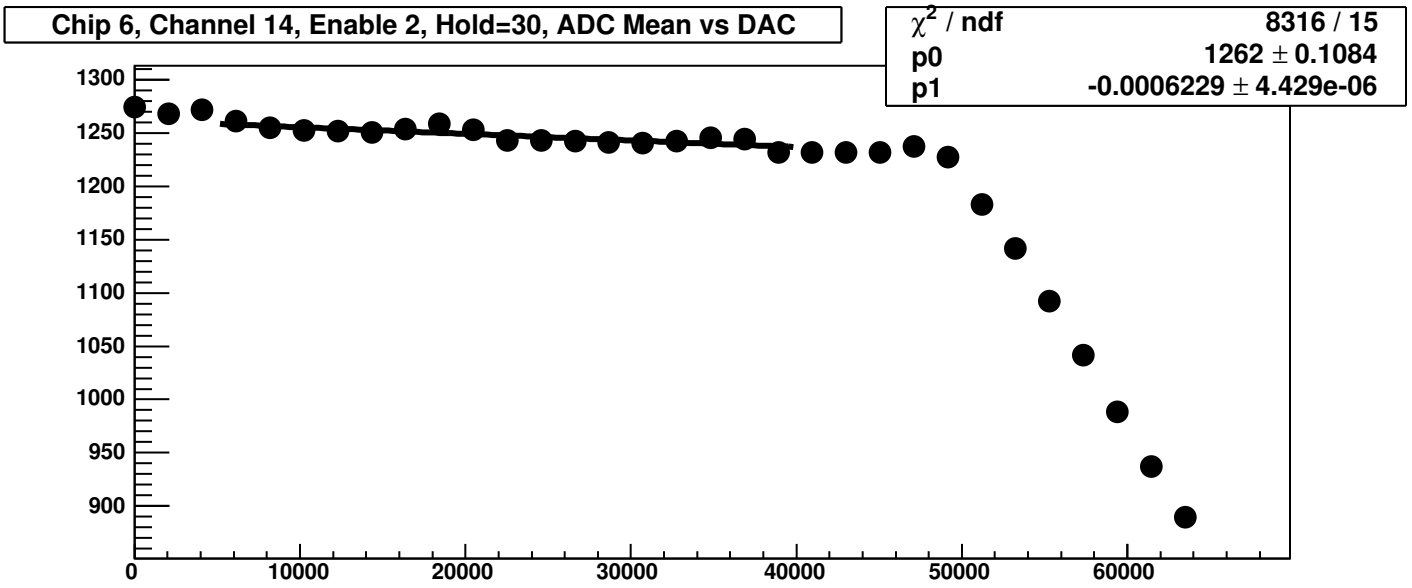

Chip 6, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

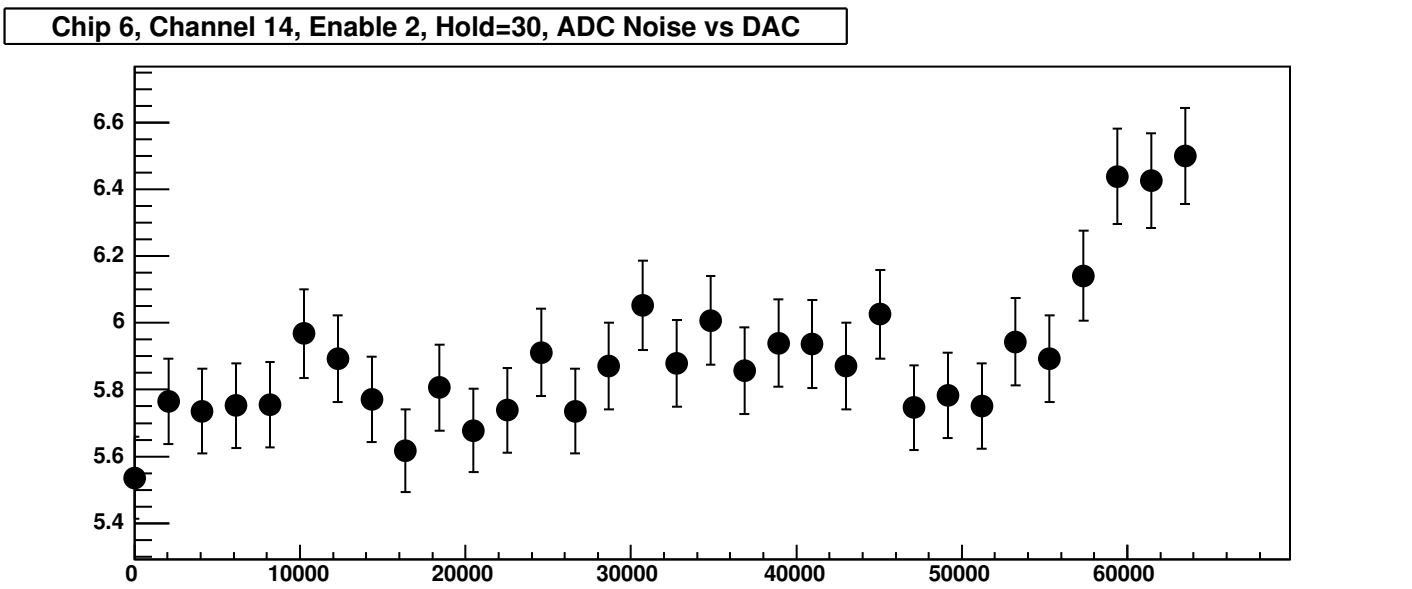

Chip 6, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

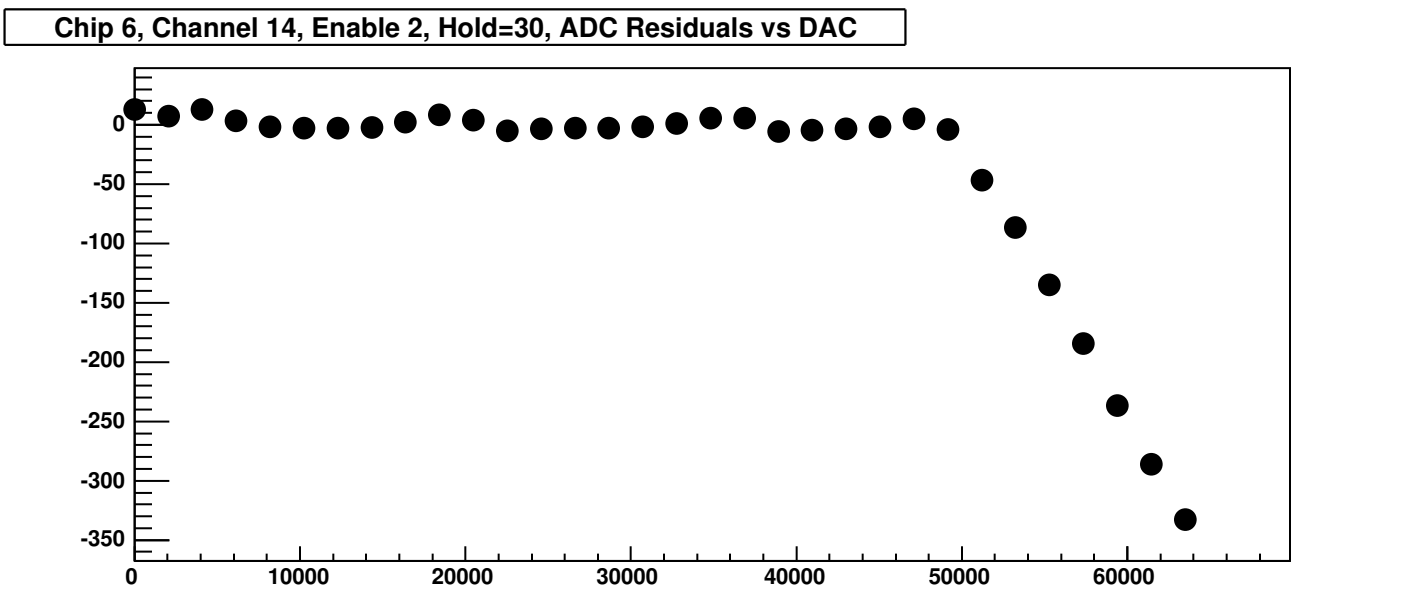

Chip 6, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 9959 / 15
p0 1371 ± 0.2755
p1 0.6301 ± 2.75e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

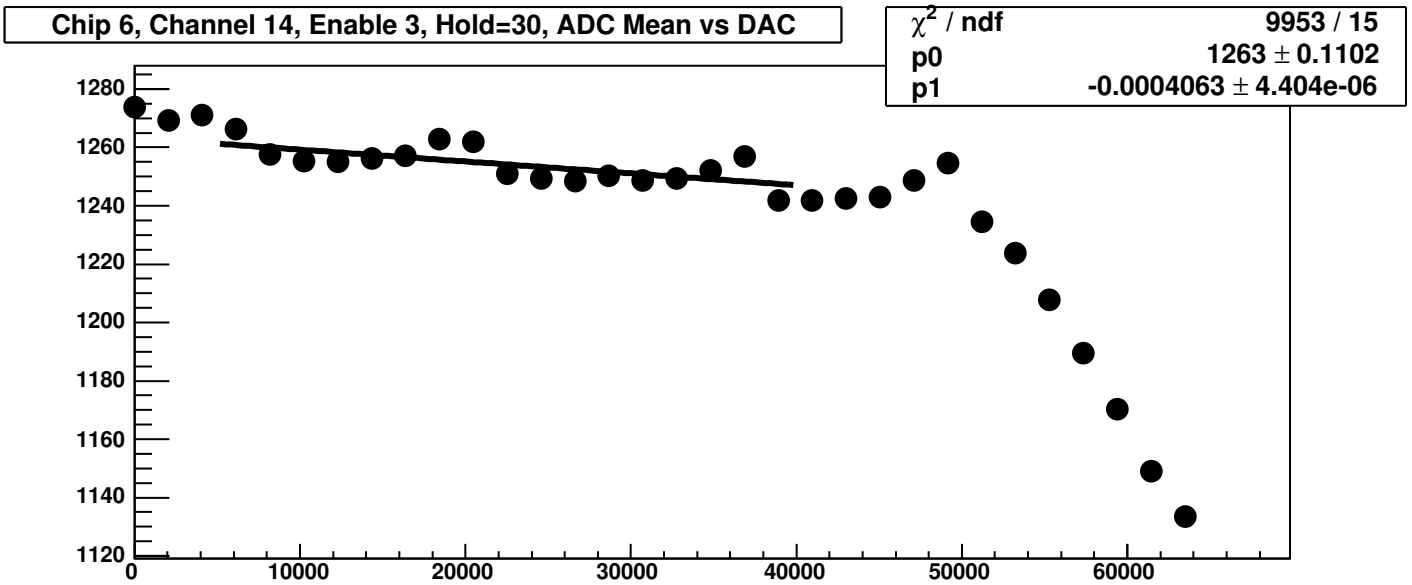

Chip 6, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

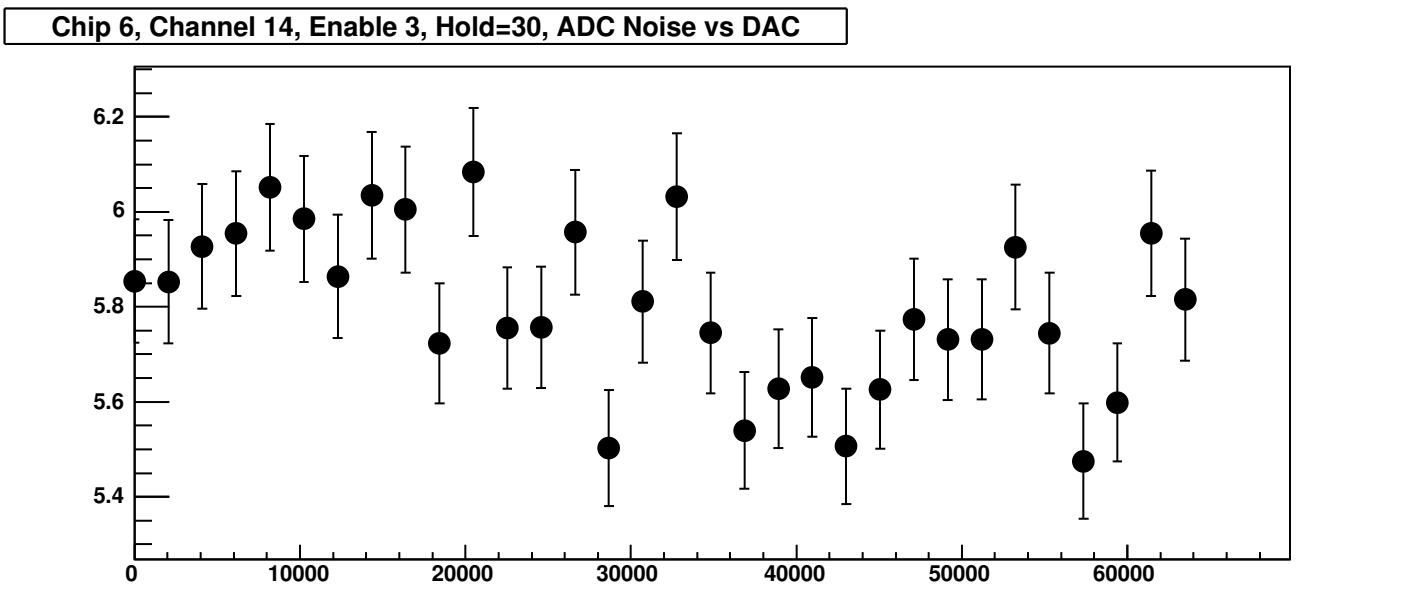

Chip 6, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

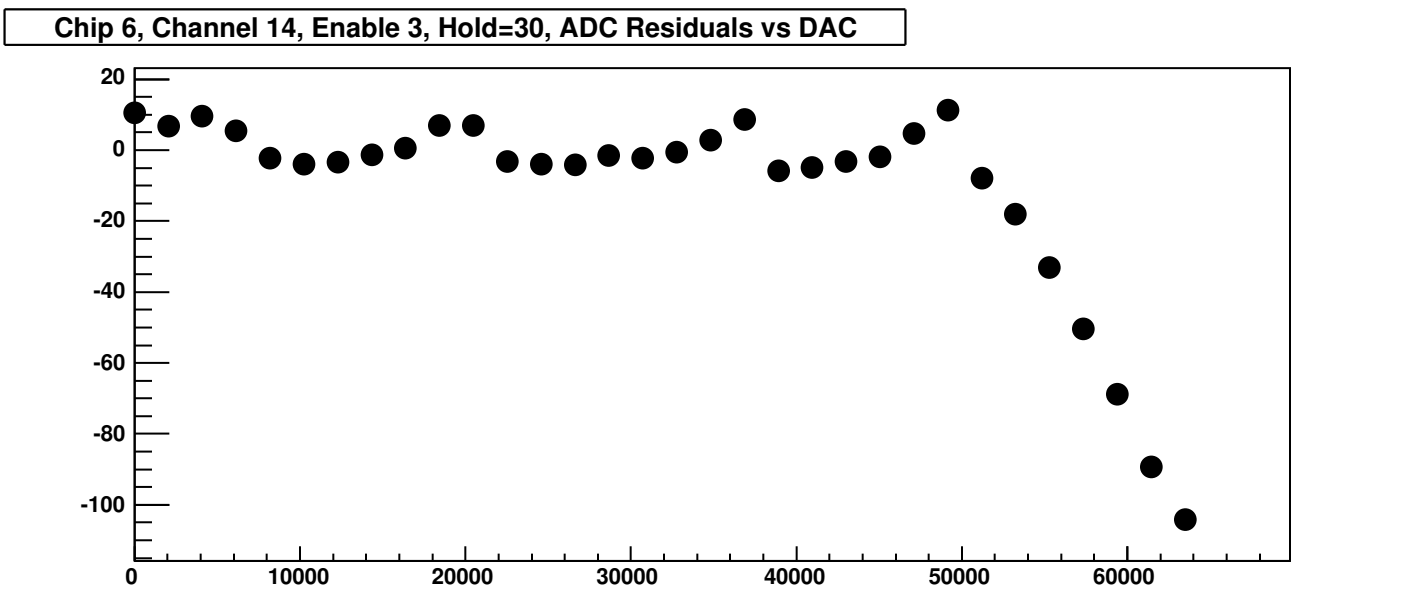

Chip 6, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8316 / 15
p0 1262 ± 0.1084
p1 -0.0006229 ± 4.429e-06
1300
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

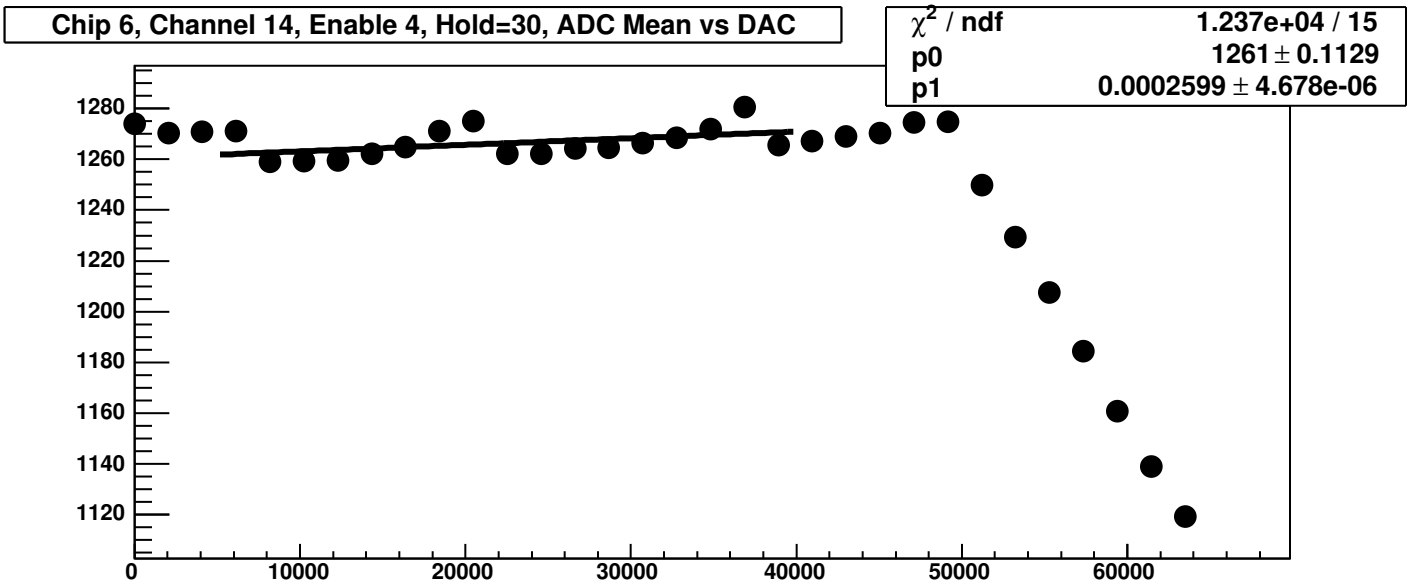

Chip 6, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

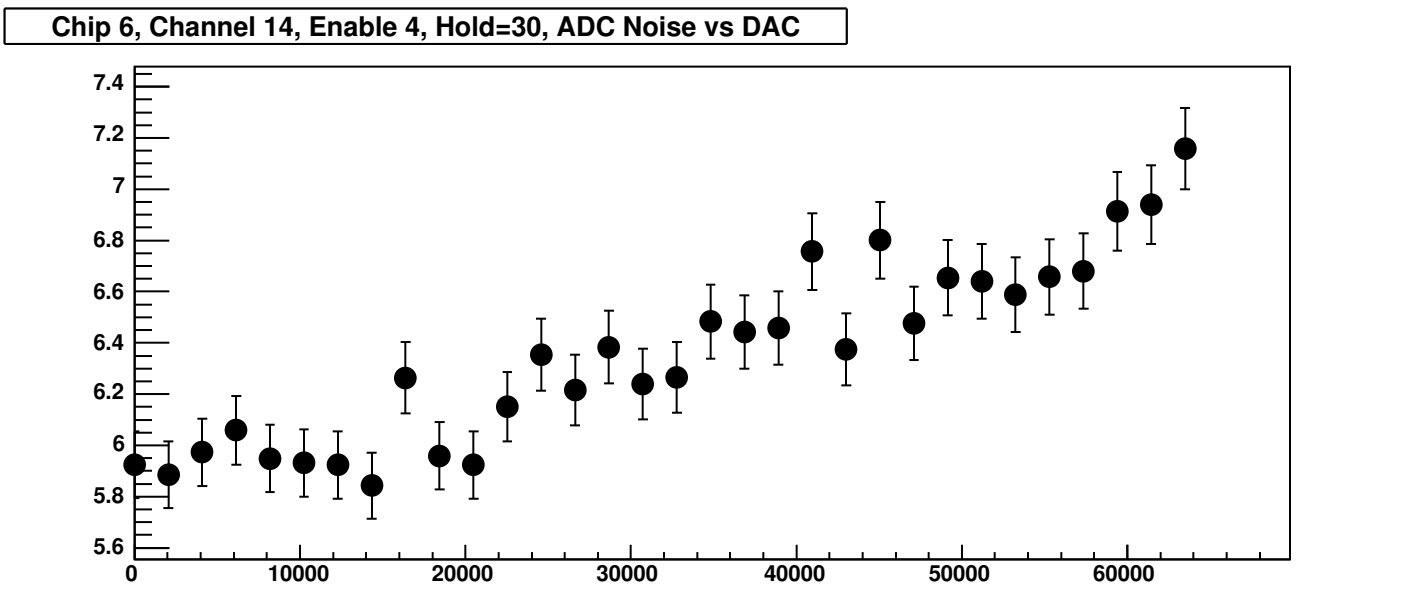

Chip 6, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

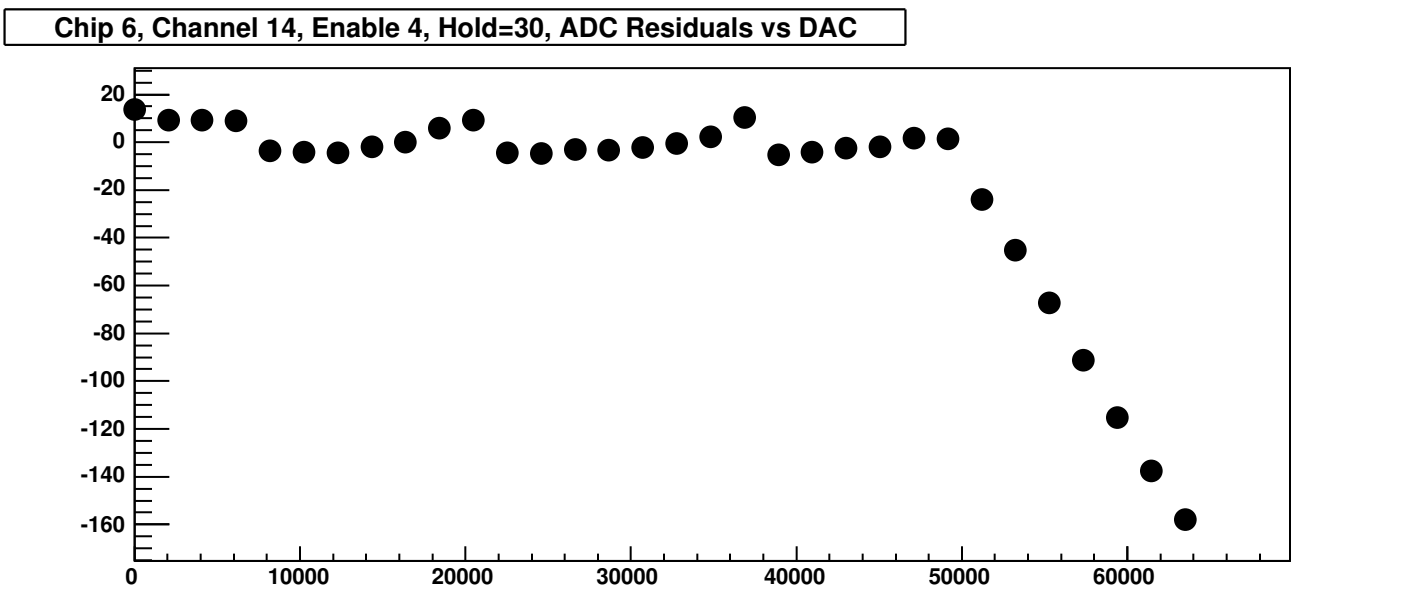

Chip 6, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9953 / 15
p0 1263 ± 0.1102
p1 -0.0004063 ± 4.404e-06
1280
1260
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

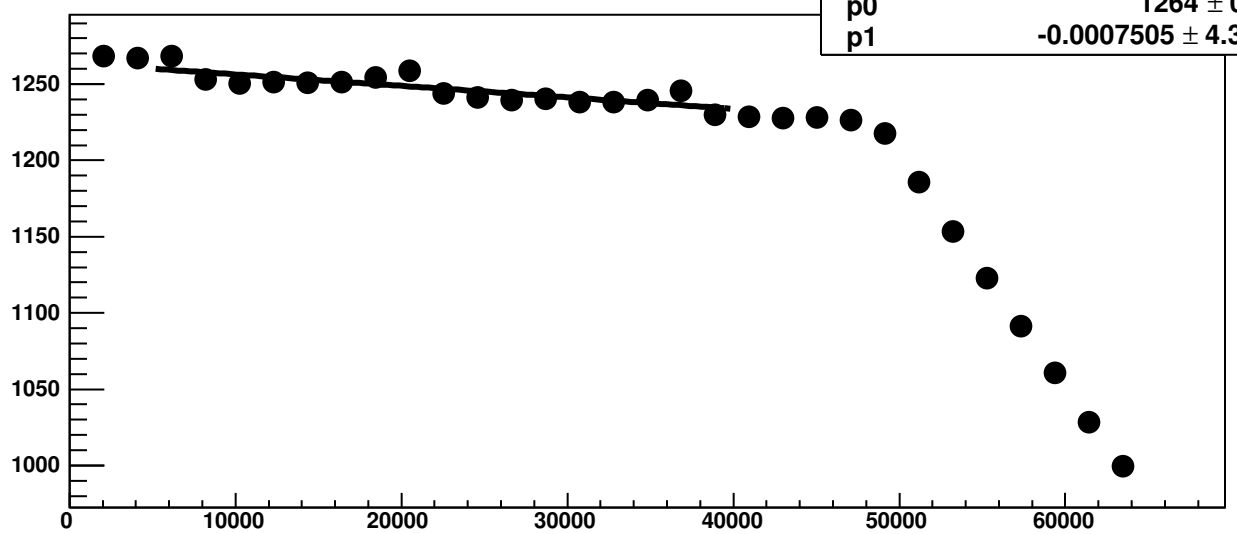

Chip 6, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.237e+04 / 15
p0 1261 ± 0.1129
p1 0.0002599 ± 4.678e-06
1280
1260
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

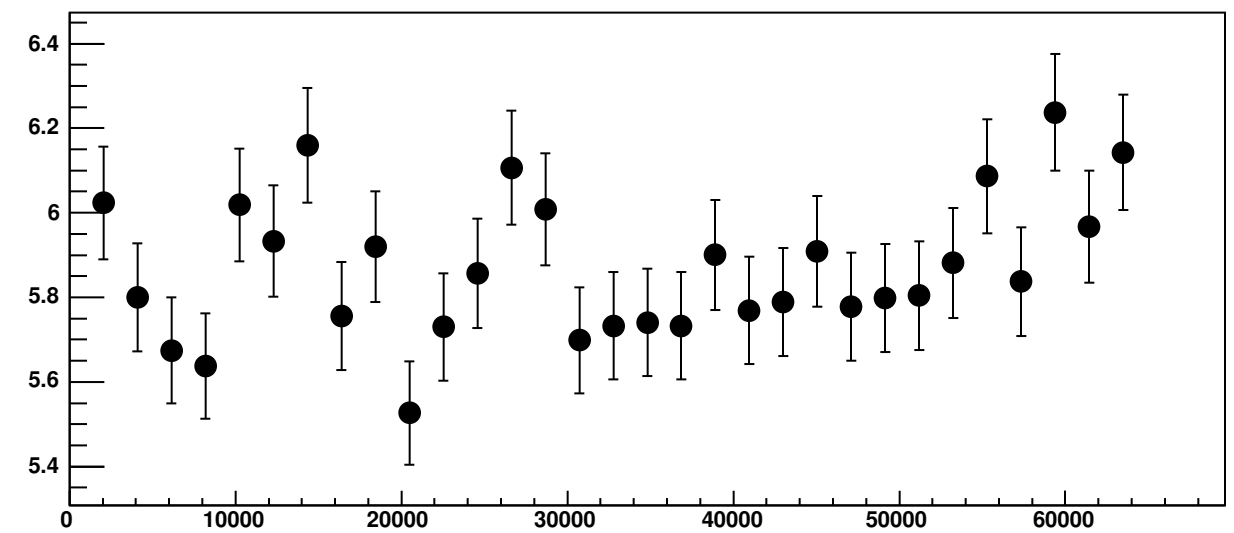

Chip 6, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

Chip 6, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC

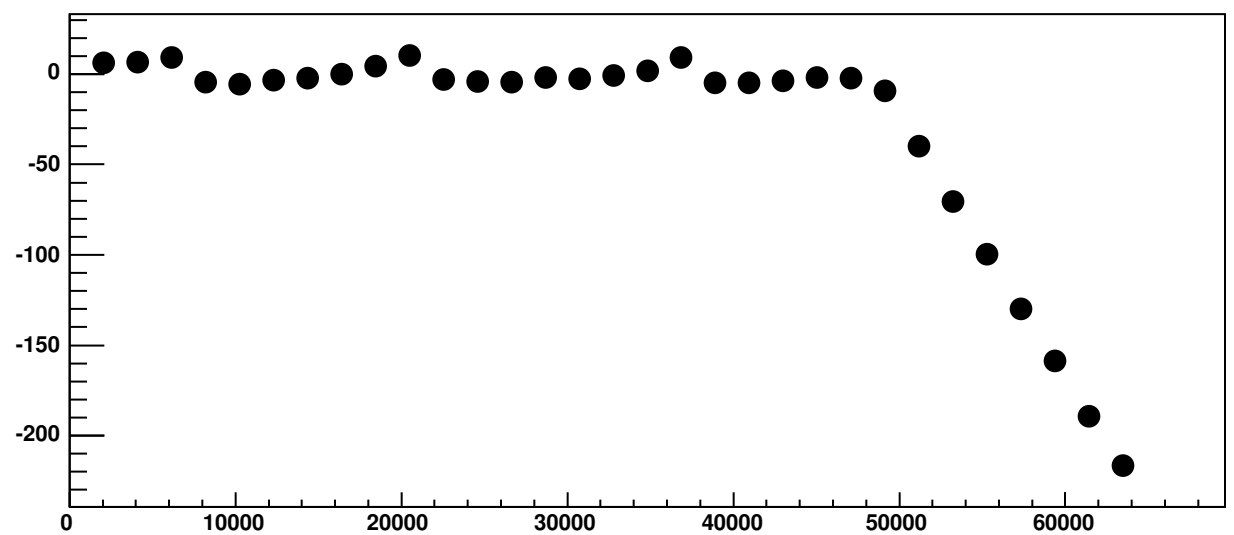
χ^2 / ndf	1.379e+04 / 15
p0	1264 ± 0.1082
p1	-0.0007505 ± 4.38e-06

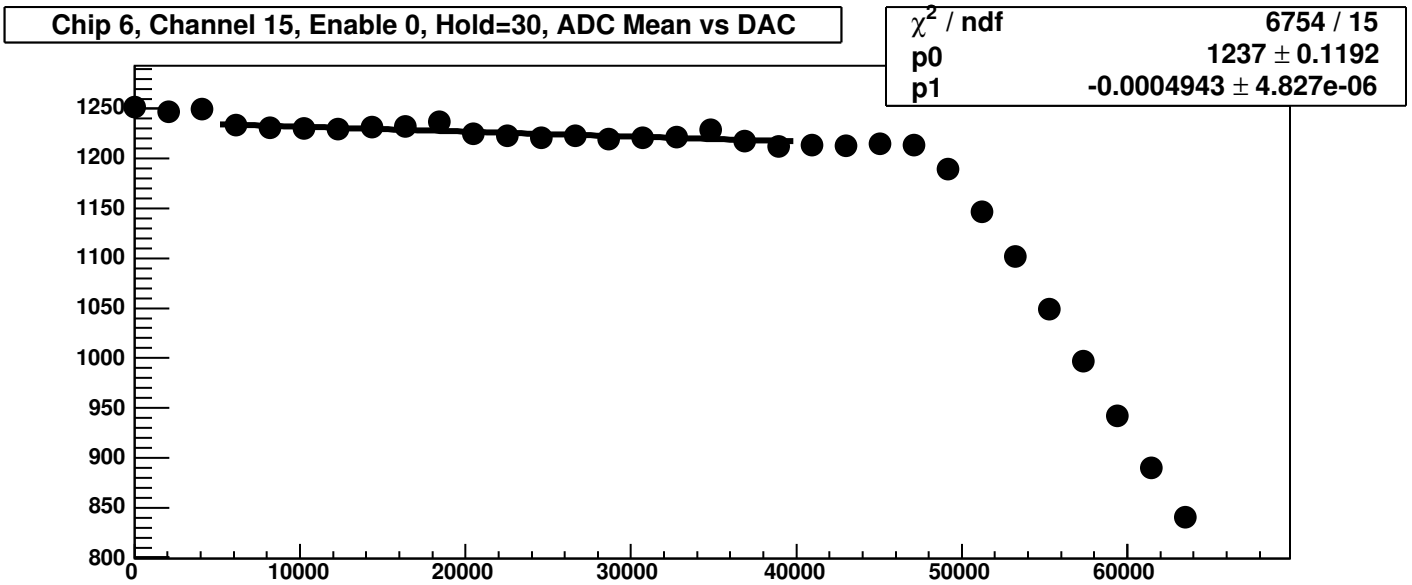


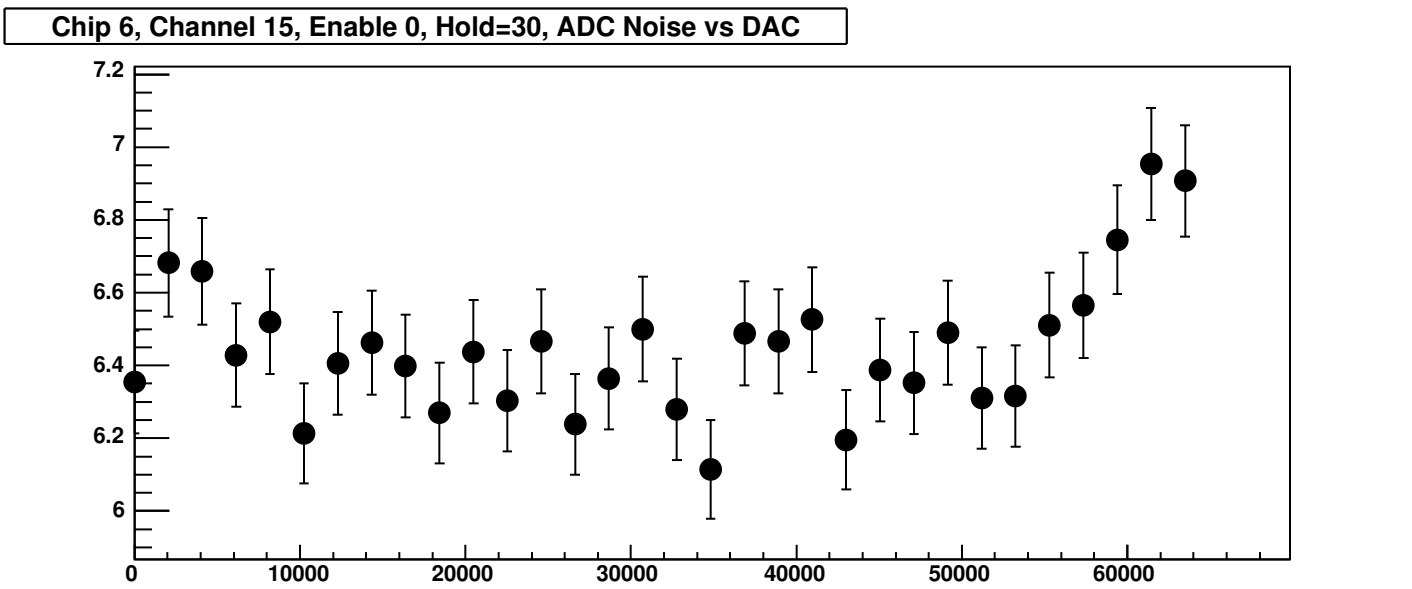
Chip 6, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC

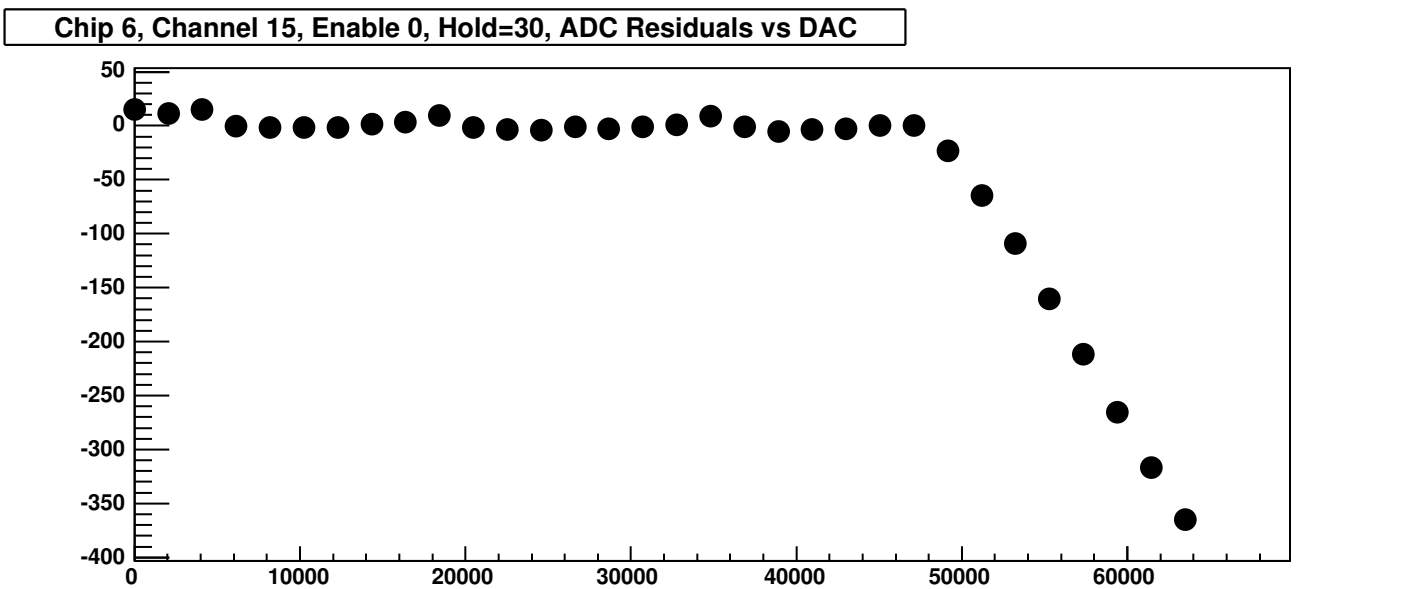


Chip 6, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC



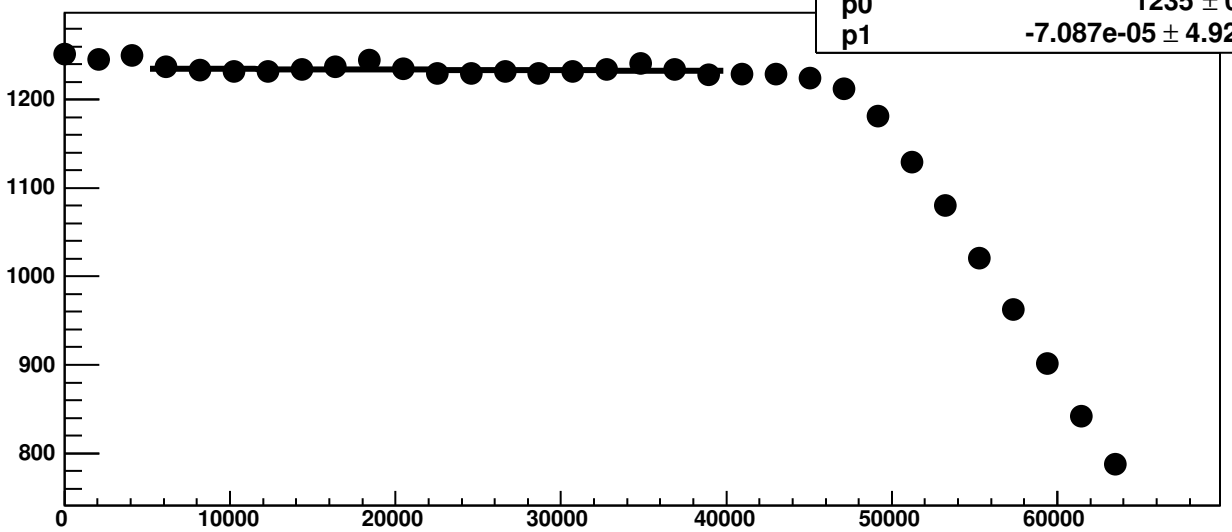

Chip 6, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6754 / 15
p0 1237 ± 0.1192
p1 -0.0004943 ± 4.827e-06
1250
1200
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

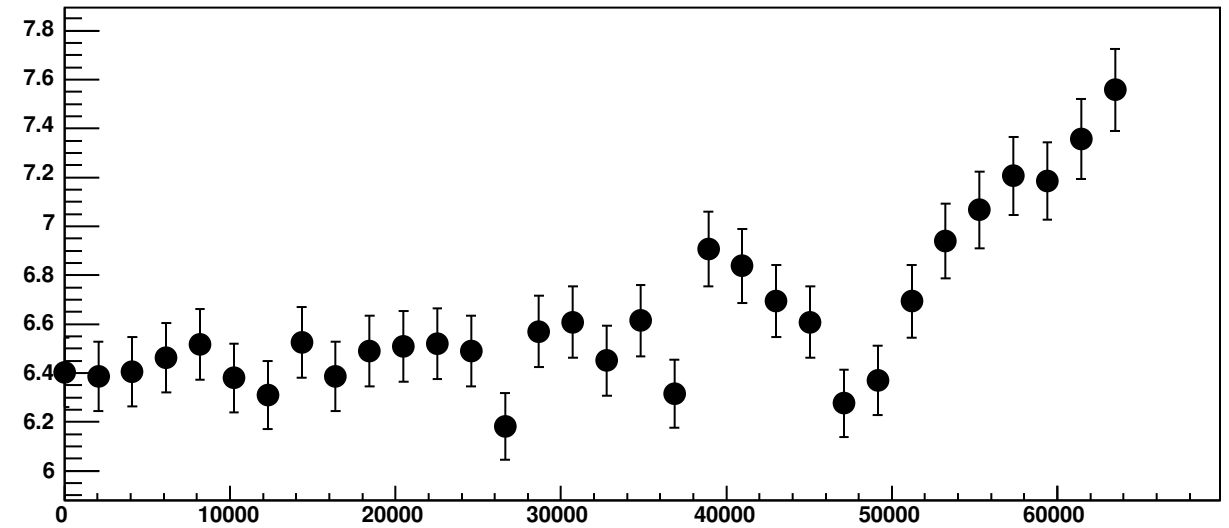

Chip 6, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

Chip 6, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

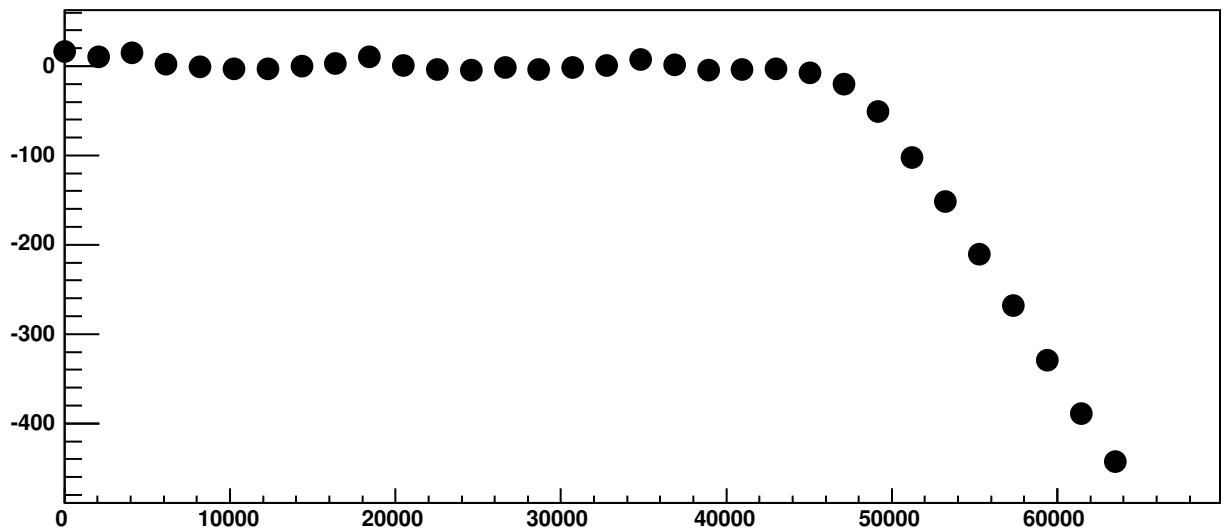
χ^2 / ndf	6573 / 15
p0	1235 ± 0.1206
p1	-7.087e-05 ± 4.923e-06

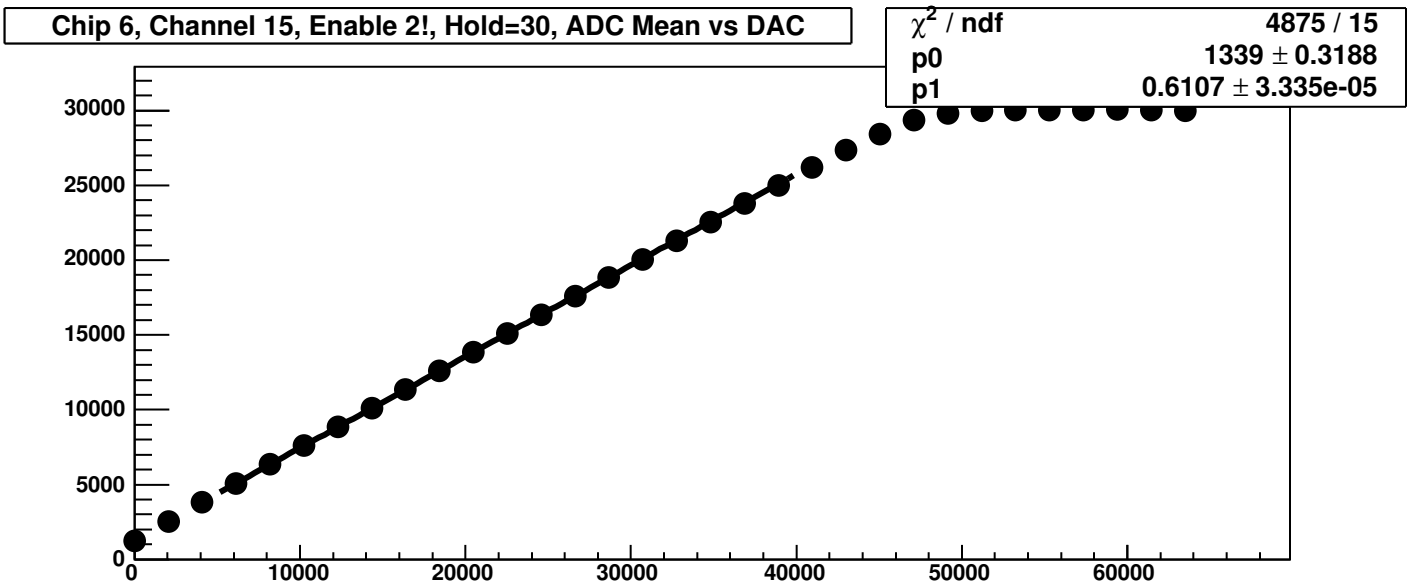


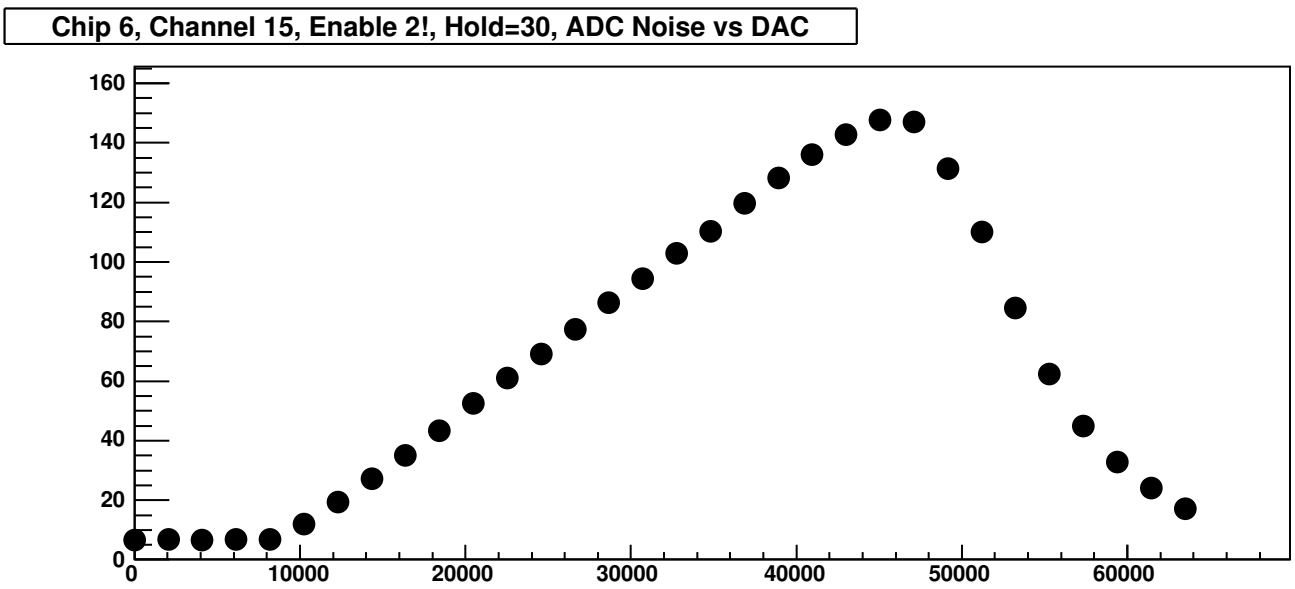
Chip 6, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC

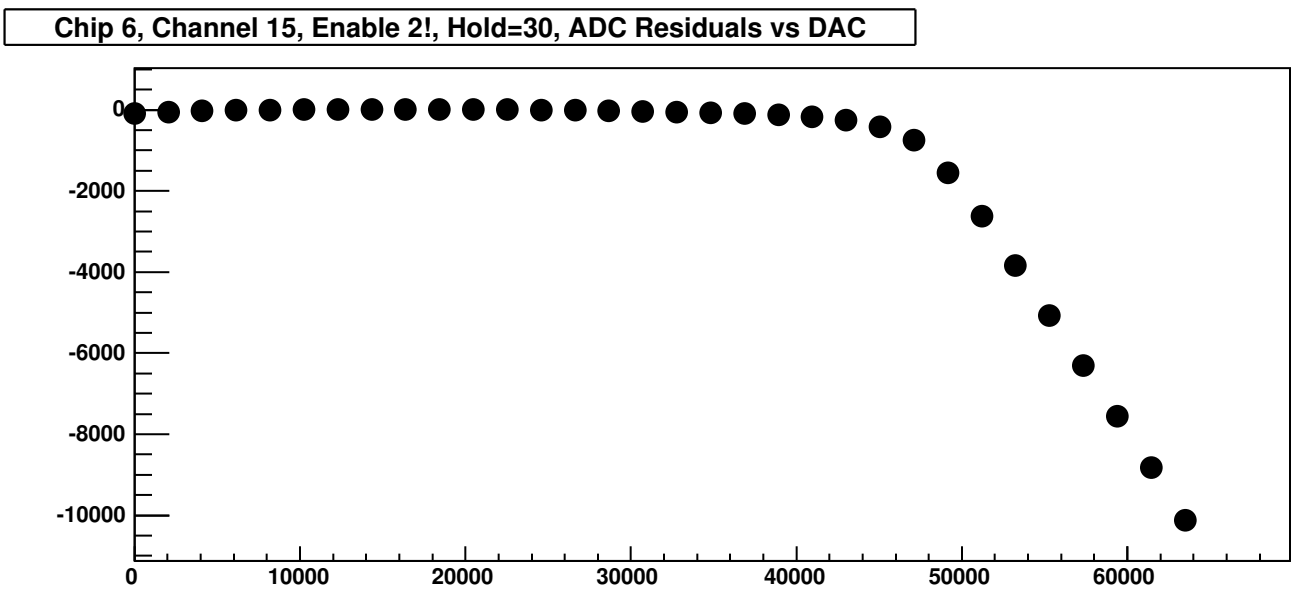


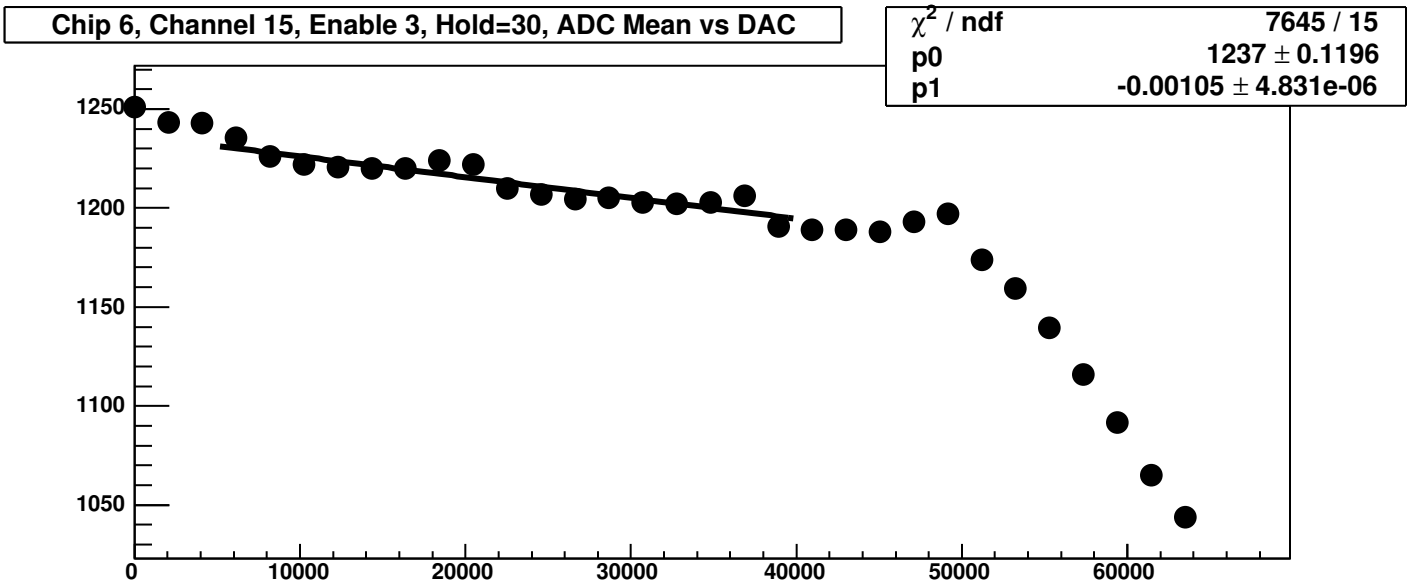
Chip 6, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

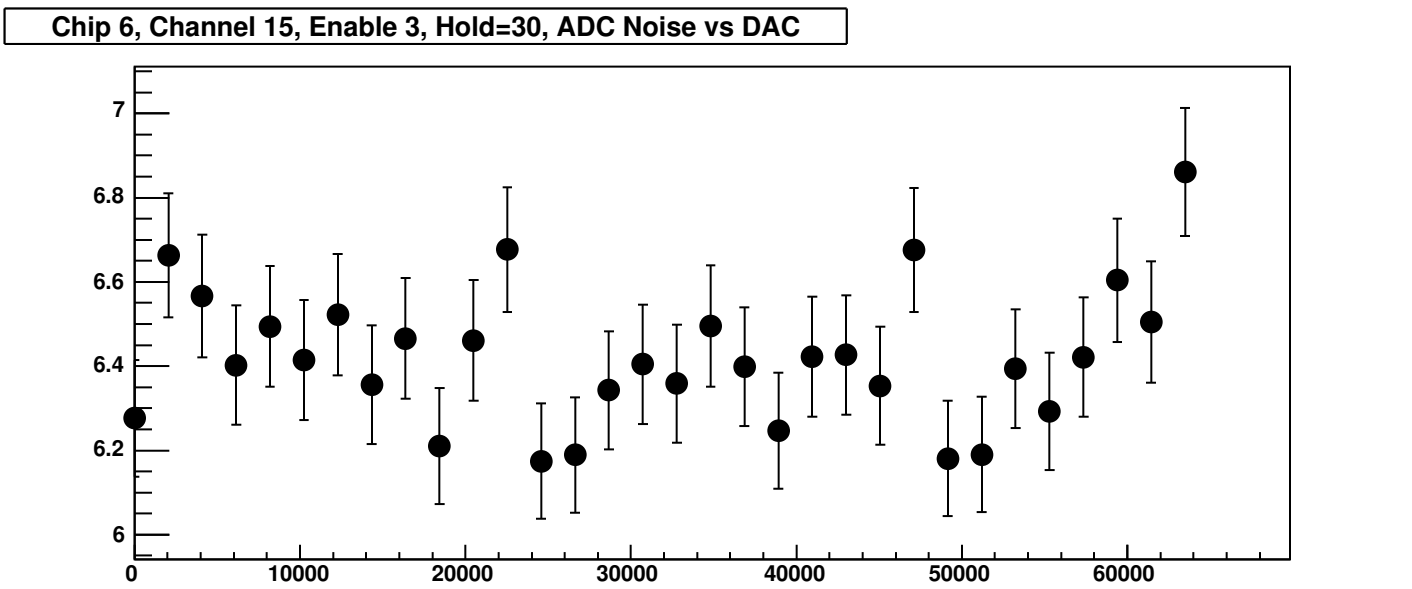


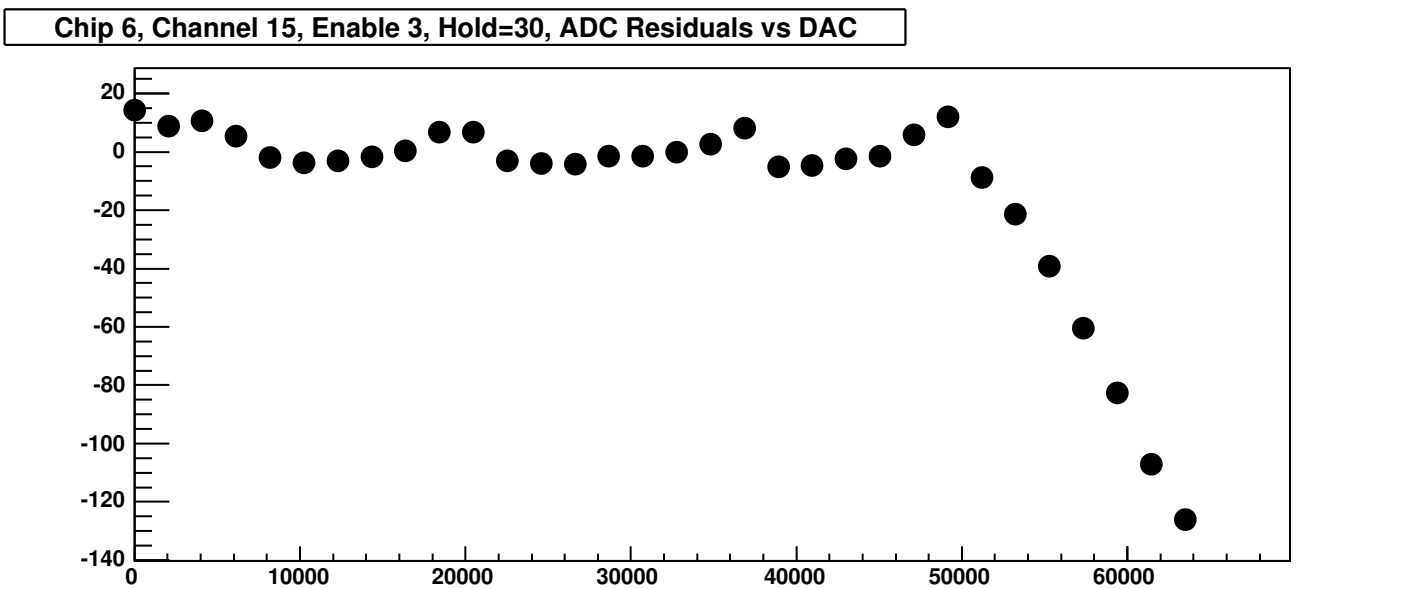

Chip 6, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 4875 / 15
p0 1339 ± 0.3188
p1 0.6107 ± 3.335e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

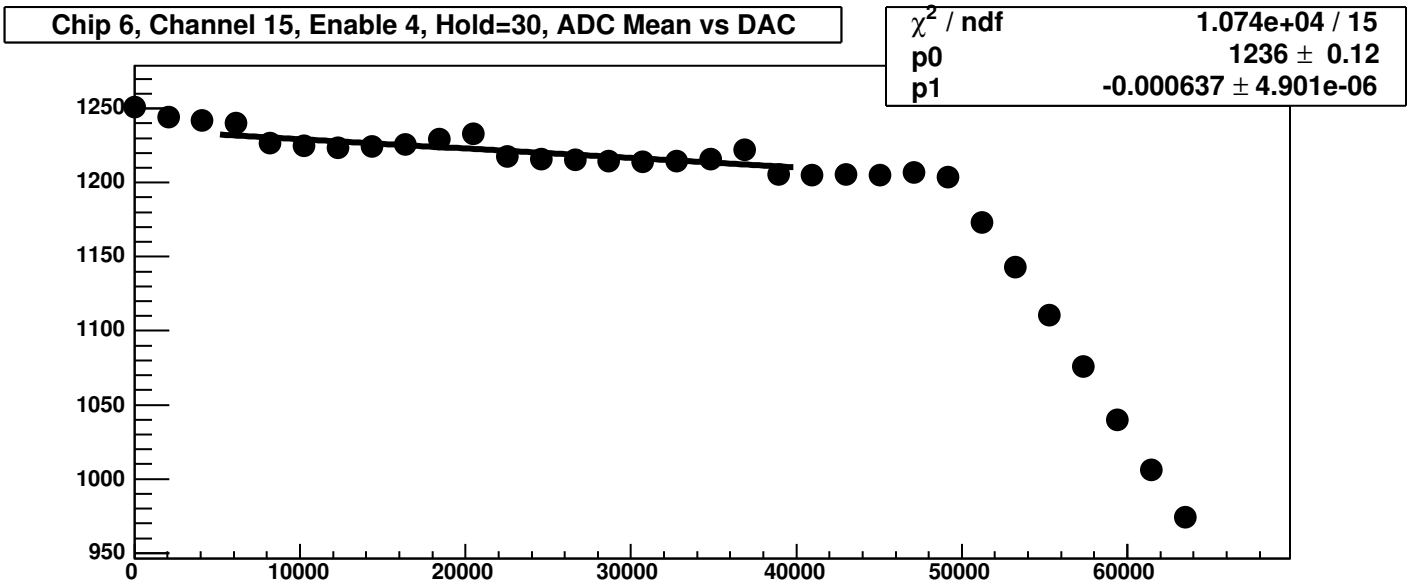

Chip 6, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

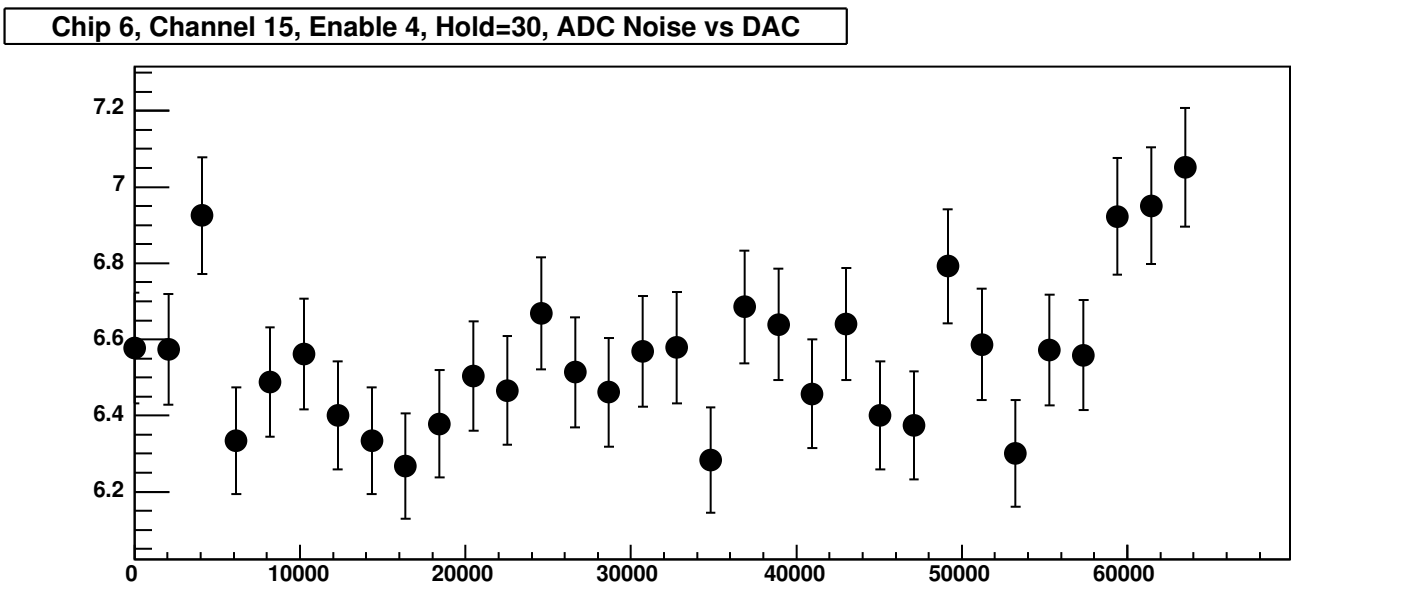

Chip 6, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

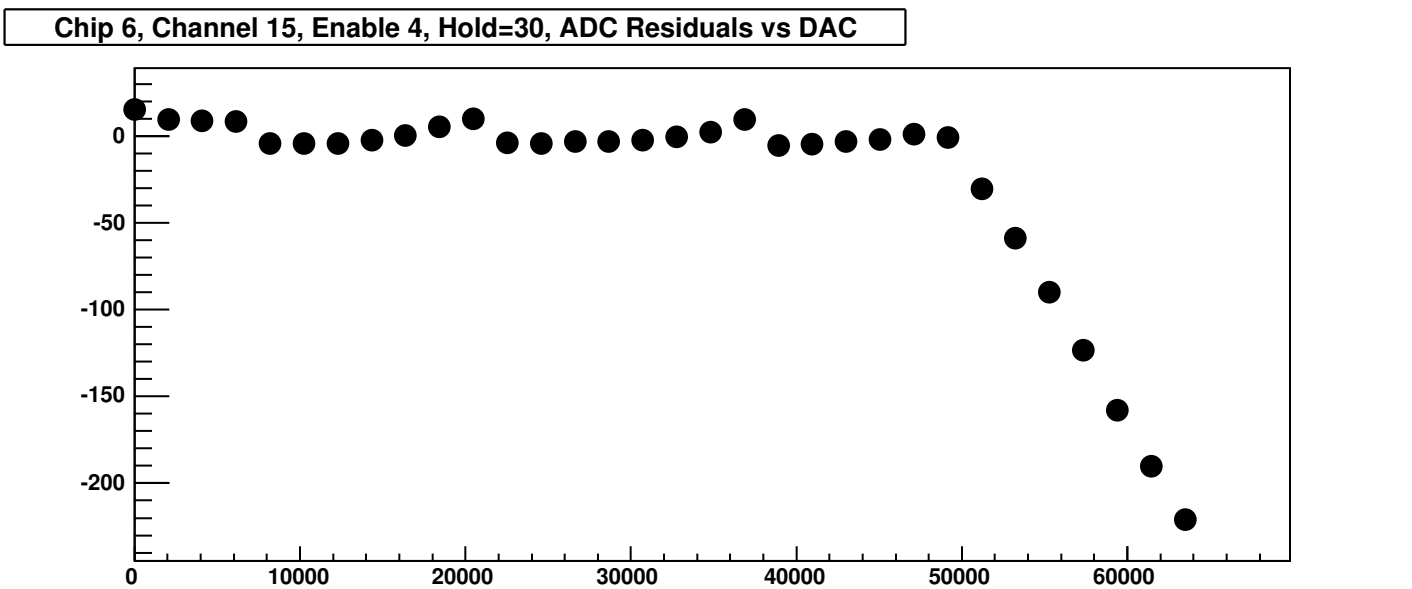

Chip 6, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7645 / 15
p0 1237 ± 0.1196
p1 -0.00105 ± 4.831e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

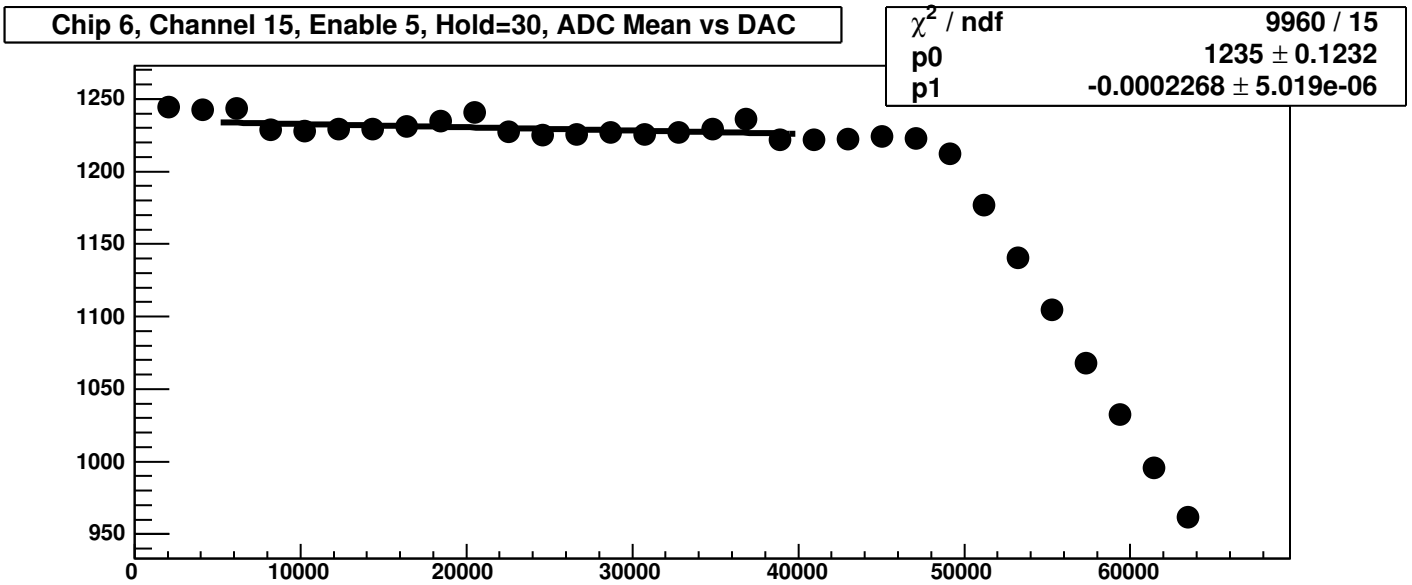

Chip 6, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

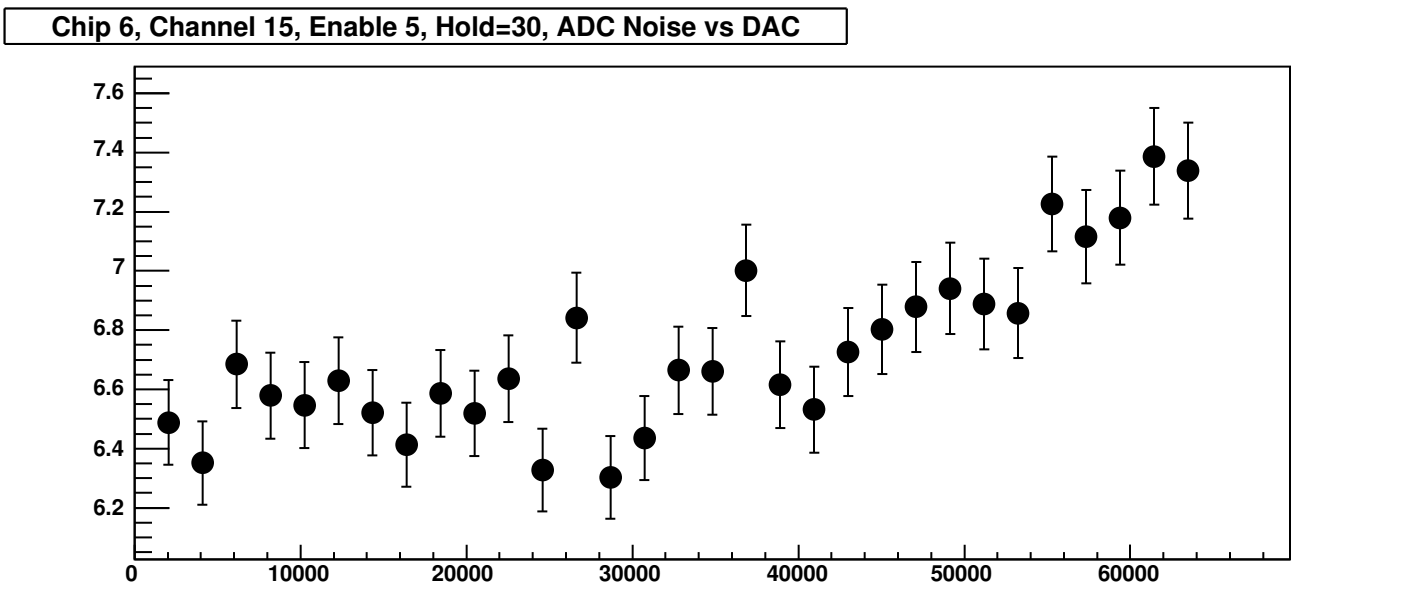

Chip 6, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

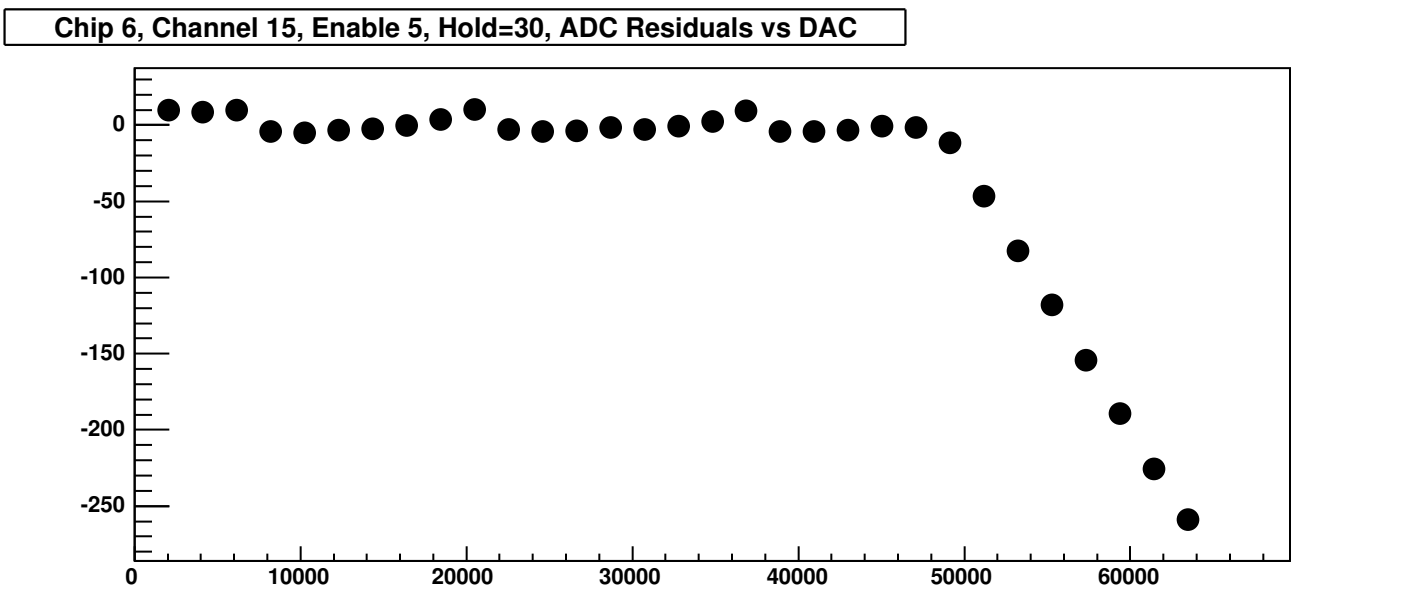

Chip 6, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.074e+04 / 15
p0 1236 ± 0.12
p1 -0.000637 ± 4.901e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000

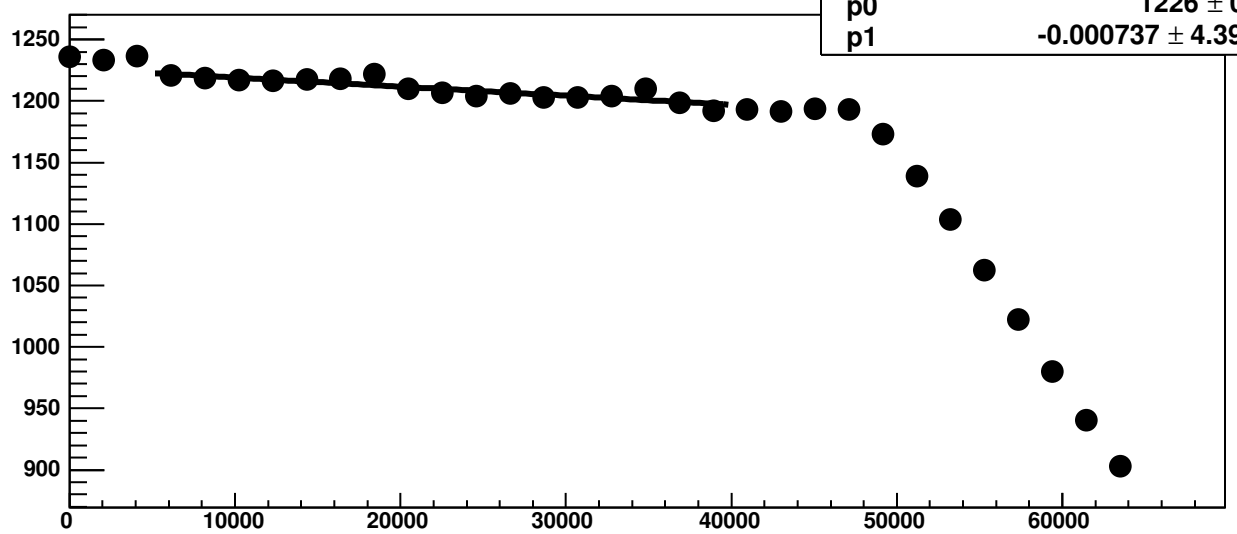

Chip 6, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9960 / 15
p0 1235 ± 0.1232
p1 -0.0002268 ± 5.019e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

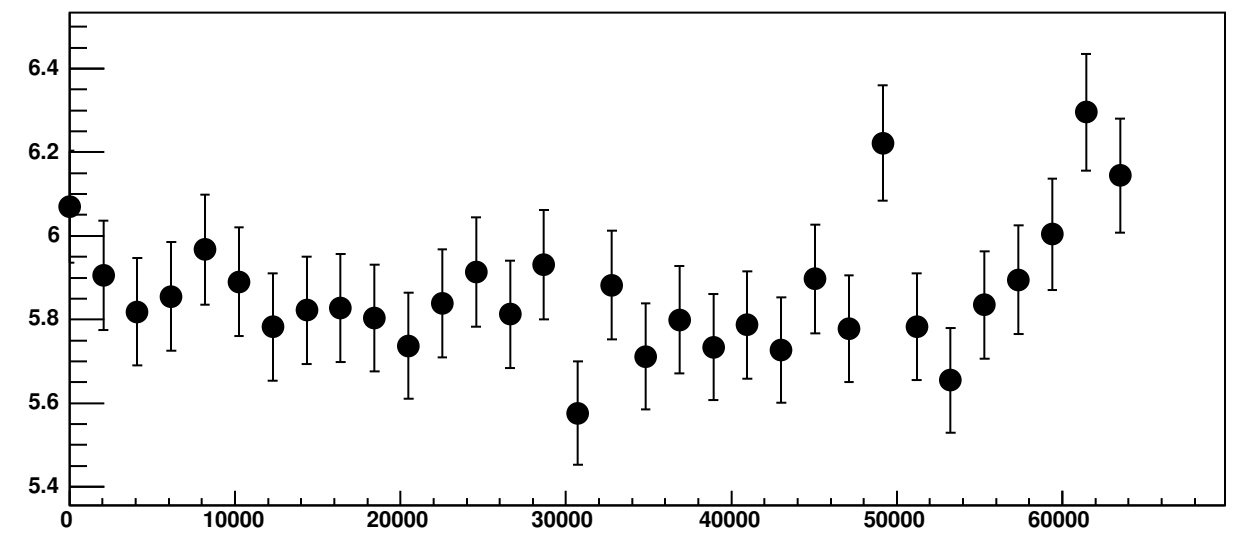

Chip 6, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

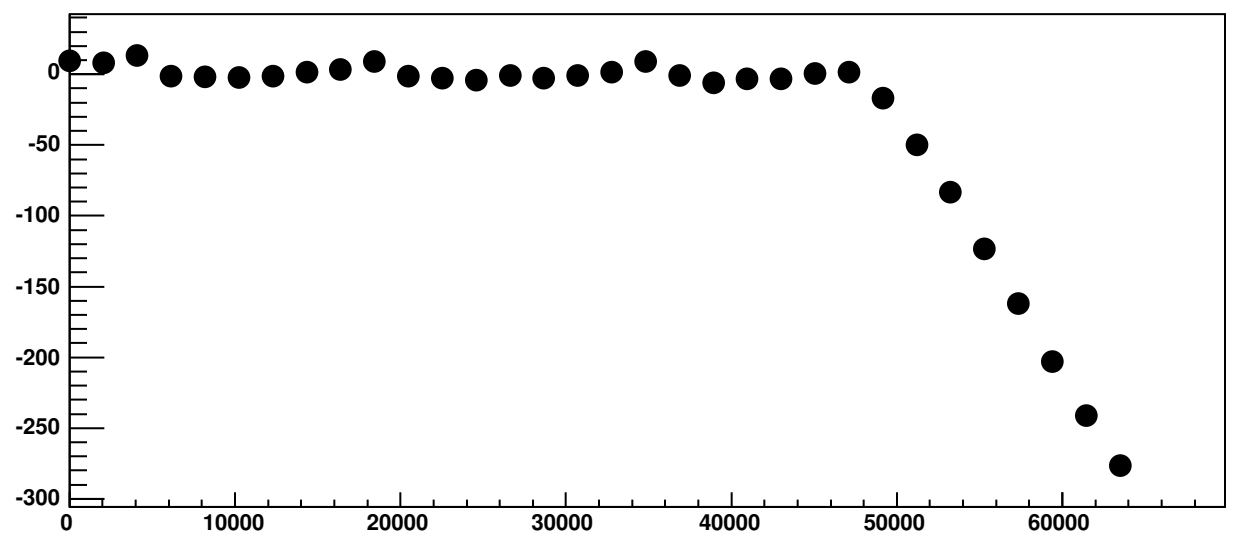
Chip 6, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC

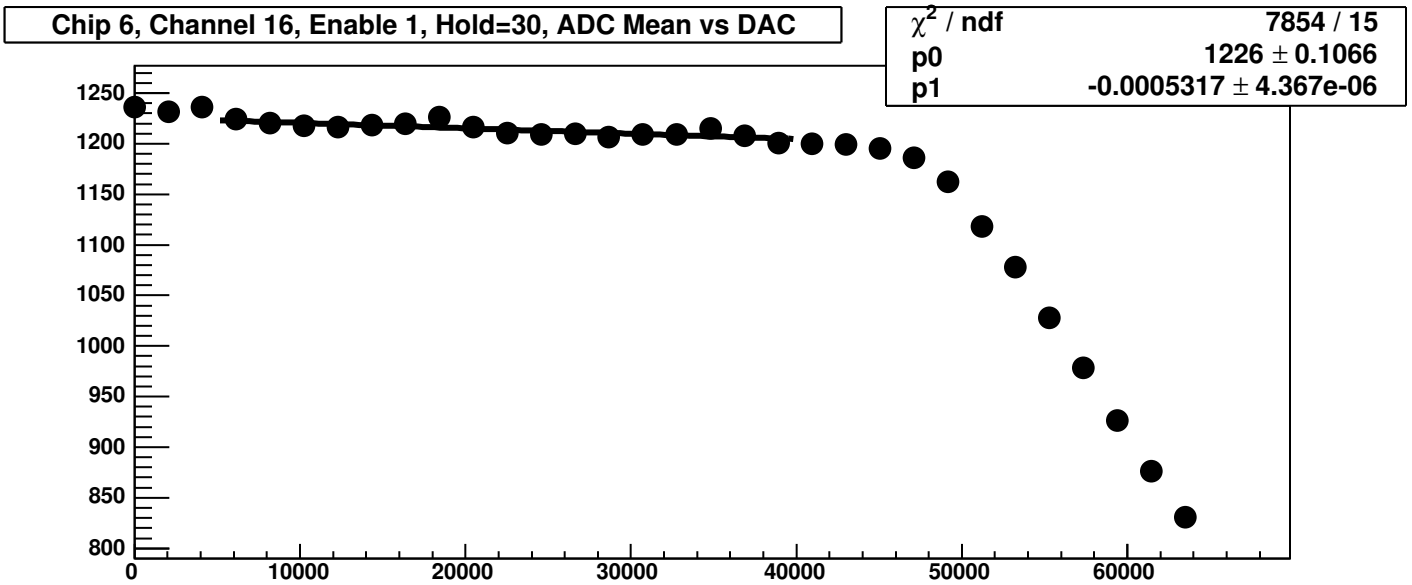


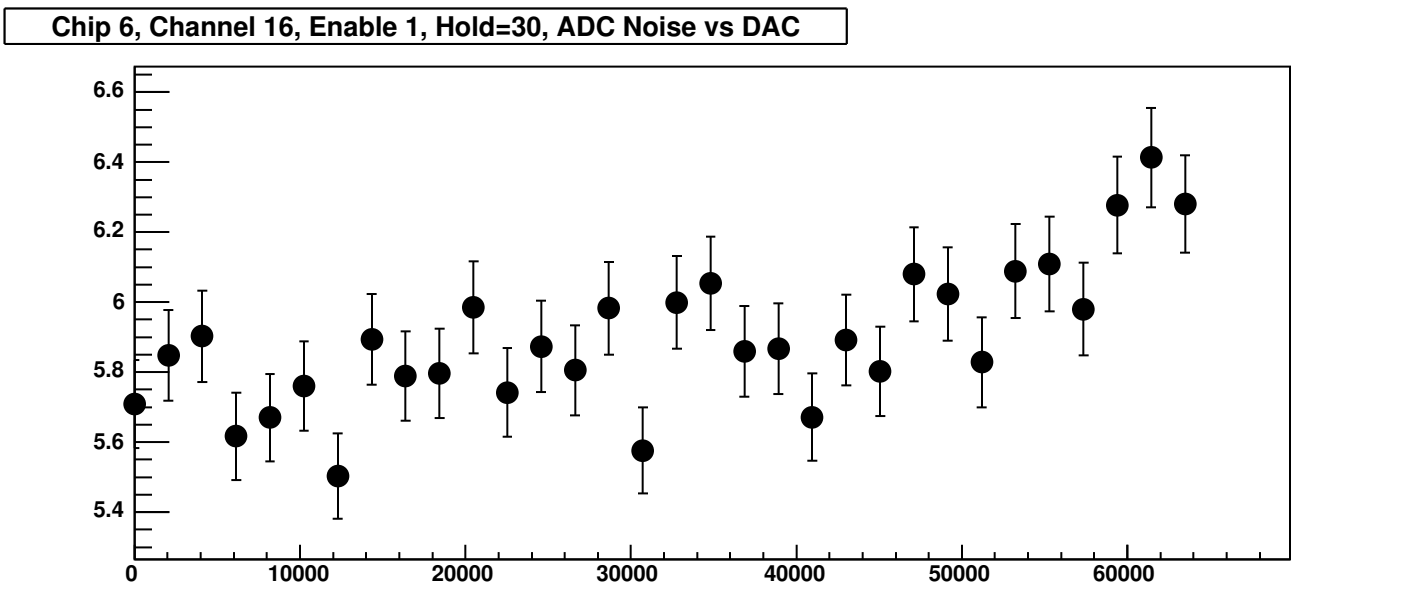
Chip 6, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC

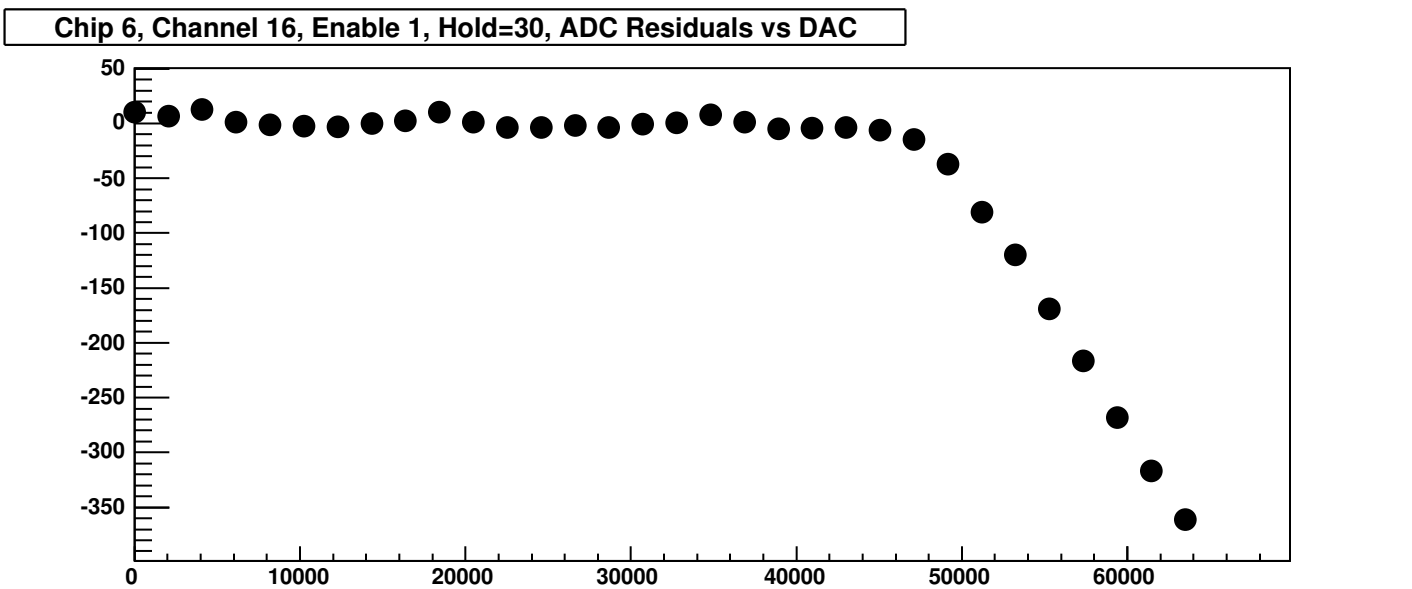


Chip 6, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC

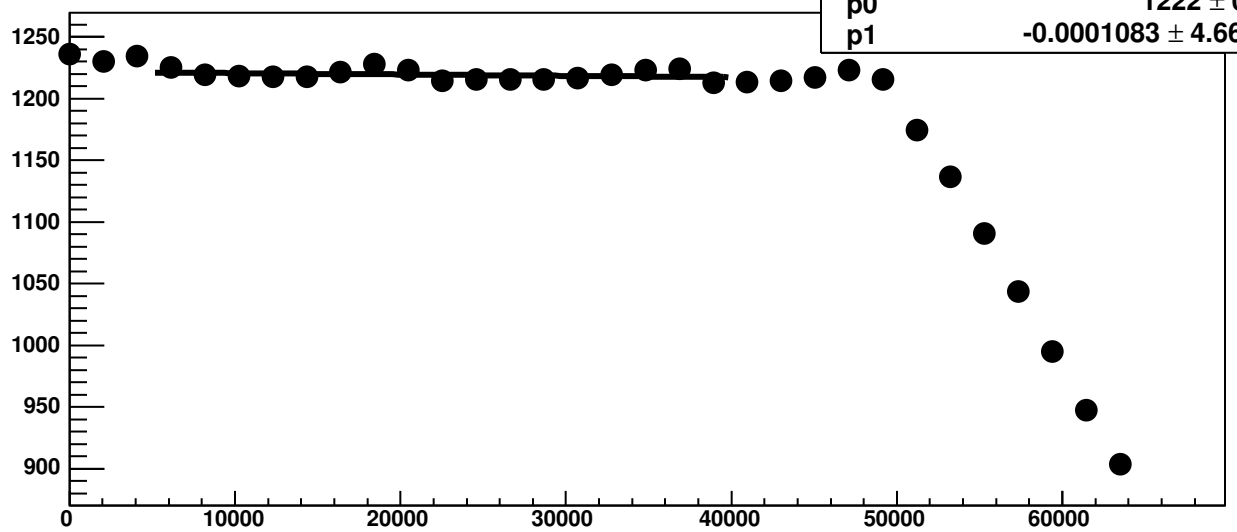



Chip 6, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7854 / 15
p0 1226 ± 0.1066
p1 -0.0005317 ± 4.367e-06
1250
1200
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

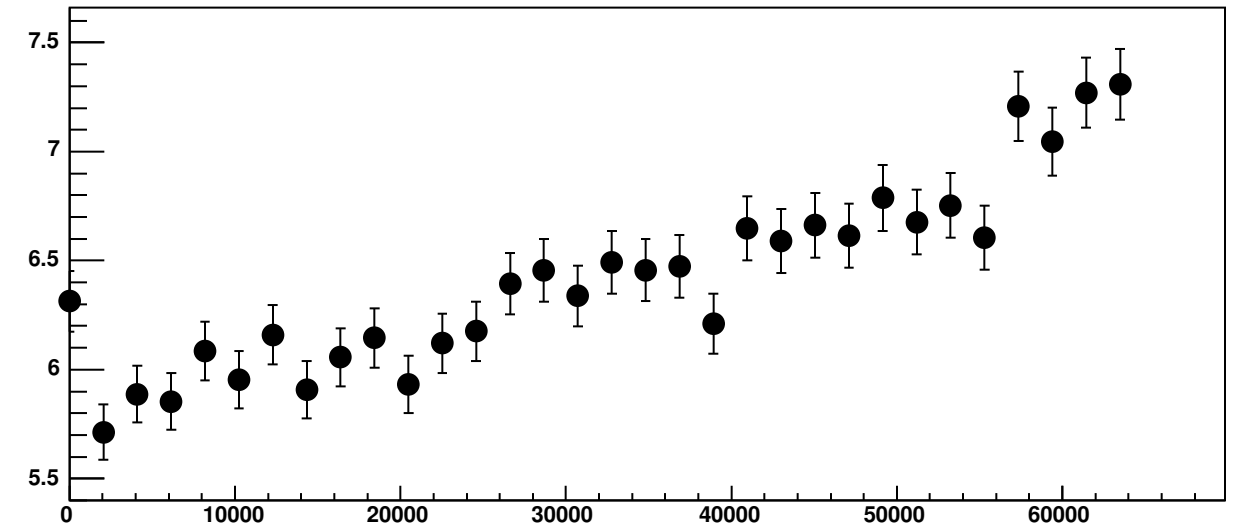

Chip 6, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

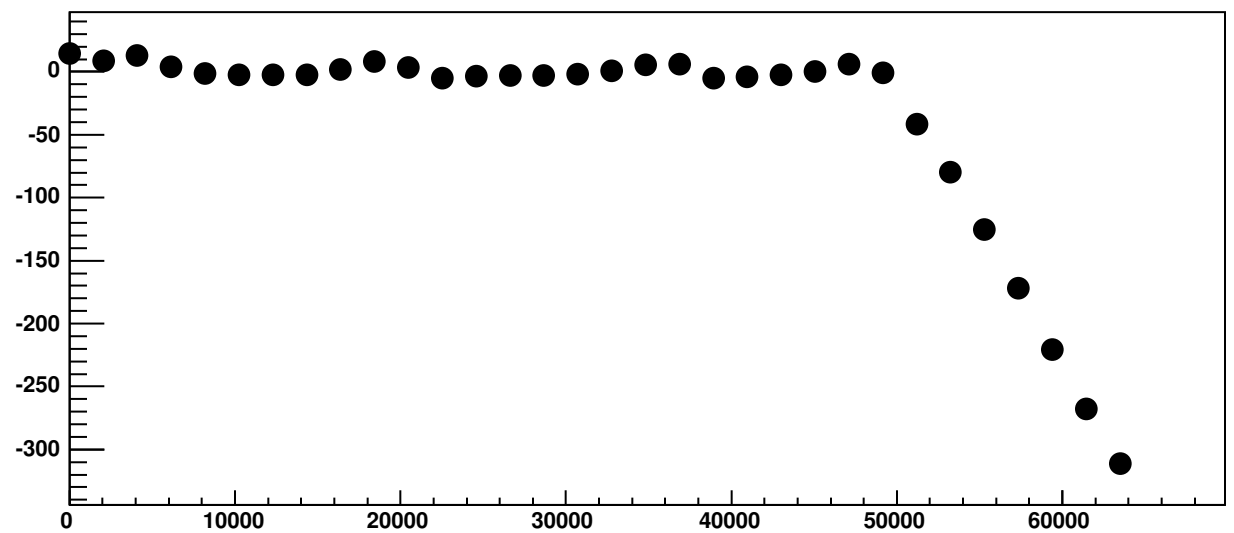
Chip 6, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC

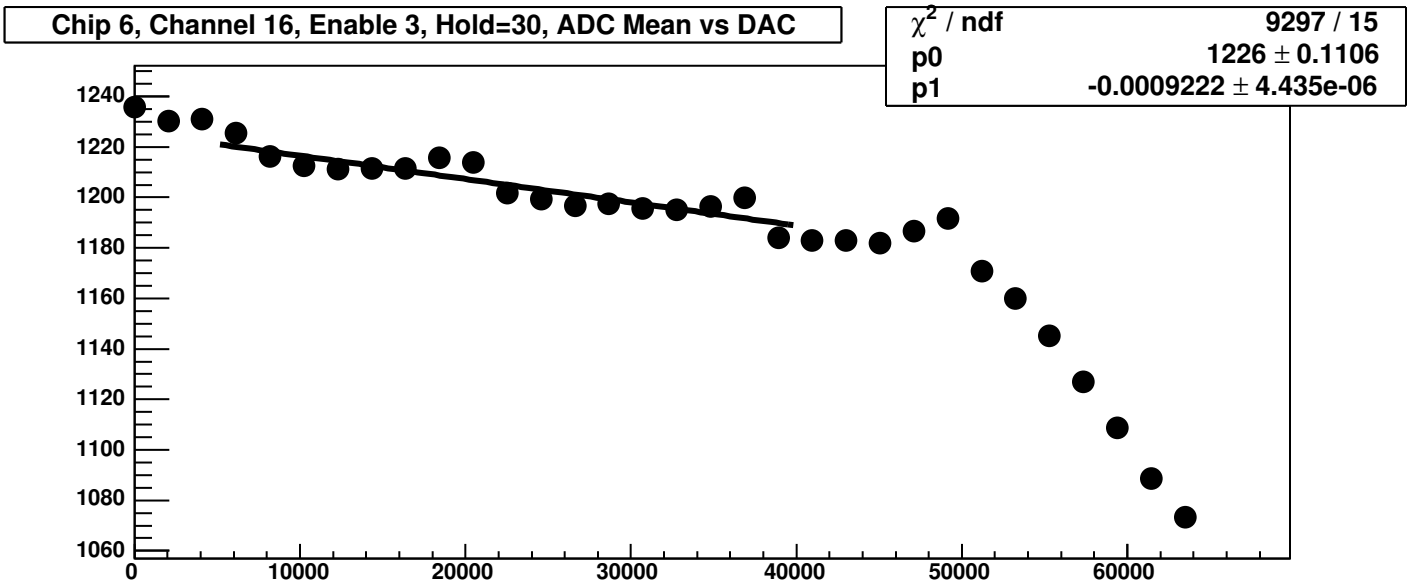


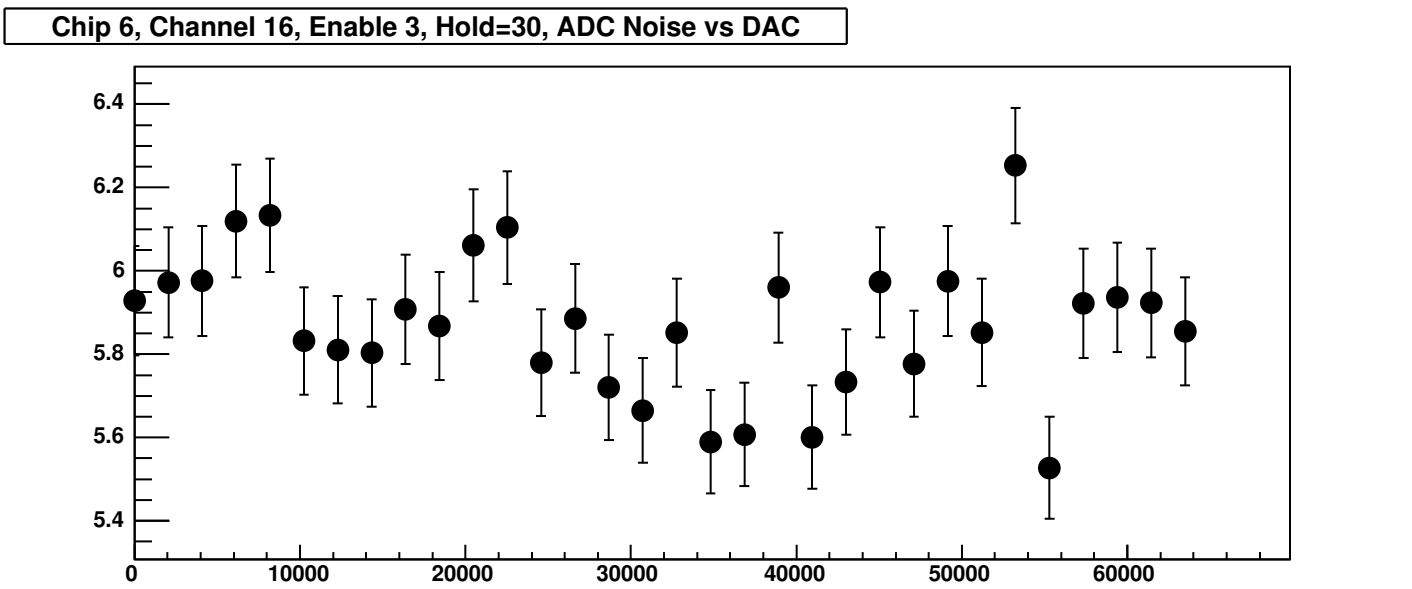
Chip 6, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC

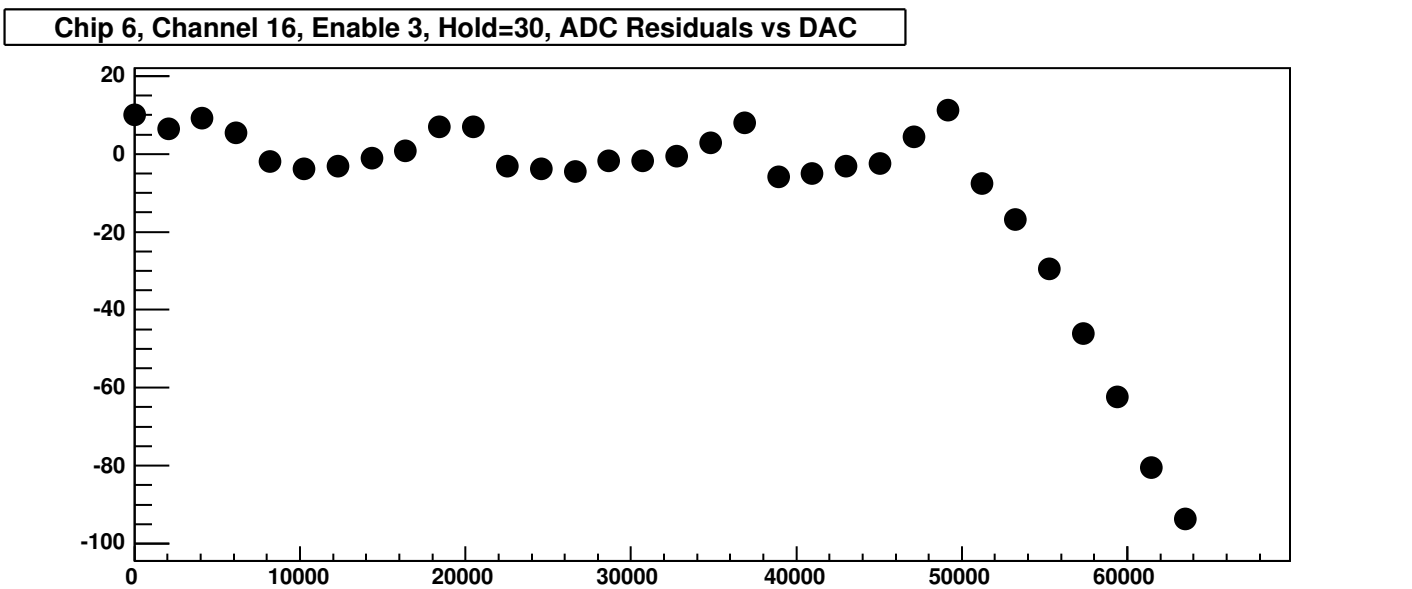


Chip 6, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC

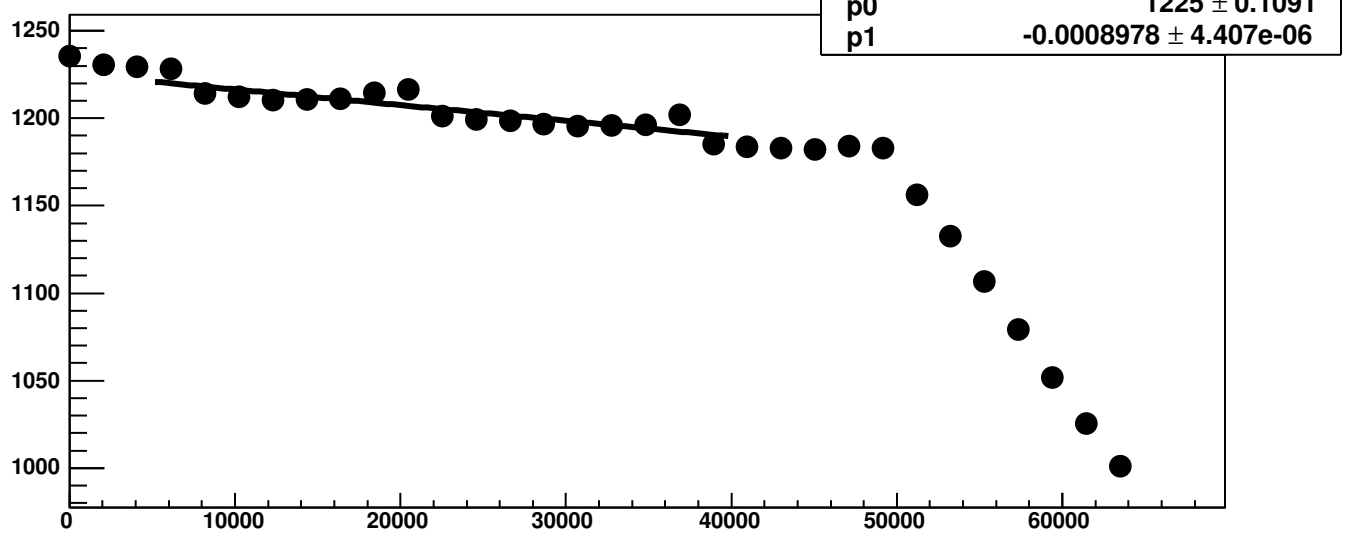



Chip 6, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9297 / 15
p0 1226 ± 0.1106
p1 -0.0009222 ± 4.435e-06
1240
1220
1200
1180
1160
1140
1120
1100
1080
1060
0
10000
20000
30000
40000
50000
60000

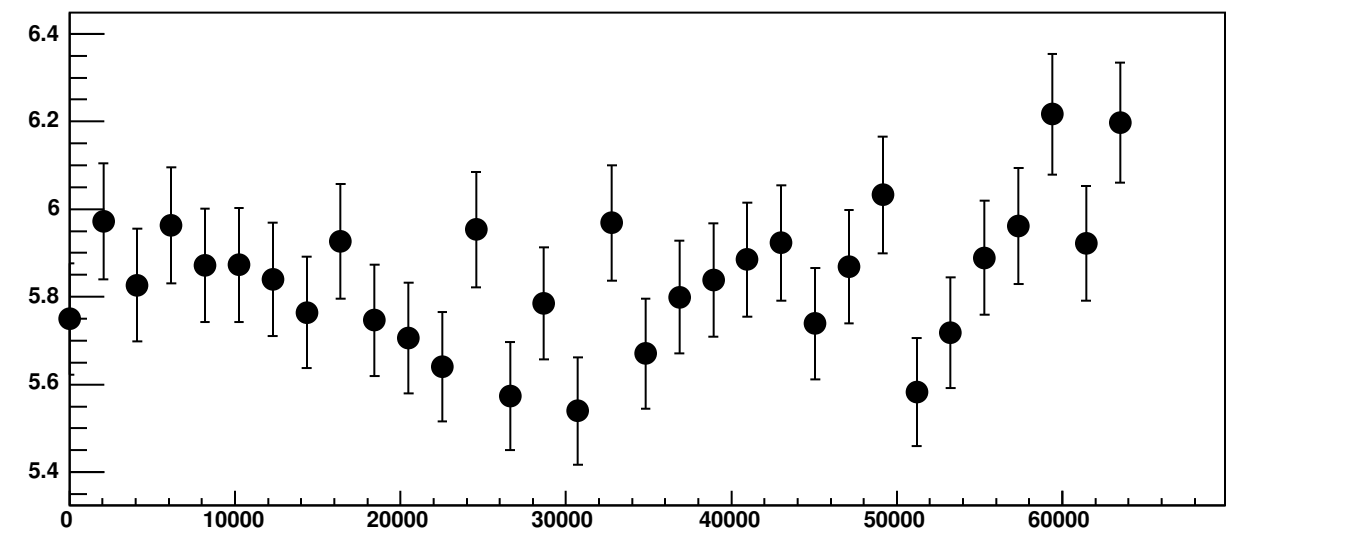

Chip 6, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

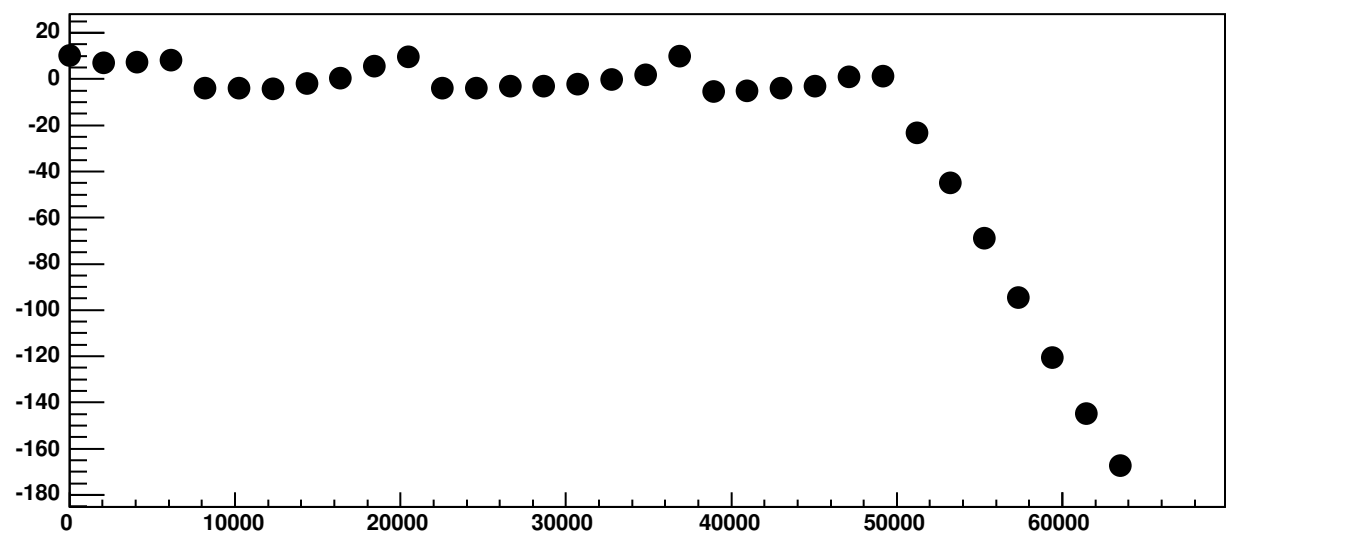
Chip 6, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

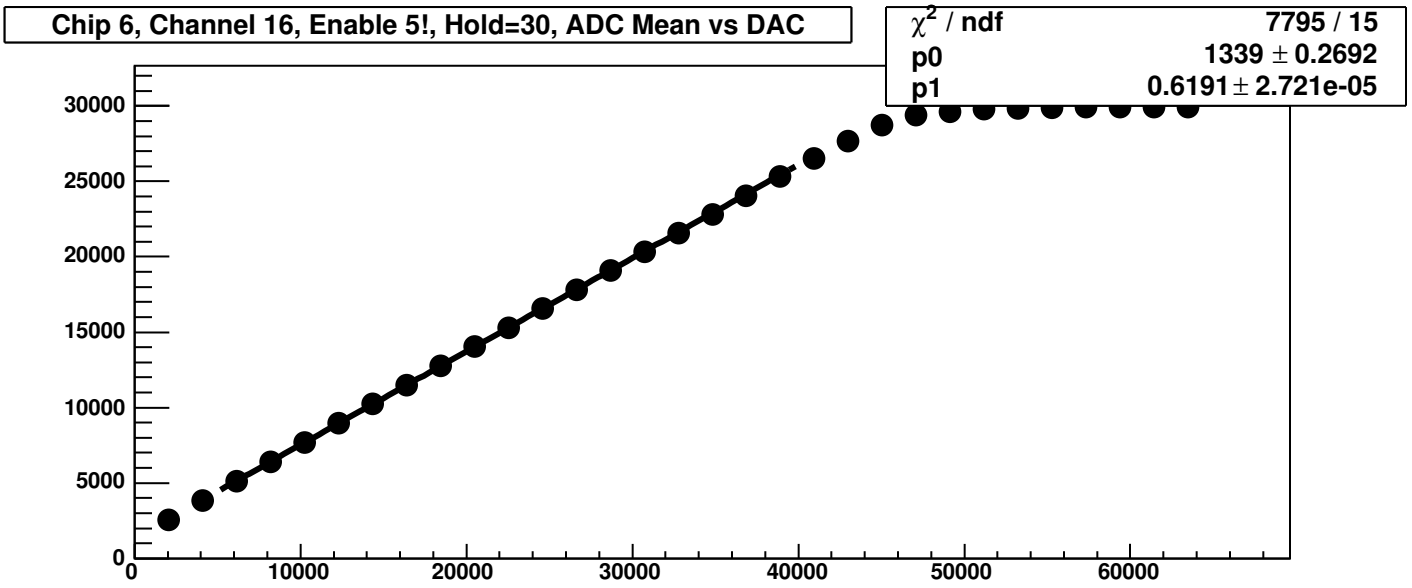


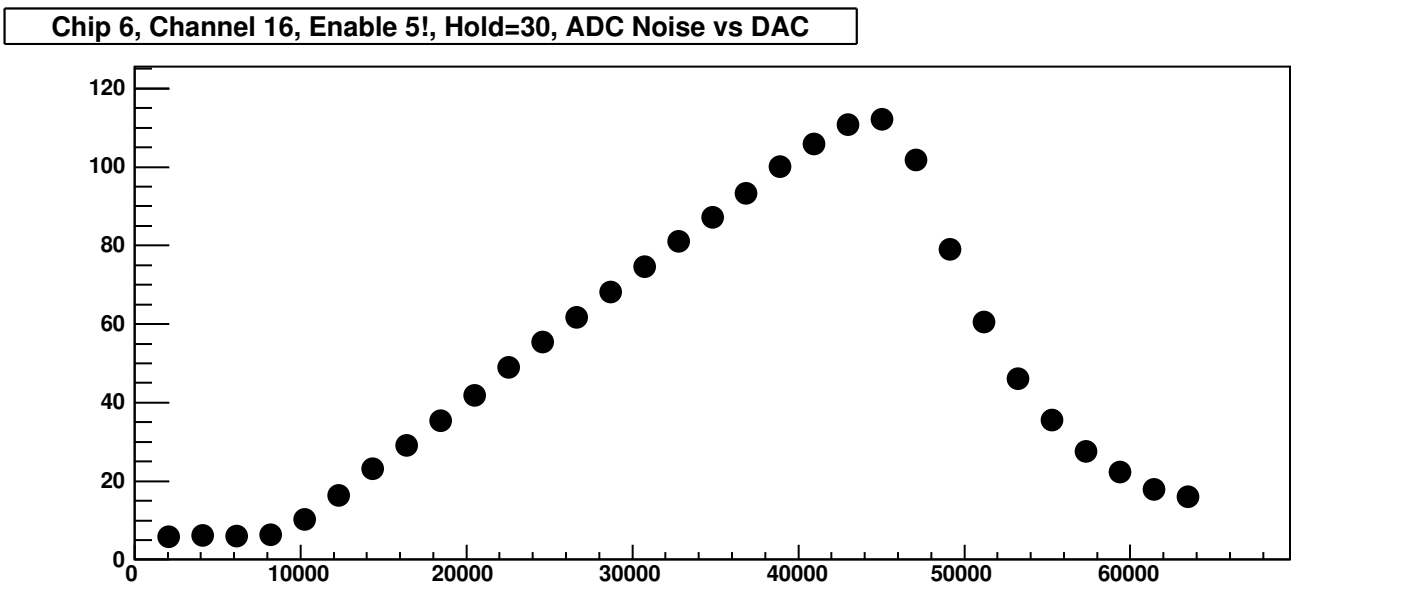
Chip 6, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC

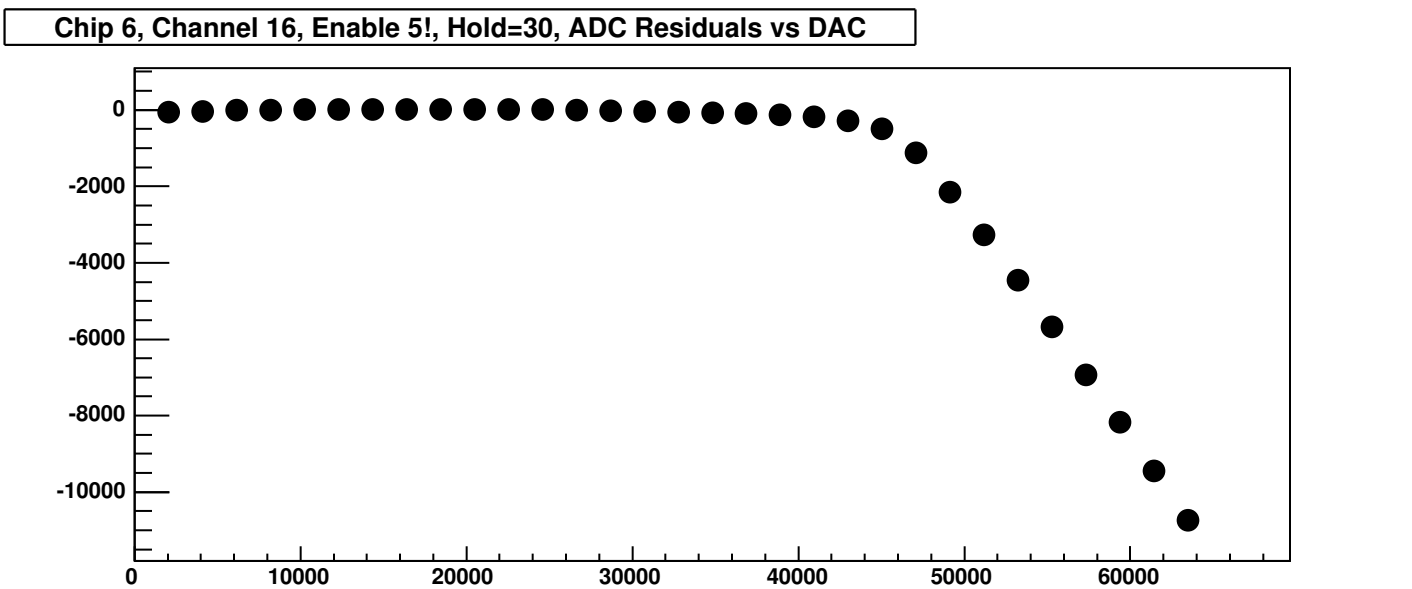


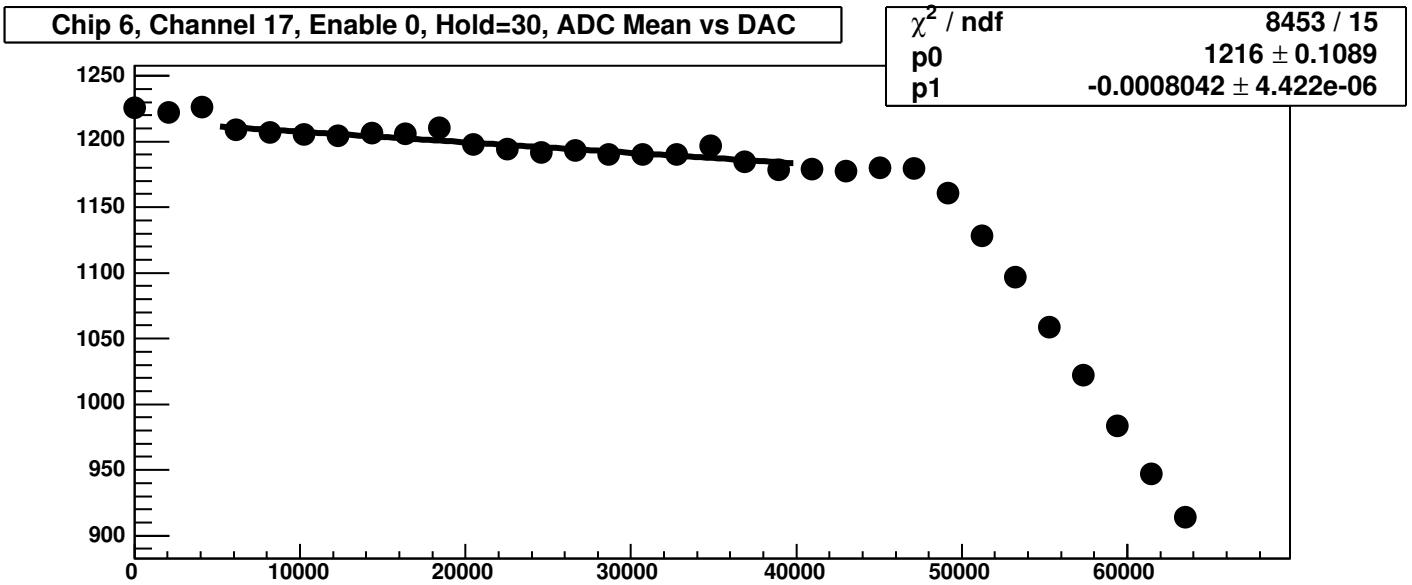
Chip 6, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC

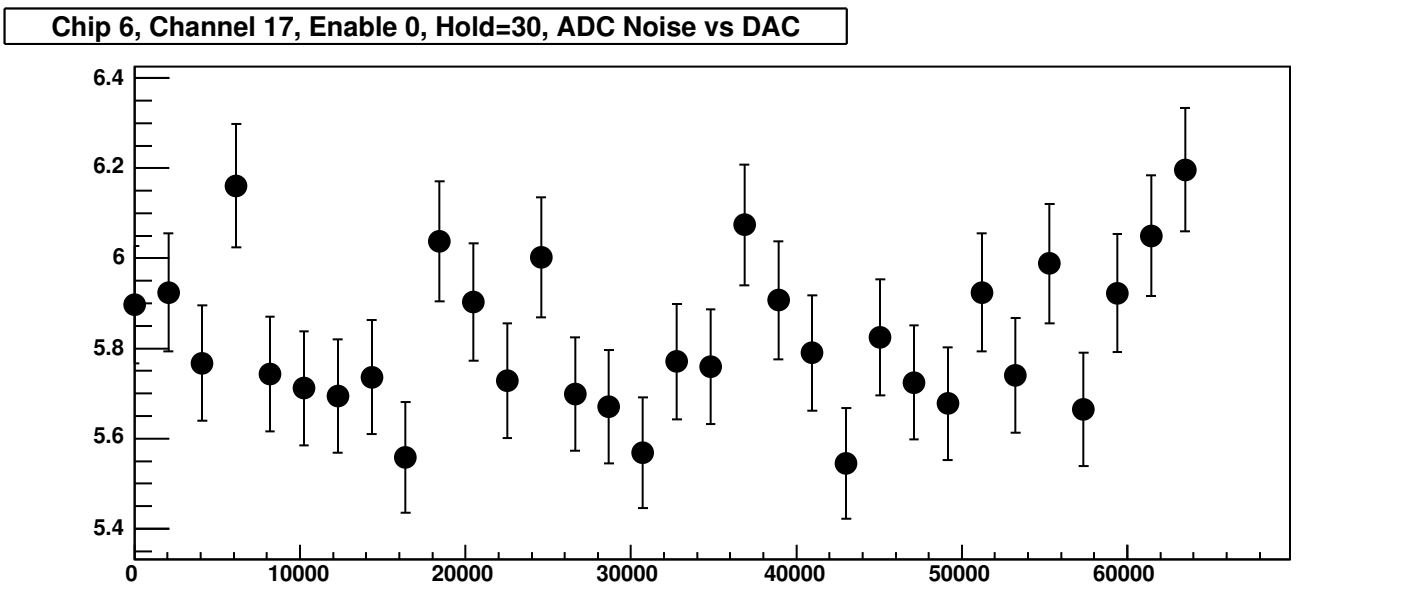


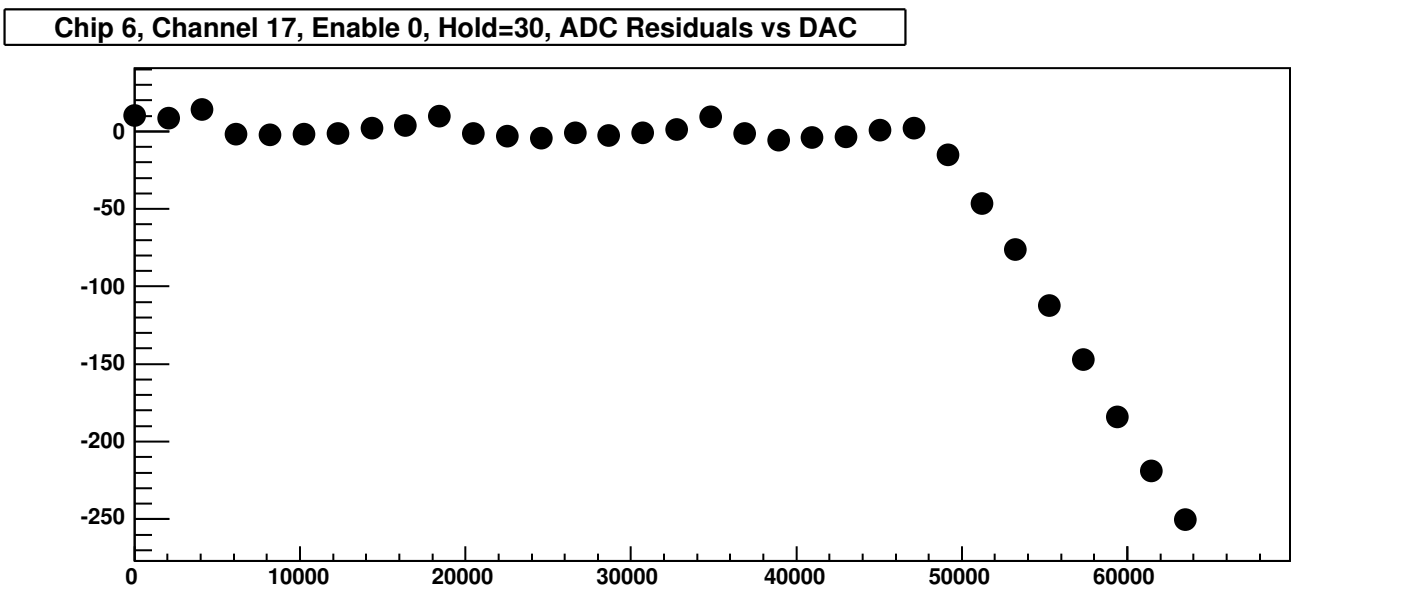

Chip 6, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7795 / 15
p0 1339 ± 0.2692
p1 0.6191 ± 2.721e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

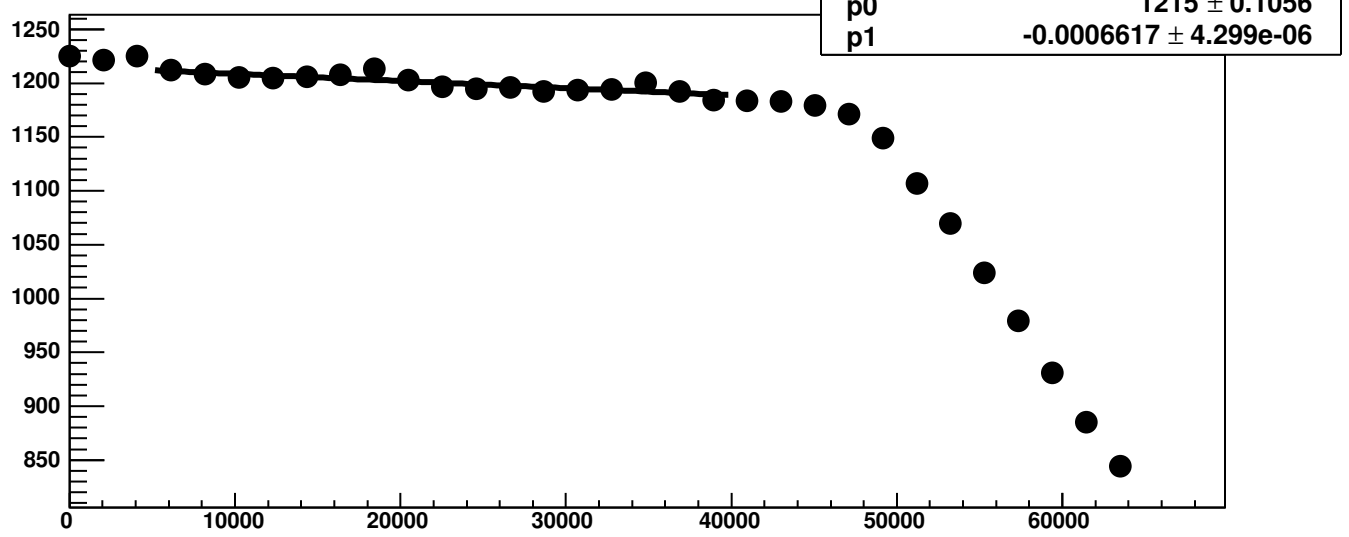

Chip 6, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8453 / 15
p0 1216 ± 0.1089
p1 -0.0008042 ± 4.422e-06
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

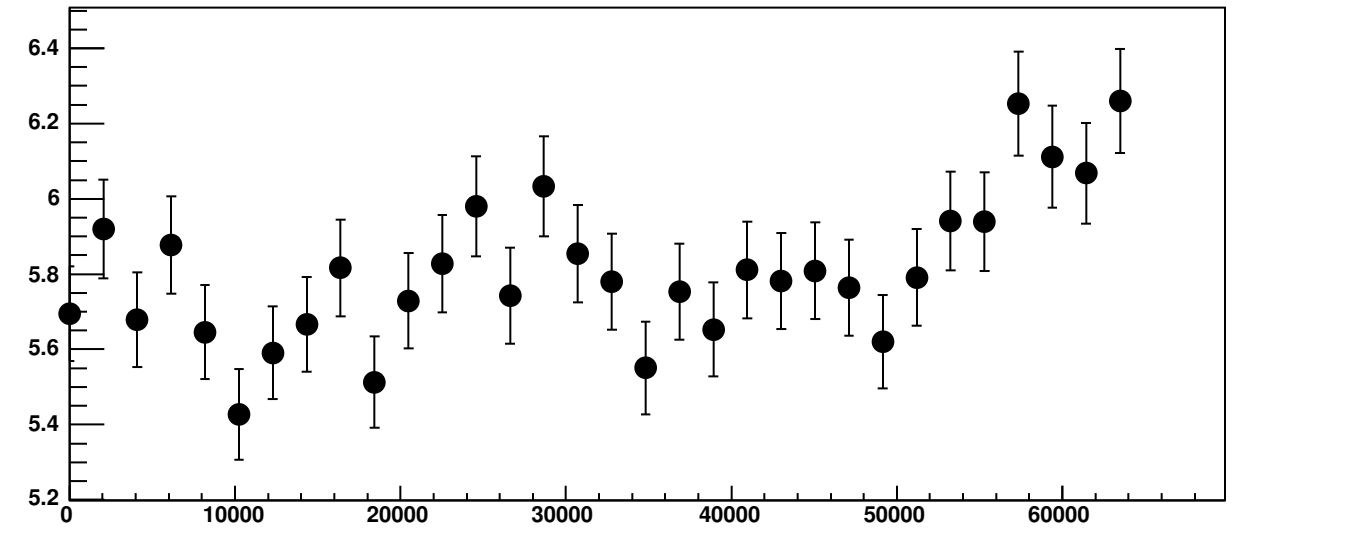

Chip 6, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

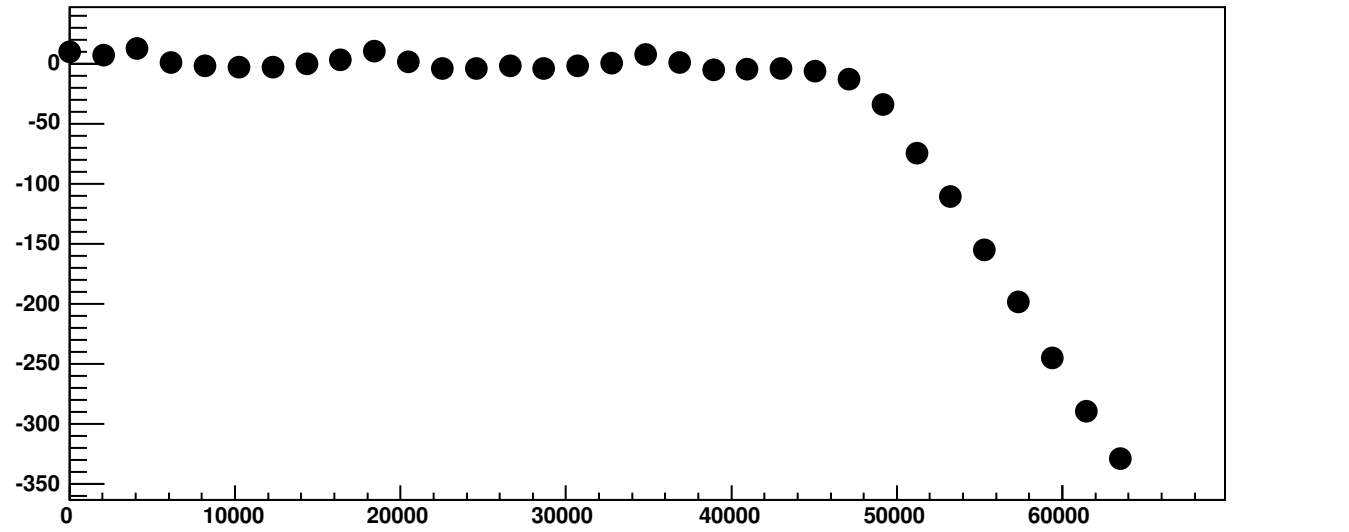
Chip 6, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

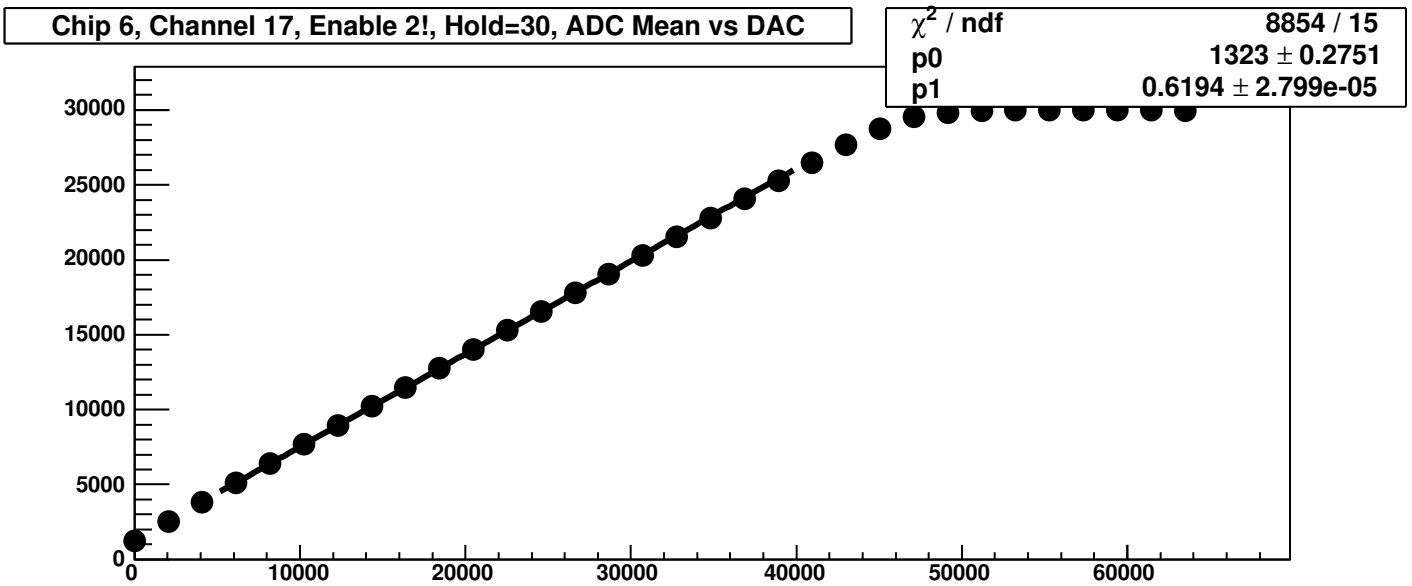


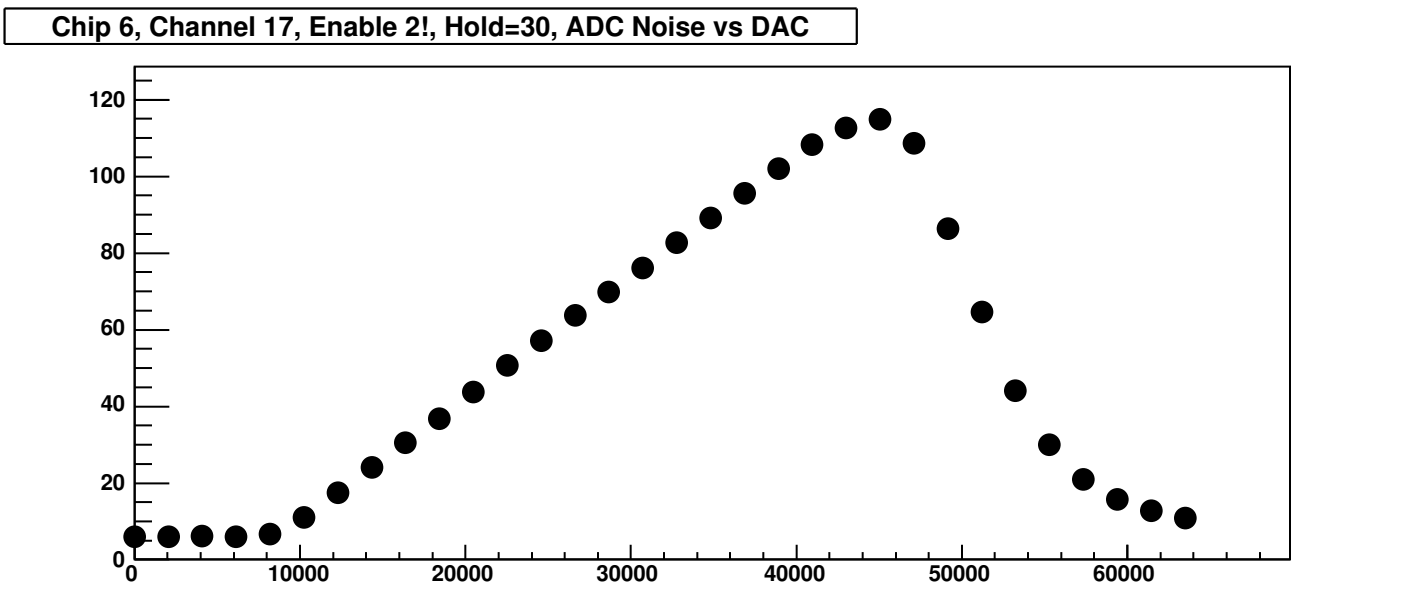
Chip 6, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC

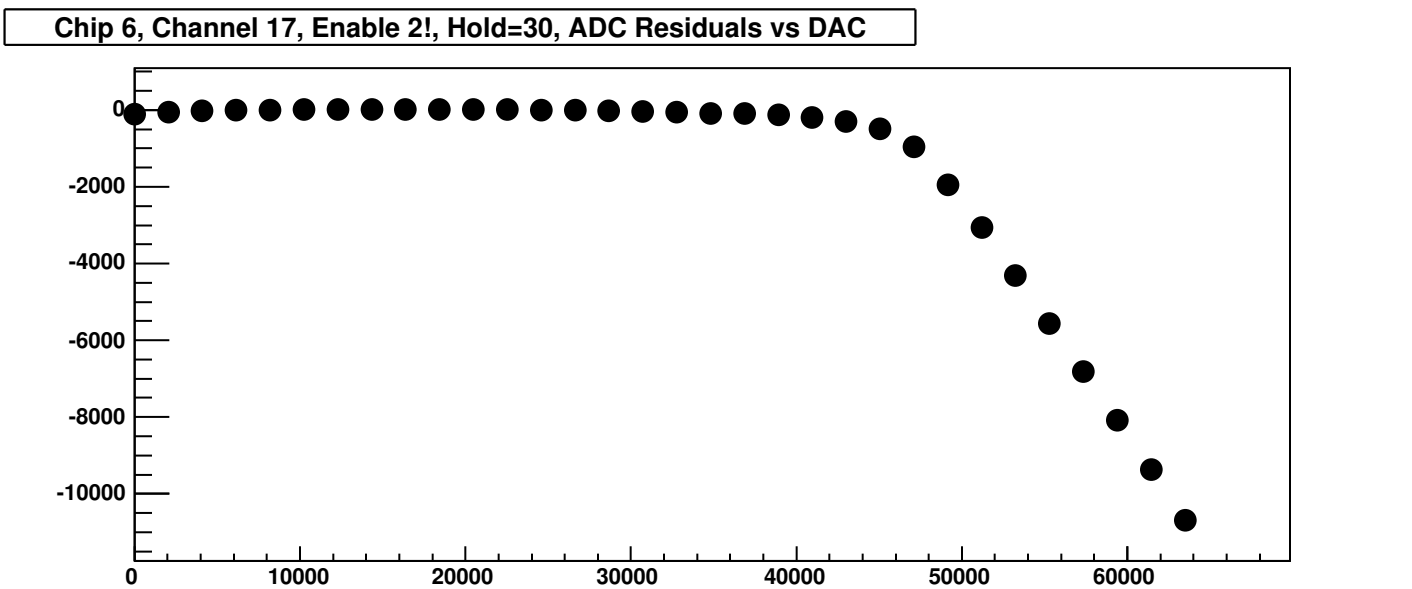


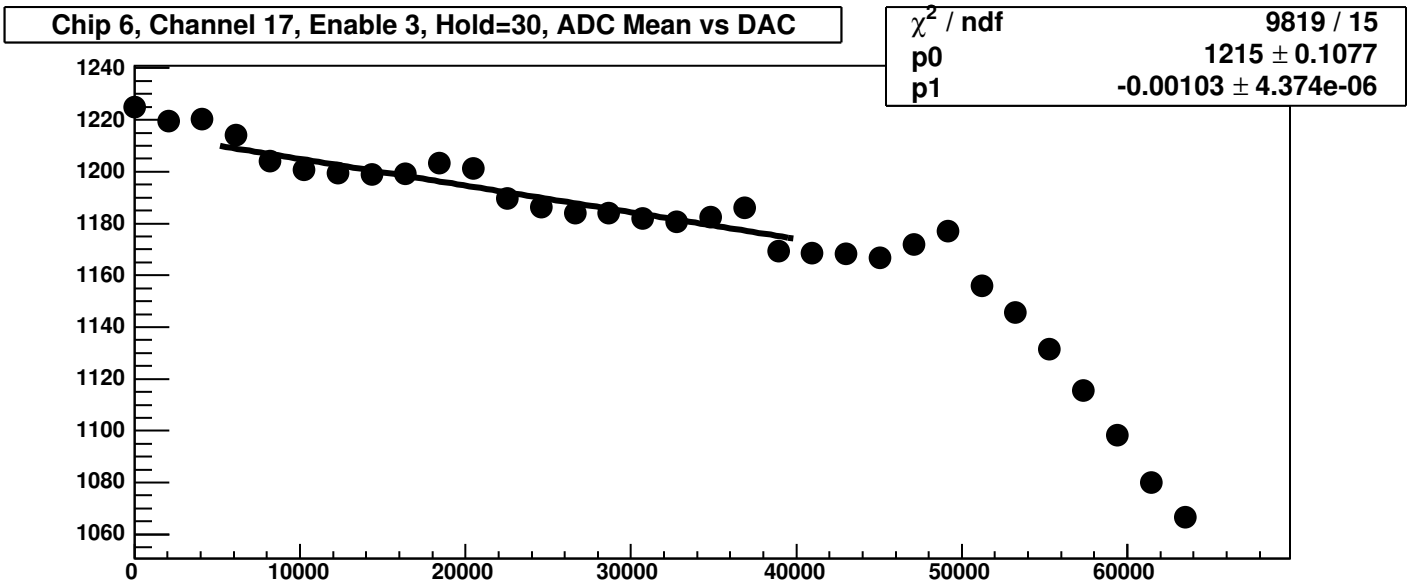
Chip 6, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

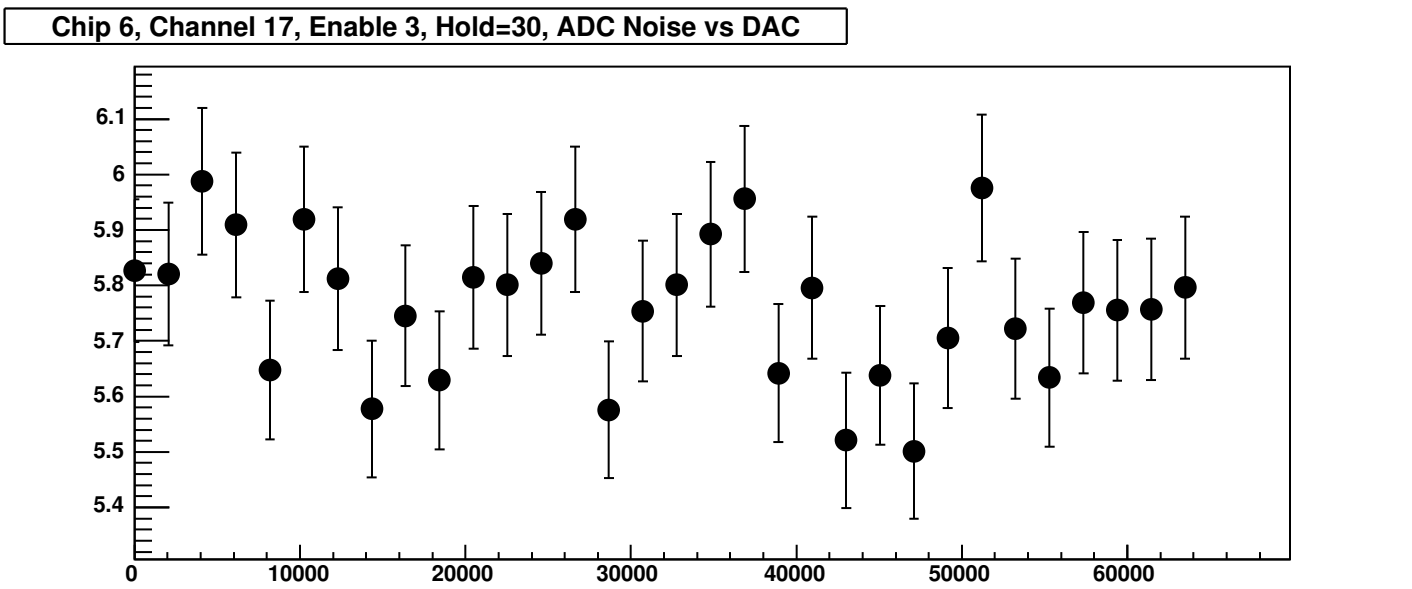


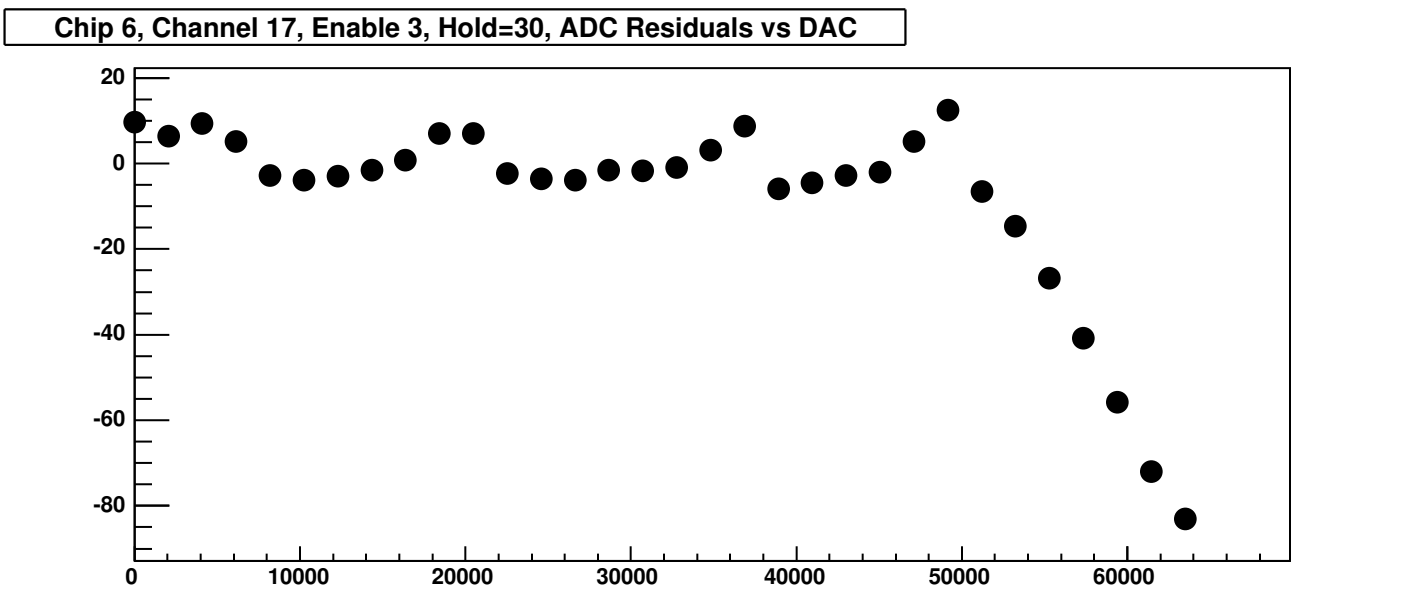

Chip 6, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 8854 / 15
p0 1323 ± 0.2751
p1 0.6194 ± 2.799e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

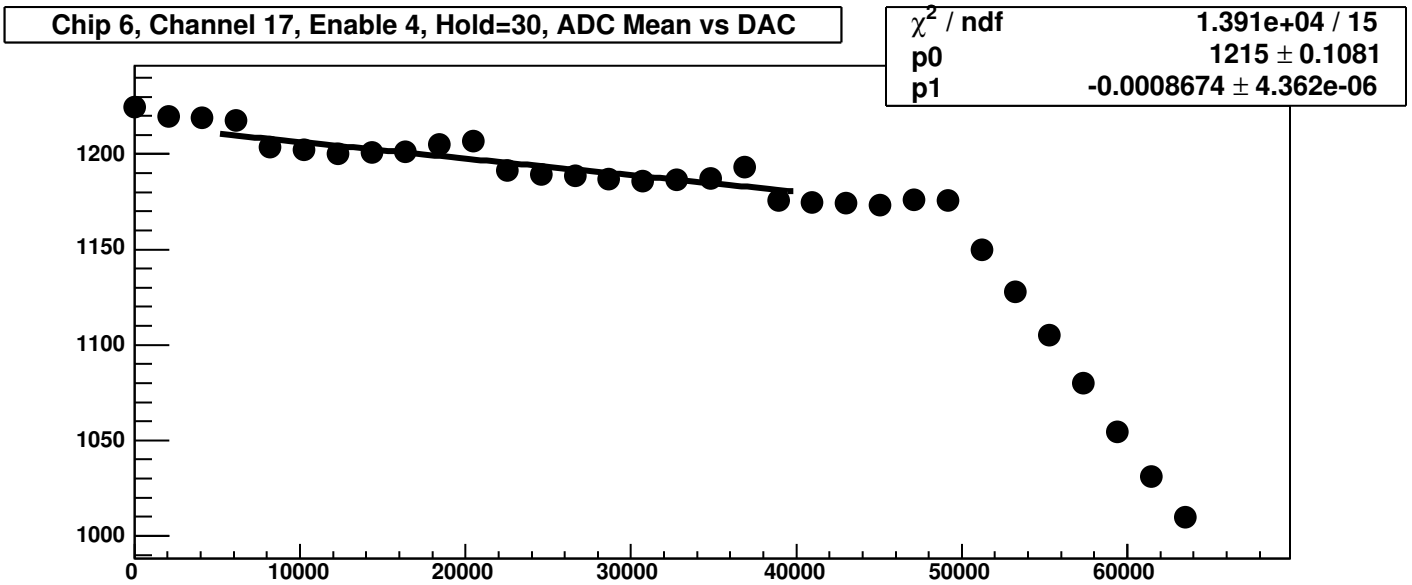

Chip 6, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

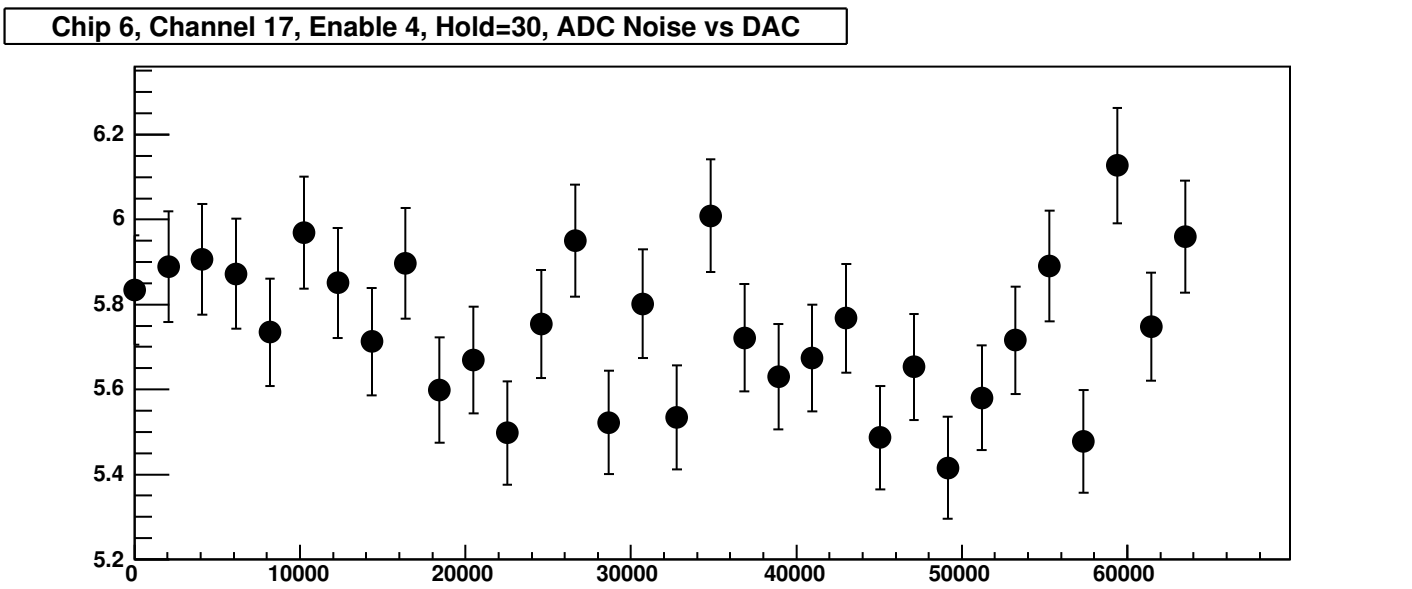

Chip 6, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

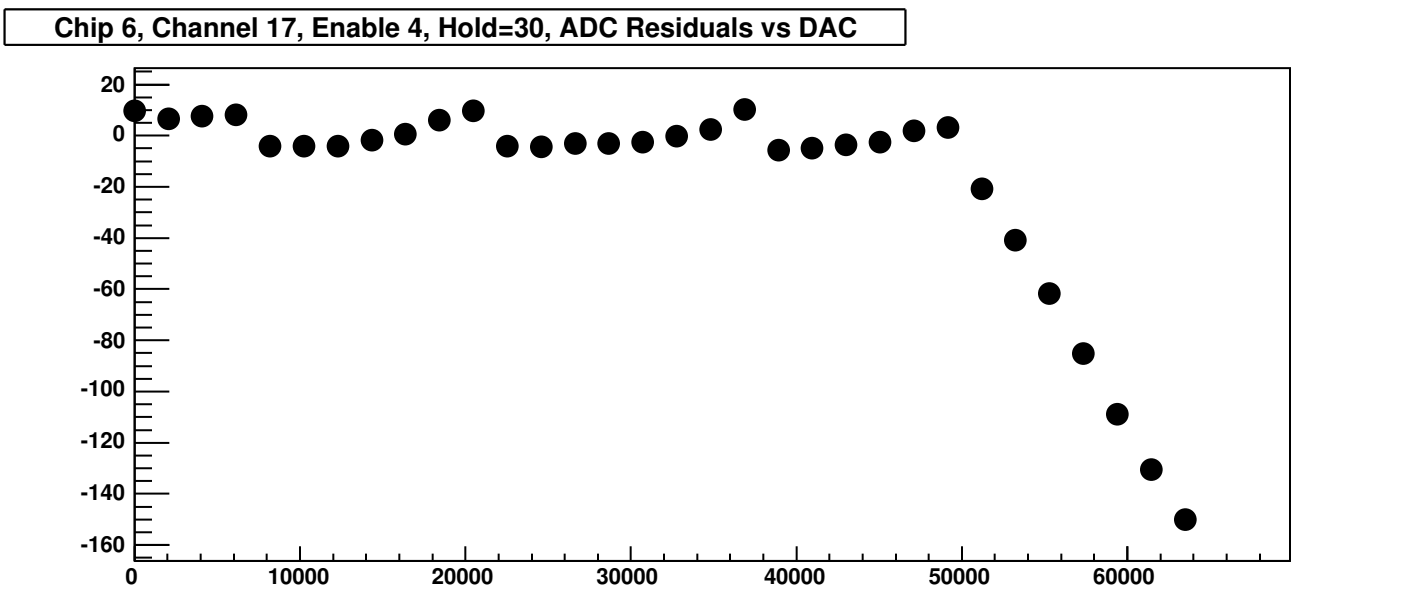

Chip 6, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9819 / 15
p0 1215 ± 0.1077
p1 -0.00103 ± 4.374e-06
1240
1220
1200
1180
1160
1140
1120
1100
1080
1060
0
10000
20000
30000
40000
50000
60000

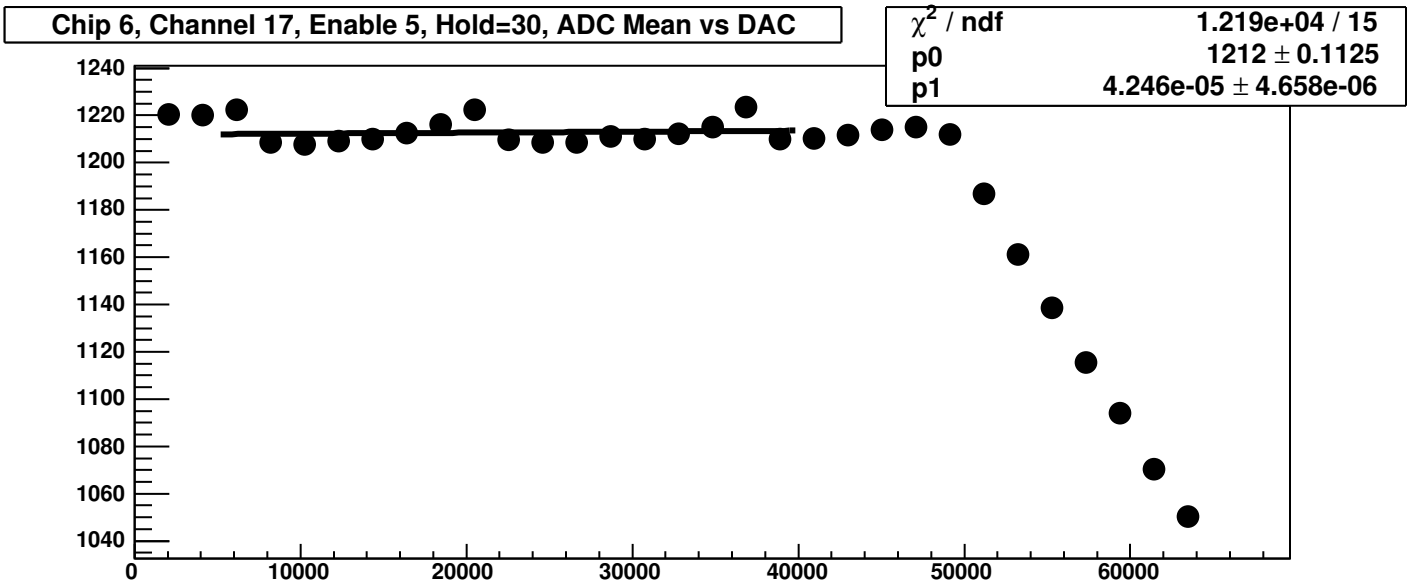

Chip 6, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

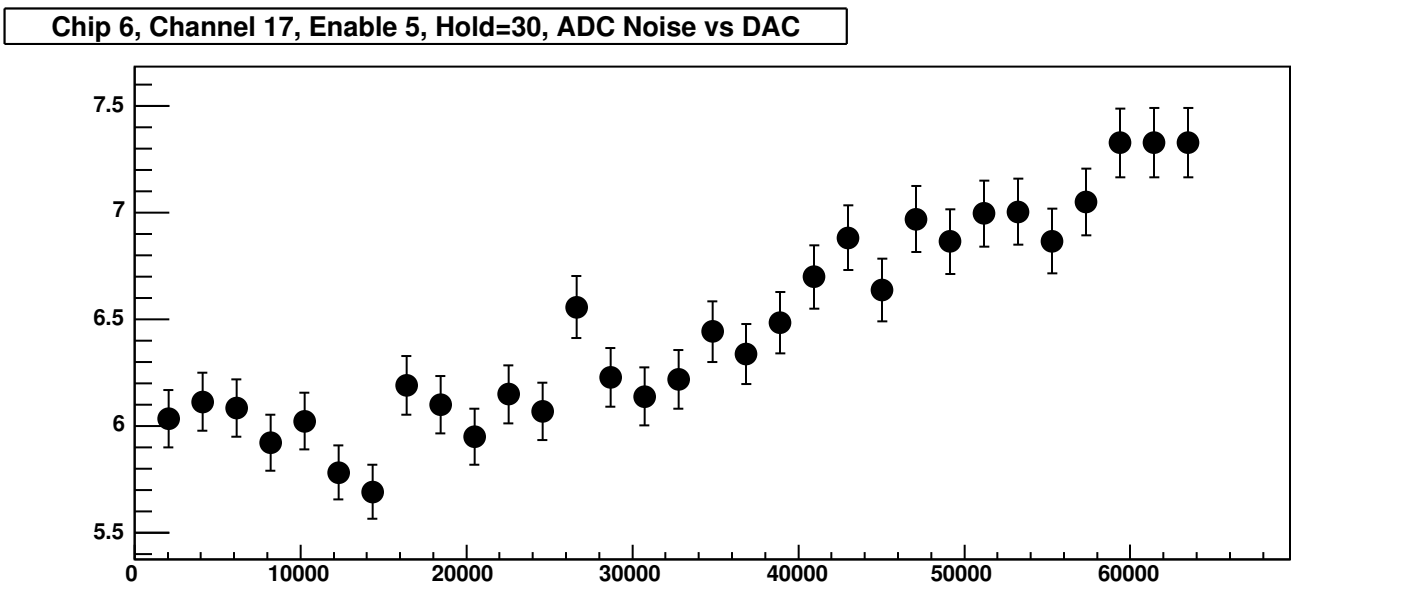

Chip 6, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

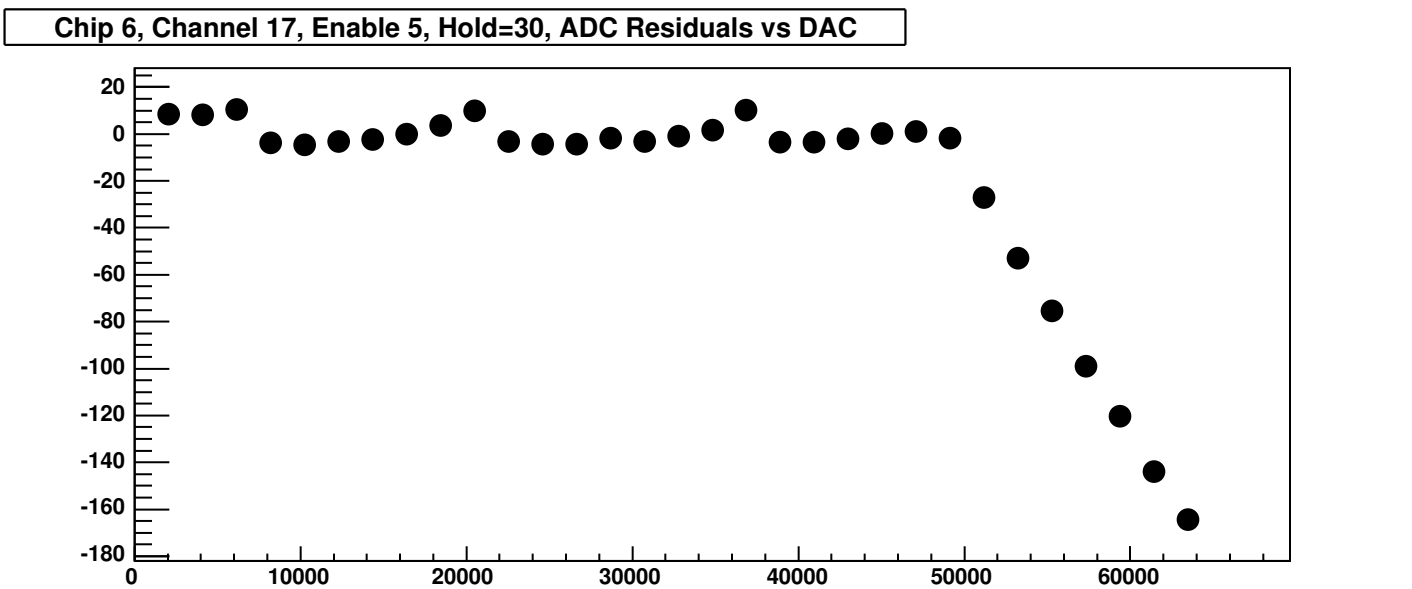

Chip 6, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.391e+04 / 15
p0 1215 ± 0.1081
p1 -0.0008674 ± 4.362e-06
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

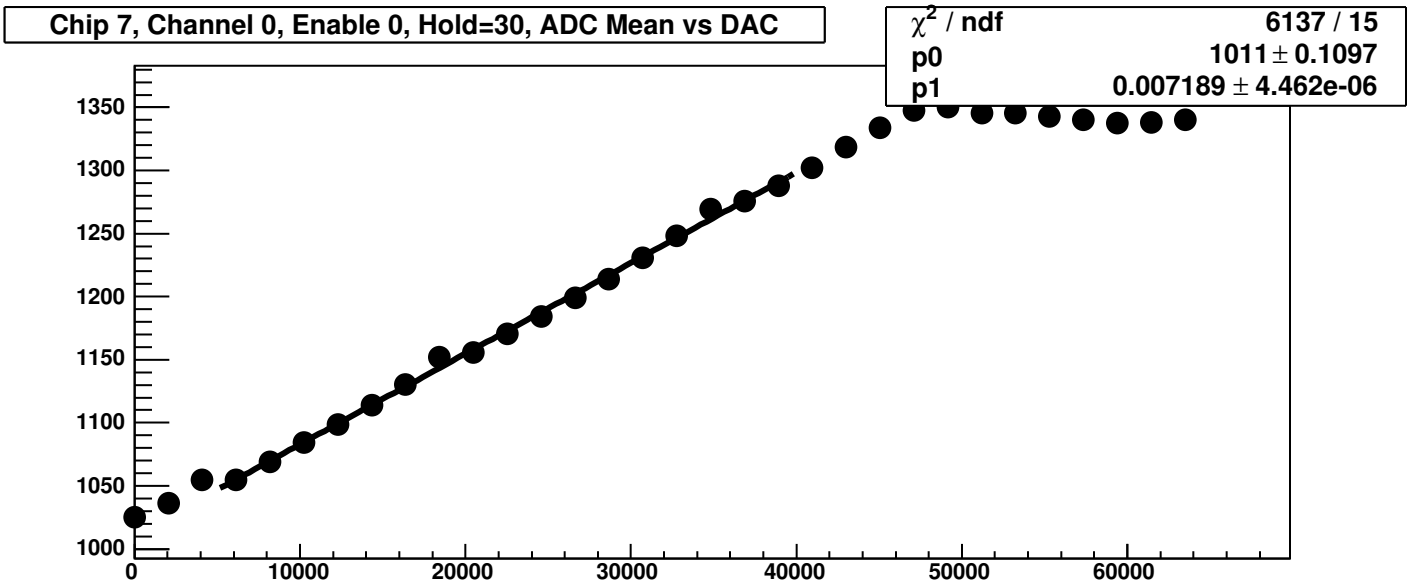

Chip 6, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

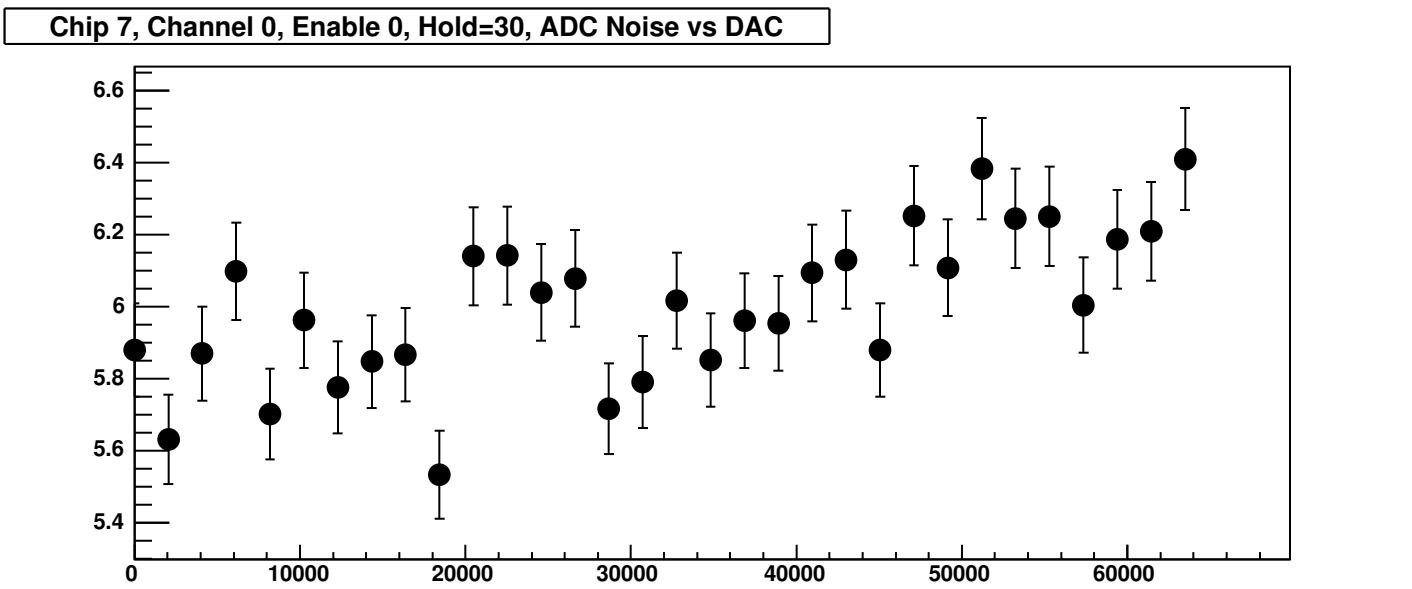

Chip 6, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

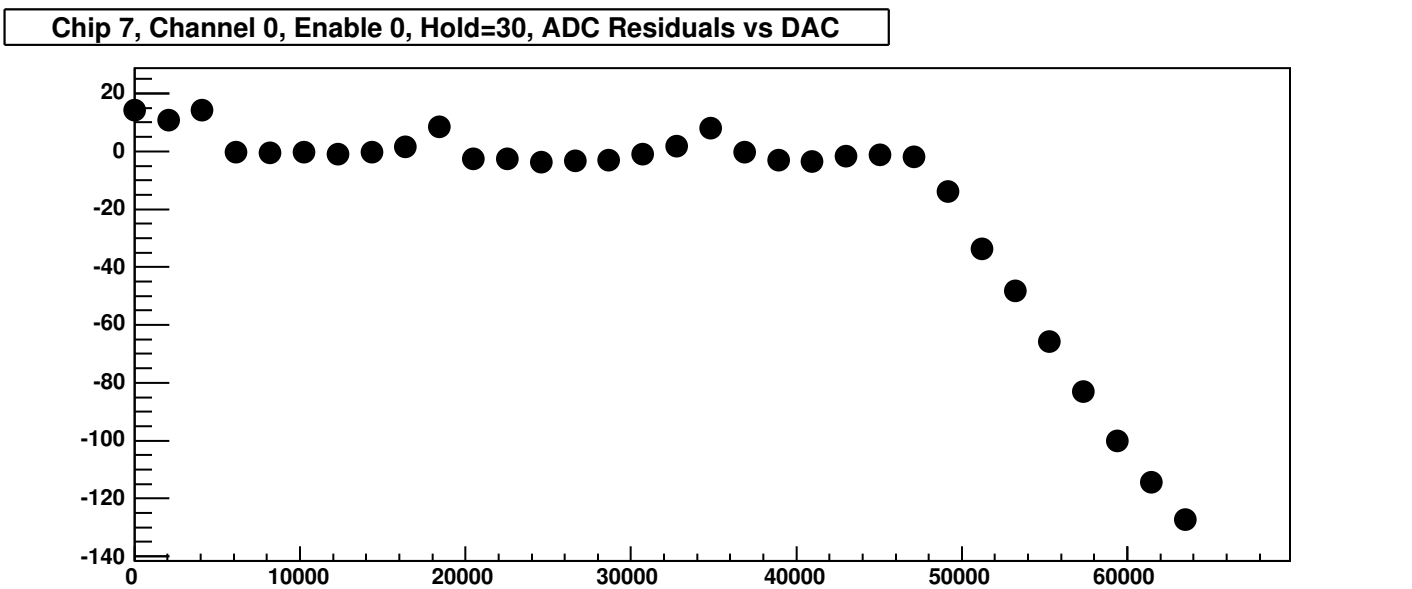

Chip 6, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.219e+04 / 15
p0 1212 ± 0.1125
p1 4.246e-05 ± 4.658e-06
1240
1220
1200
1180
1160
1140
1120
1100
1080
1060
1040
0
10000
20000
30000
40000
50000
60000


Chip 6, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000

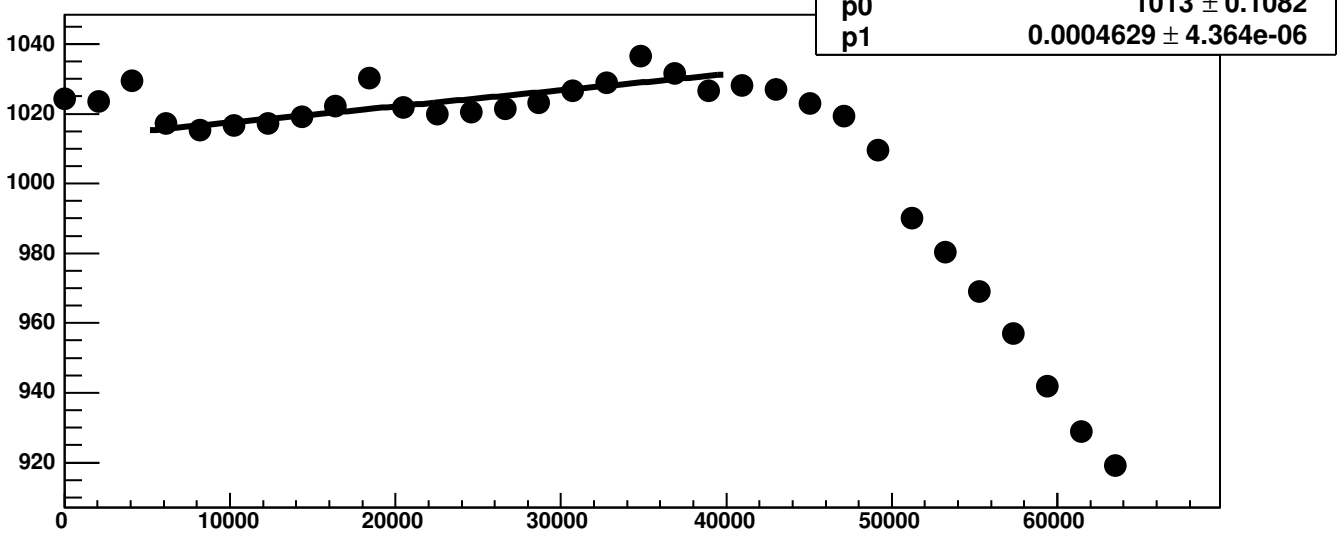

Chip 6, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6137 / 15
p0 1011 ± 0.1097
p1 0.007189 ± 4.462e-06
1350
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

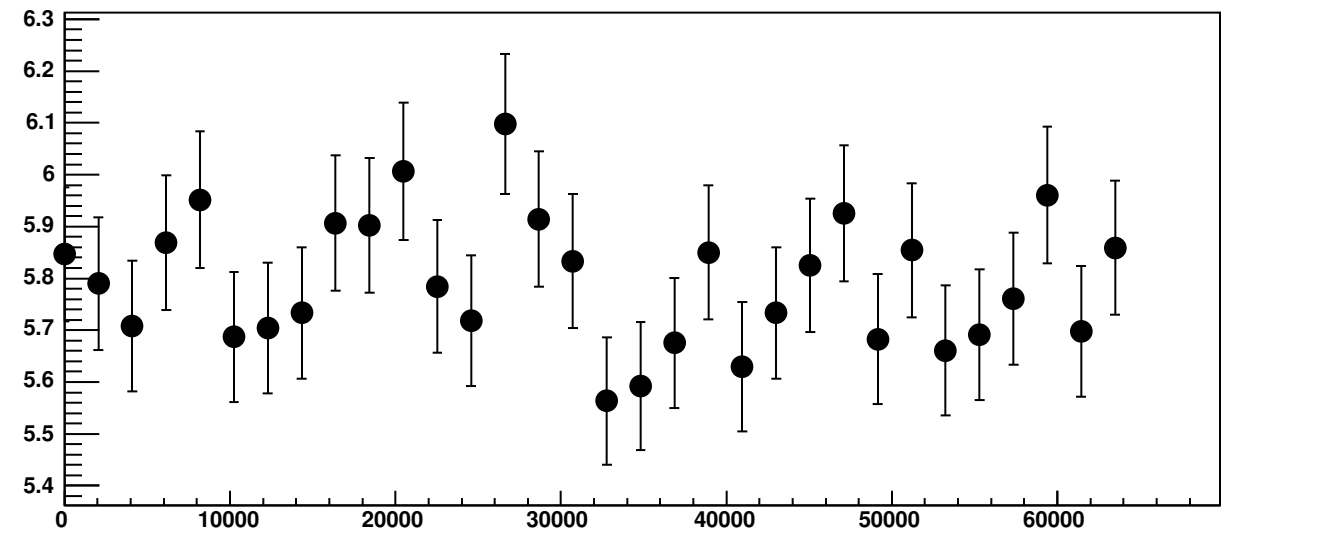

Chip 7, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

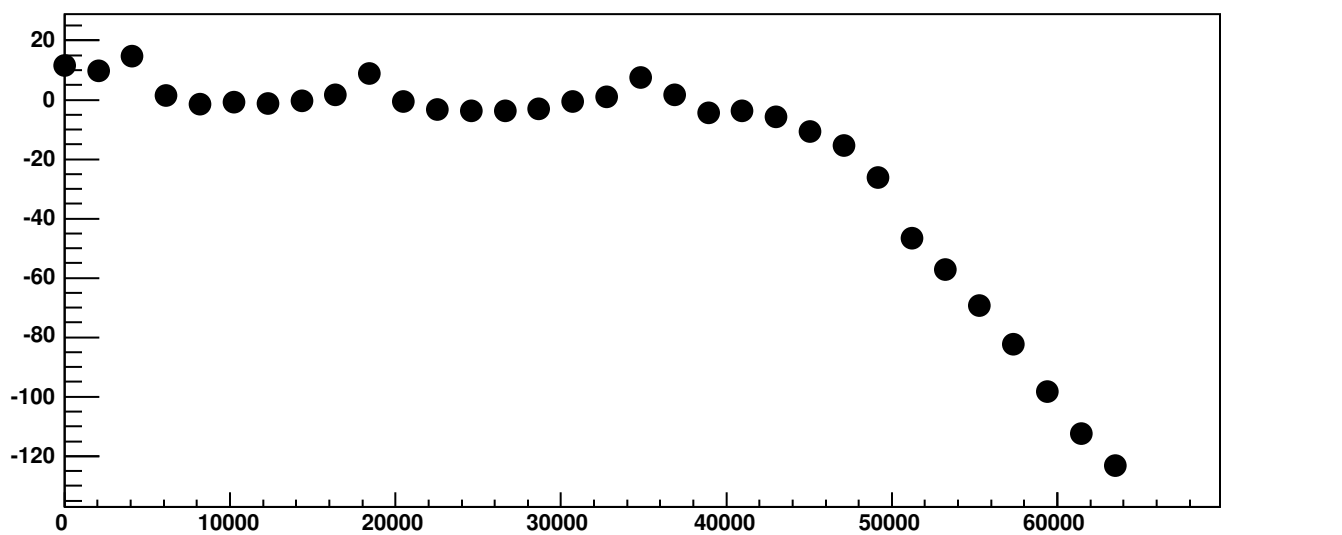
Chip 7, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC



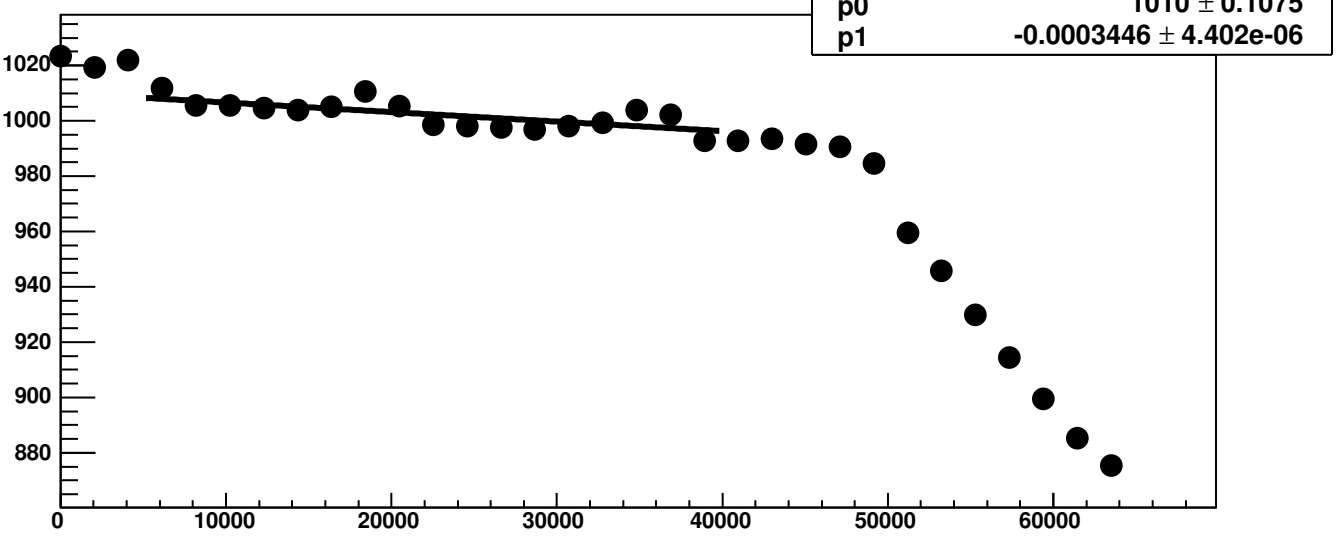
Chip 7, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC



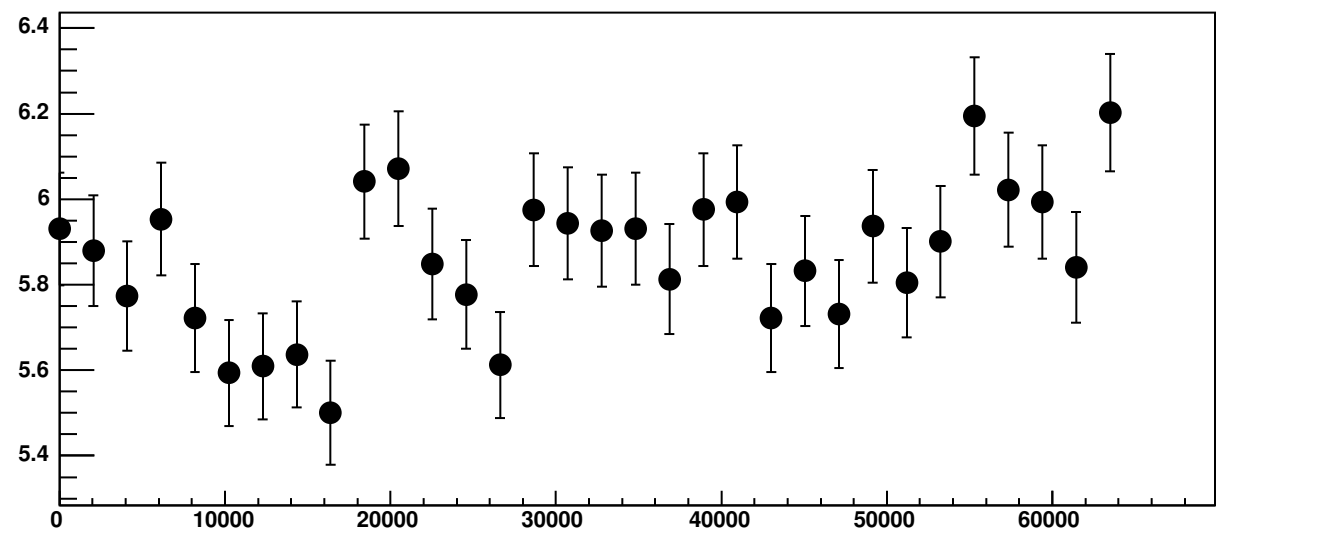
Chip 7, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC



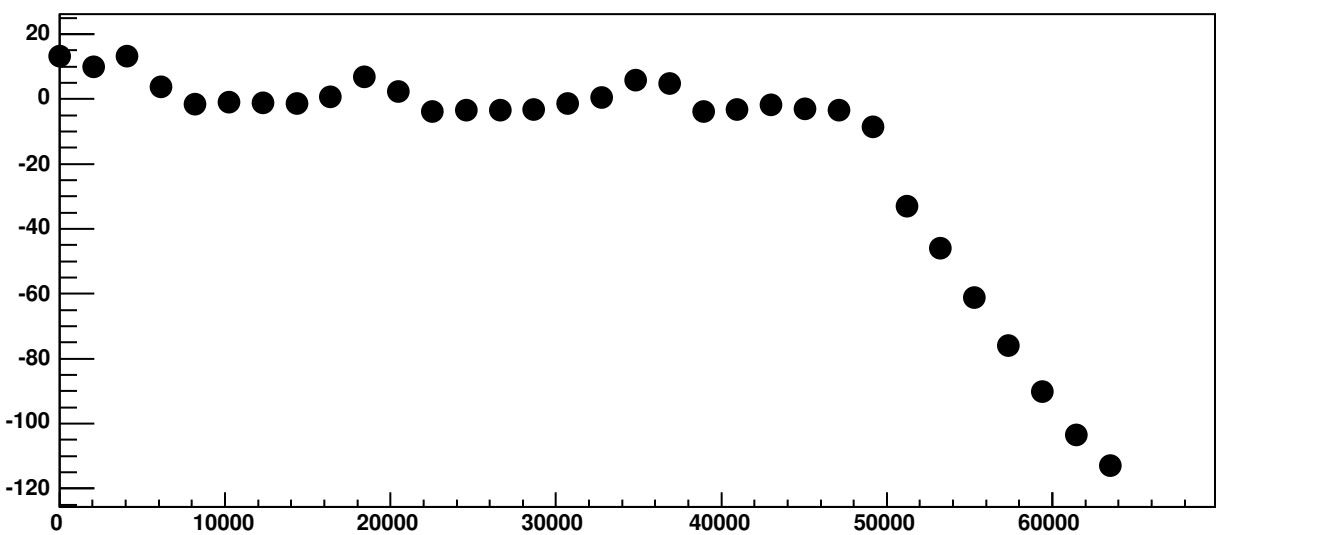
Chip 7, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC

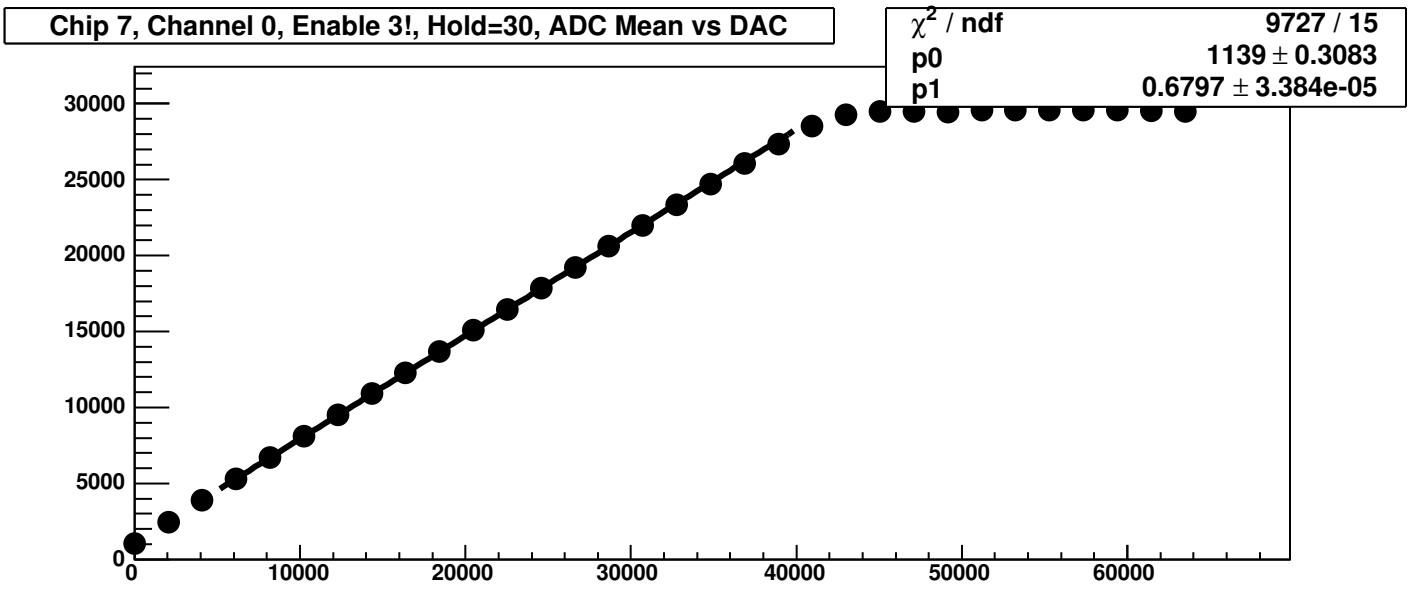


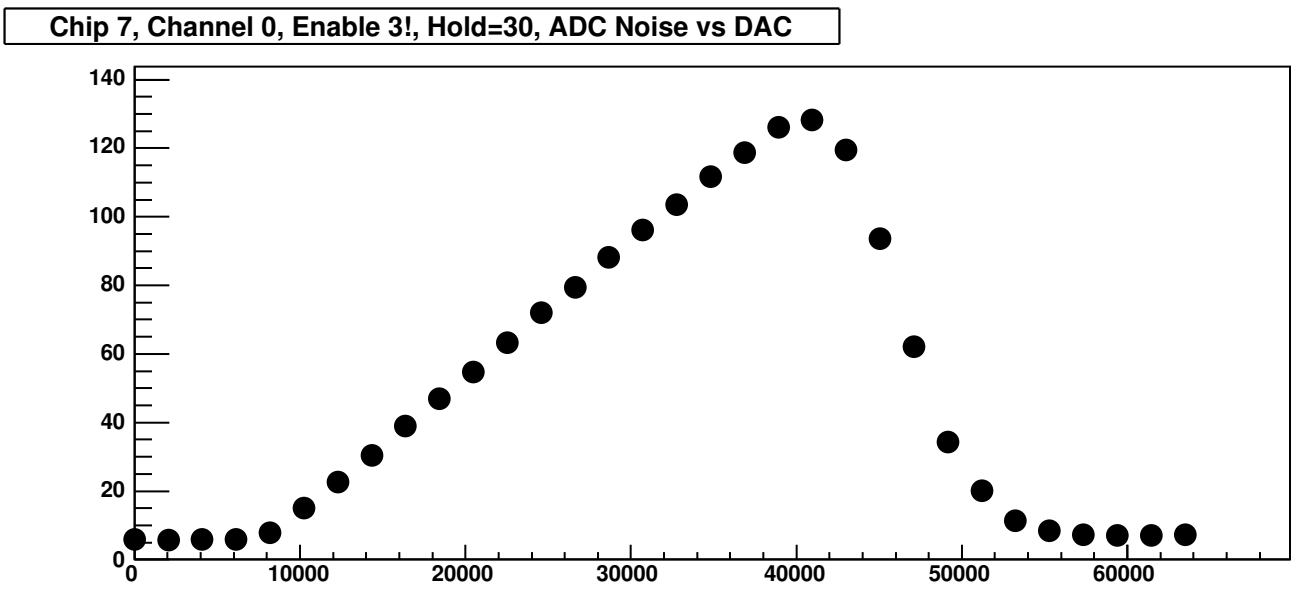
Chip 7, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC

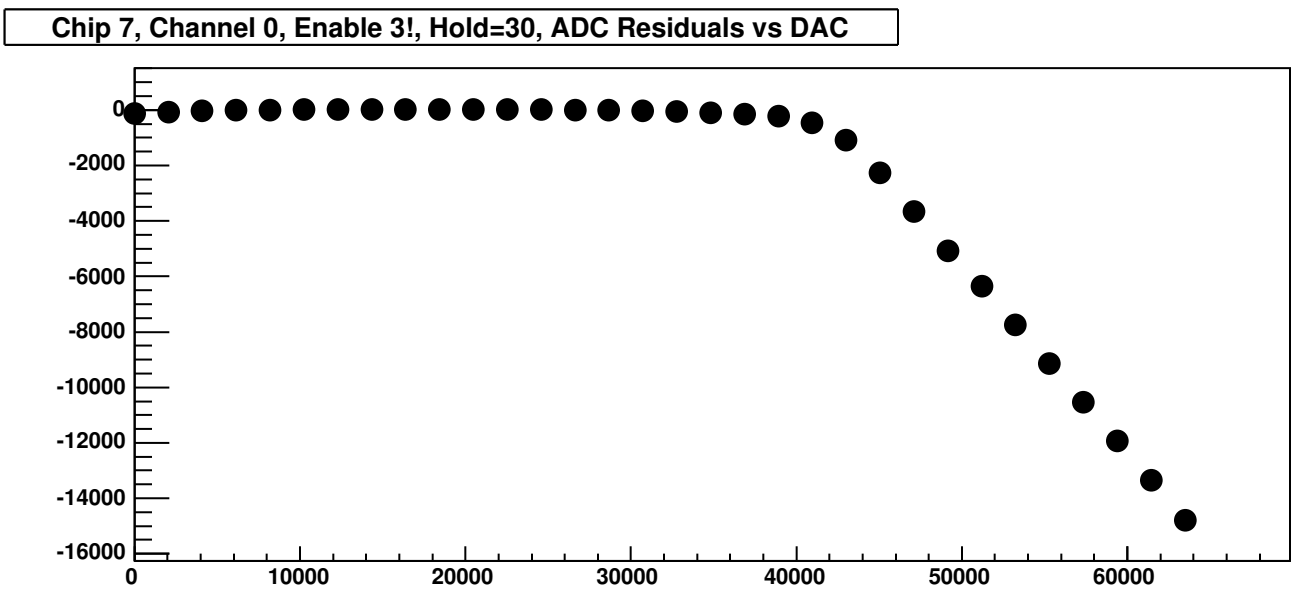


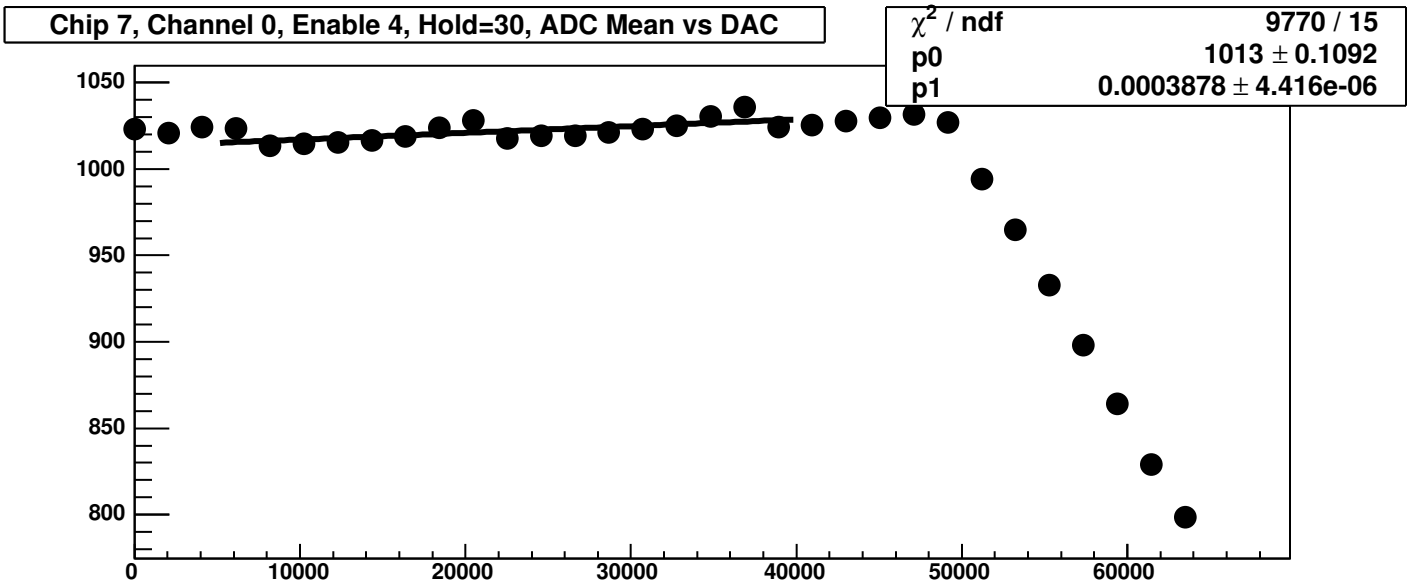
Chip 7, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

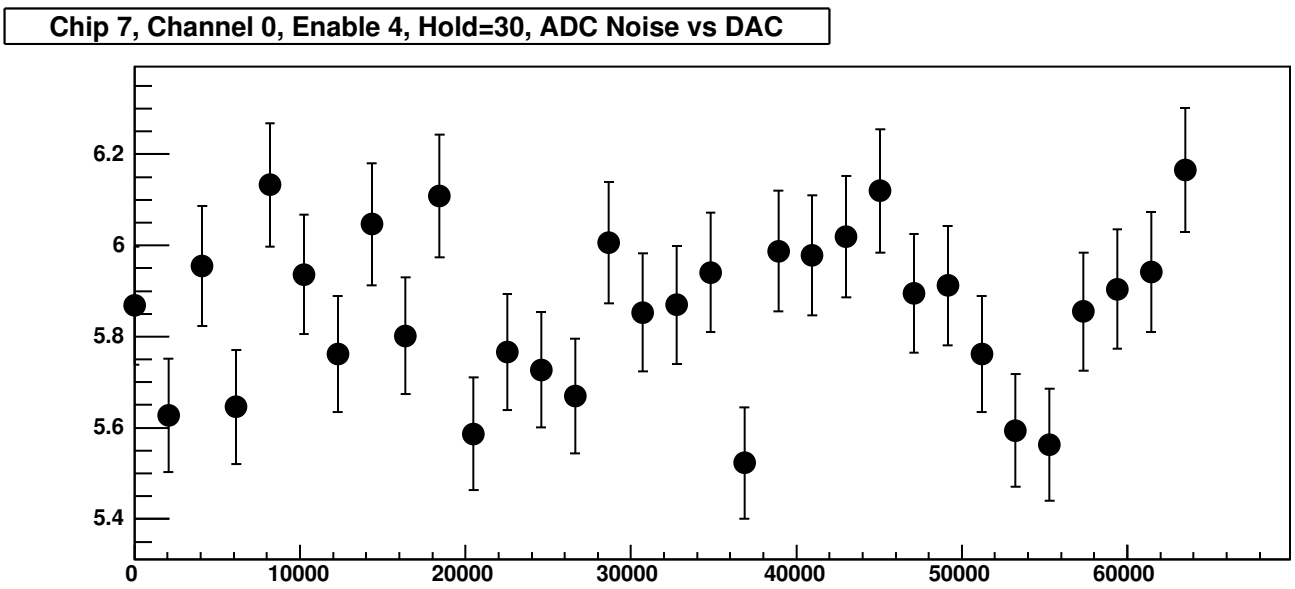


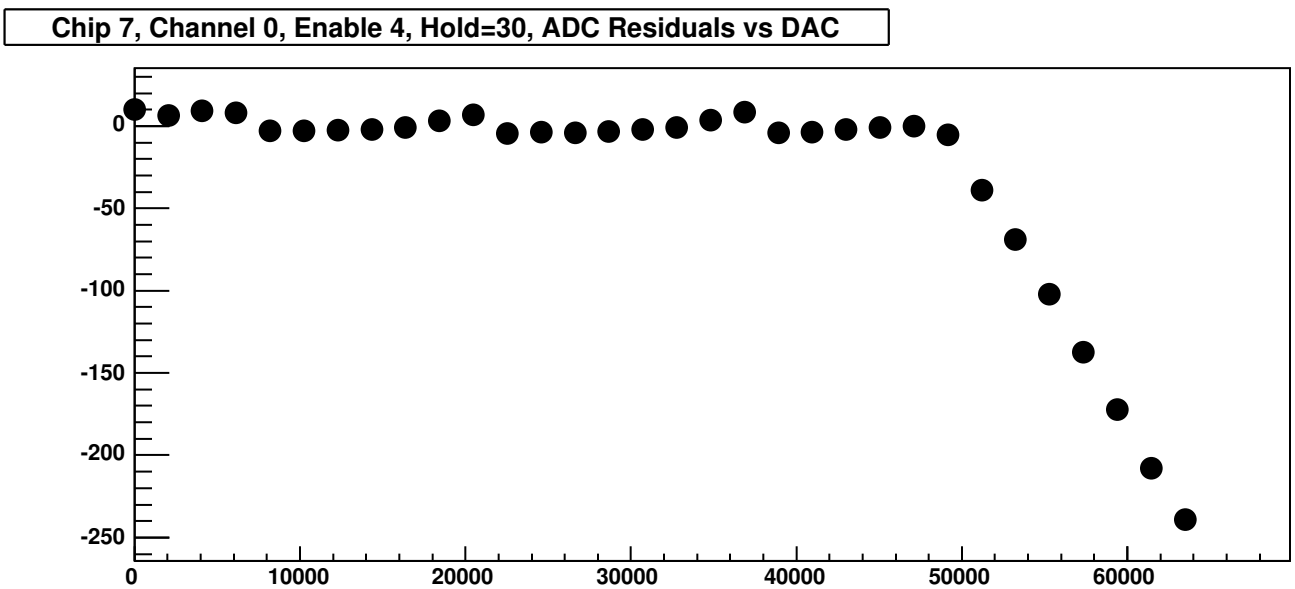

Chip 7, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 9727 / 15
p0 1139 ± 0.3083
p1 0.6797 ± 3.384e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

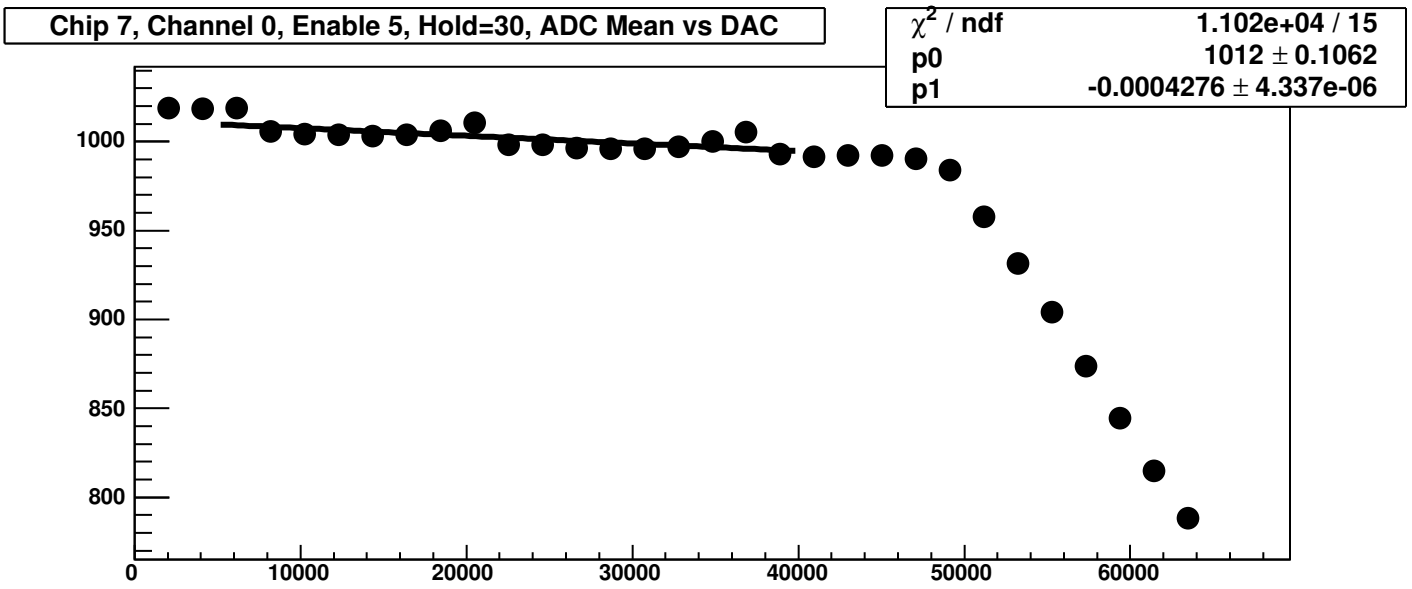

Chip 7, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

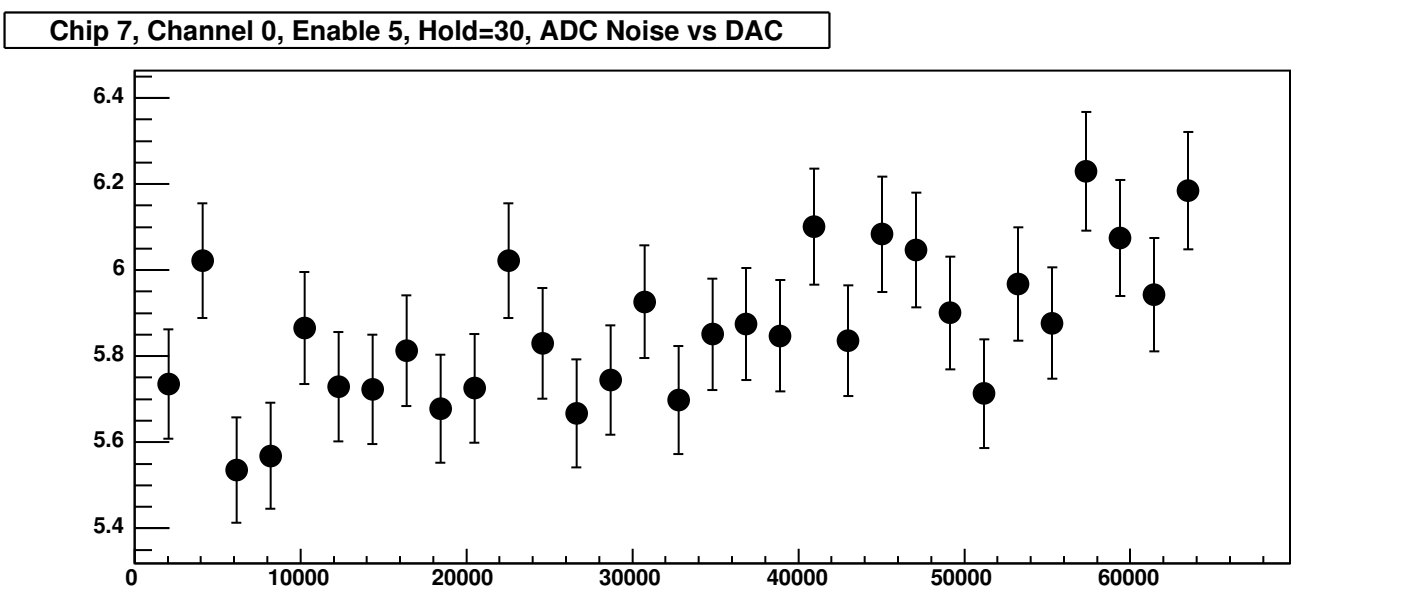

Chip 7, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
10000
20000
30000
40000
50000
60000

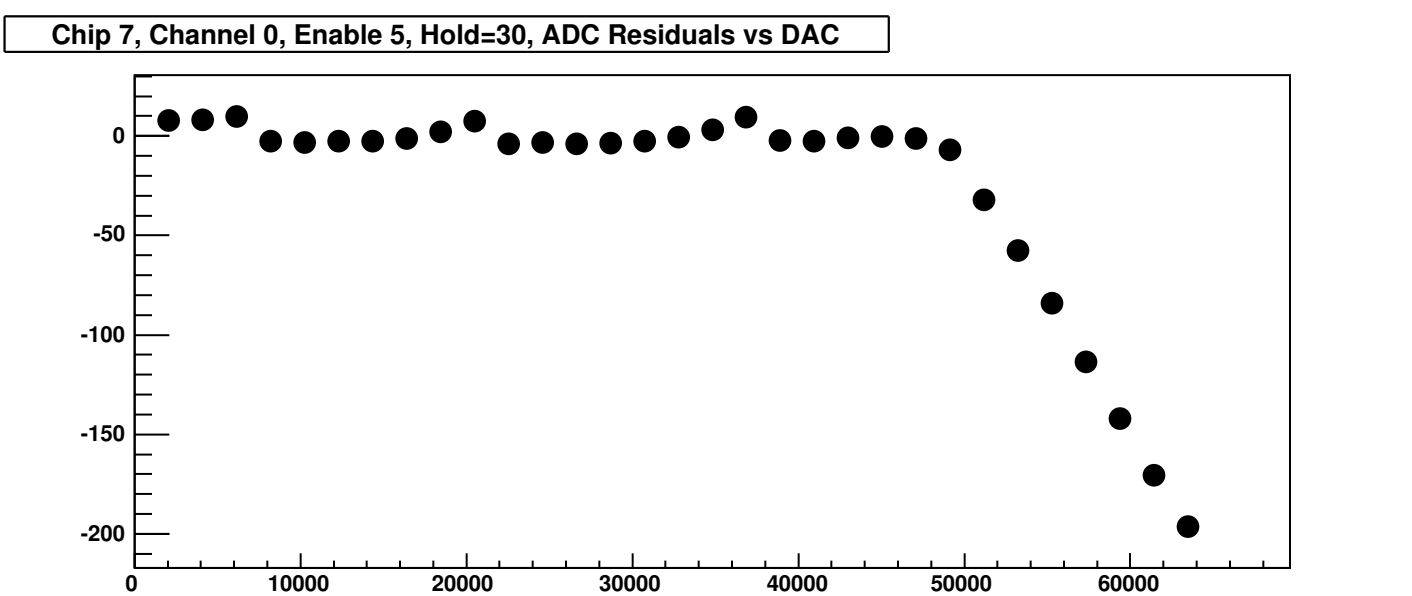

Chip 7, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9770 / 15
p0 1013 ± 0.1092
p1 0.0003878 ± 4.416e-06
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

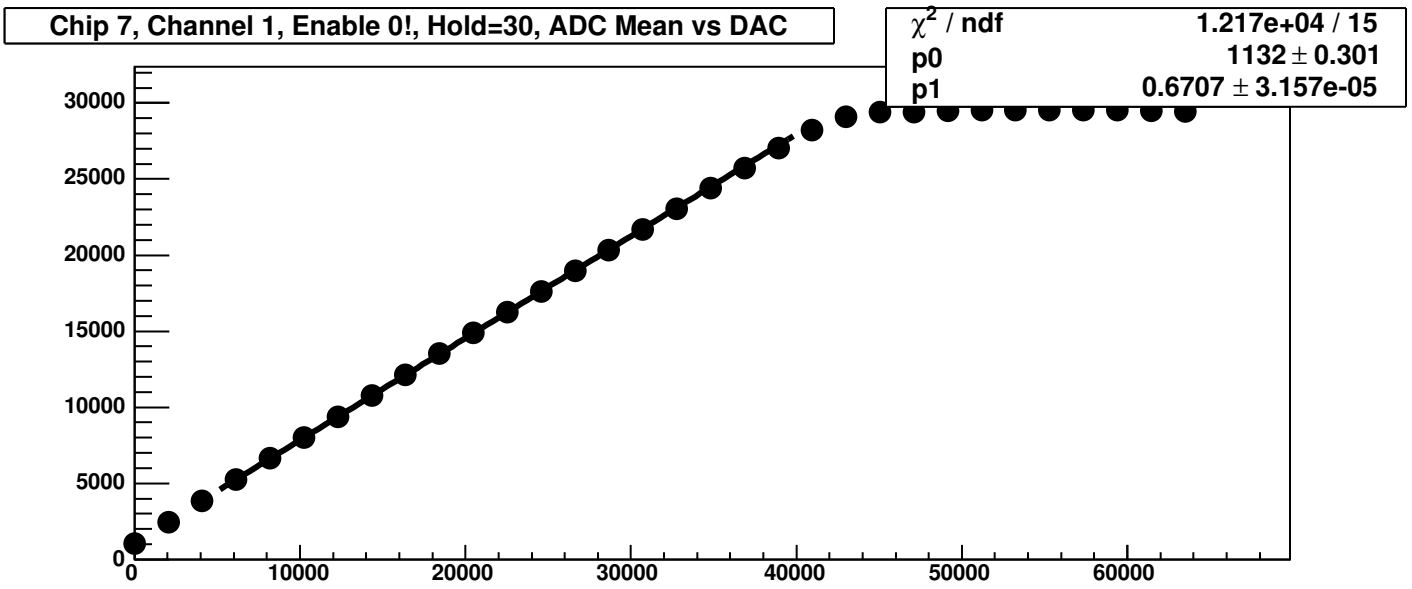

Chip 7, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

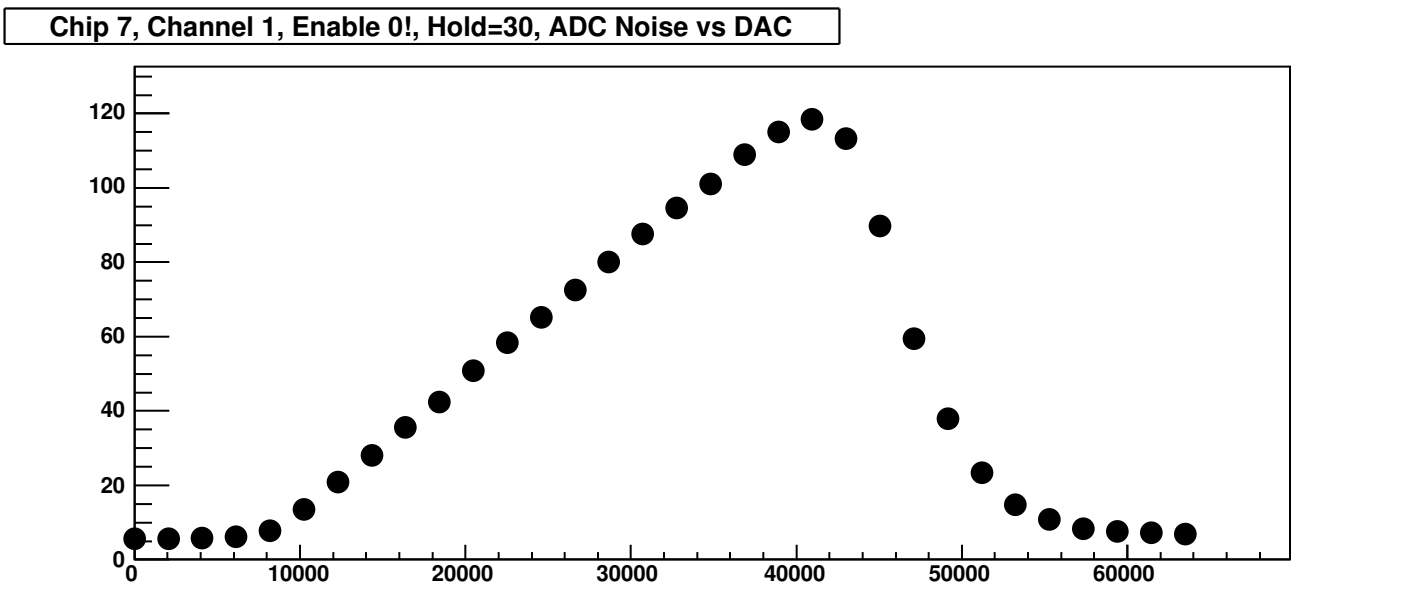

Chip 7, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

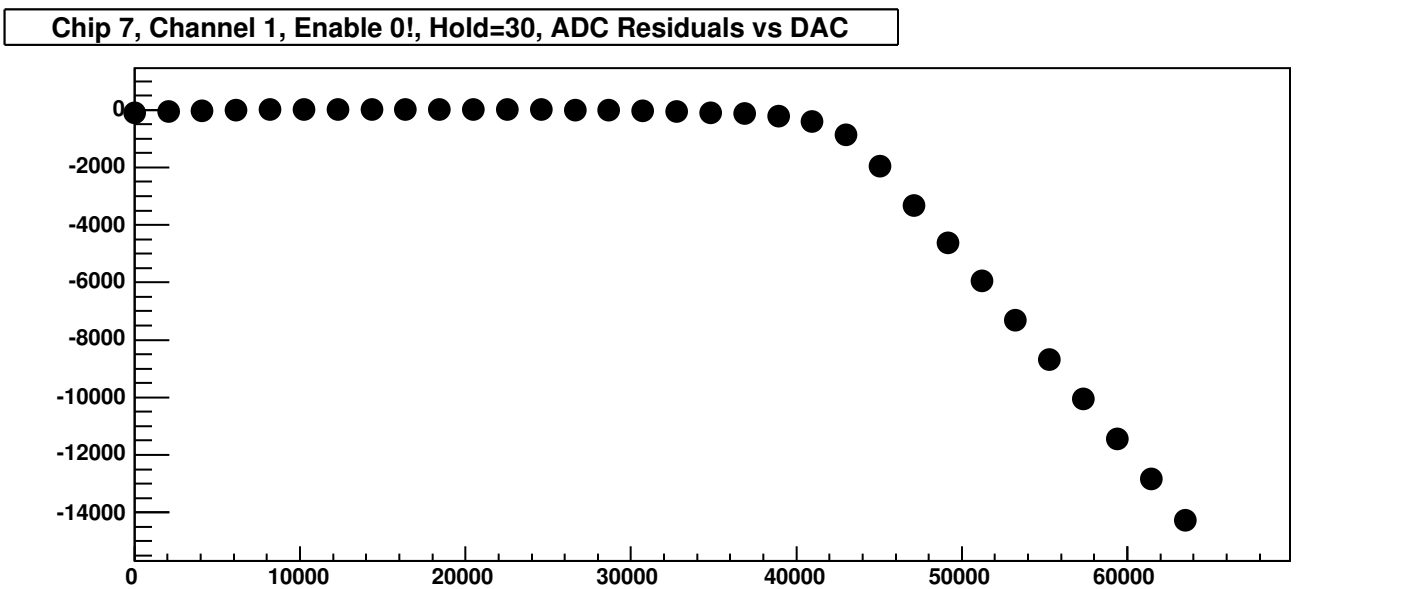

Chip 7, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.102e+04 / 15
p0 1012 ± 0.1062
p1 -0.0004276 ± 4.337e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

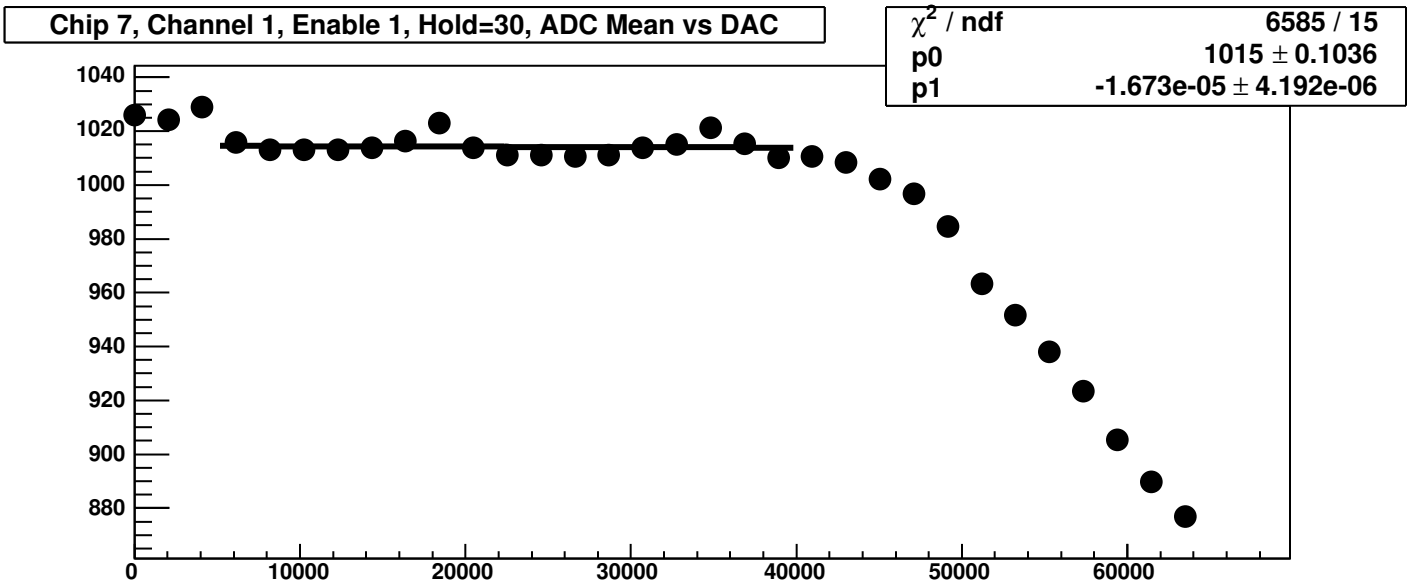

Chip 7, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

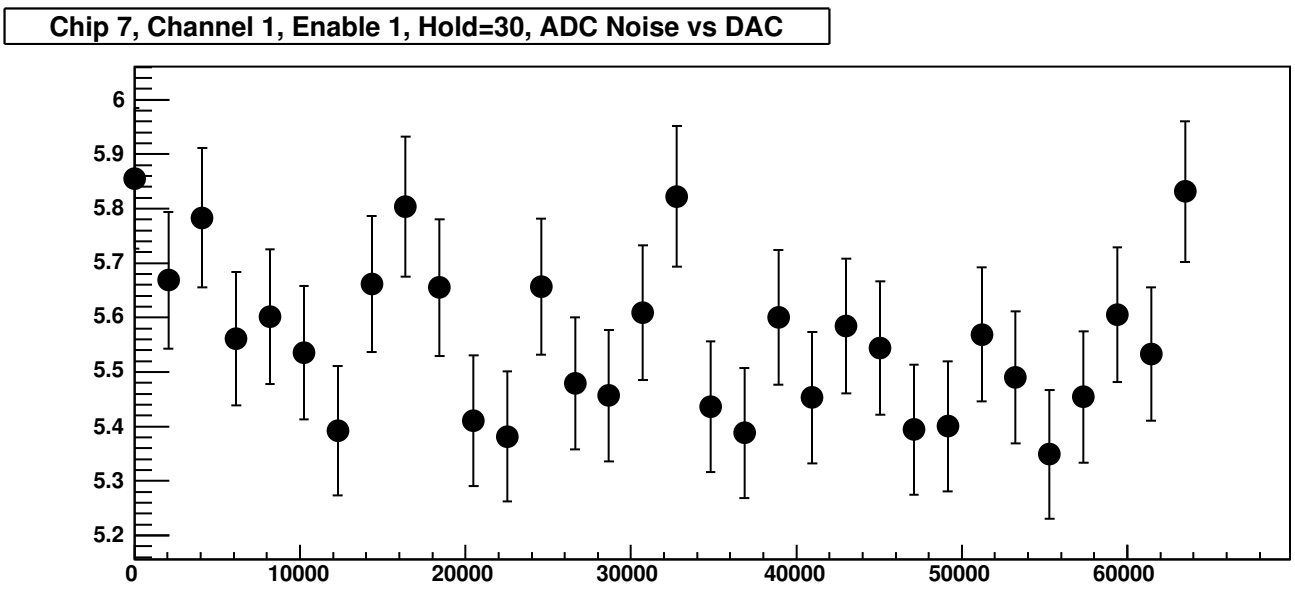

Chip 7, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

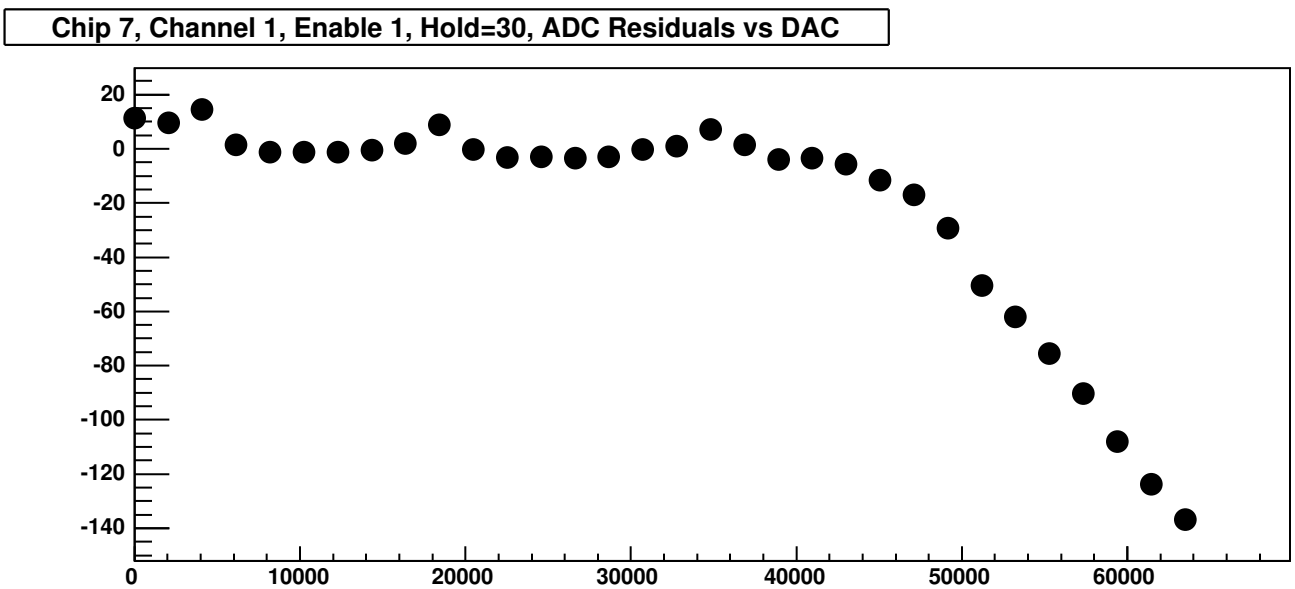

Chip 7, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.217e+04 / 15
p0 1132 ± 0.301
p1 0.6707 ± 3.157e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

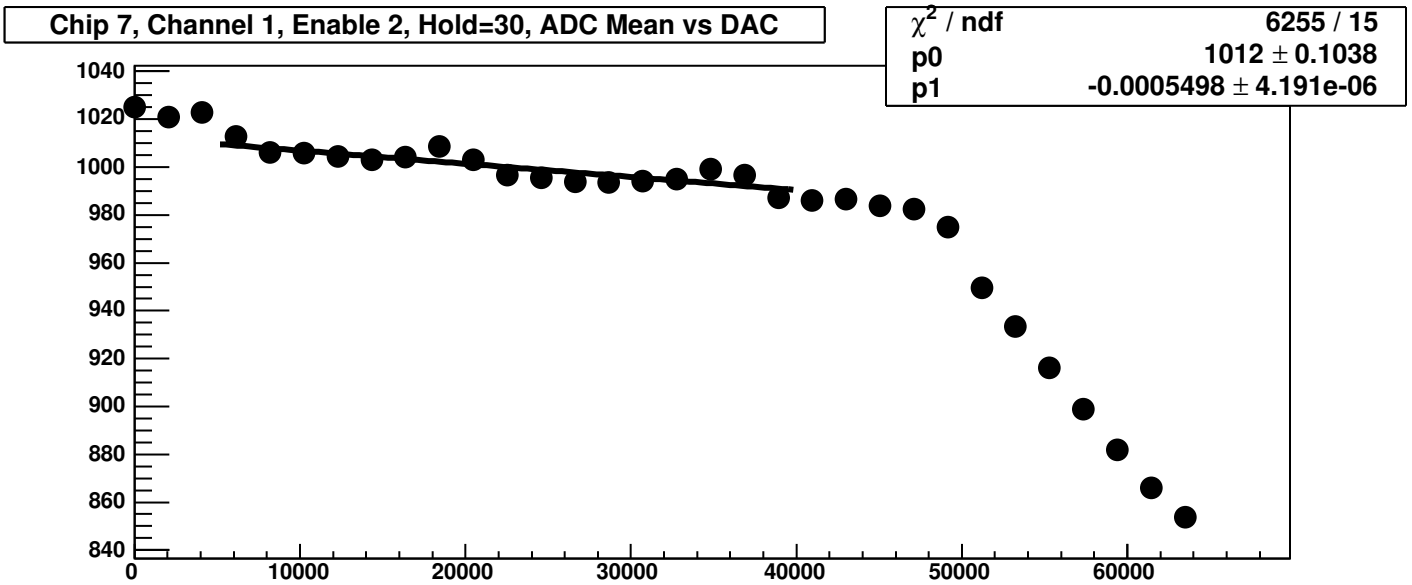

Chip 7, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

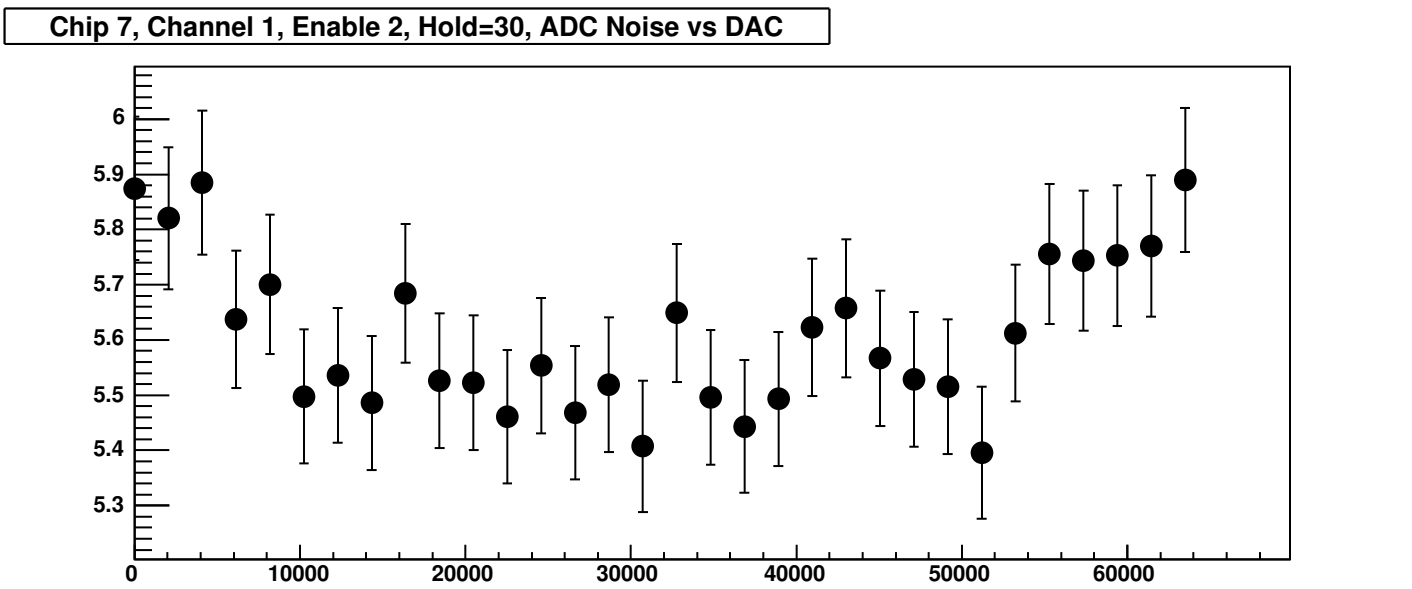

Chip 7, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

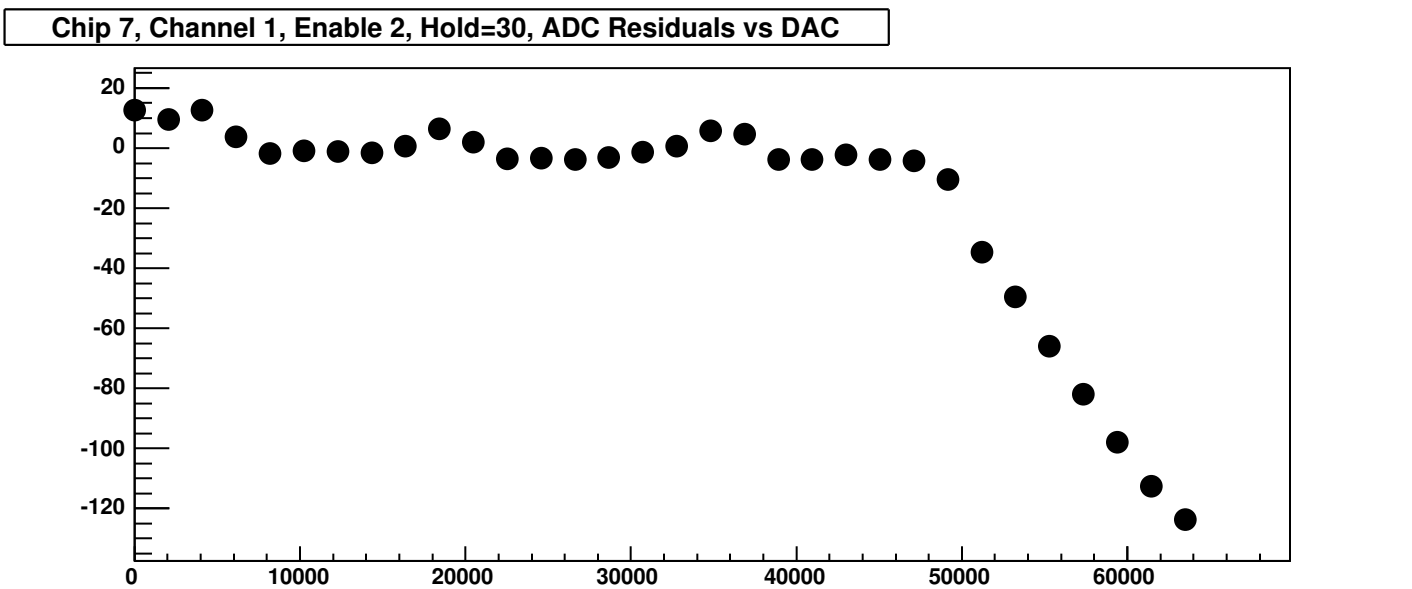

Chip 7, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6585 / 15
p0 1015 ± 0.1036
p1 -1.673e-05 ± 4.192e-06
1040
1020
1000
980
960
940
920
900
880
0
10000
20000
30000
40000
50000
60000

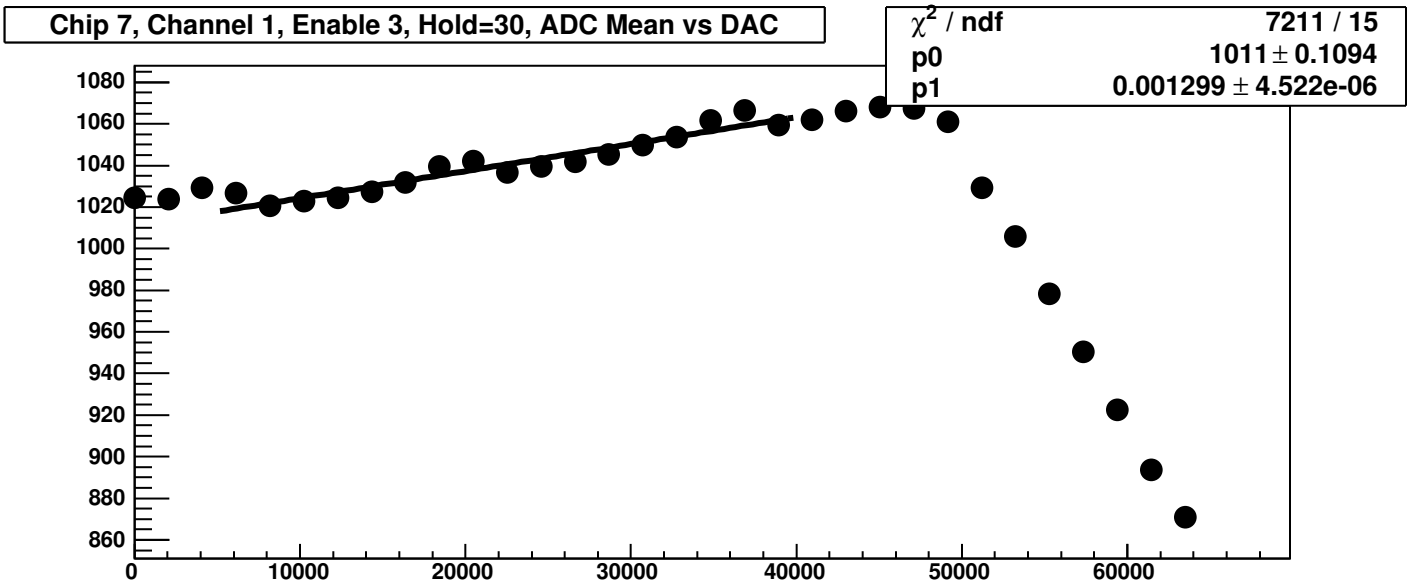

Chip 7, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
0
10000
20000
30000
40000
50000
60000

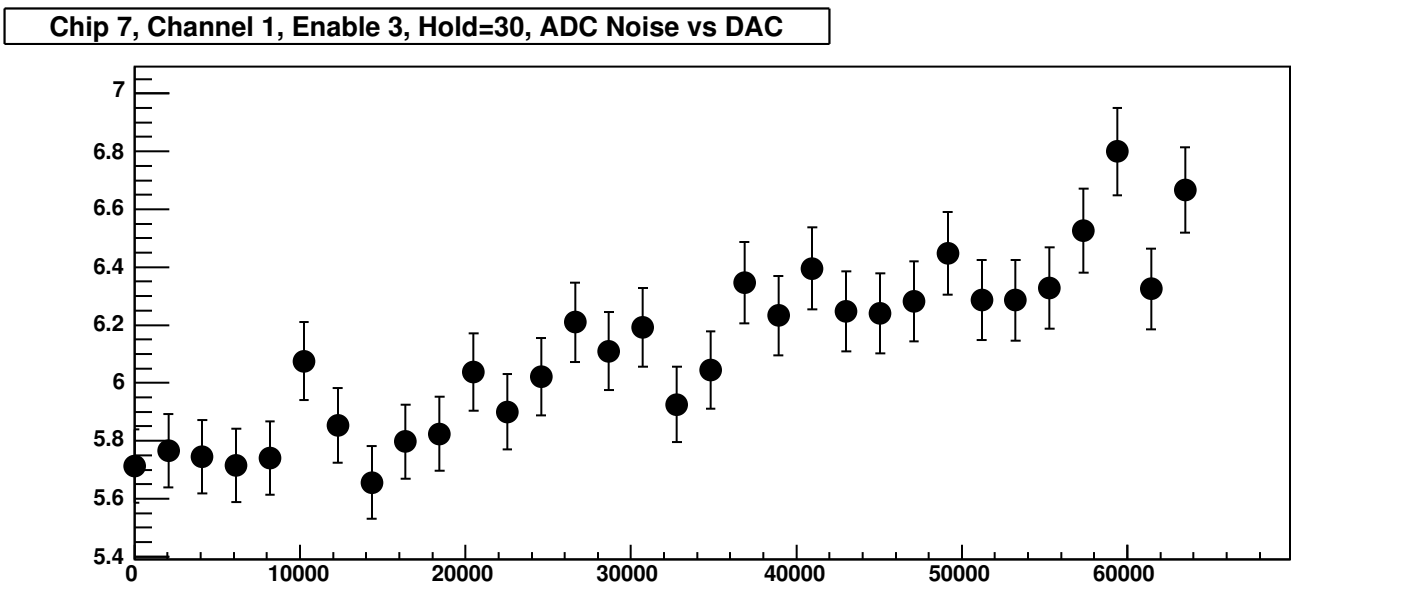

Chip 7, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

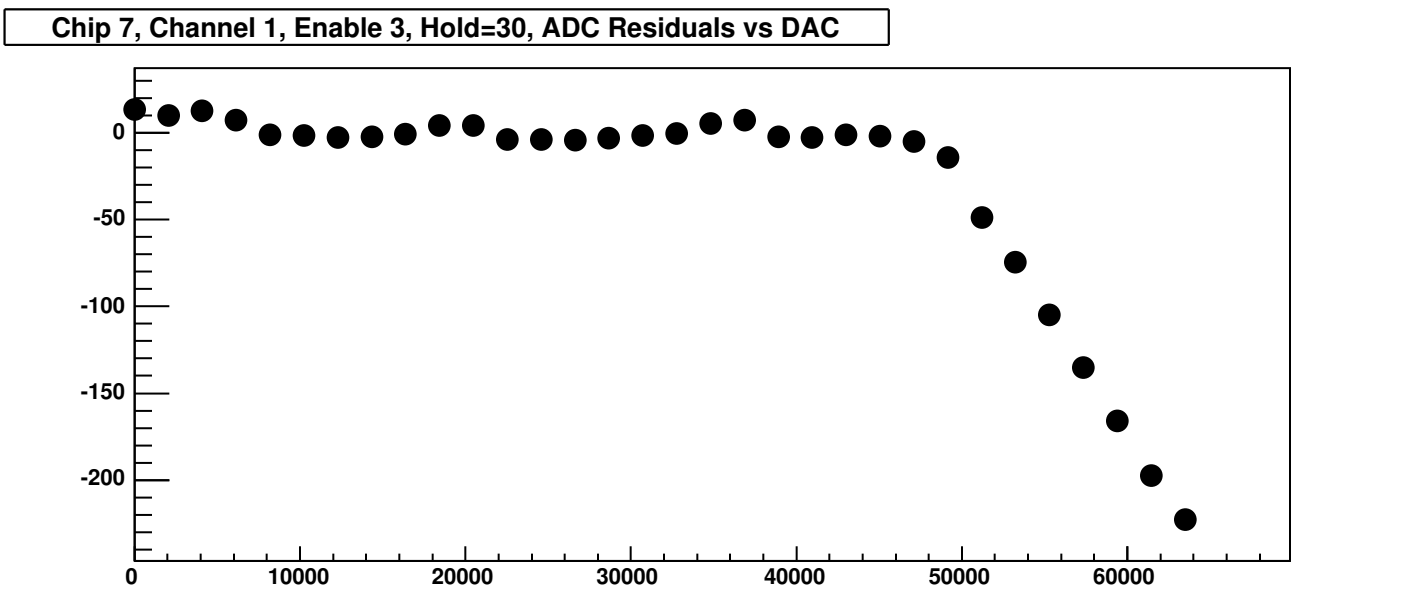

Chip 7, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6255 / 15
p0 1012 ± 0.1038
p1 -0.0005498 ± 4.191e-06
1040
1020
1000
980
960
940
920
900
880
860
840
0
10000
20000
30000
40000
50000
60000

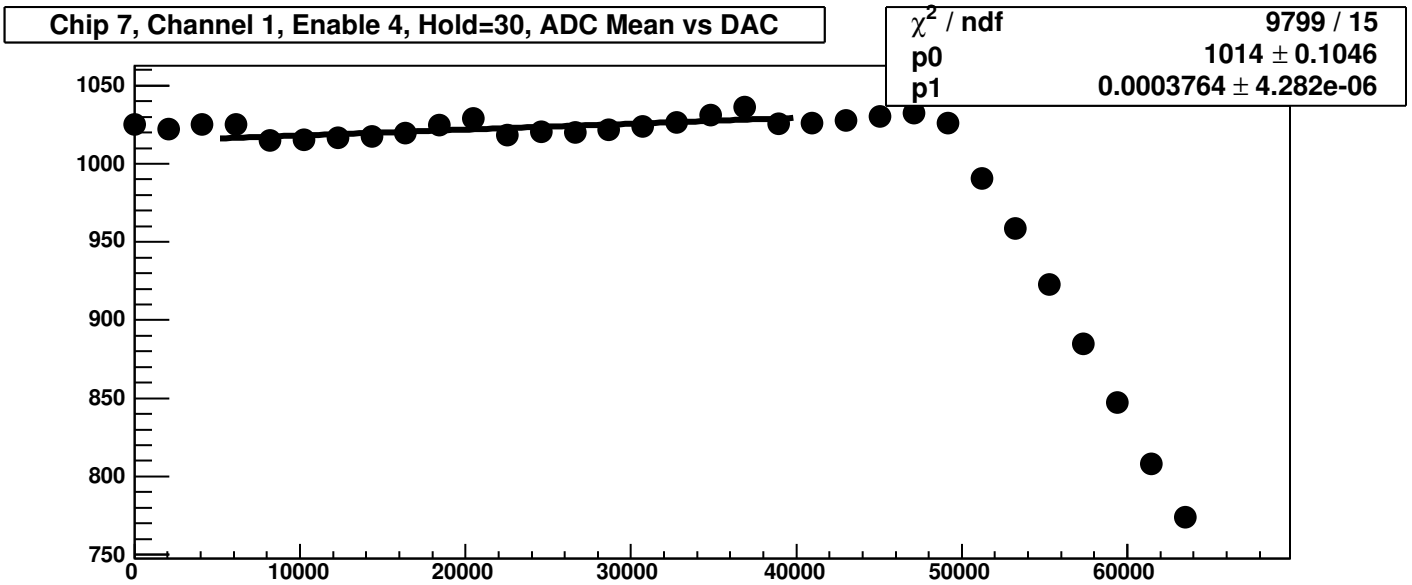

Chip 7, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
0
10000
20000
30000
40000
50000
60000

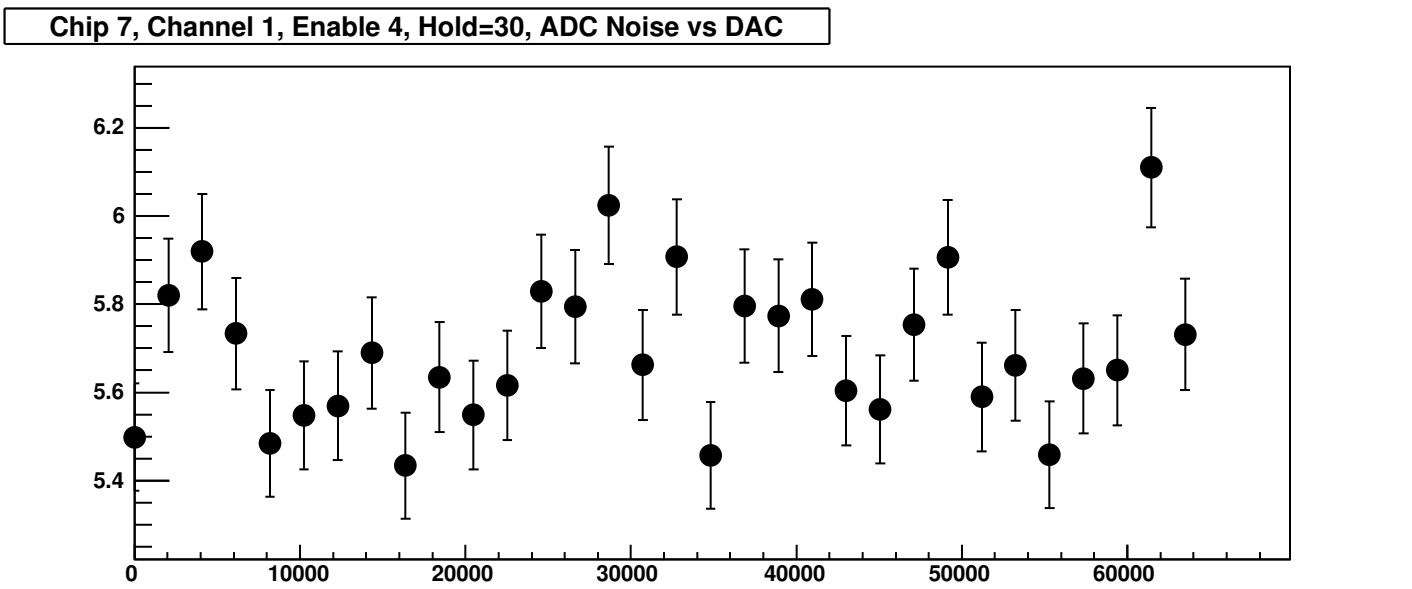

Chip 7, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

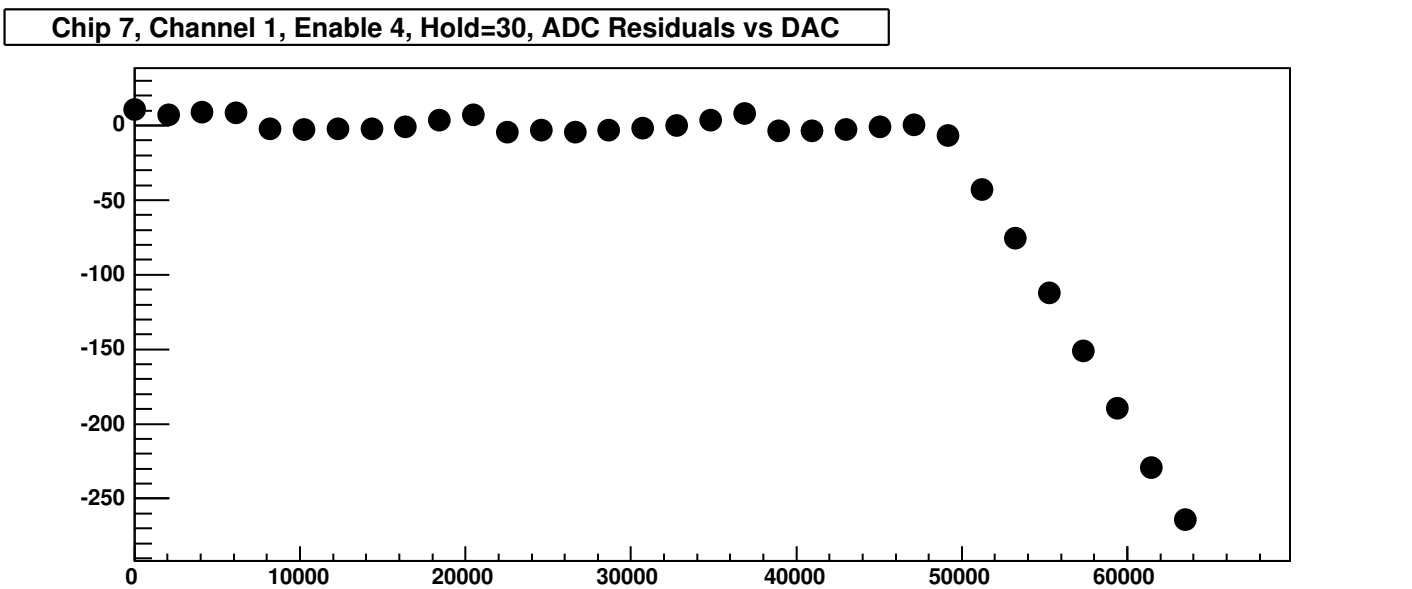

Chip 7, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7211 / 15
p0 1011 ± 0.1094
p1 0.001299 ± 4.522e-06
1080
1060
1040
1020
1000
980
960
940
920
900
880
860
0
10000
20000
30000
40000
50000
60000

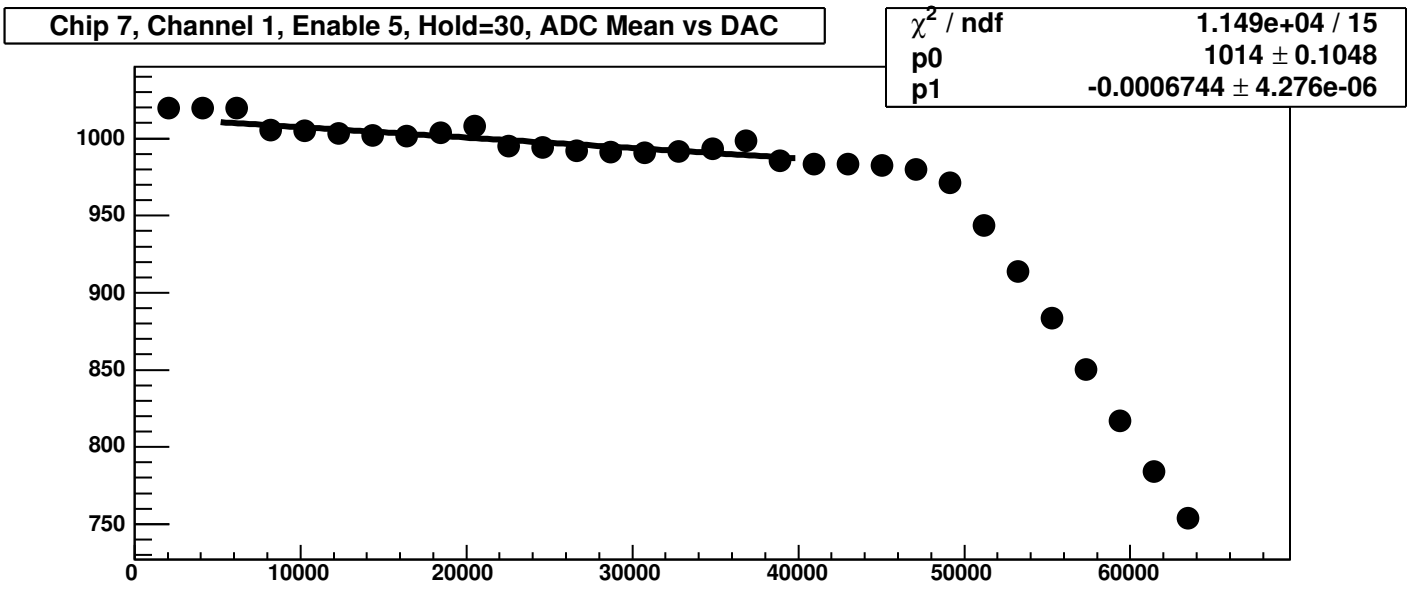

Chip 7, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

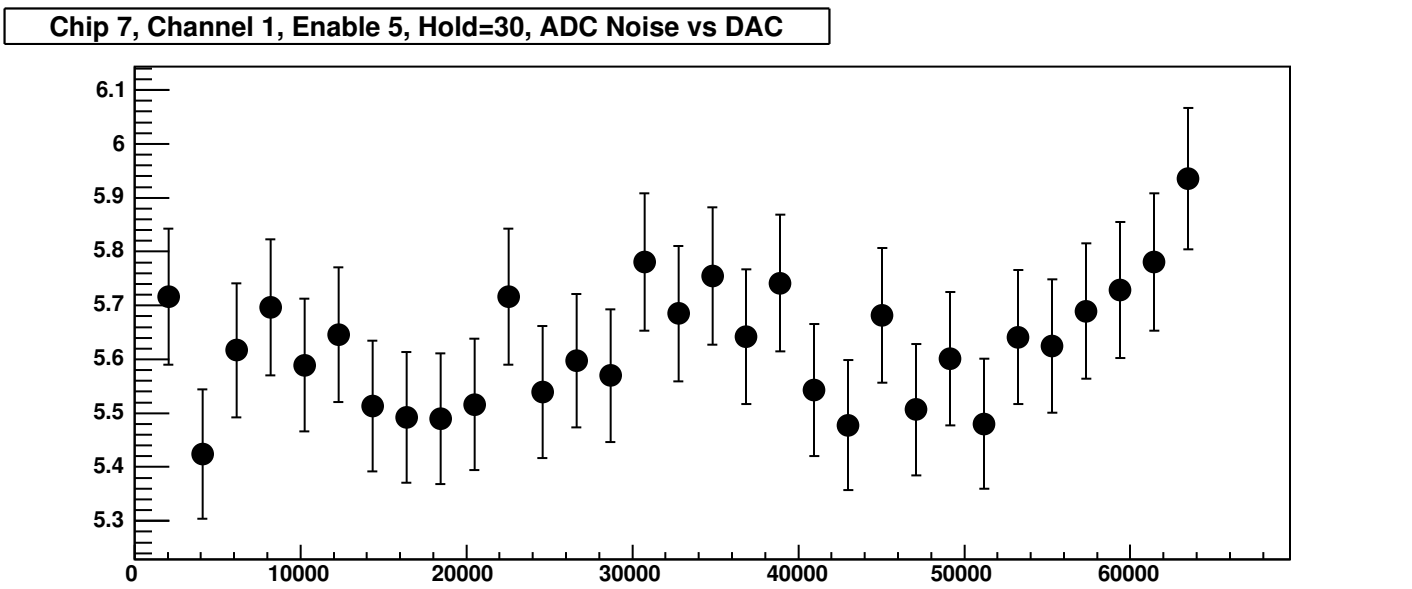

Chip 7, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

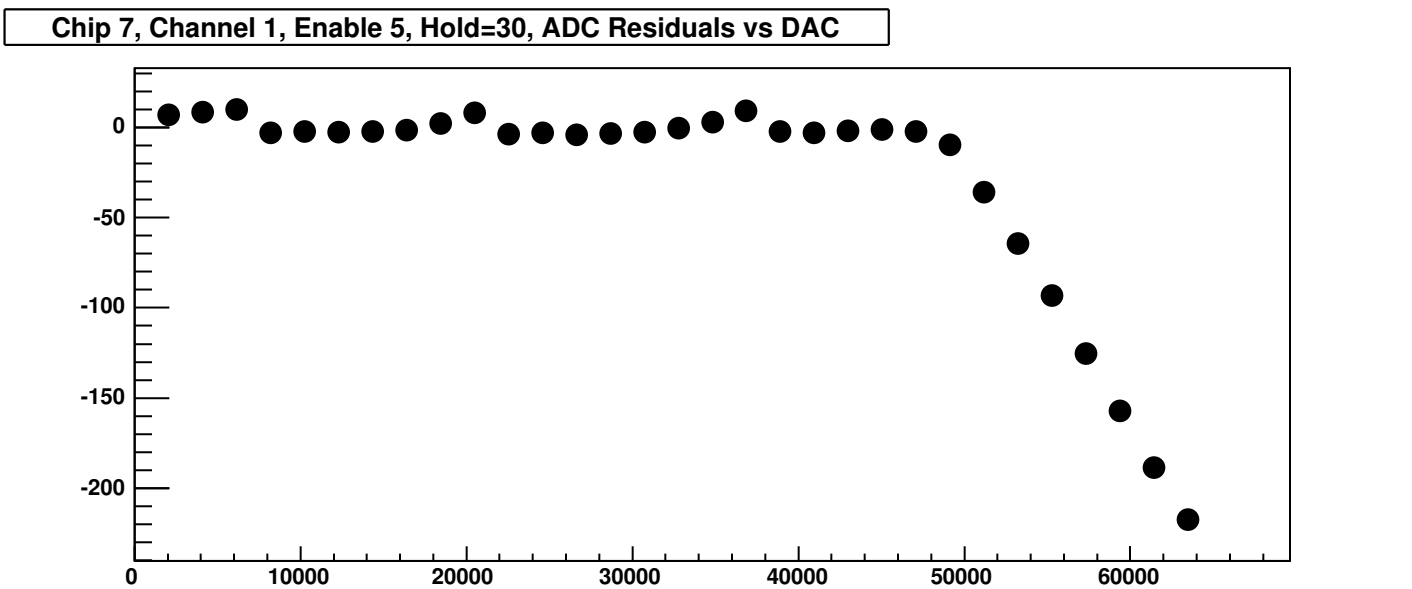

Chip 7, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9799 / 15
p0 1014 ± 0.1046
p1 0.0003764 ± 4.282e-06
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

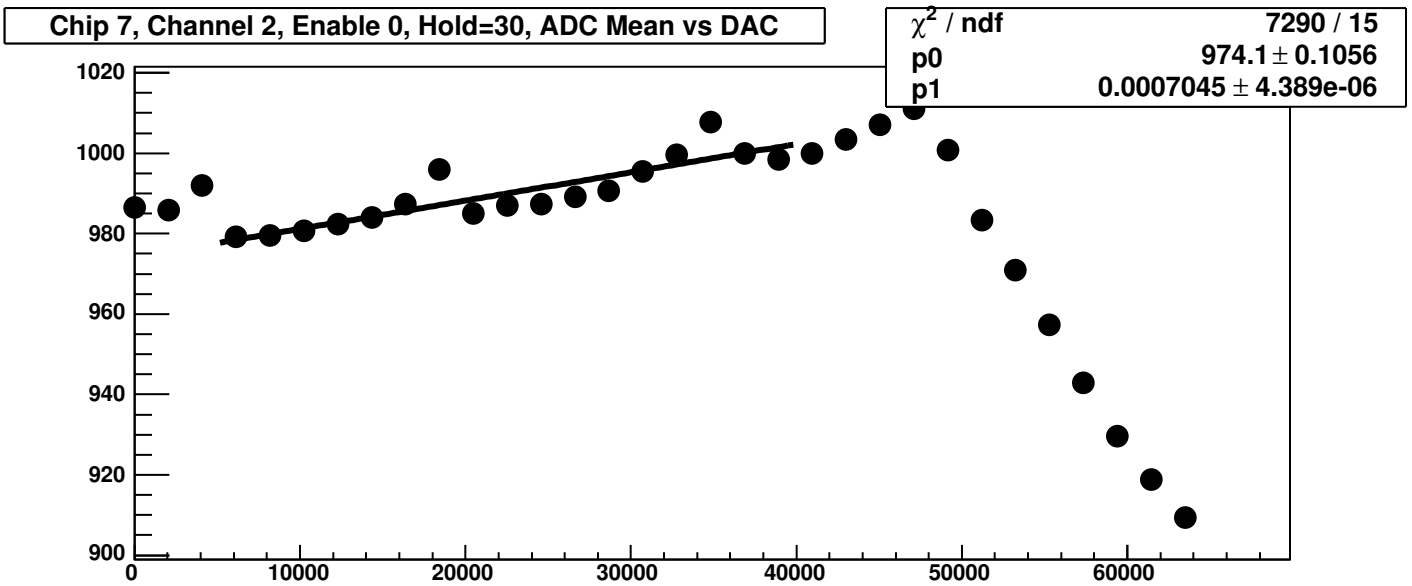

Chip 7, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

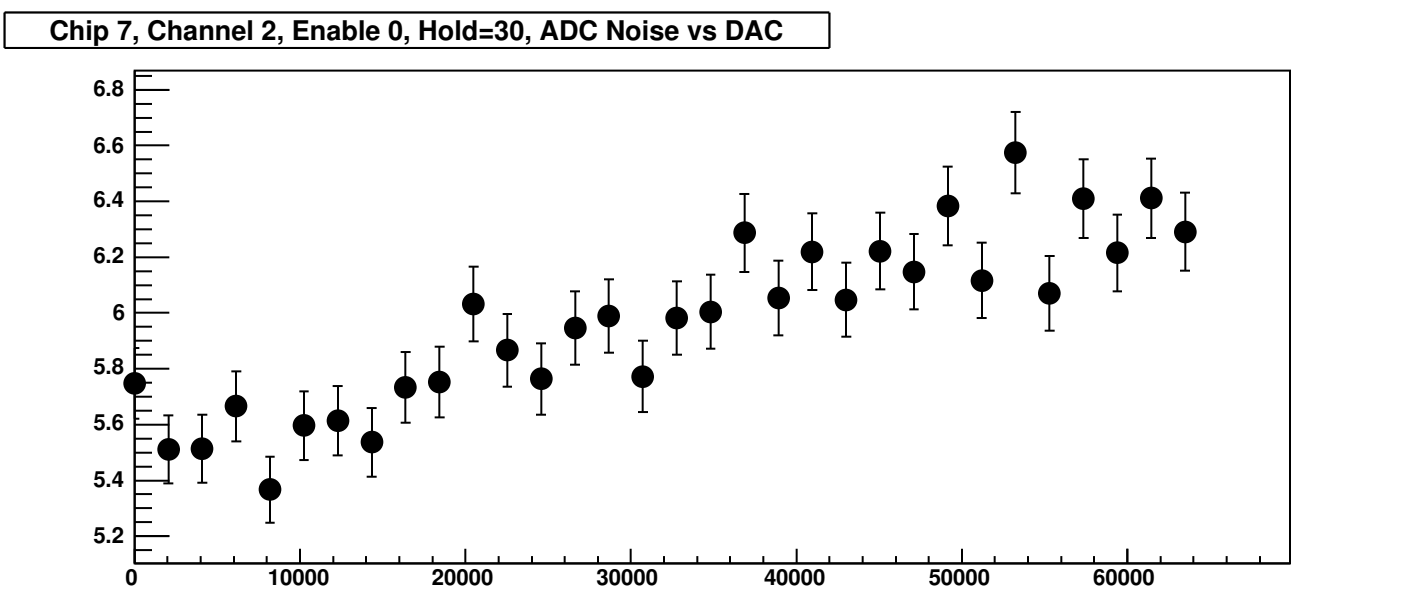

Chip 7, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

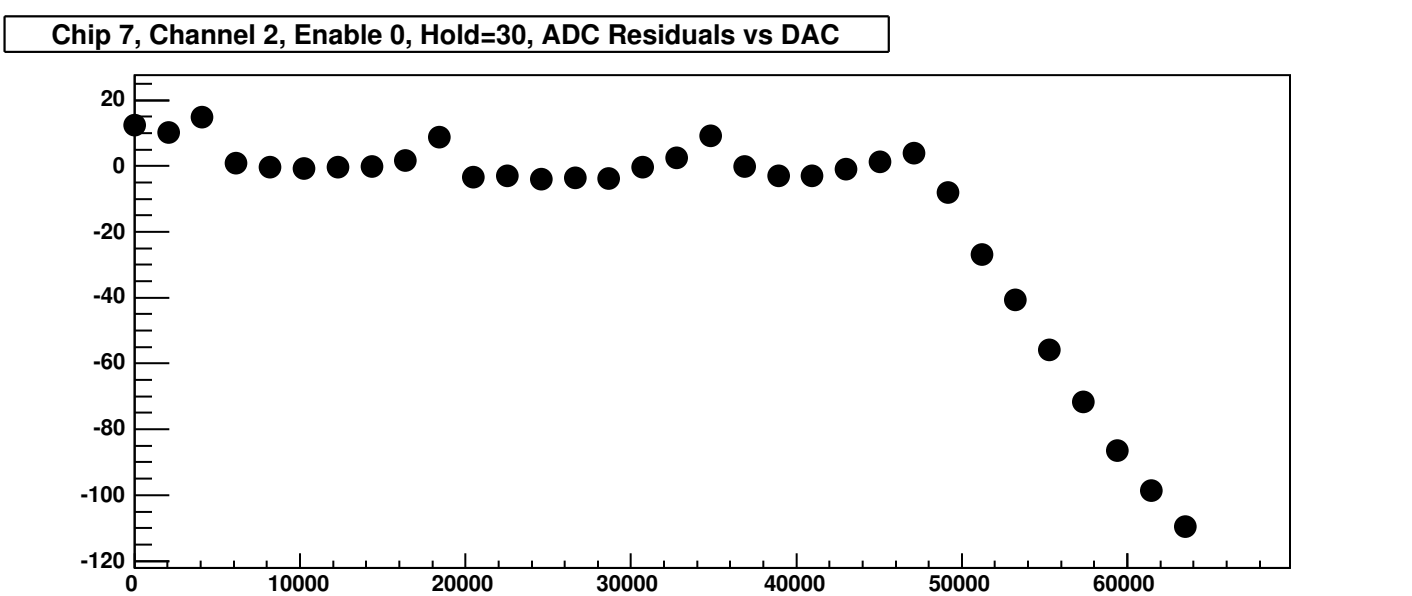

Chip 7, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.149e+04 / 15
p0 1014 ± 0.1048
p1 -0.0006744 ± 4.276e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

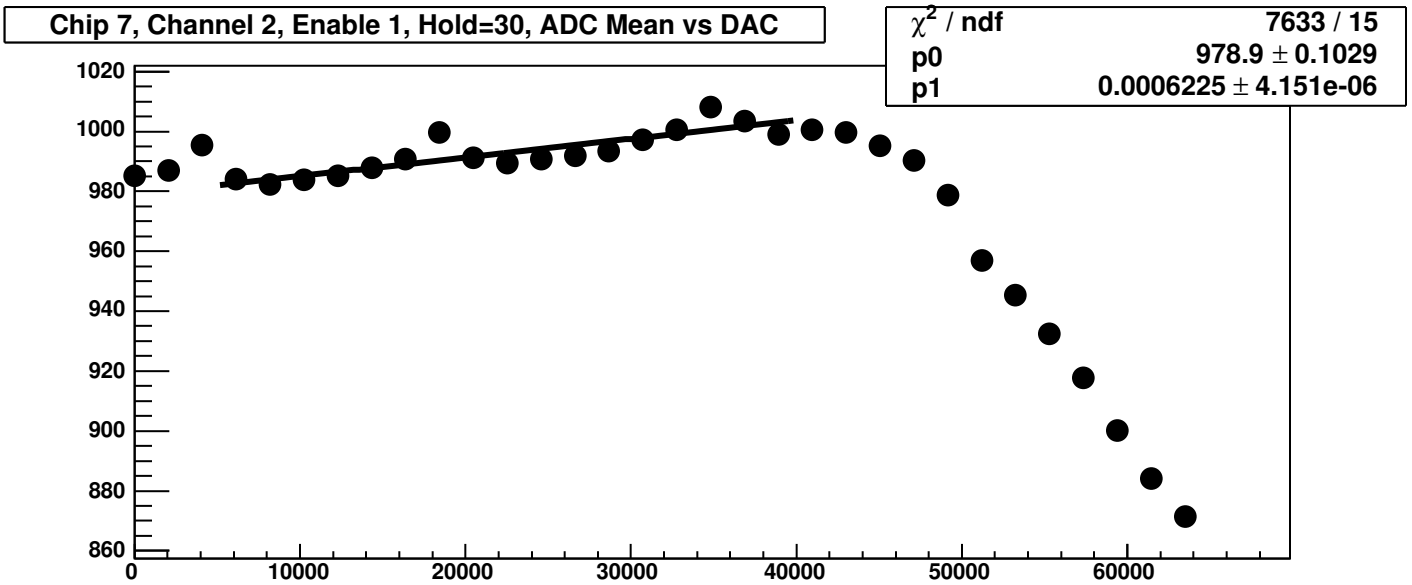

Chip 7, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
0
10000
20000
30000
40000
50000
60000

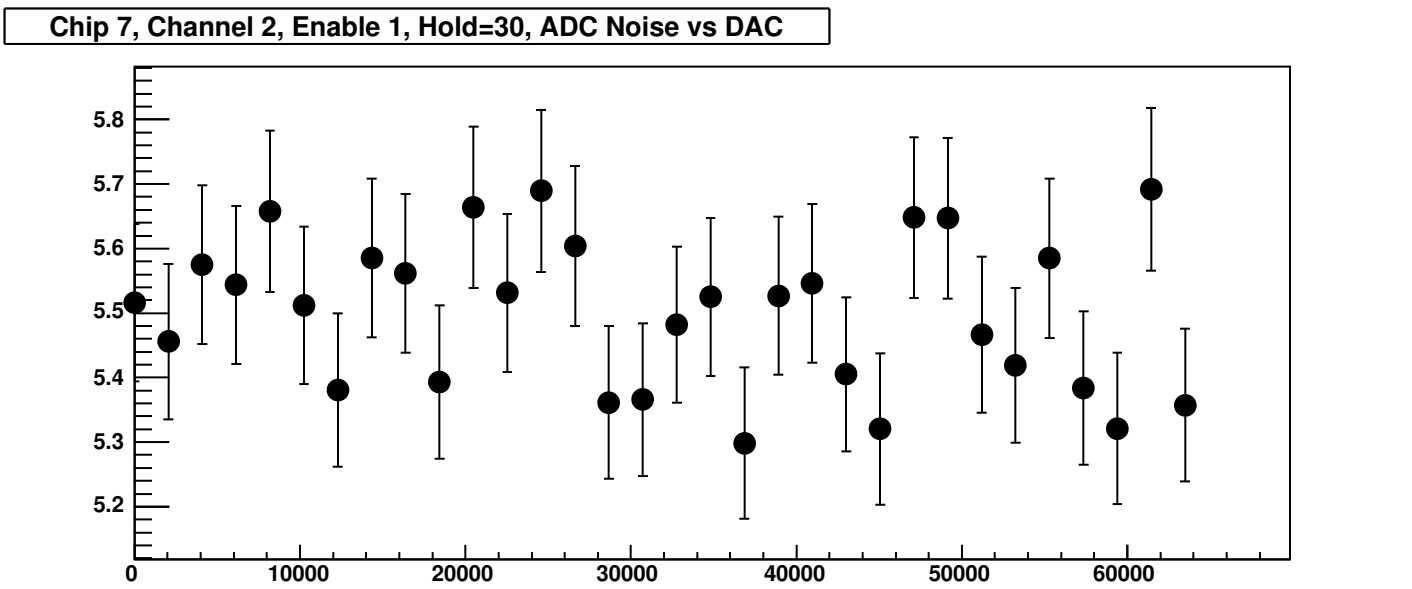

Chip 7, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

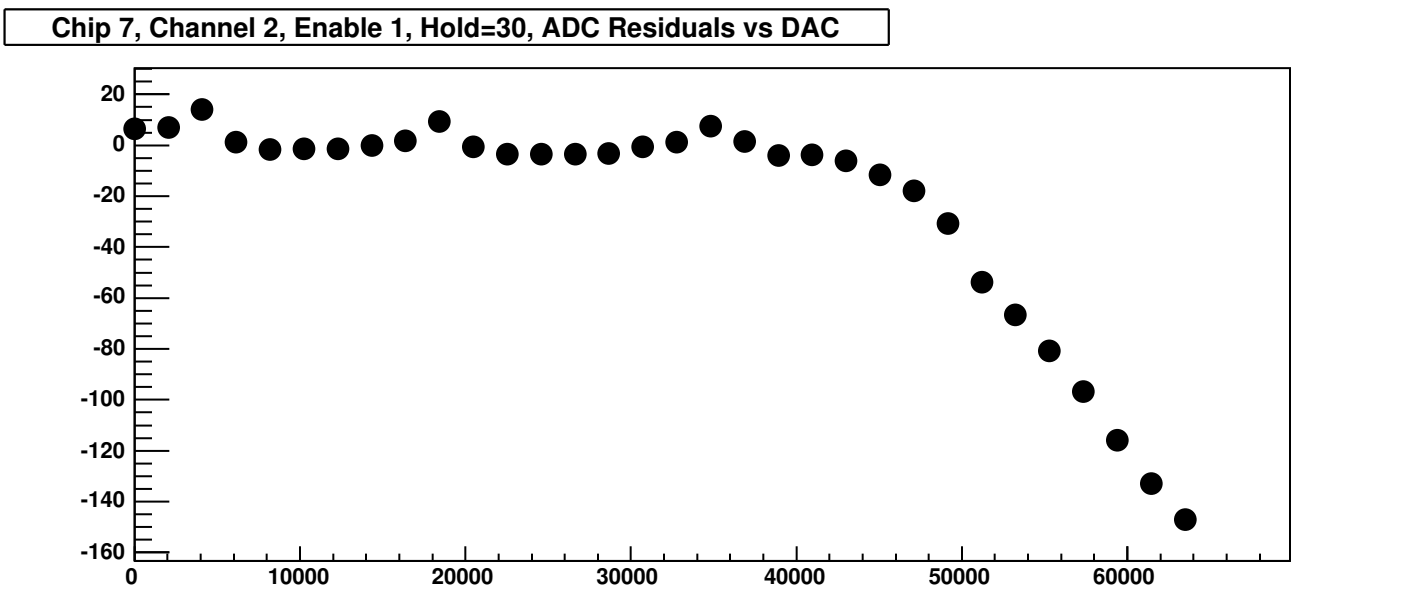

Chip 7, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7290 / 15
p0 974.1 ± 0.1056
p1 0.0007045 ± 4.389e-06
1020
1000
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000

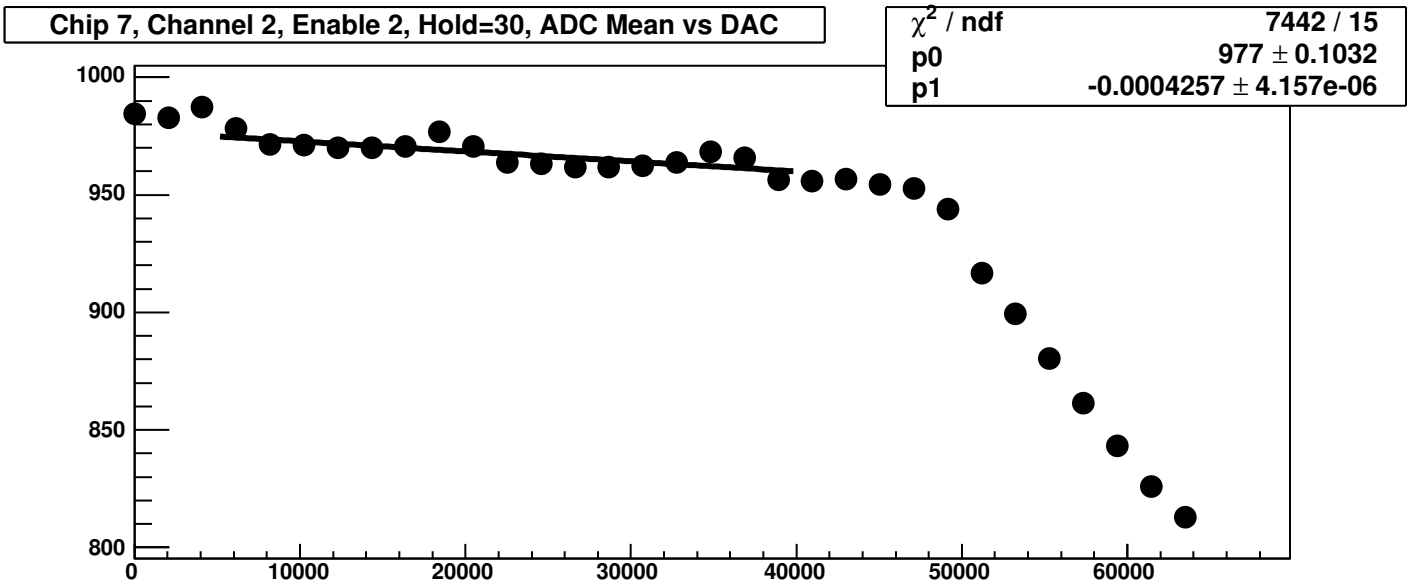

Chip 7, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

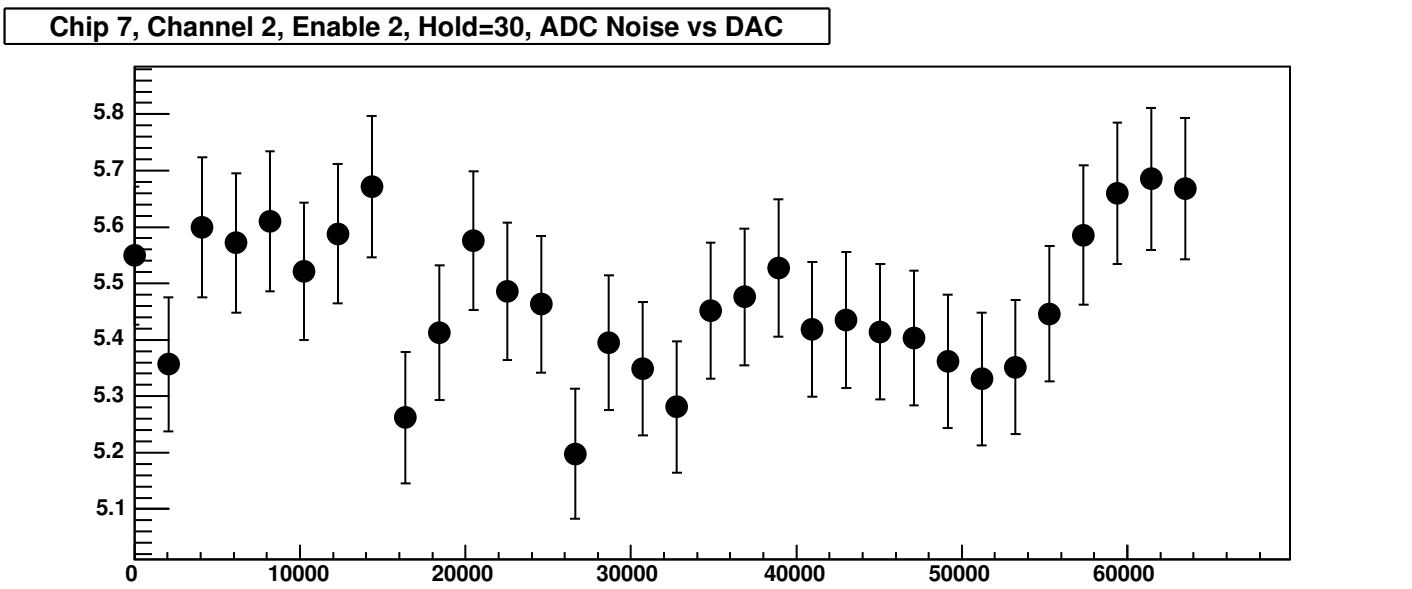

Chip 7, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

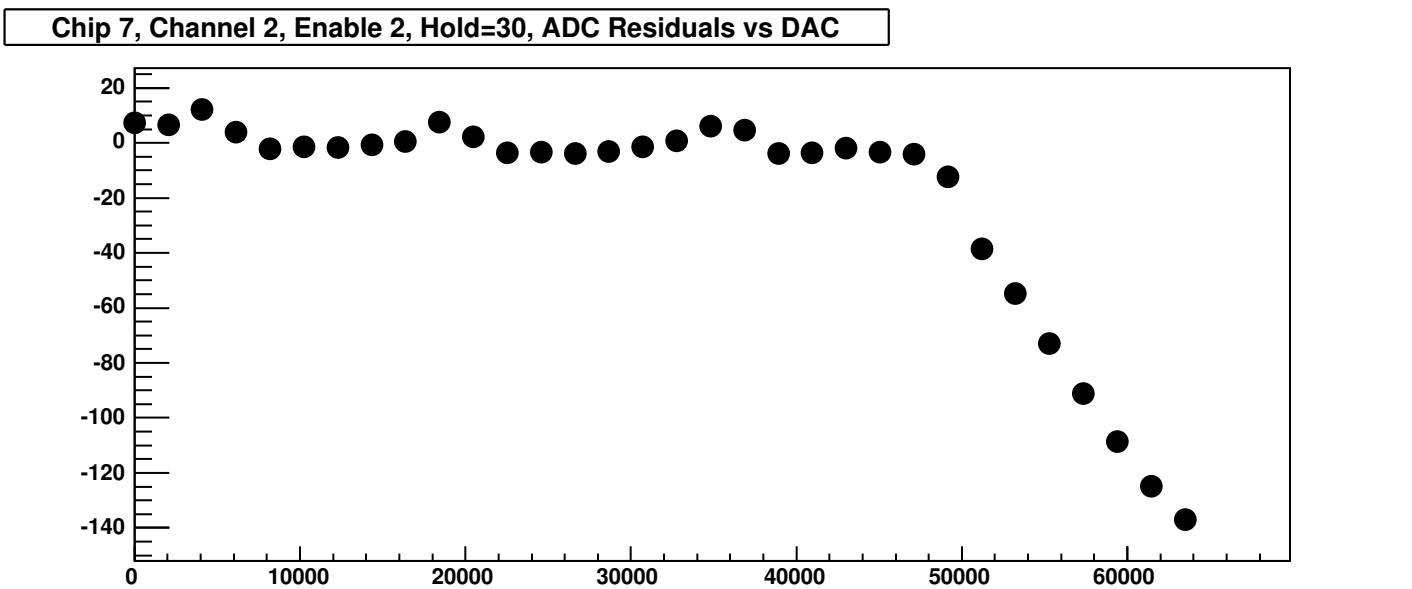

Chip 7, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7633 / 15
p0 978.9 ± 0.1029
p1 0.0006225 ± 4.151e-06
1020
1000
980
960
940
920
900
880
860
0
10000
20000
30000
40000
50000
60000

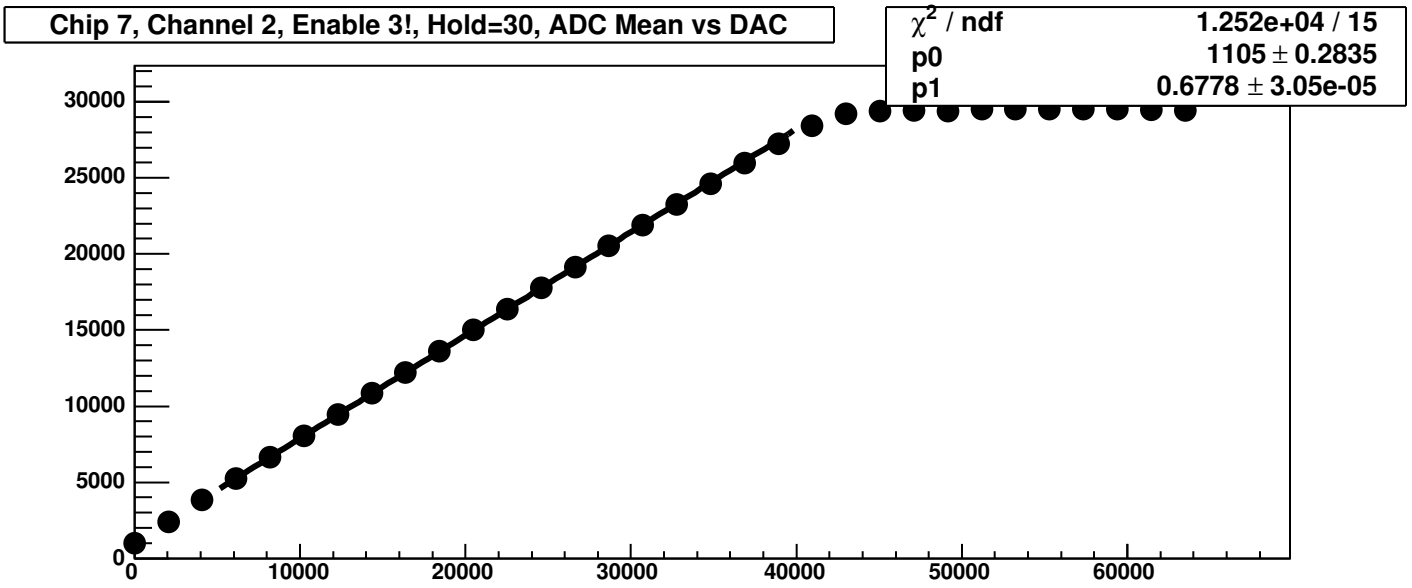

Chip 7, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
5.8
5.7
5.6
5.5
5.4
5.3
5.2
0
10000
20000
30000
40000
50000
60000

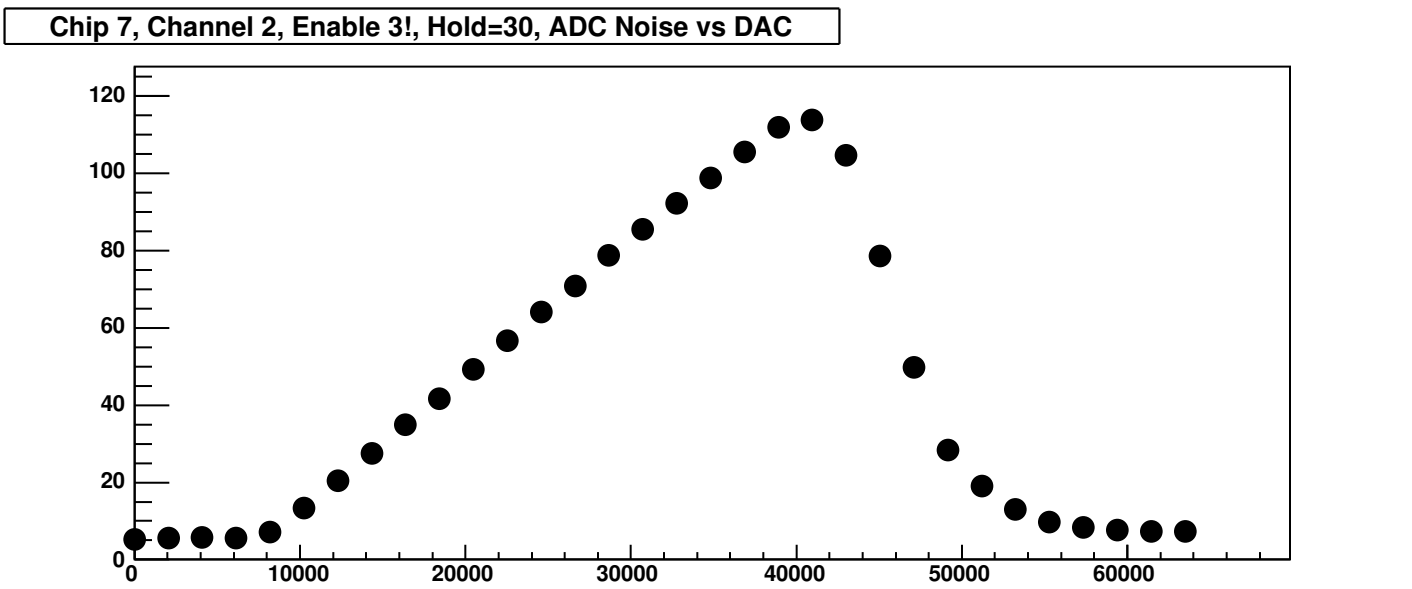

Chip 7, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

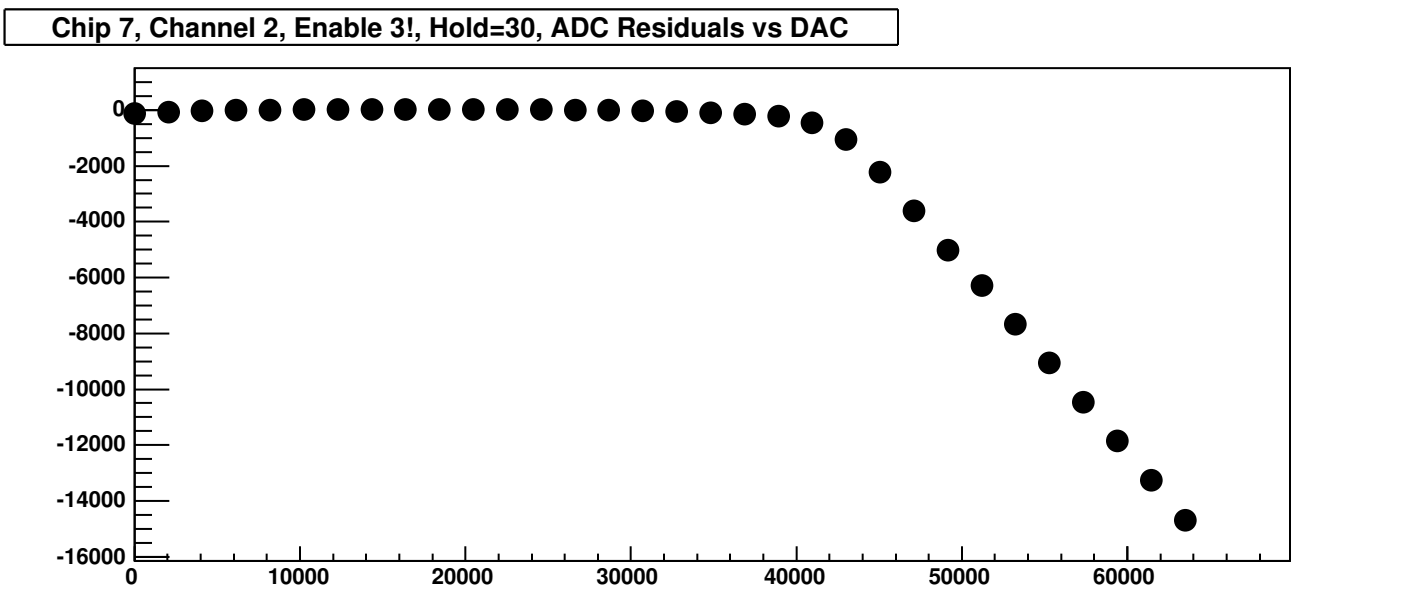

Chip 7, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7442 / 15
p0 977 ± 0.1032
p1 -0.0004257 ± 4.157e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

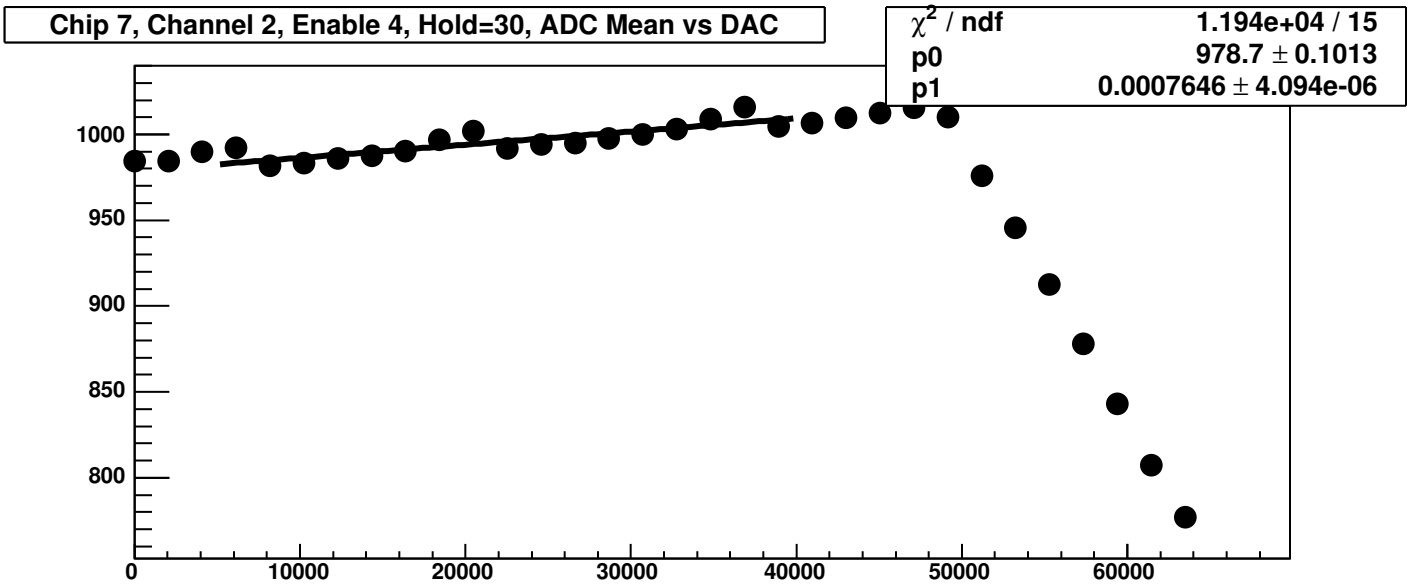

Chip 7, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
0
10000
20000
30000
40000
50000
60000

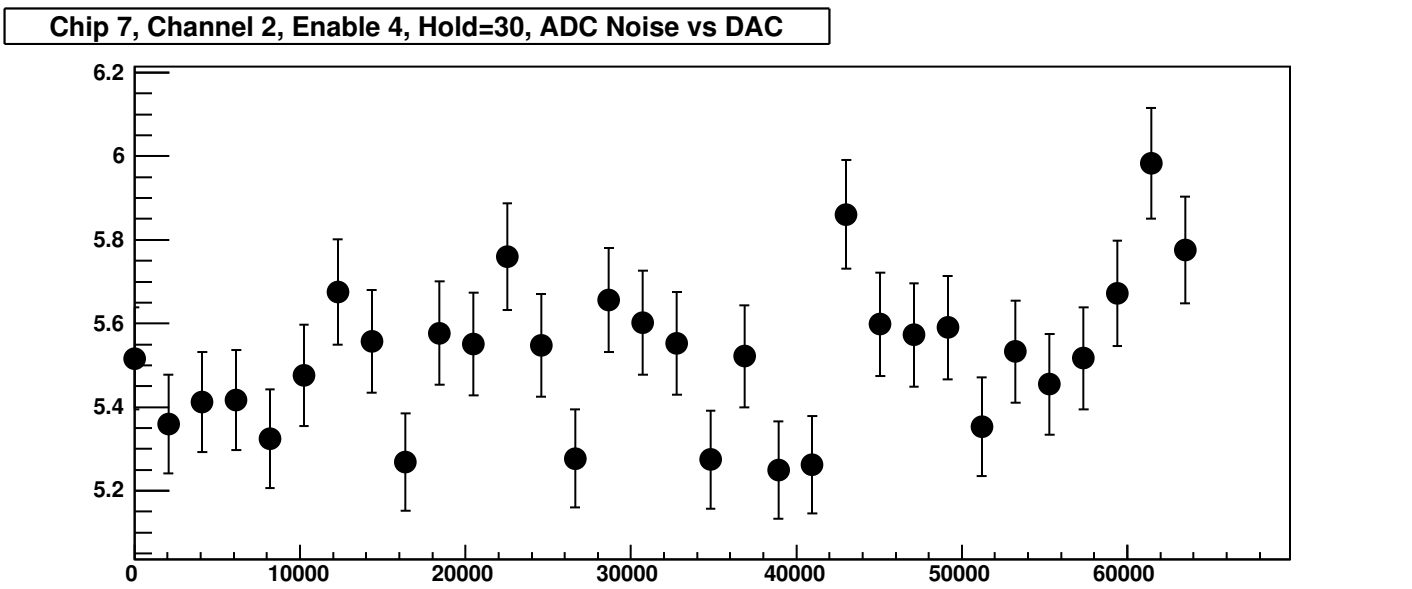

Chip 7, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

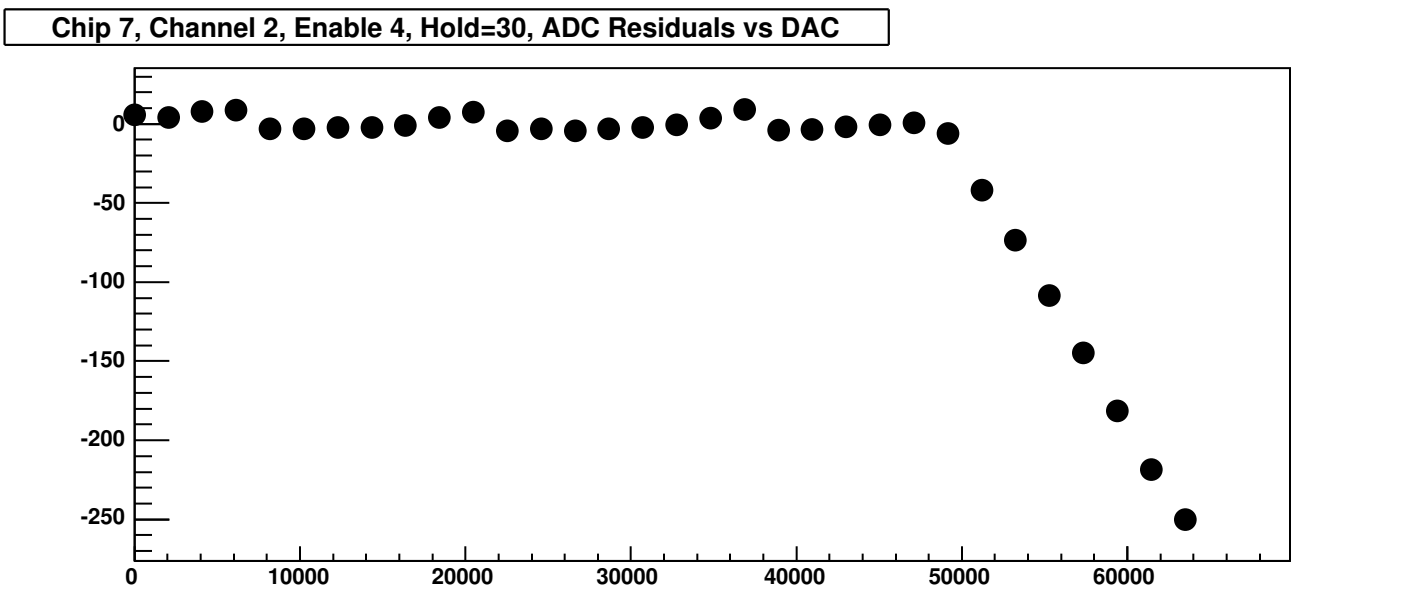

Chip 7, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.252e+04 / 15
p0 1105 ± 0.2835
p1 0.6778 ± 3.05e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

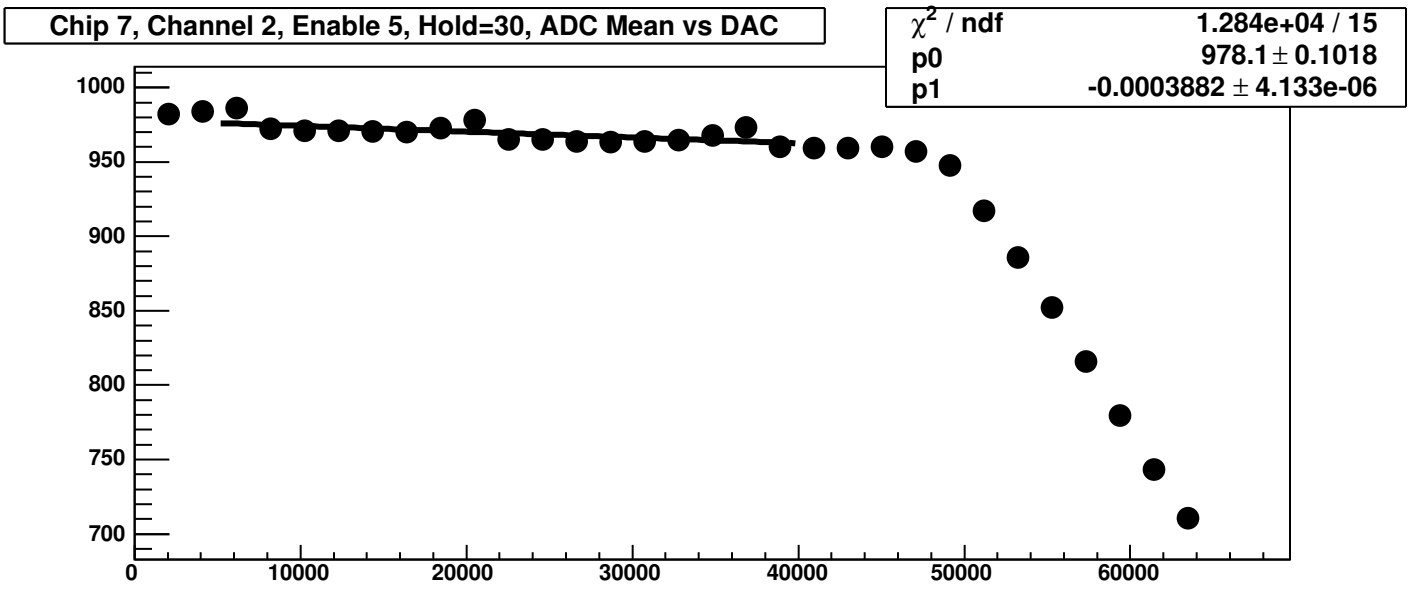

Chip 7, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

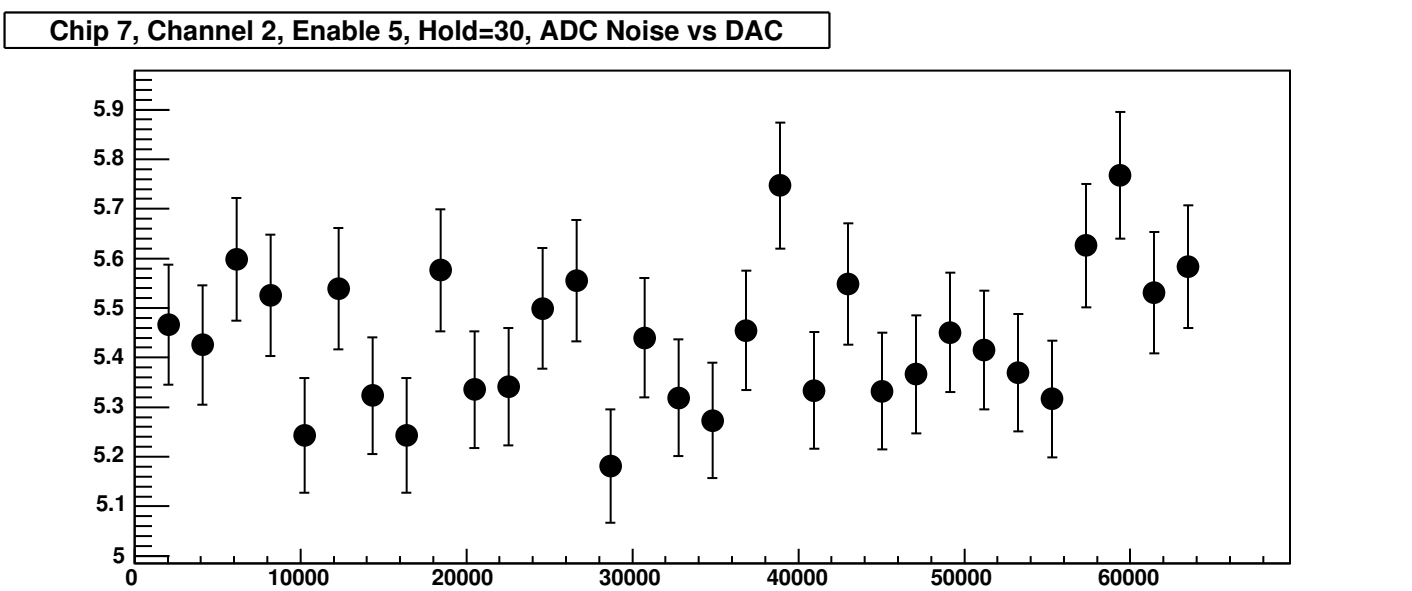

Chip 7, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
10000
20000
30000
40000
50000
60000

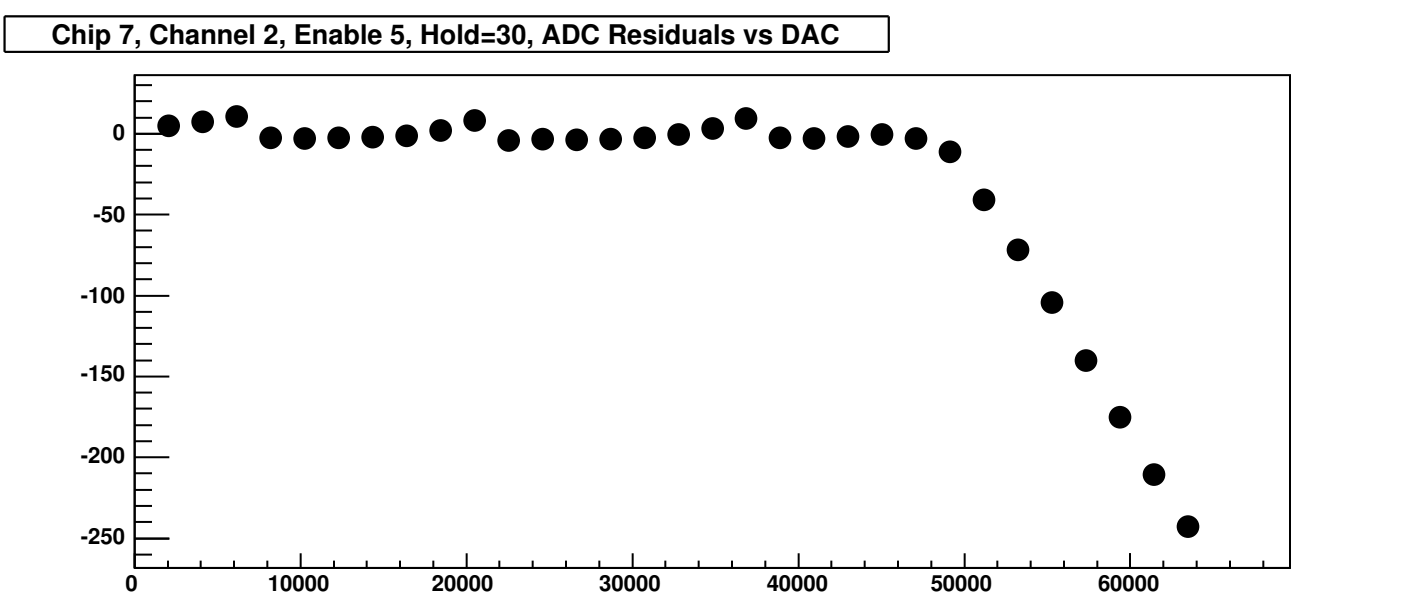

Chip 7, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.194e+04 / 15
p0 978.7 ± 0.1013
p1 0.0007646 ± 4.094e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

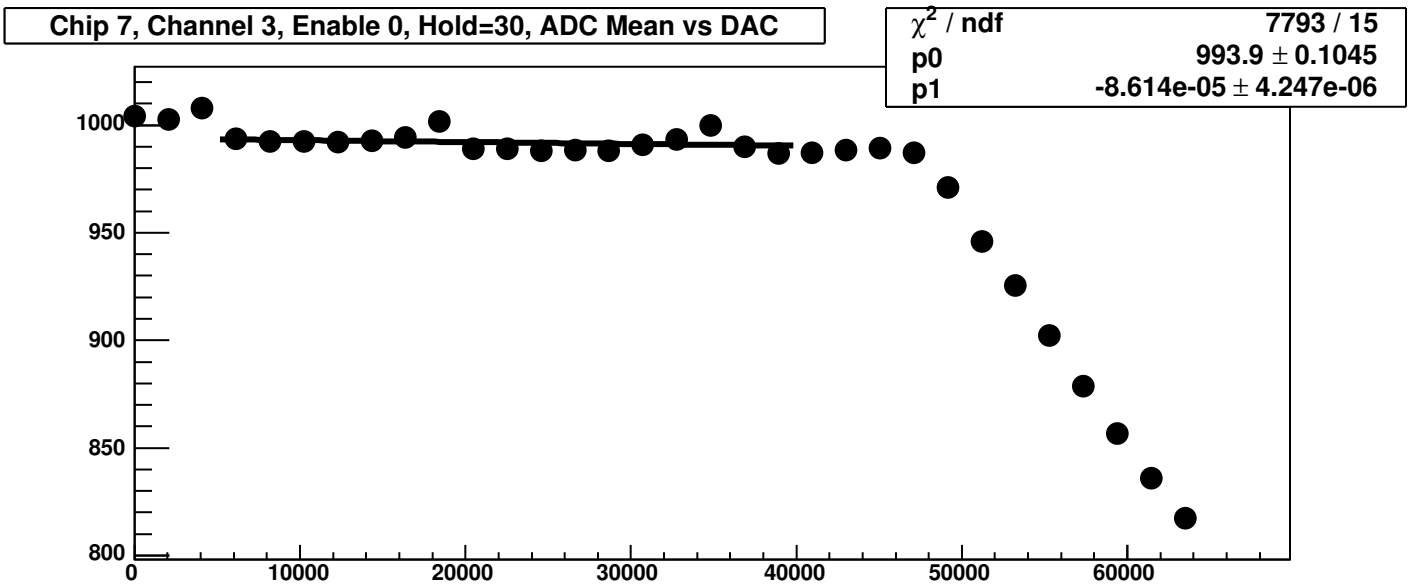

Chip 7, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

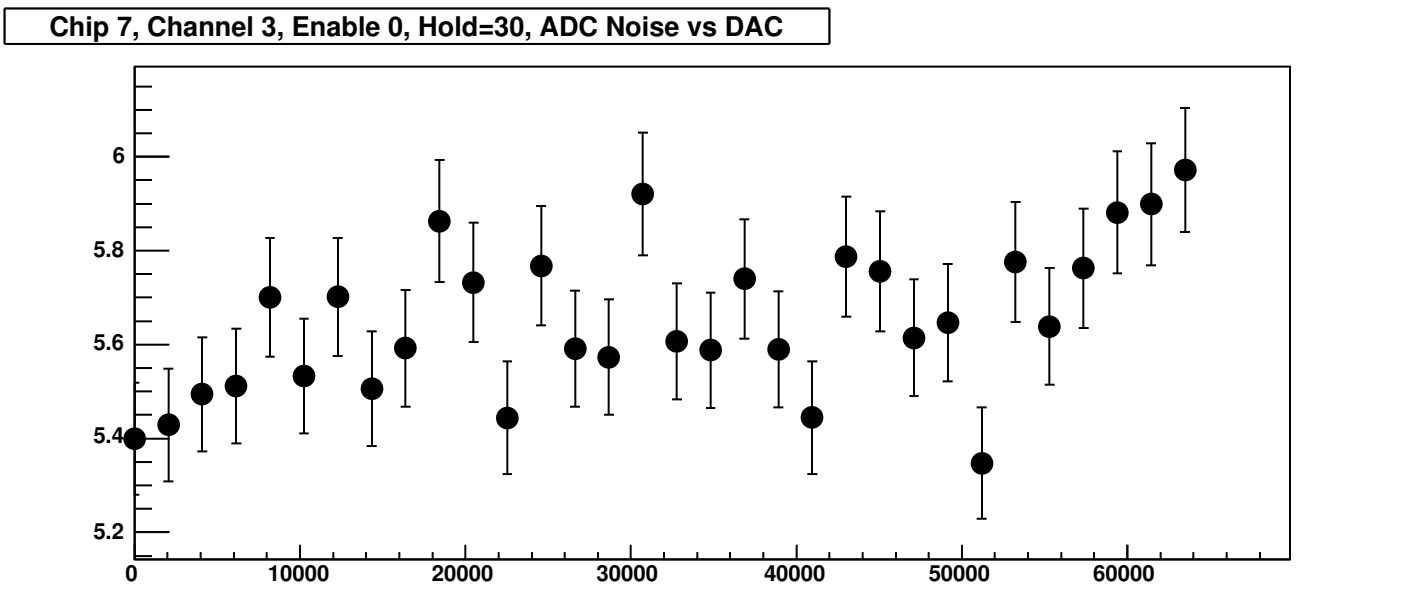

Chip 7, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

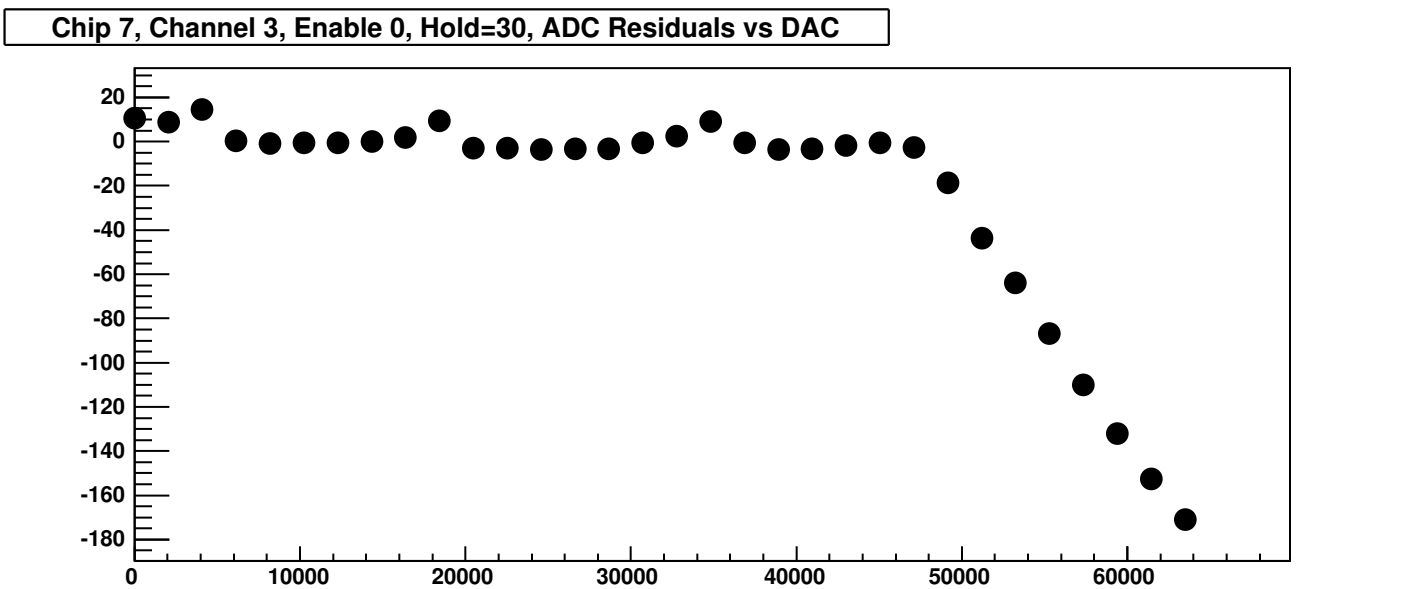

Chip 7, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.284e+04 / 15
p0 978.1 ± 0.1018
p1 -0.0003882 ± 4.133e-06
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

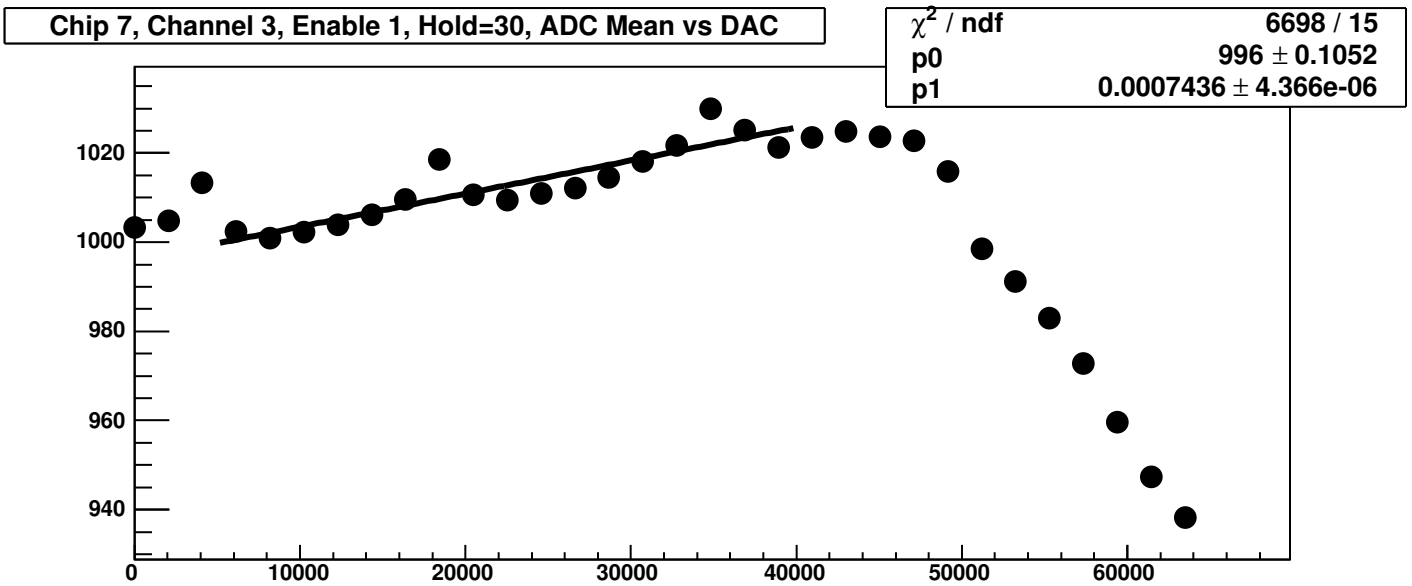

Chip 7, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
5
0
10000
20000
30000
40000
50000
60000

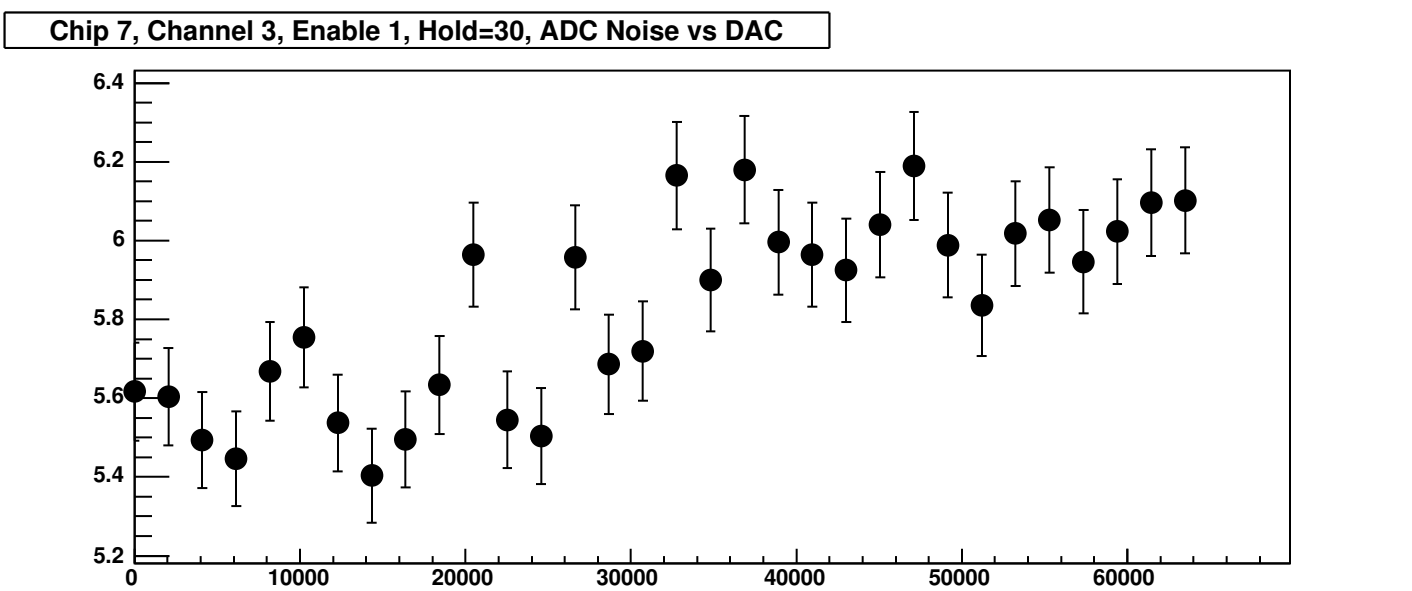

Chip 7, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

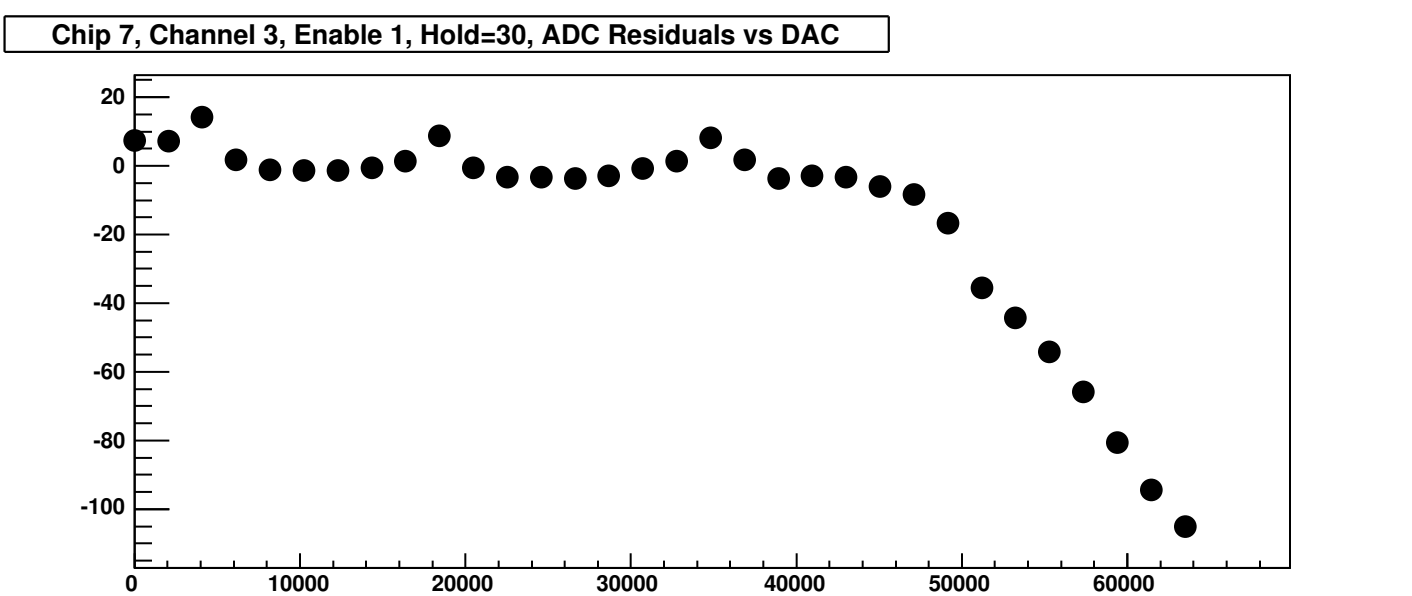

Chip 7, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7793 / 15
p0 993.9 ± 0.1045
p1 -8.614e-05 ± 4.247e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

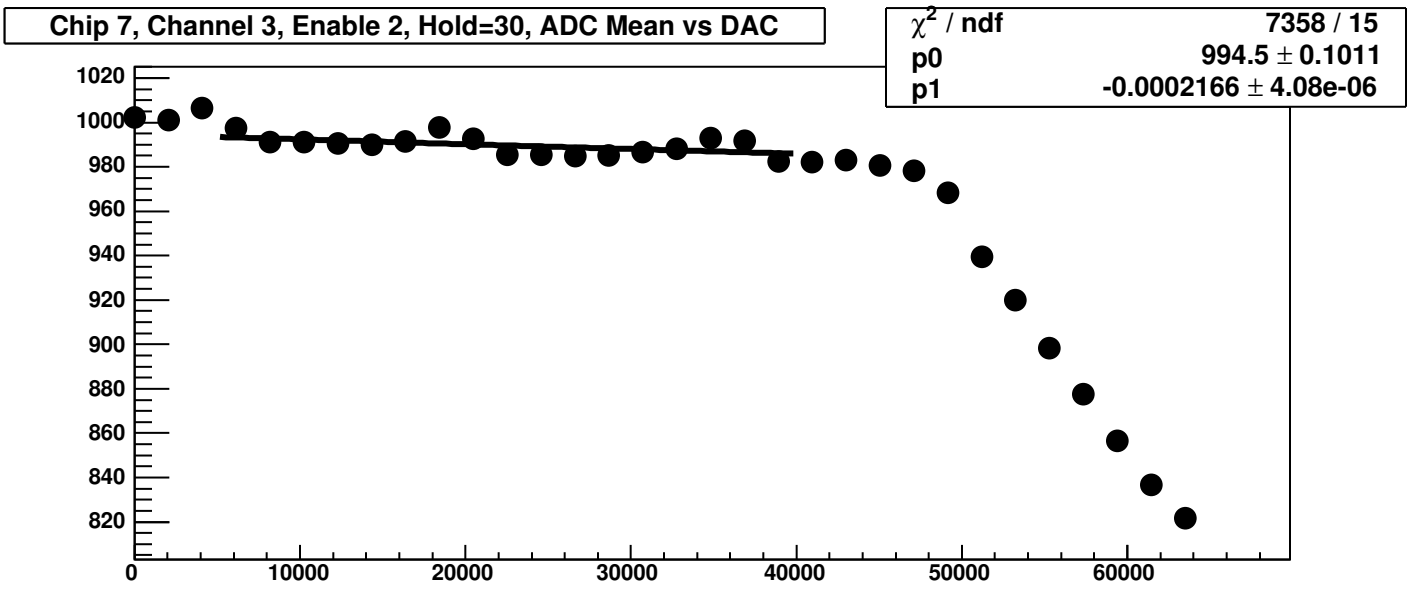

Chip 7, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

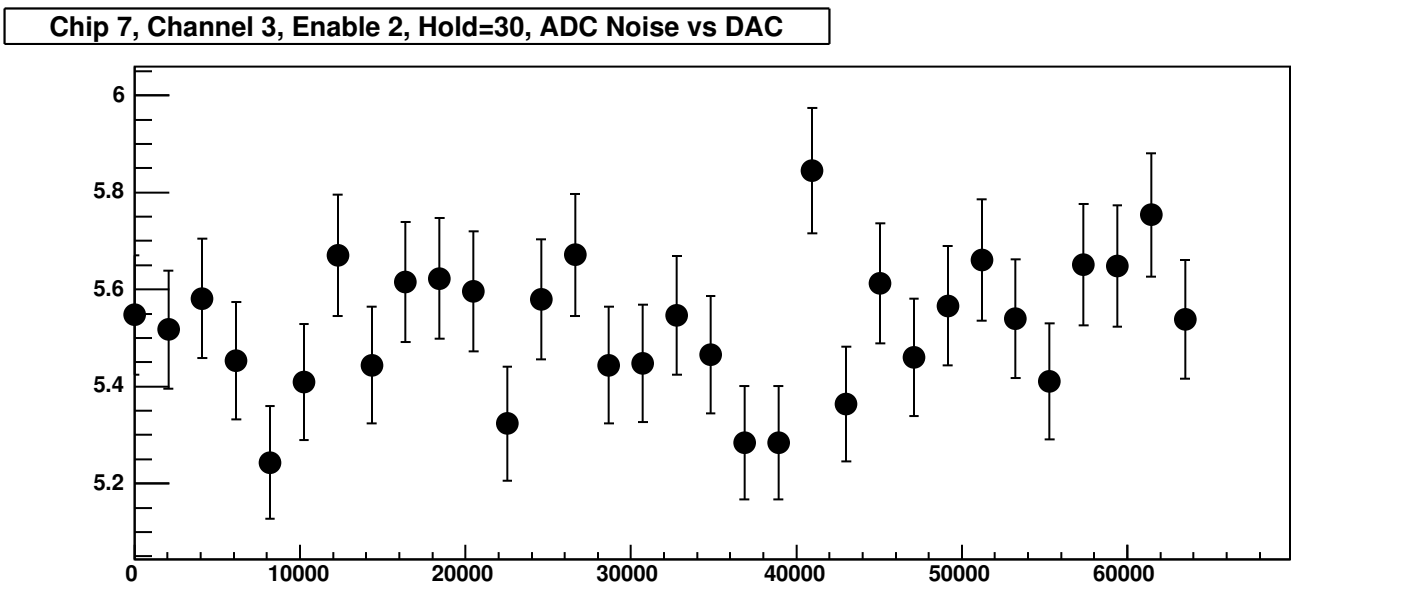

Chip 7, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

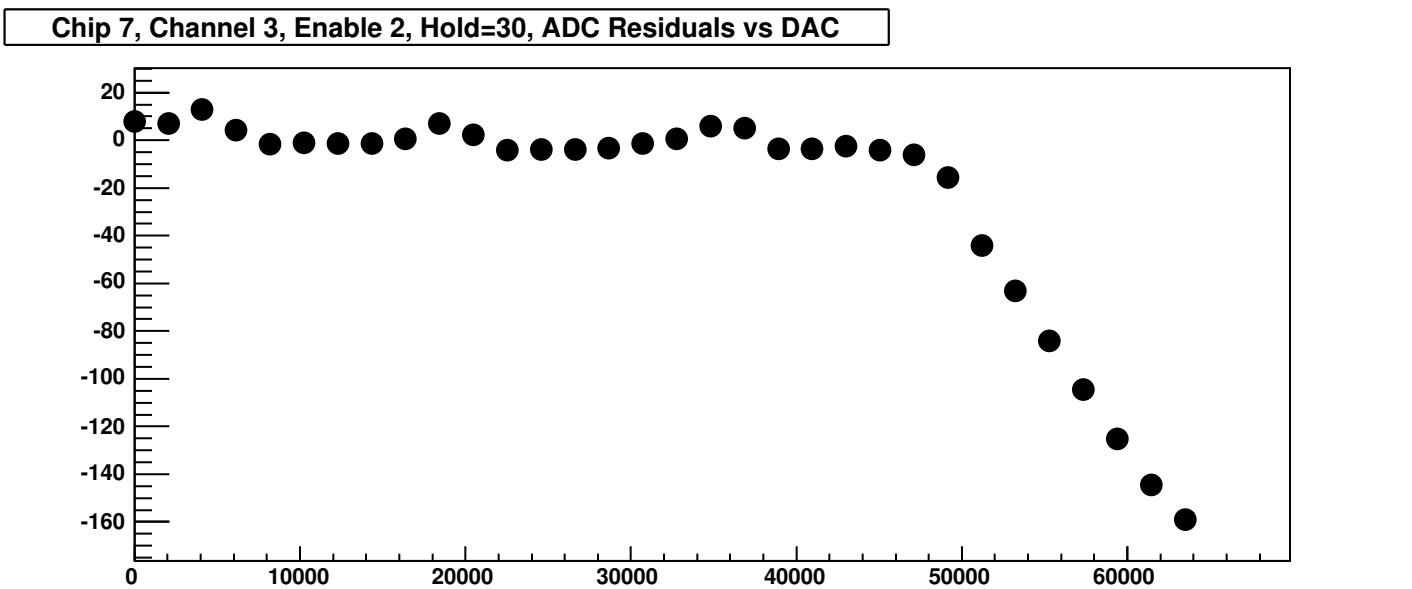

Chip 7, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6698 / 15
p0 996 ± 0.1052
p1 0.0007436 ± 4.366e-06
1020
1000
980
960
940
0
10000
20000
30000
40000
50000
60000

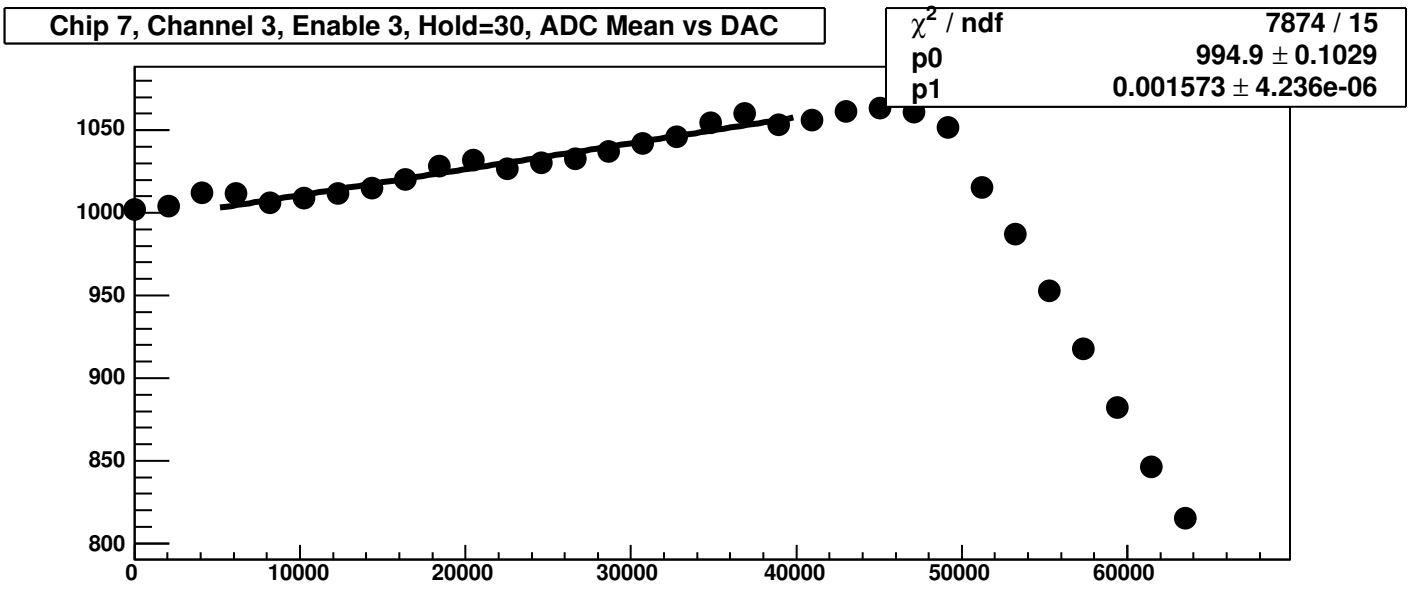

Chip 7, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

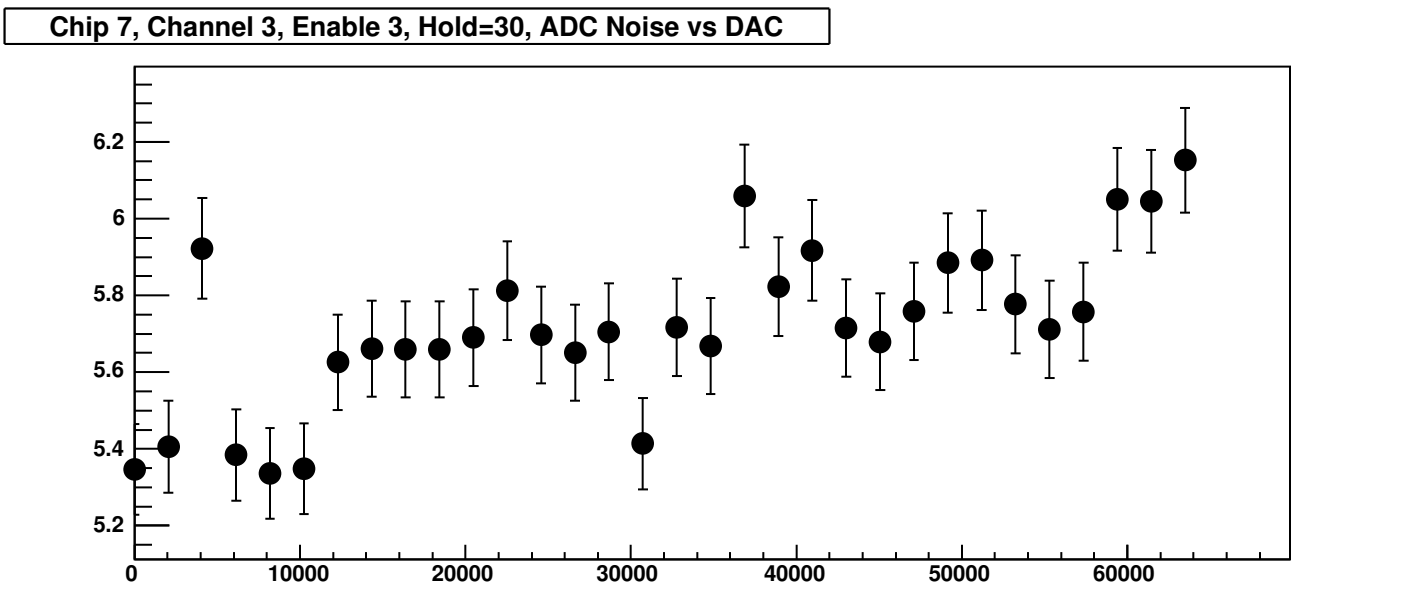

Chip 7, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

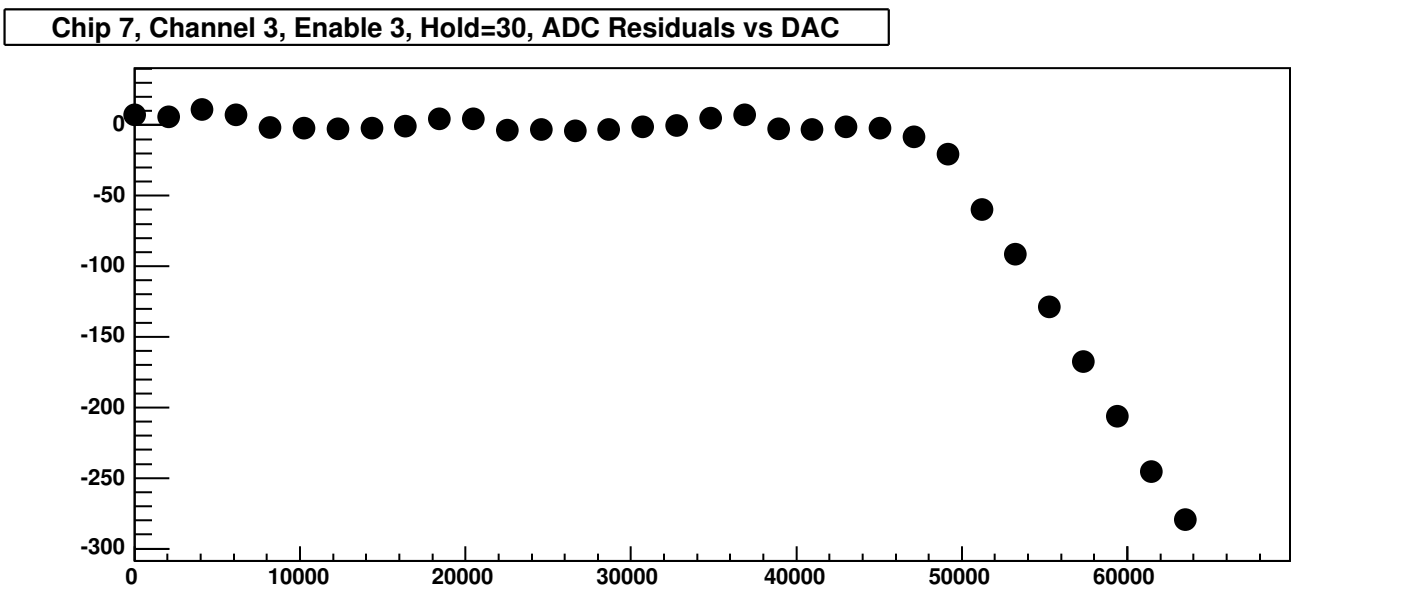

Chip 7, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7358 / 15
p0 994.5 ± 0.1011
p1 -0.0002166 ± 4.08e-06
1020
1000
980
960
940
920
900
880
860
840
820
0
10000
20000
30000
40000
50000
60000

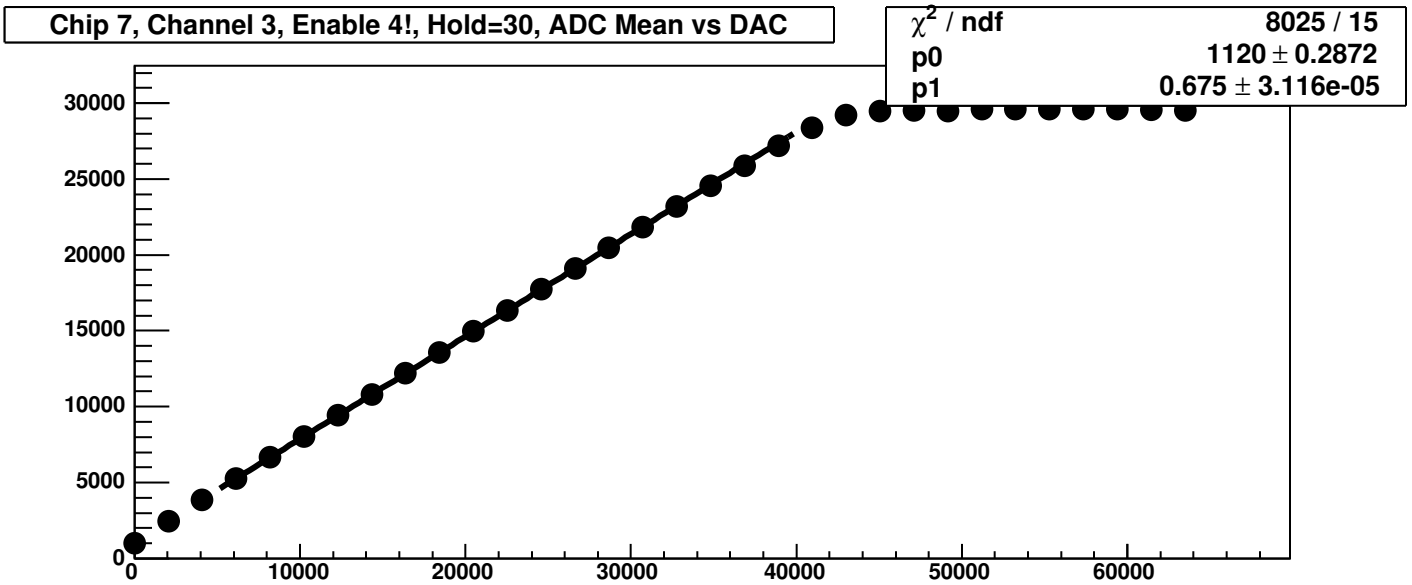

Chip 7, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

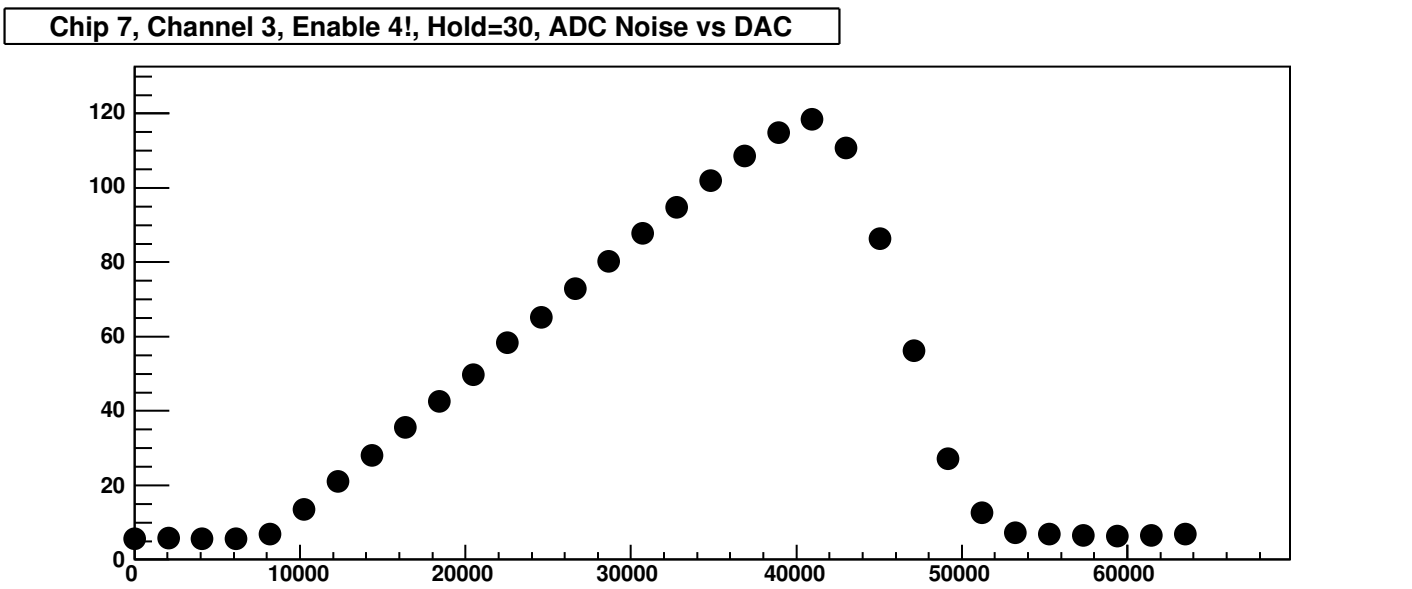

Chip 7, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

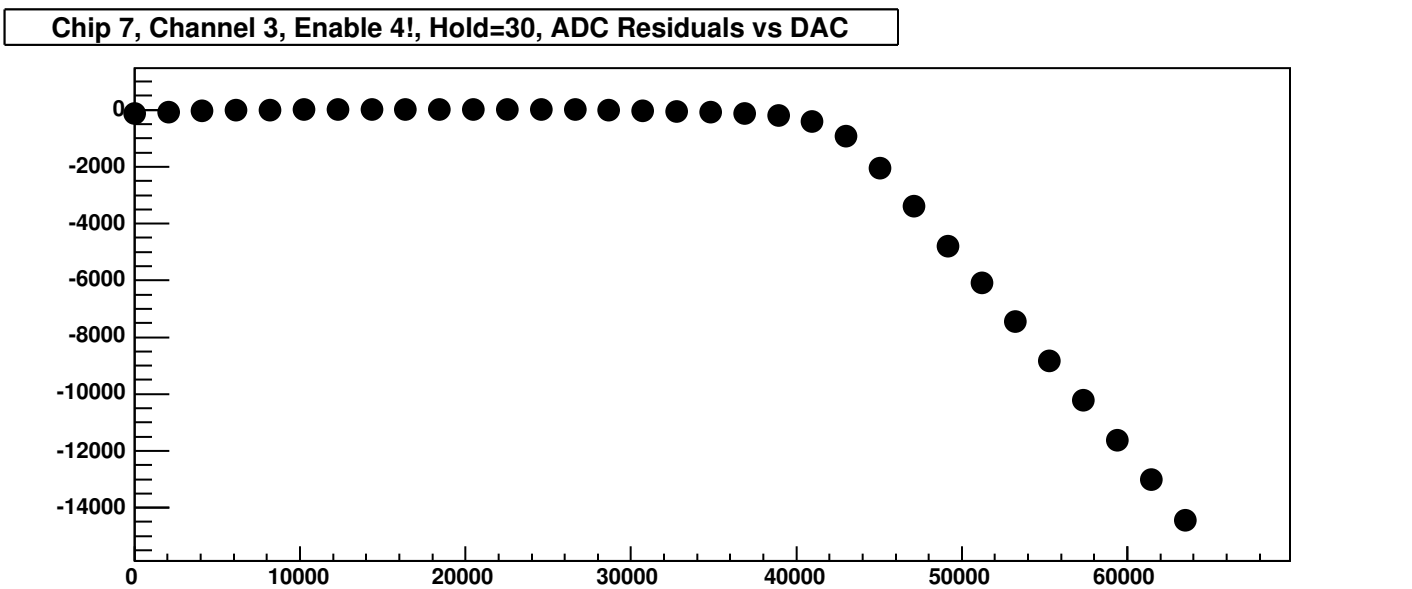

Chip 7, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7874 / 15
p0 994.9 ± 0.1029
p1 0.001573 ± 4.236e-06
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

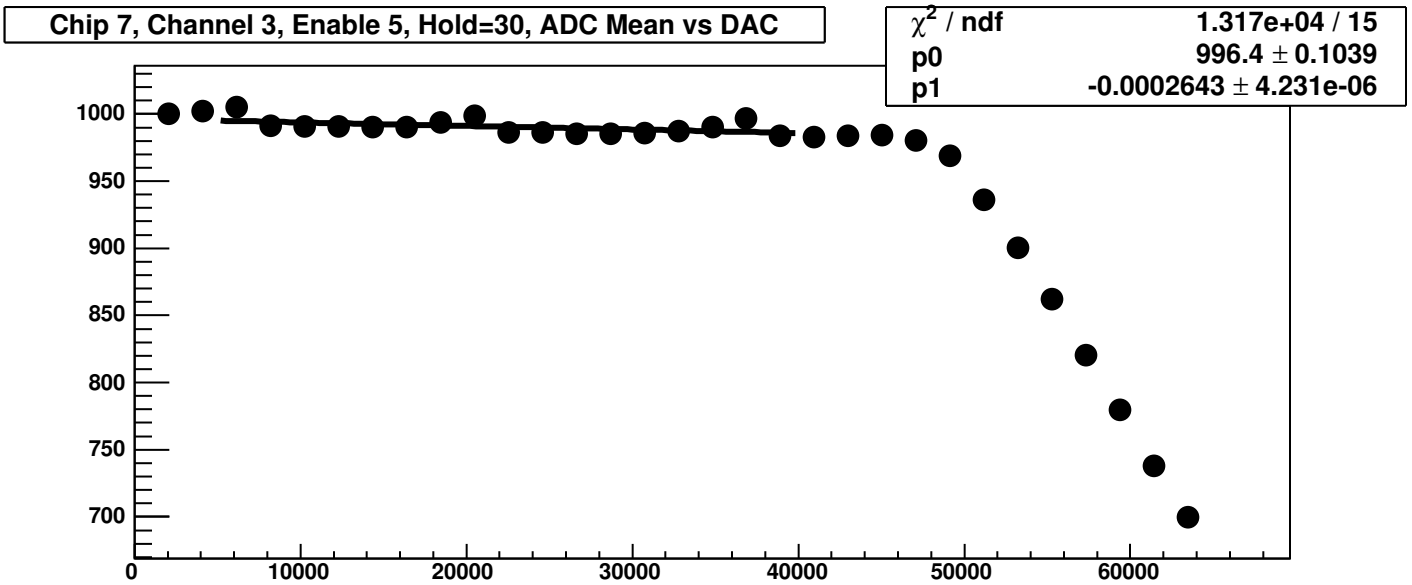

Chip 7, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

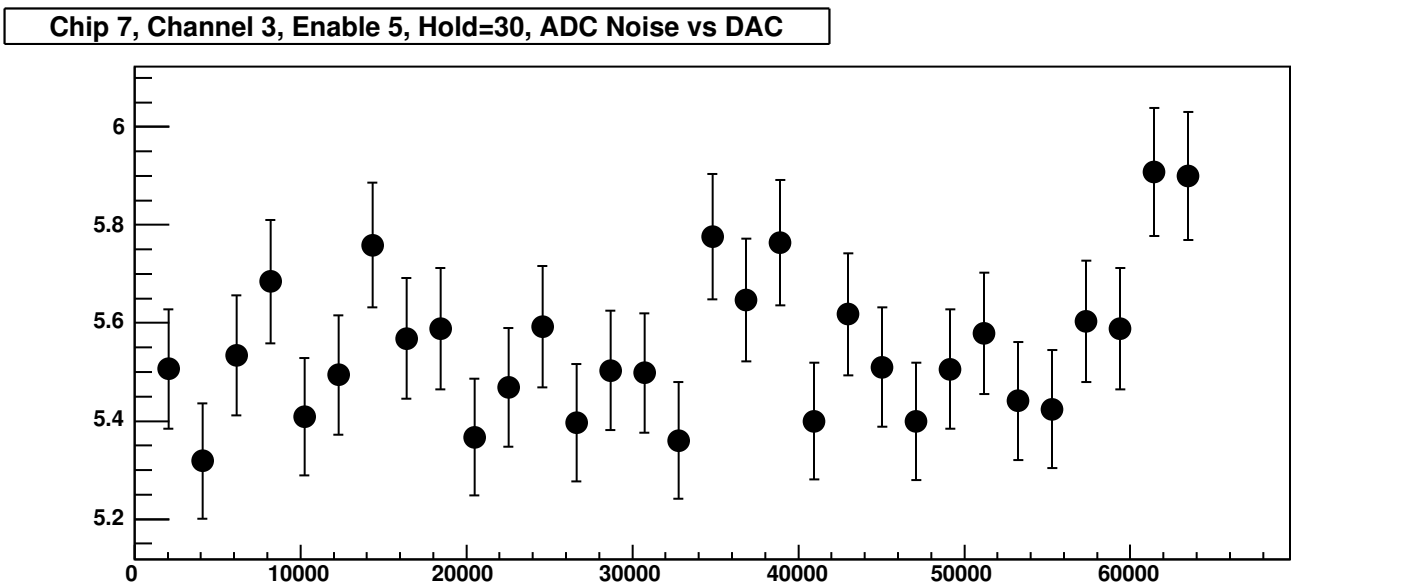

Chip 7, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

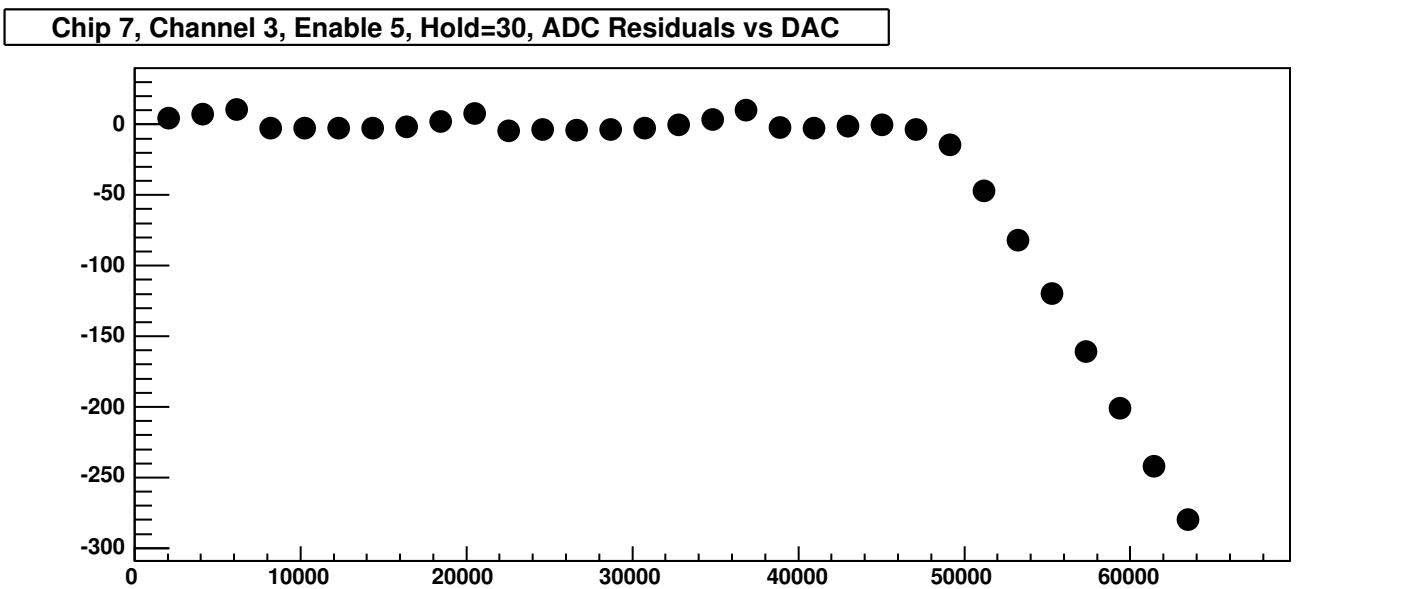

Chip 7, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 8025 / 15
p0 1120 ± 0.2872
p1 0.675 ± 3.116e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

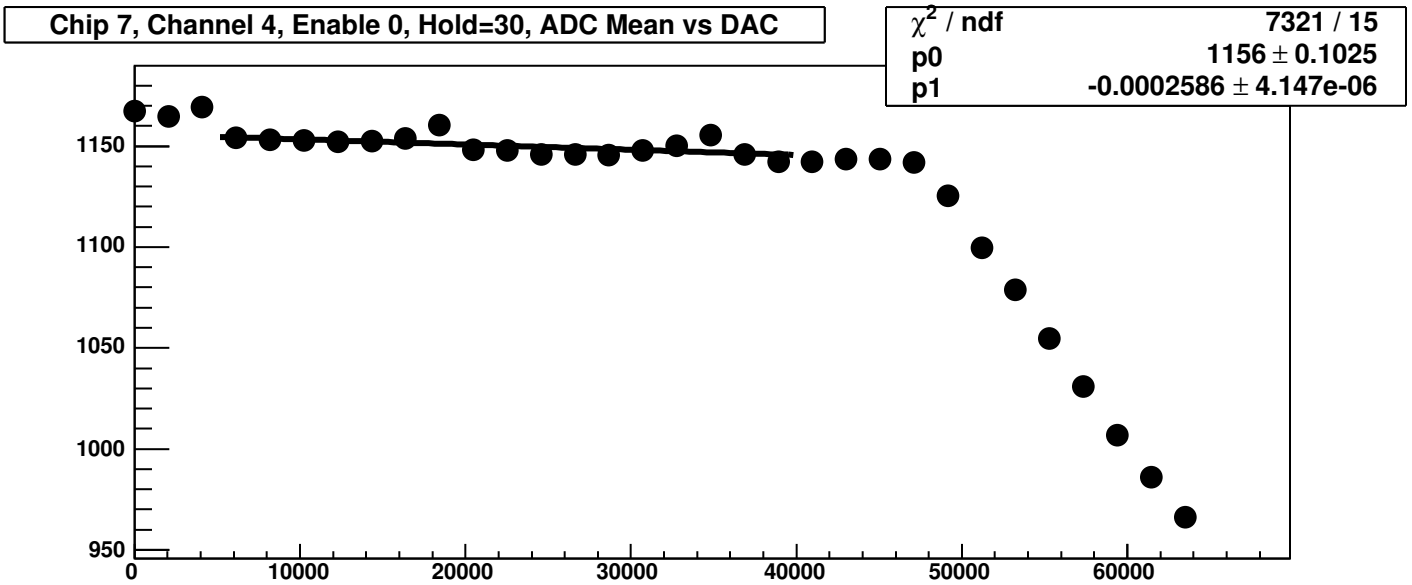

Chip 7, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

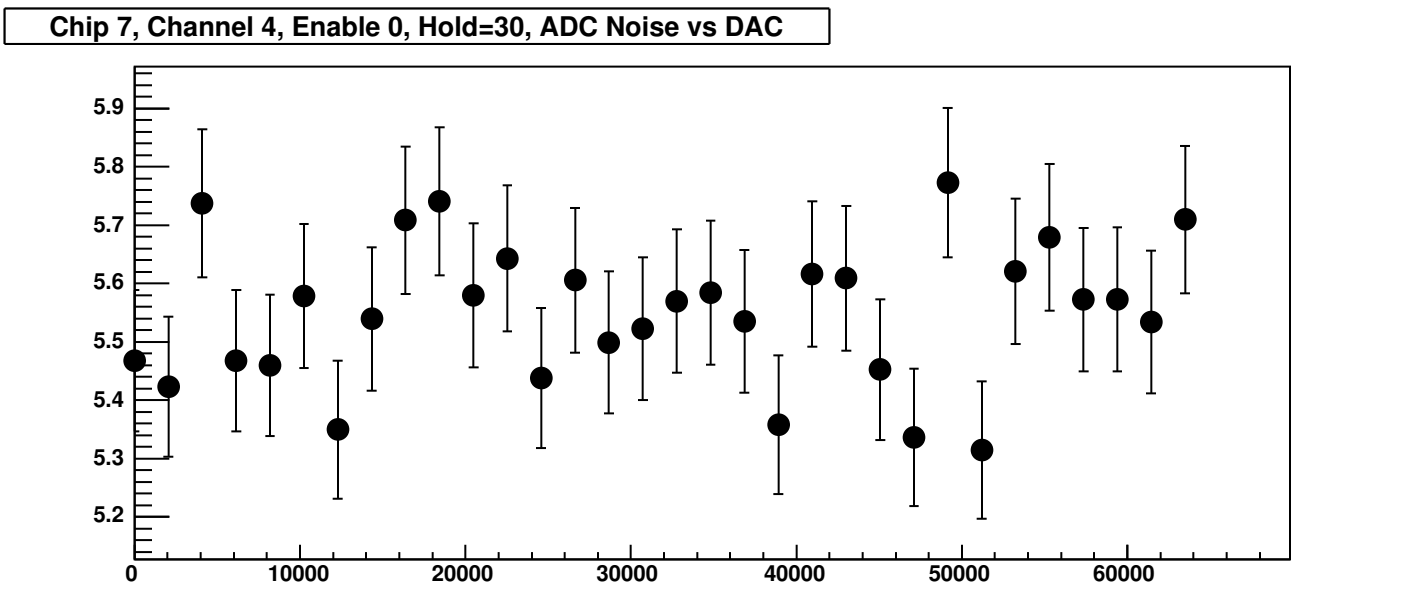

Chip 7, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

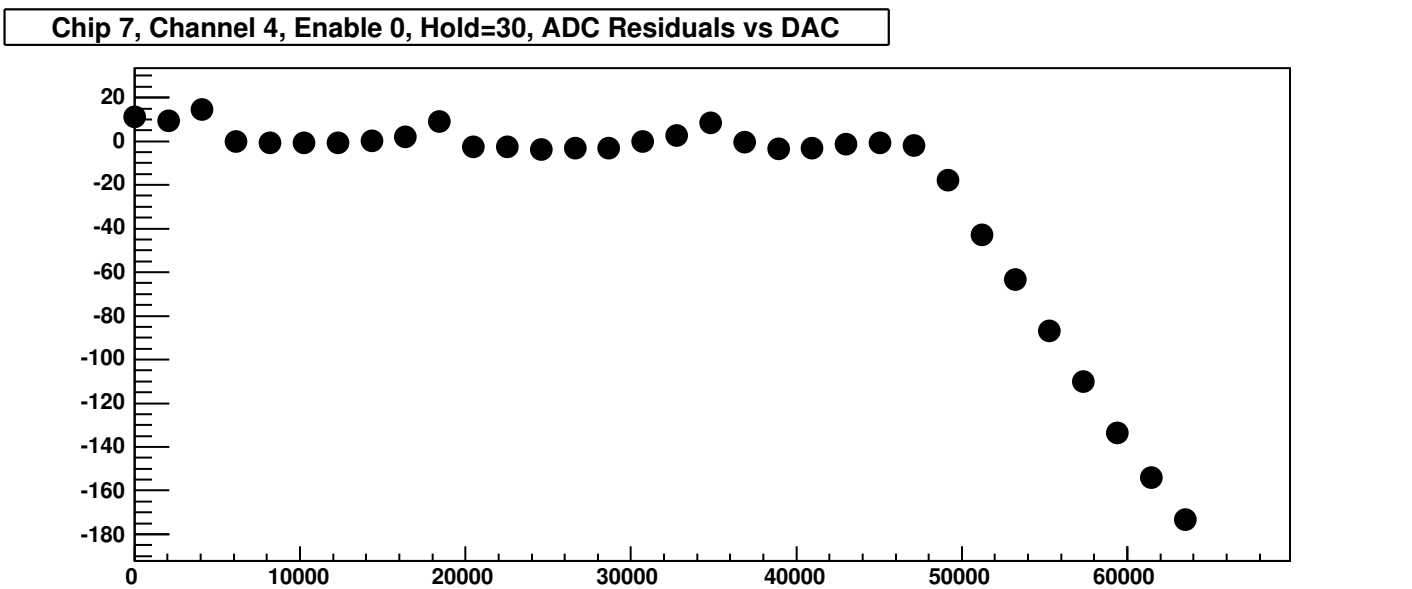

Chip 7, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.317e+04 / 15
p0 996.4 ± 0.1039
p1 -0.0002643 ± 4.231e-06
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

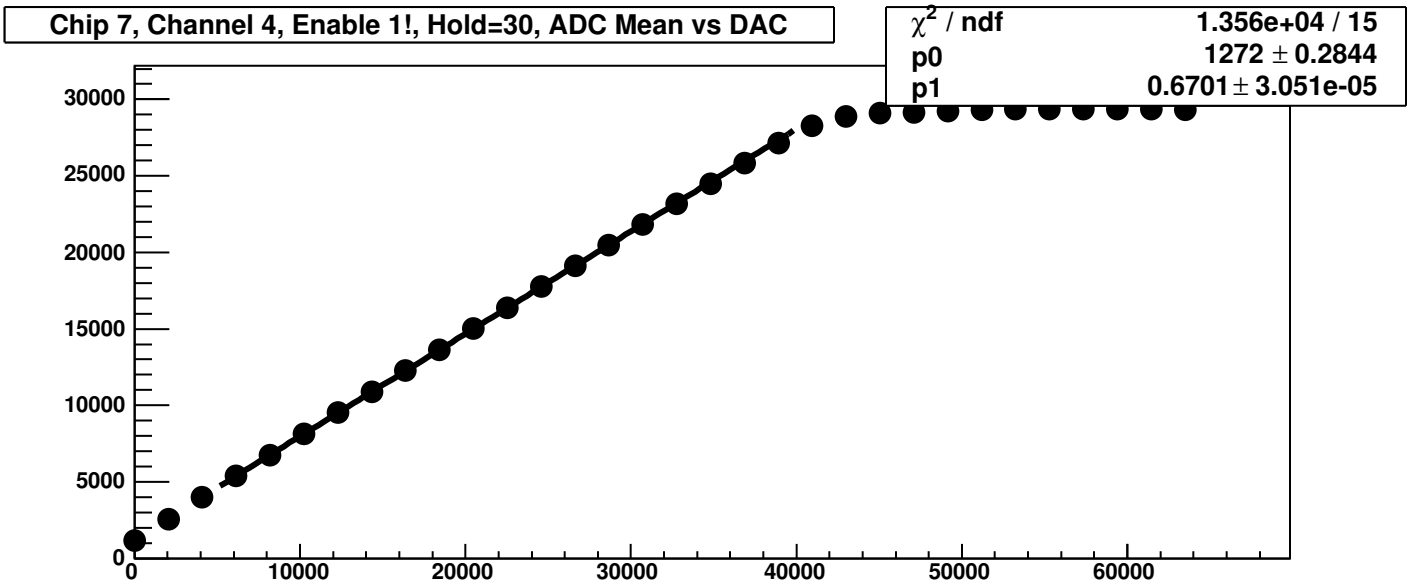

Chip 7, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

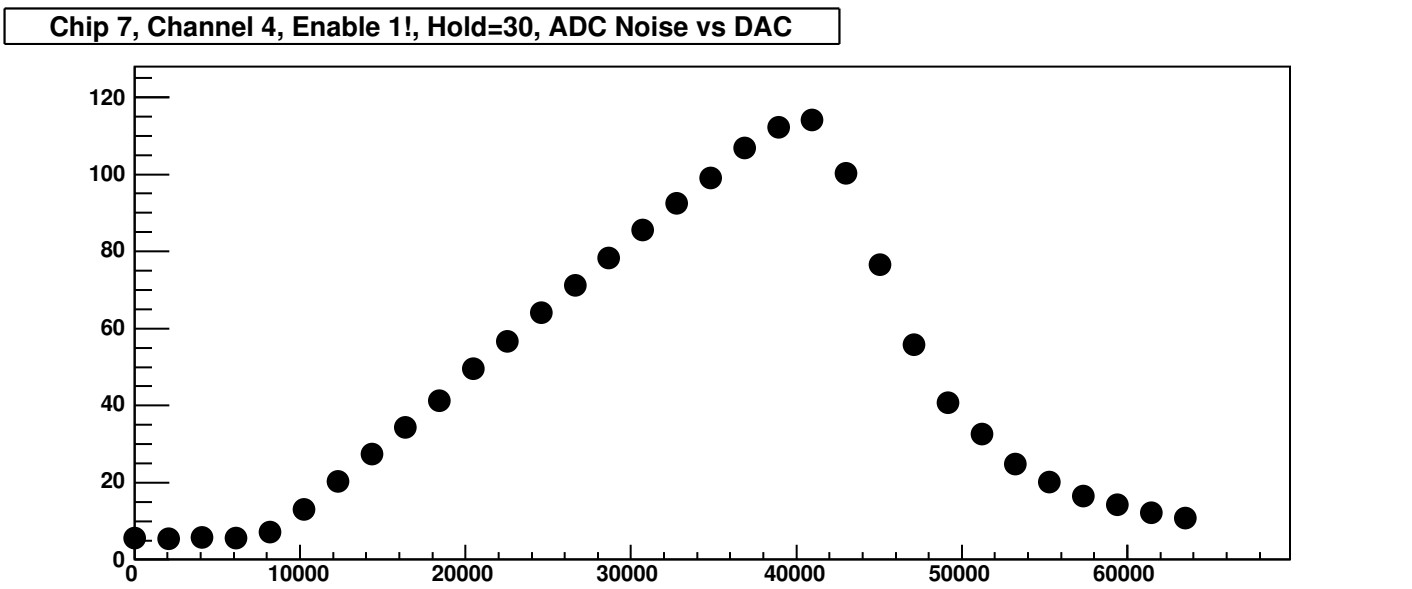

Chip 7, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

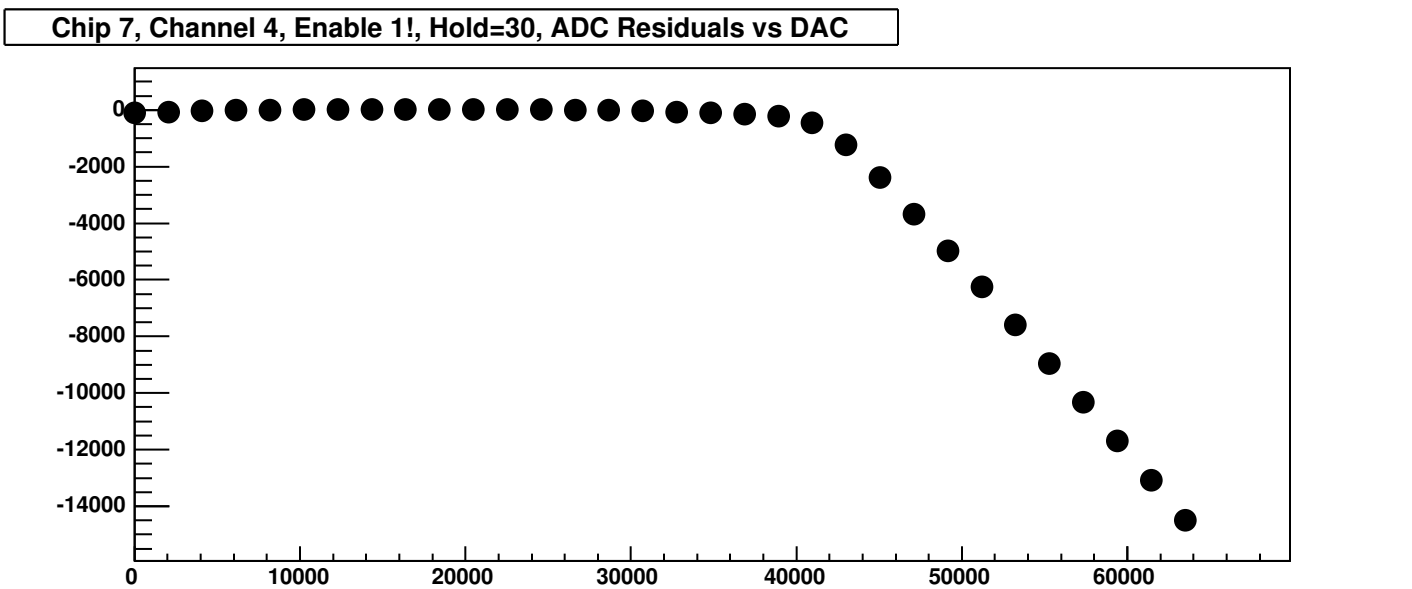

Chip 7, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7321 / 15
p0 1156 ± 0.1025
p1 -0.0002586 ± 4.147e-06
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
0
10000
20000
30000
40000
50000
60000

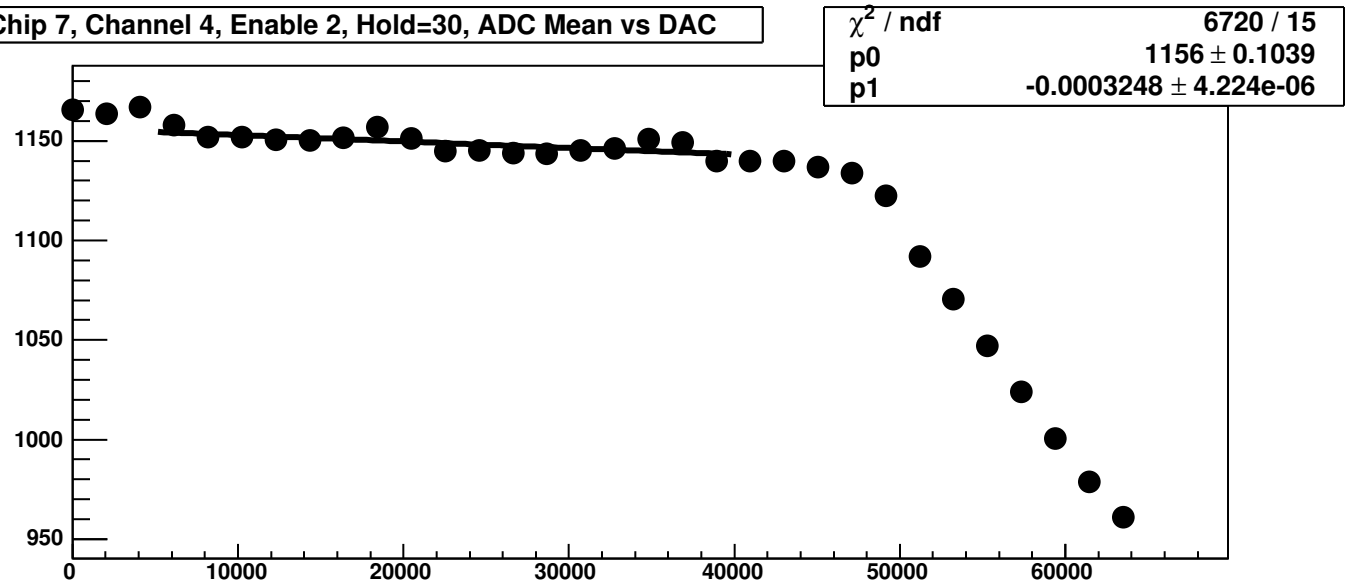

Chip 7, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.356e+04 / 15
p0 1272 ± 0.2844
p1 0.6701 ± 3.051e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

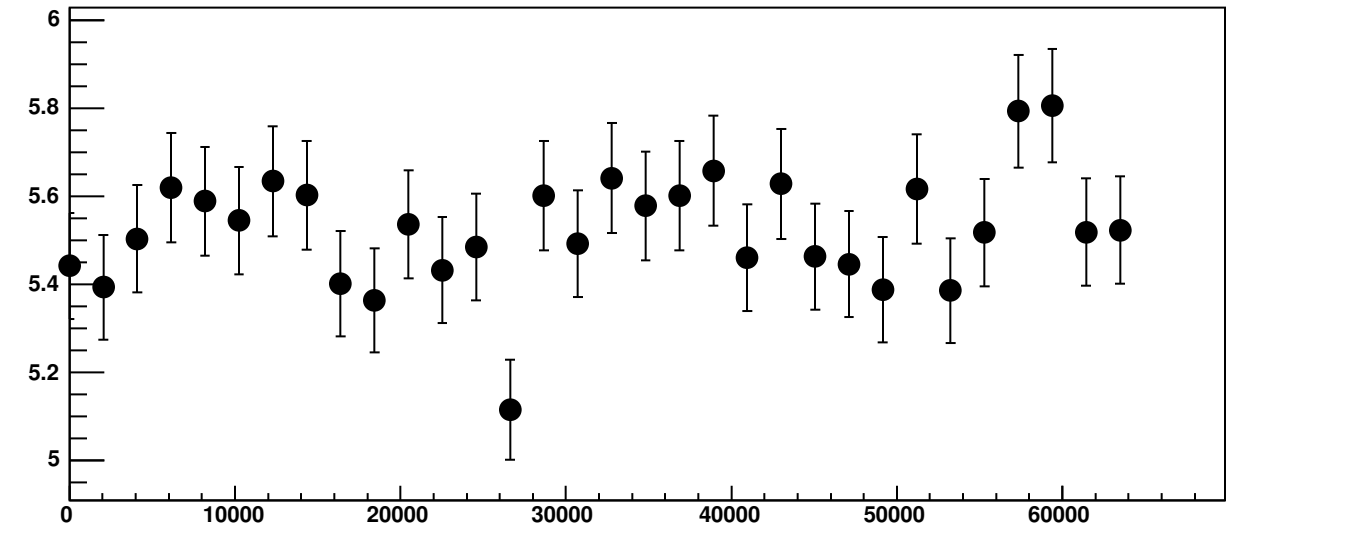

Chip 7, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

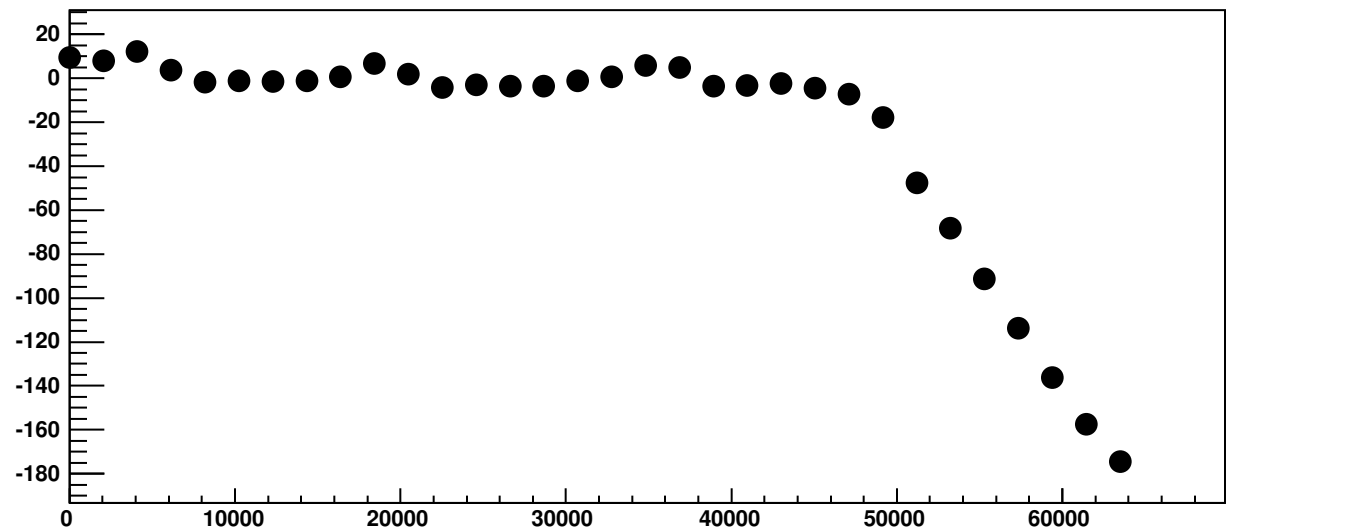
Chip 7, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

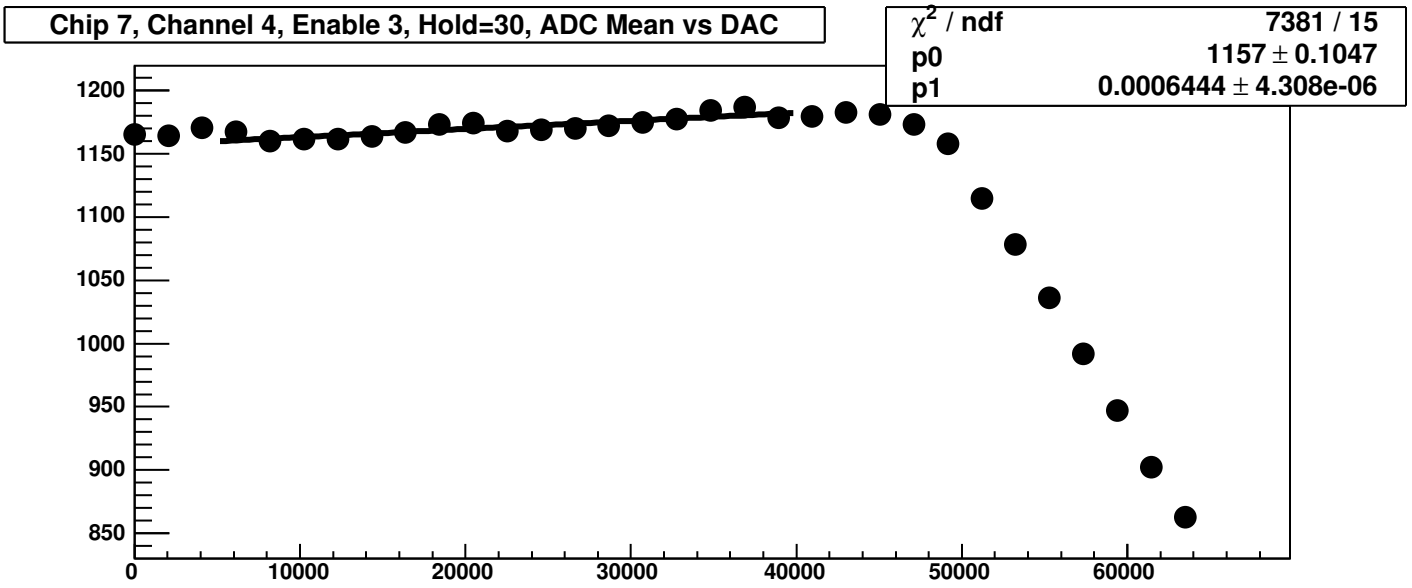


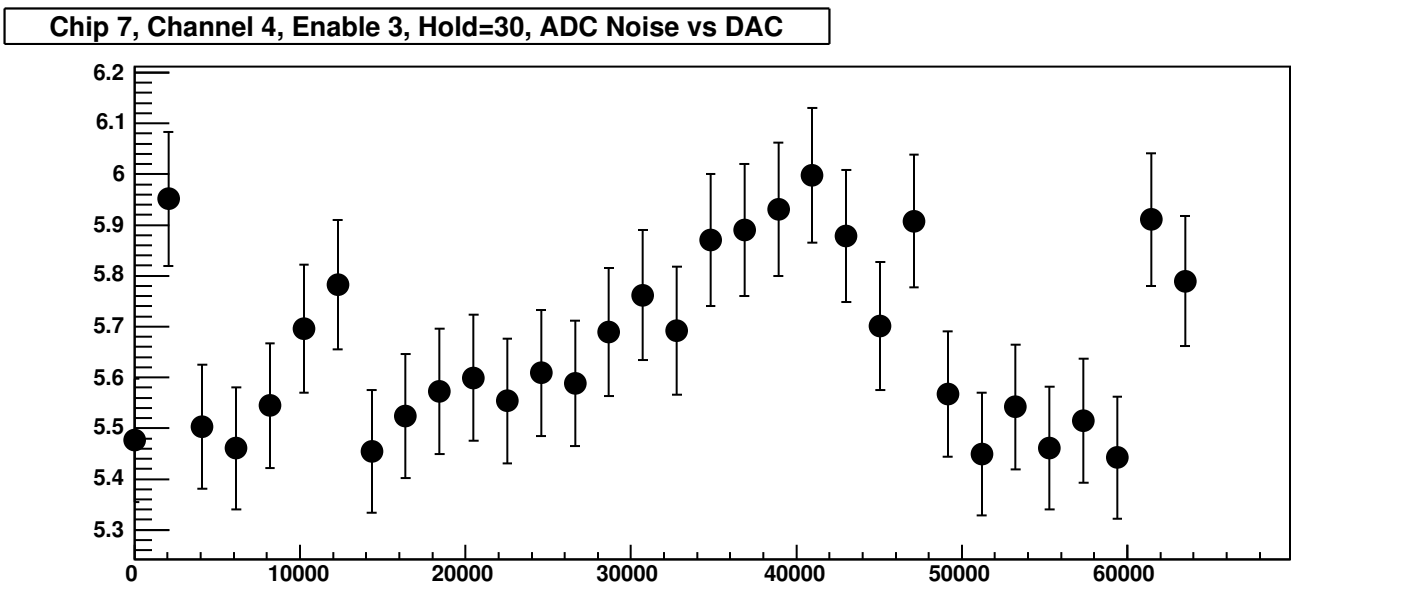
Chip 7, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC

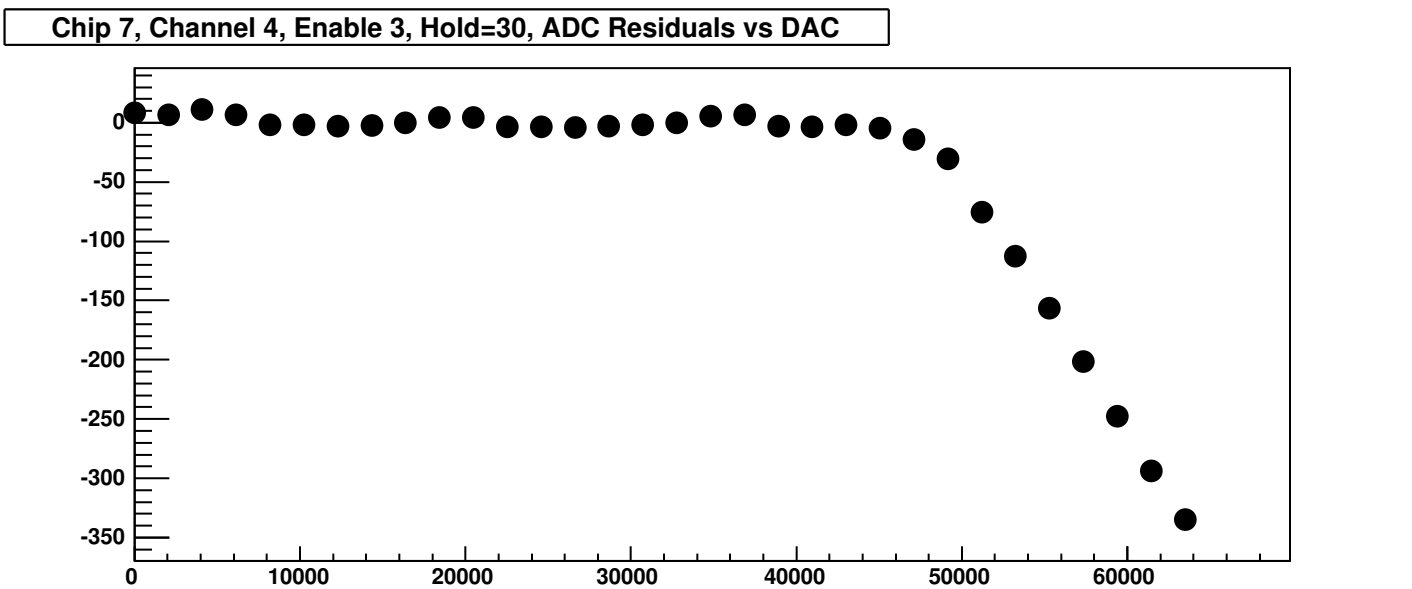


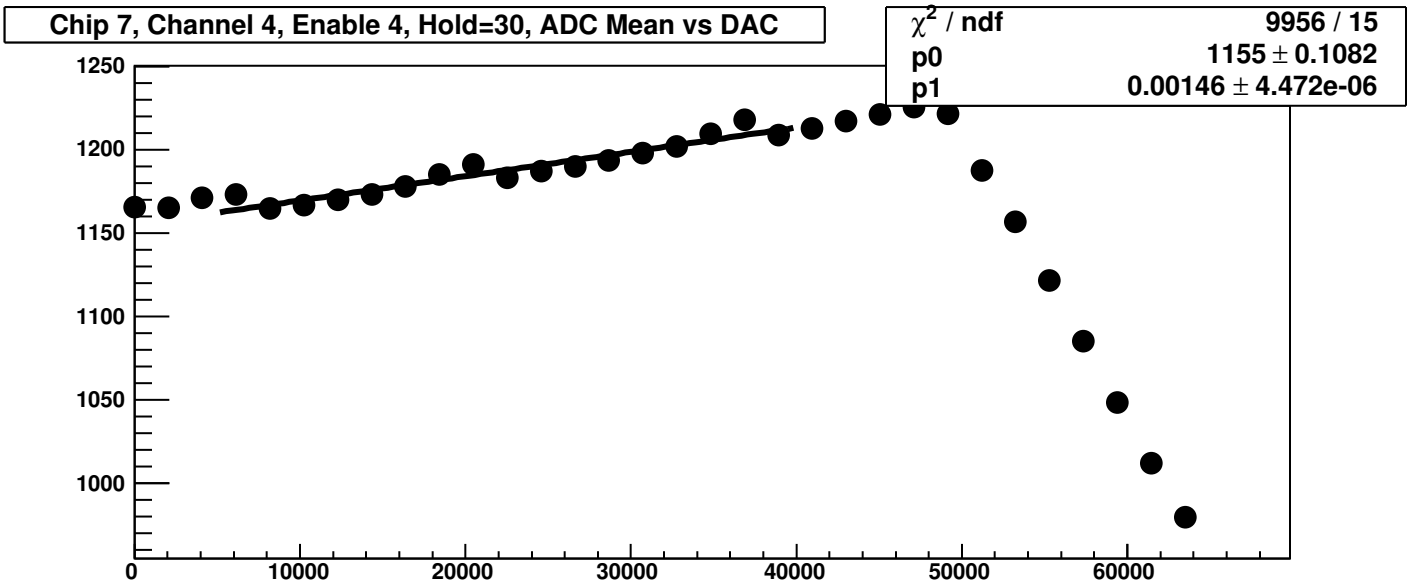
Chip 7, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

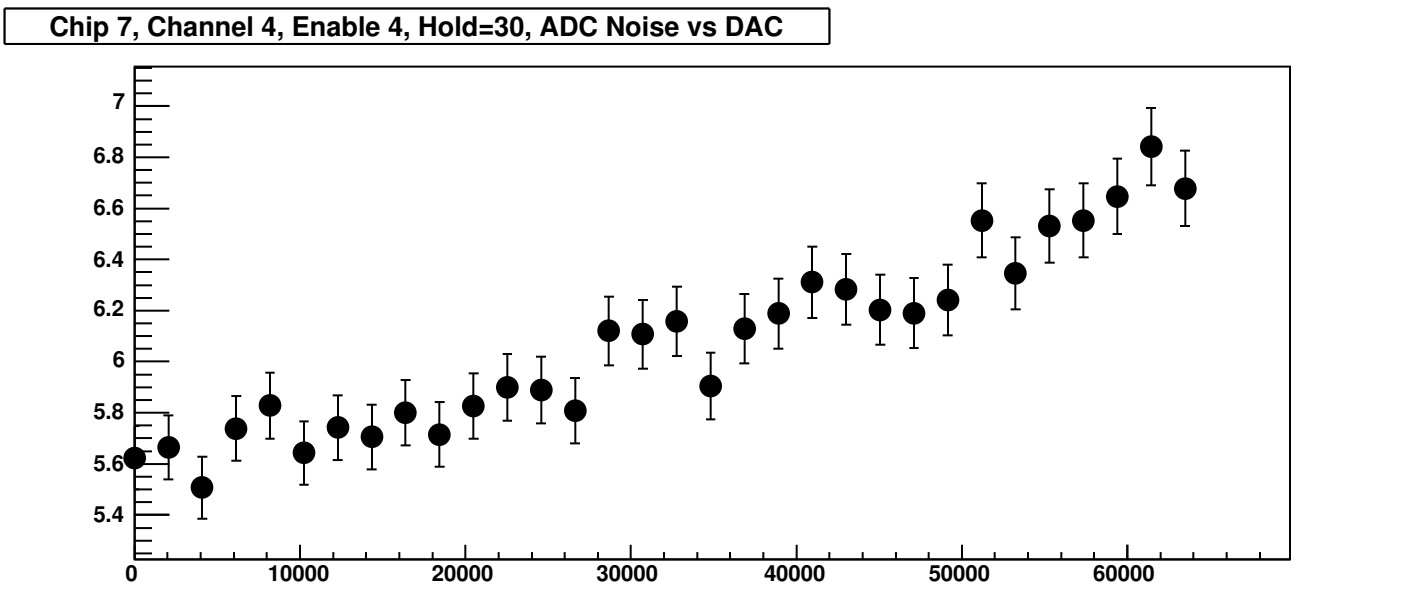


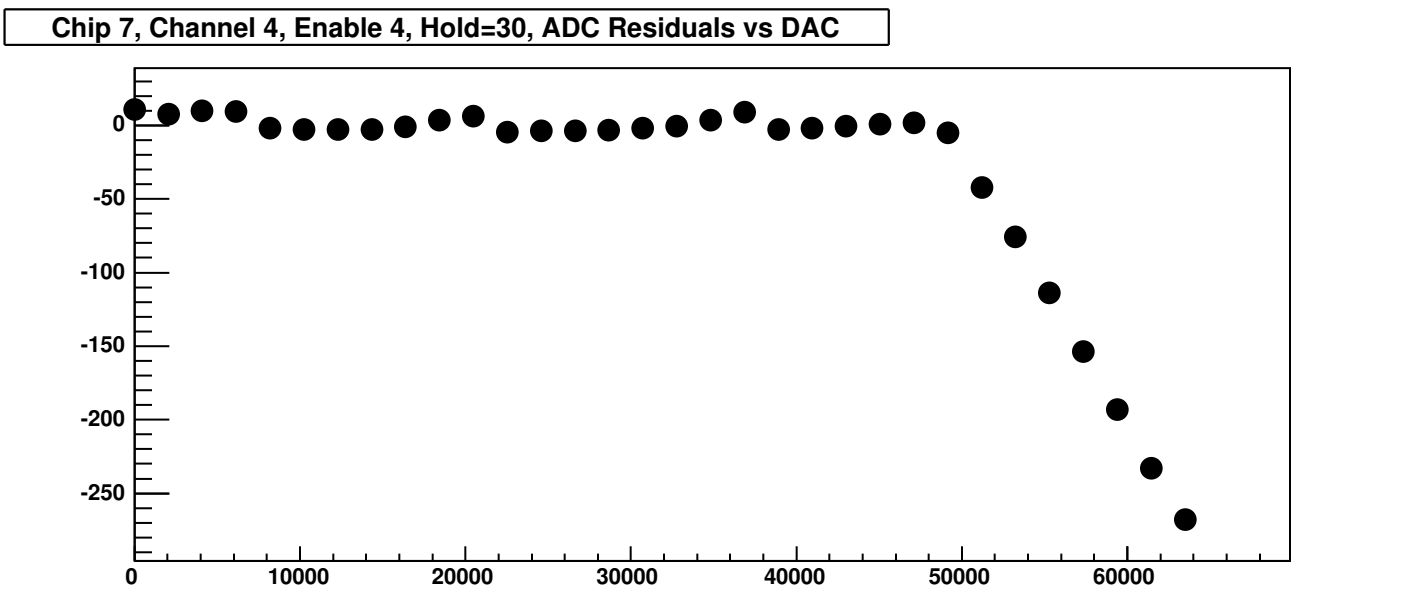

Chip 7, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7381 / 15
p0 1157 ± 0.1047
p1 0.0006444 ± 4.308e-06
1200
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

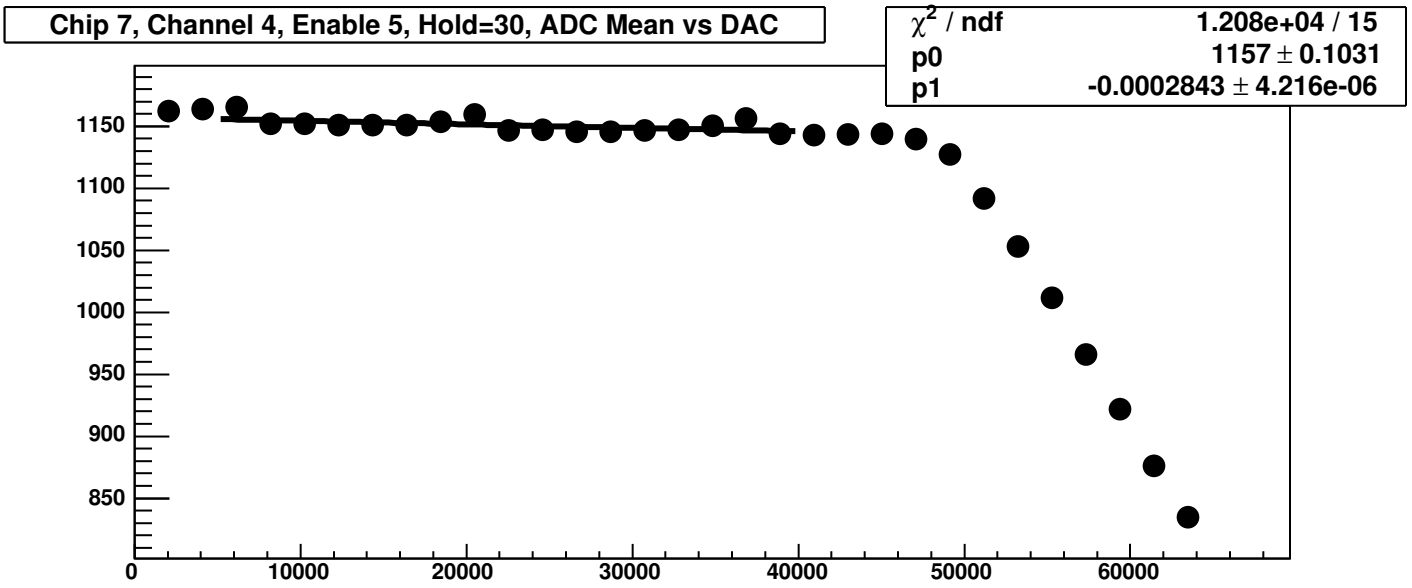

Chip 7, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
0
10000
20000
30000
40000
50000
60000

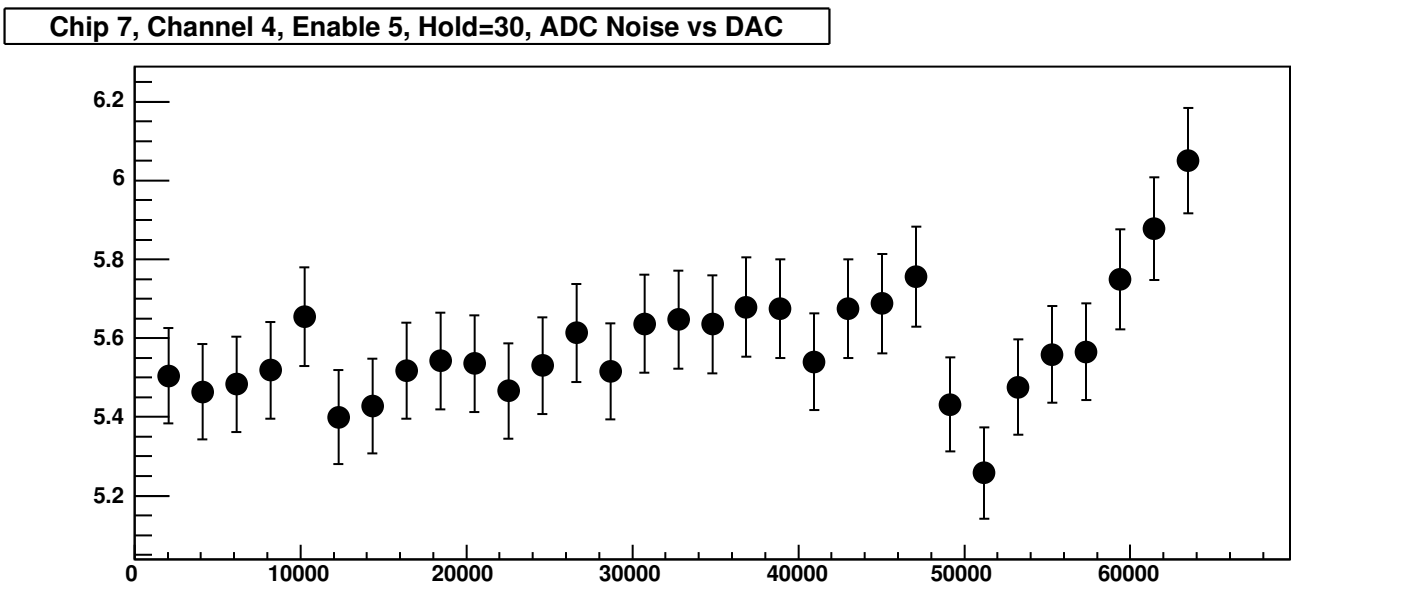

Chip 7, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

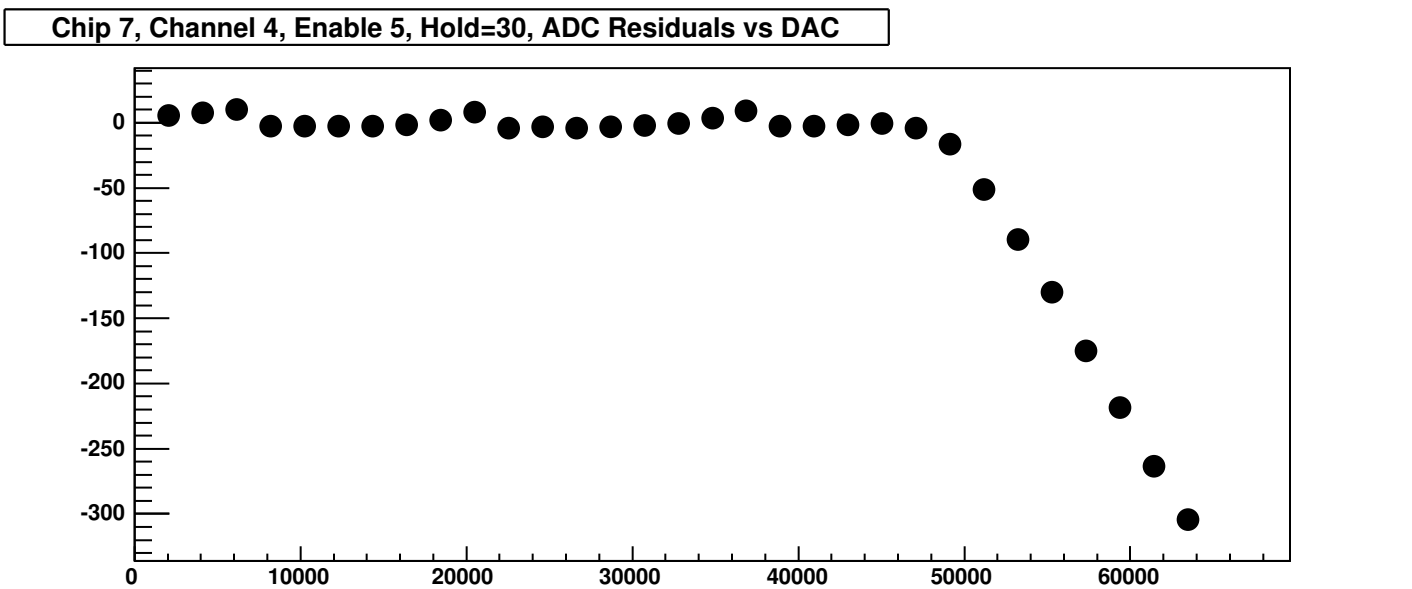

Chip 7, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9956 / 15
p0 1155 ± 0.1082
p1 0.00146 ± 4.472e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

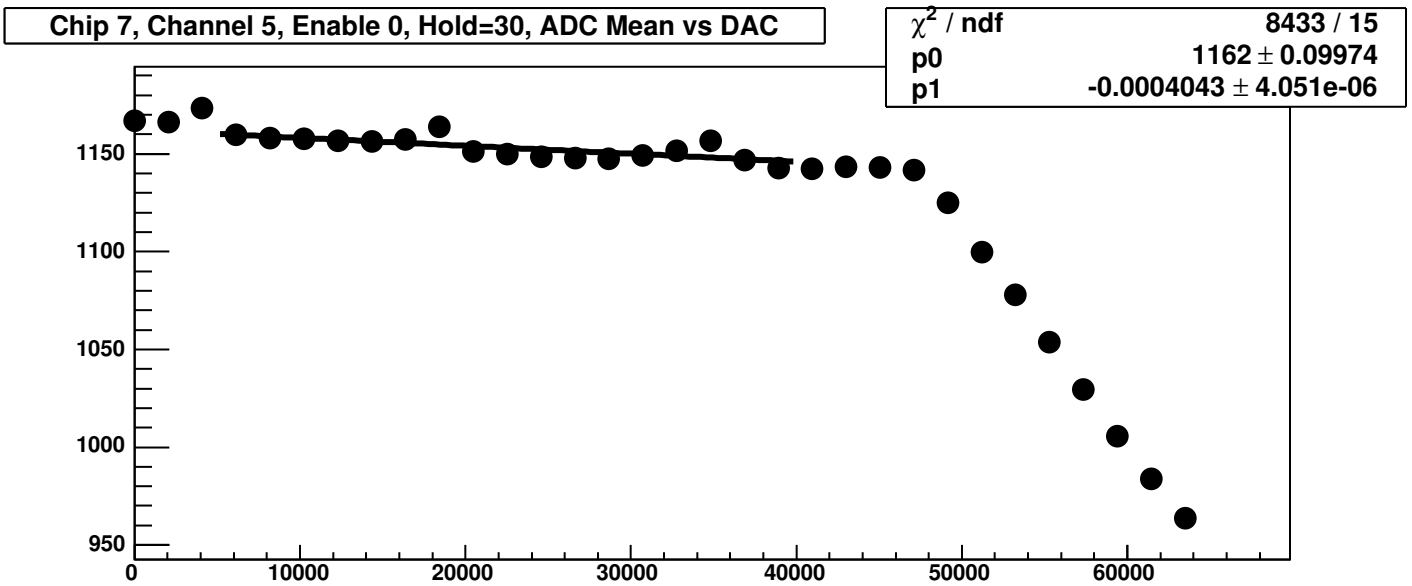

Chip 7, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

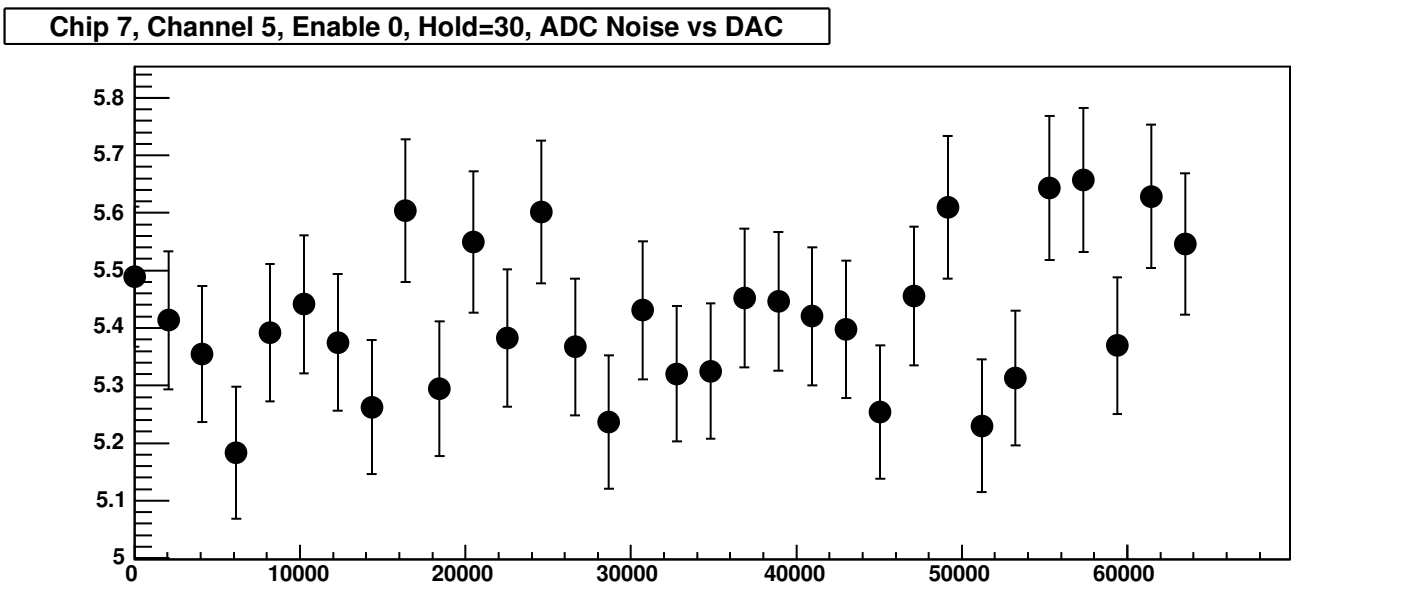

Chip 7, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

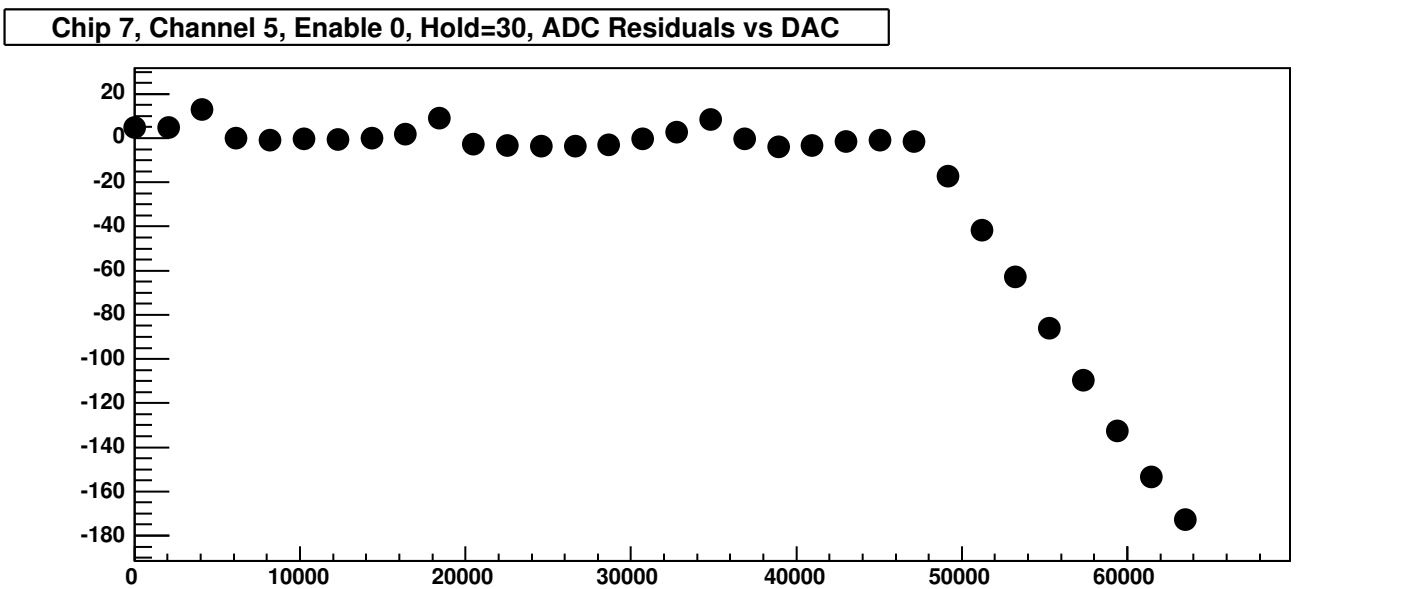

Chip 7, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.208e+04 / 15
p0 1157 ± 0.1031
p1 -0.0002843 ± 4.216e-06
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

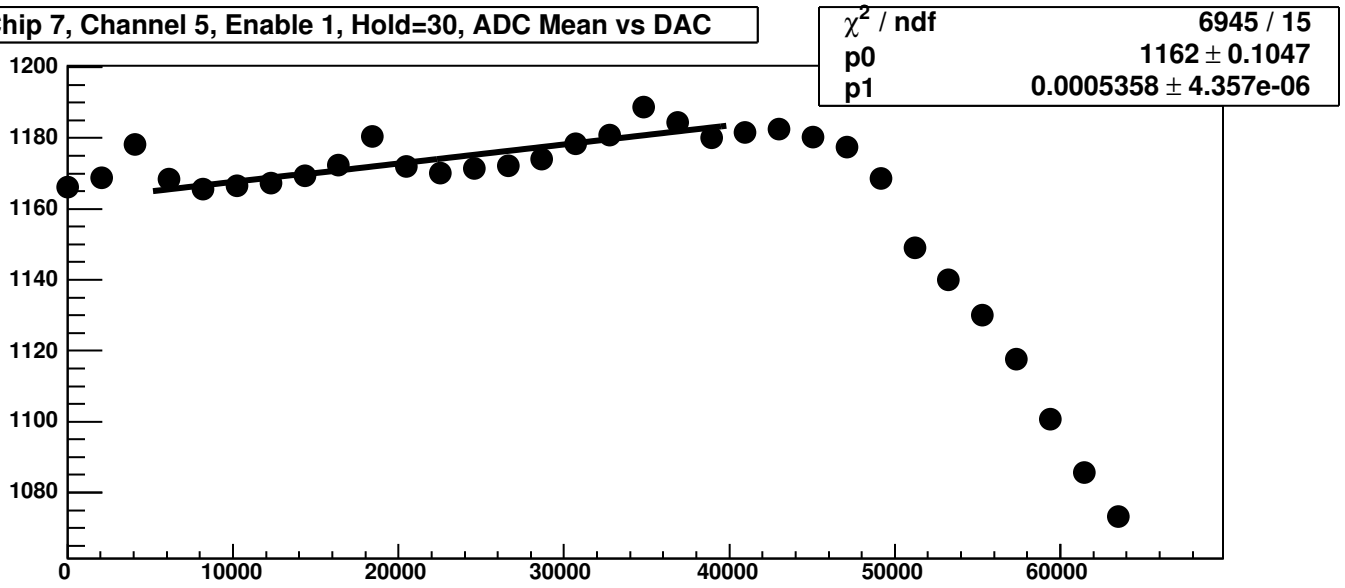

Chip 7, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8433 / 15
p0 1162 ± 0.09974
p1 -0.0004043 ± 4.051e-06
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

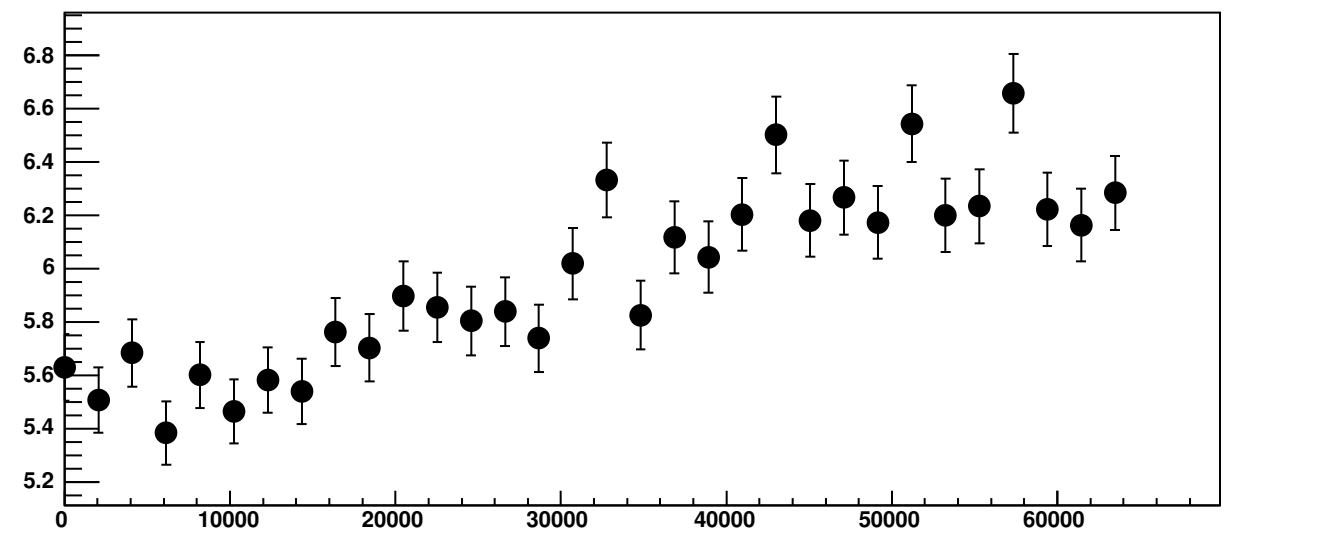

Chip 7, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
5
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

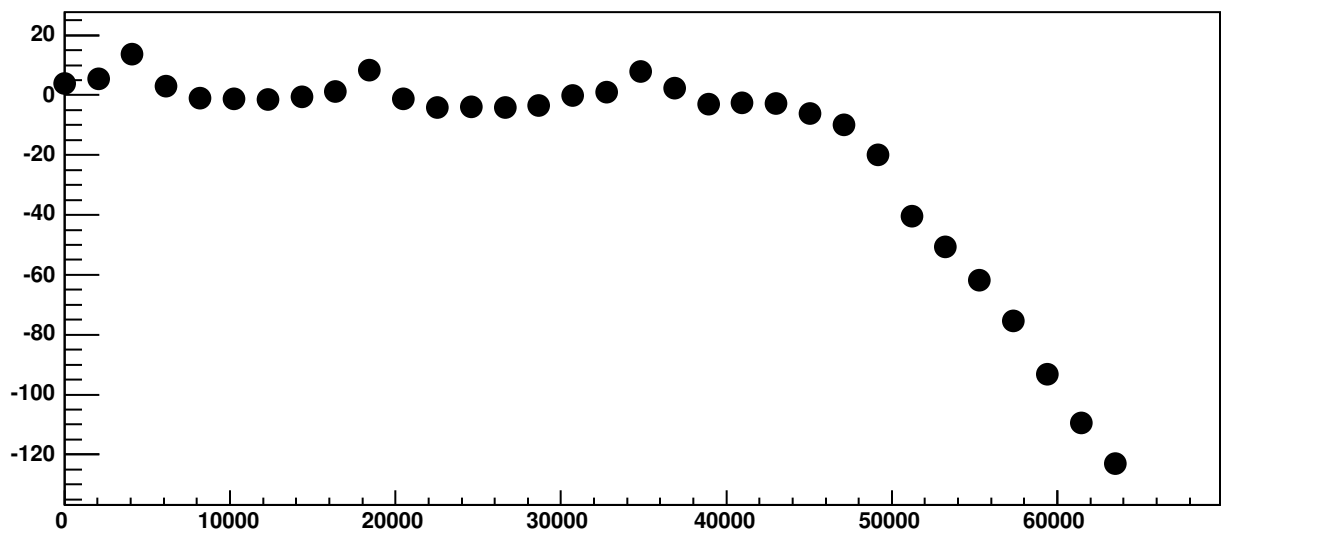
Chip 7, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC

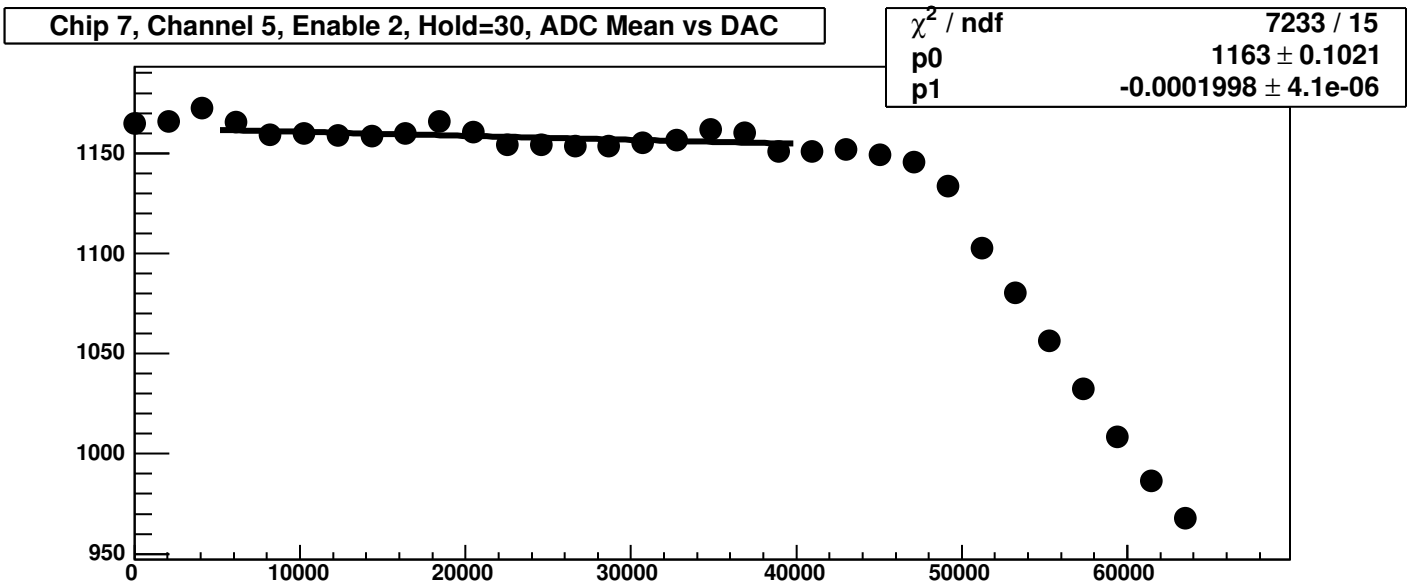


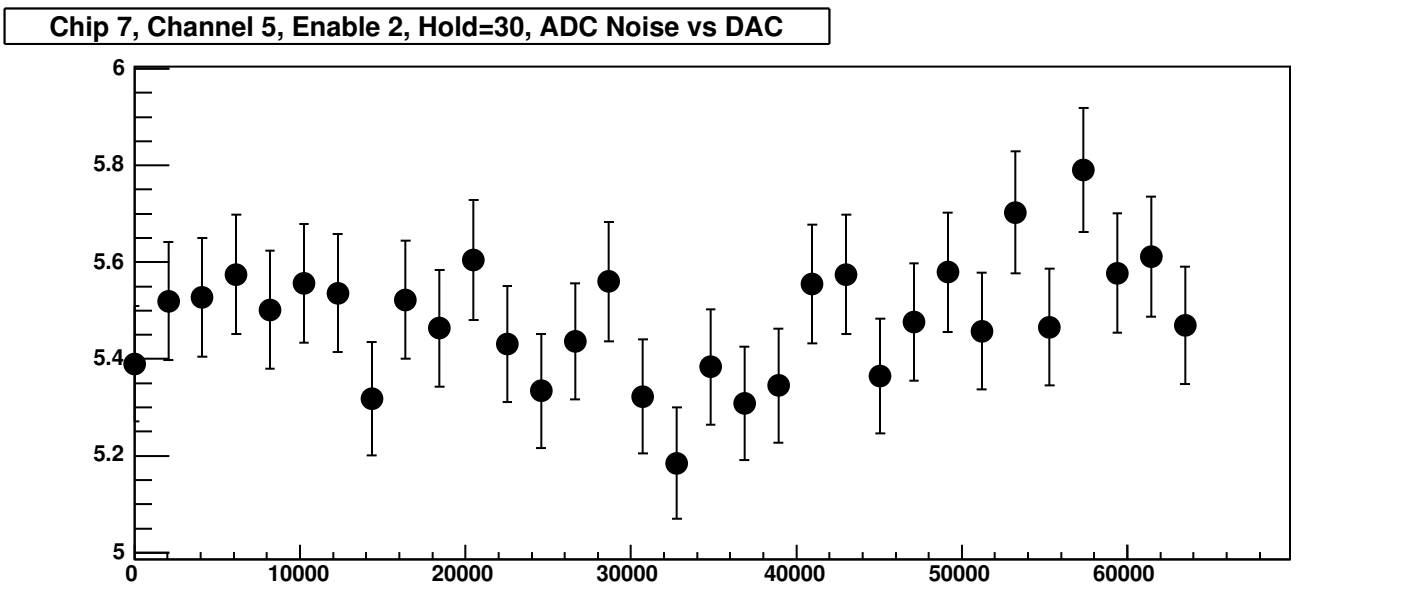
Chip 7, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC

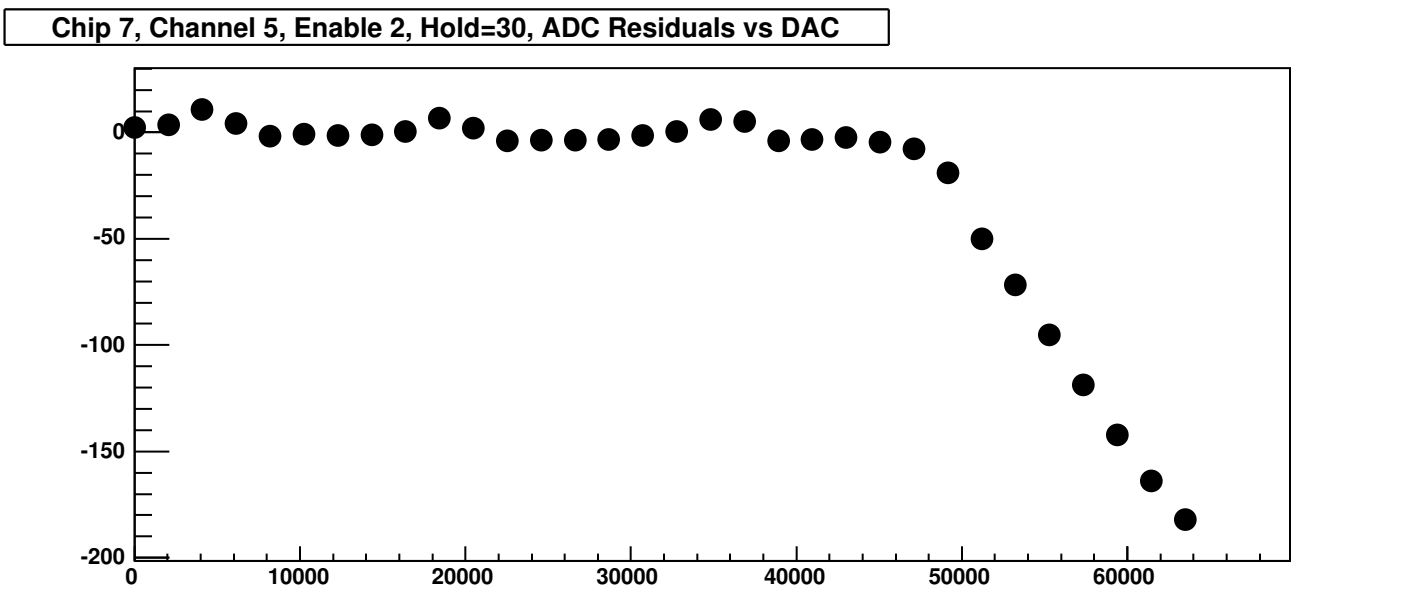


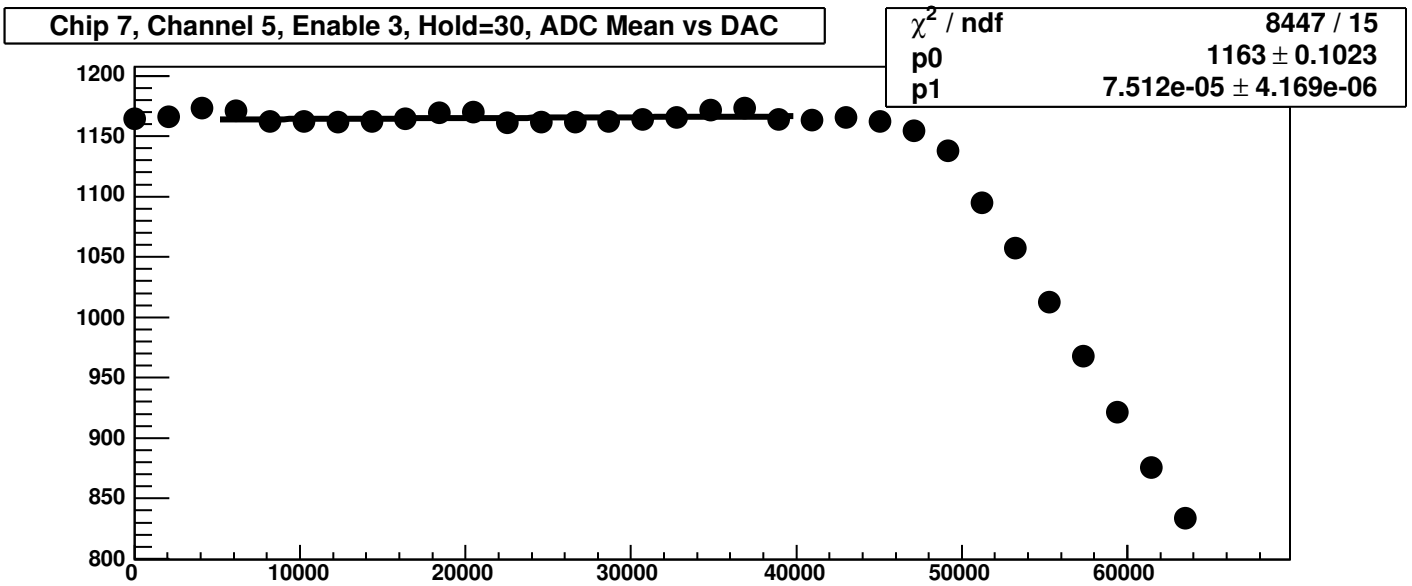
Chip 7, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC

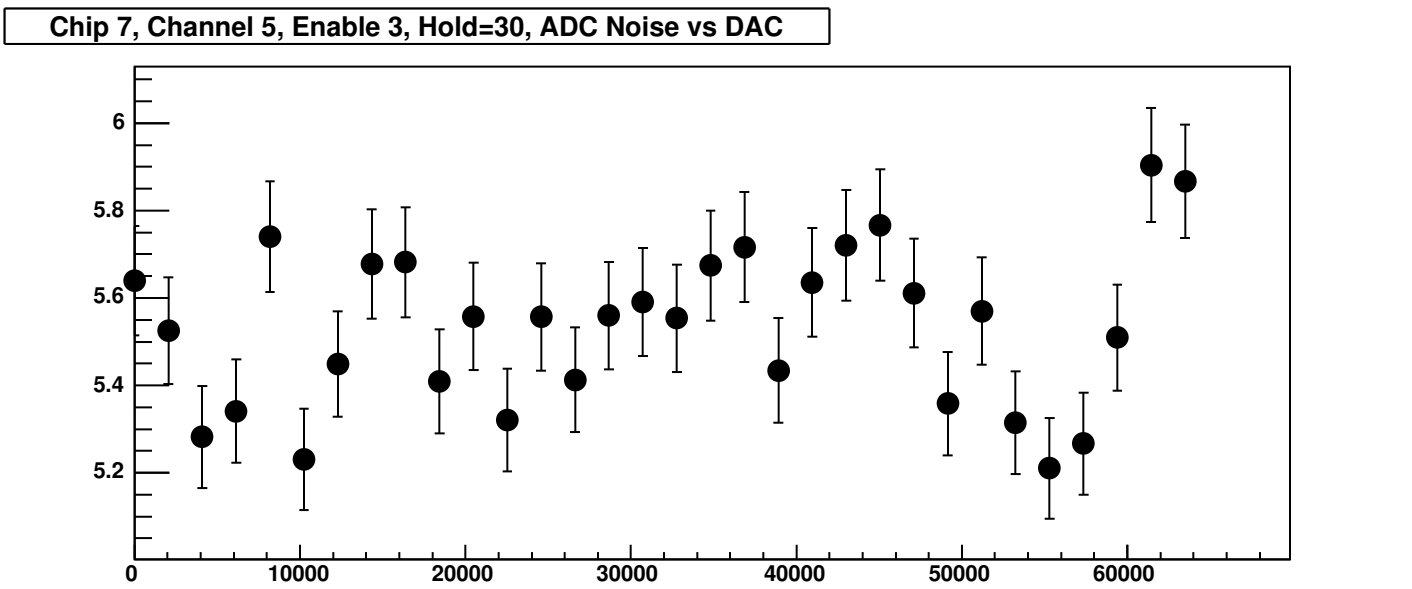


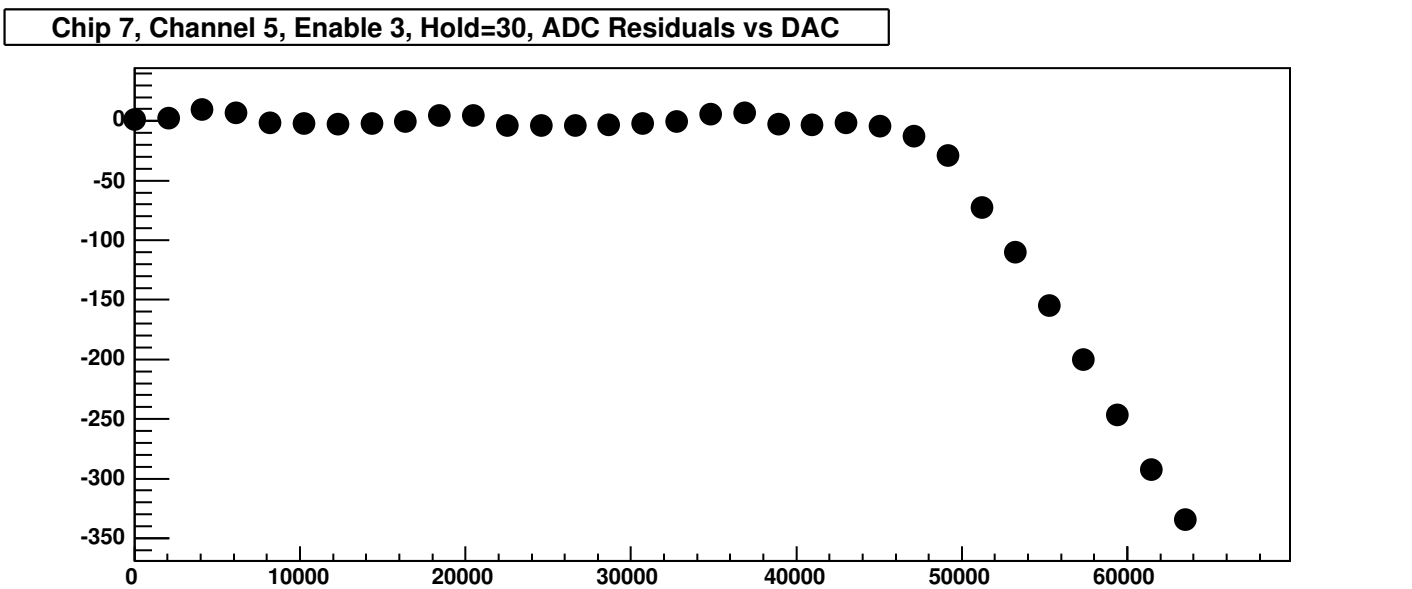

Chip 7, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7233 / 15
p0 1163 ± 0.1021
p1 -0.0001998 ± 4.1e-06
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

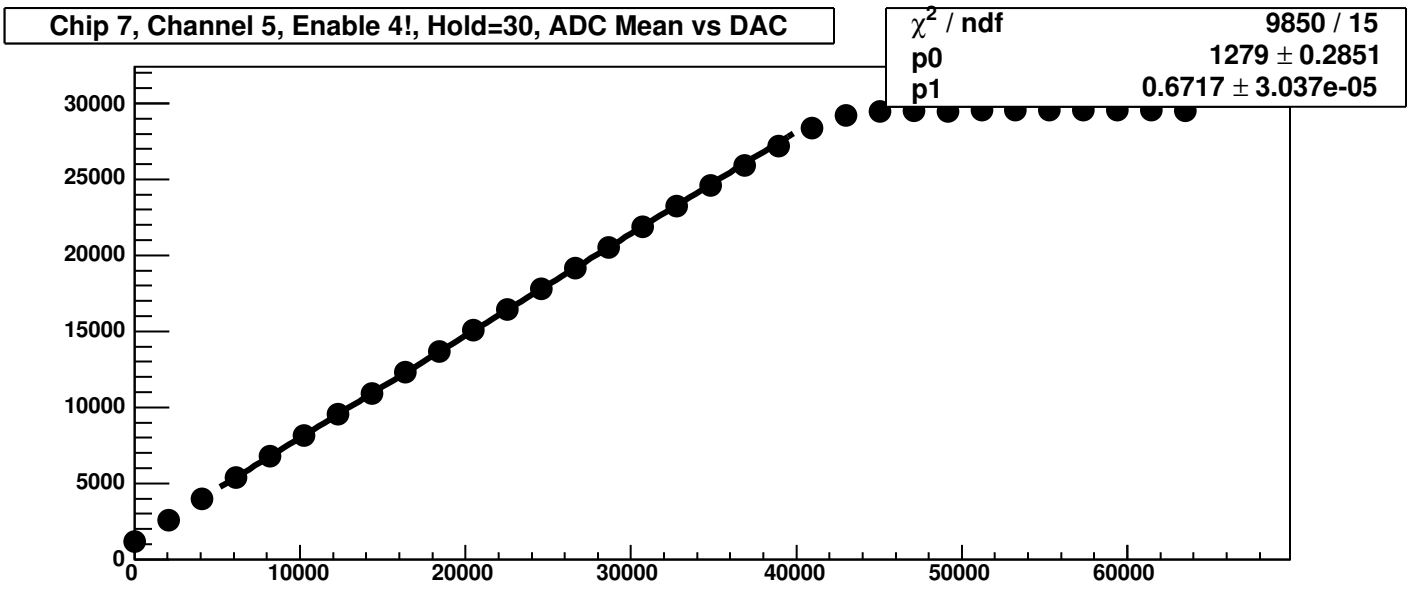

Chip 7, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

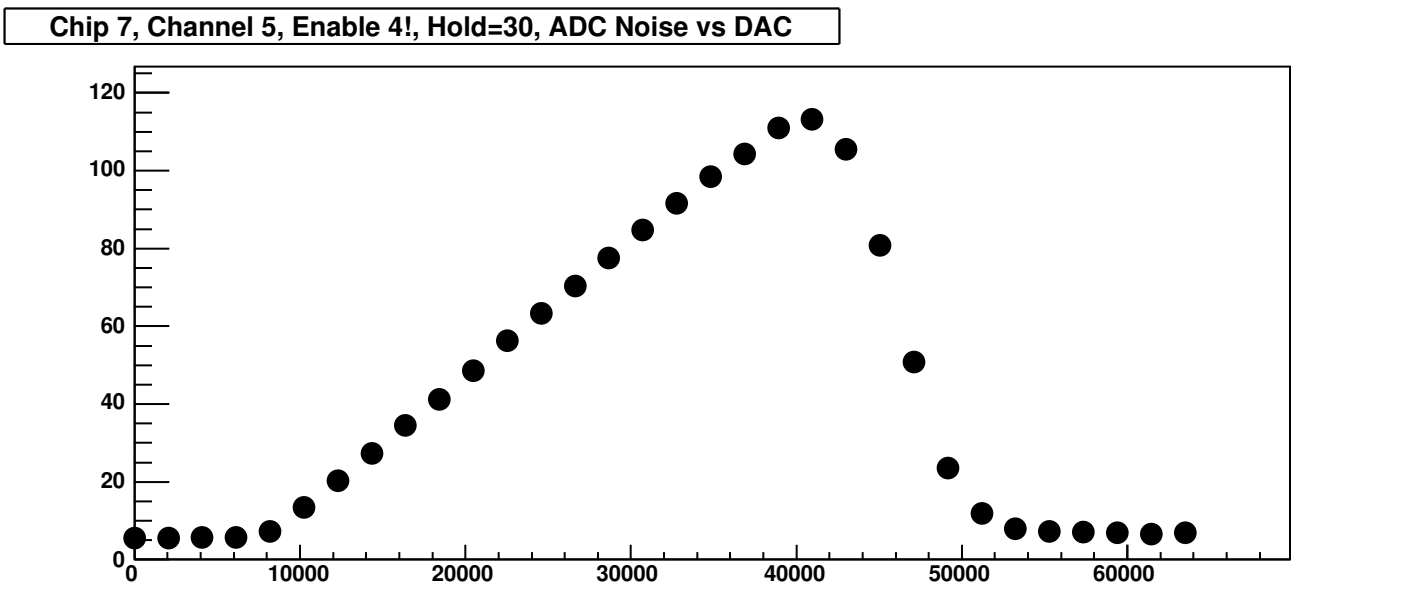

Chip 7, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

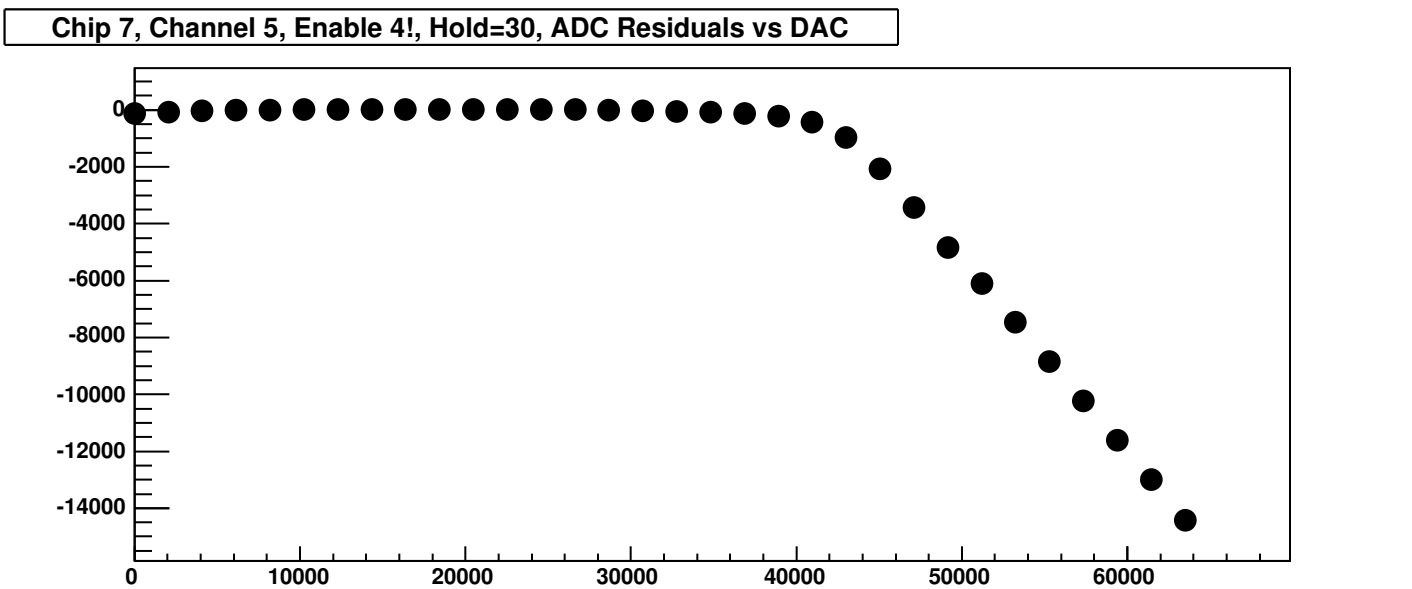

Chip 7, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8447 / 15
p0 1163 ± 0.1023
p1 7.512e-05 ± 4.169e-06
1200
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

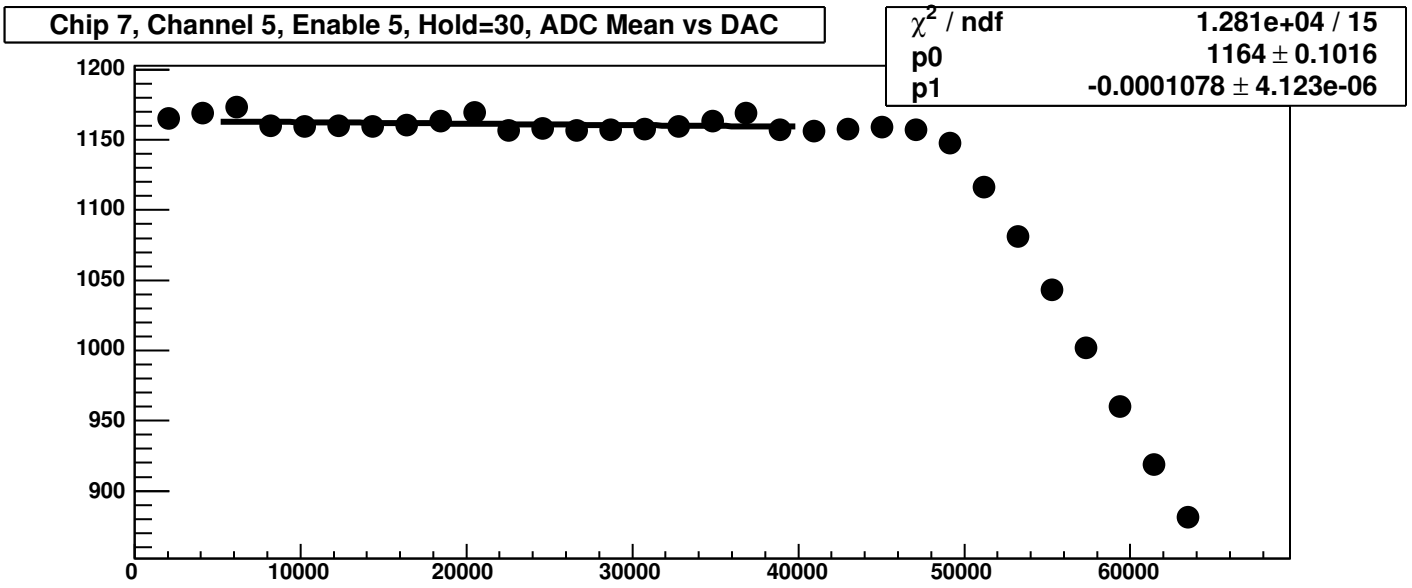

Chip 7, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

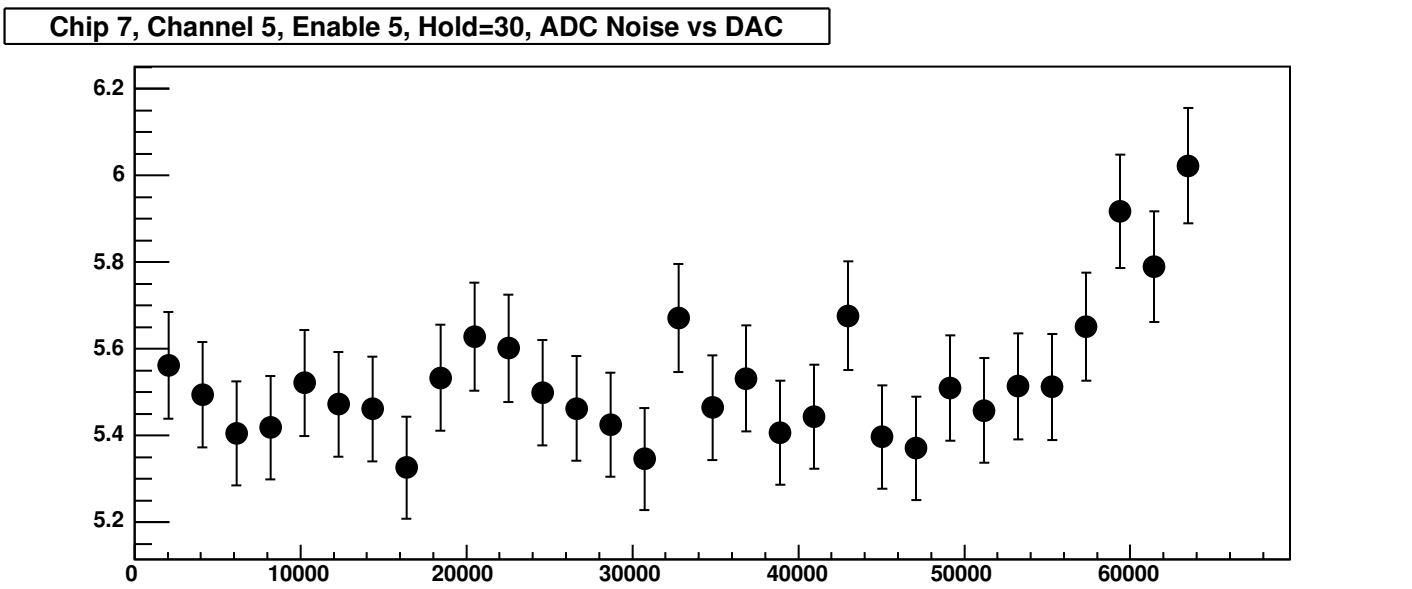

Chip 7, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

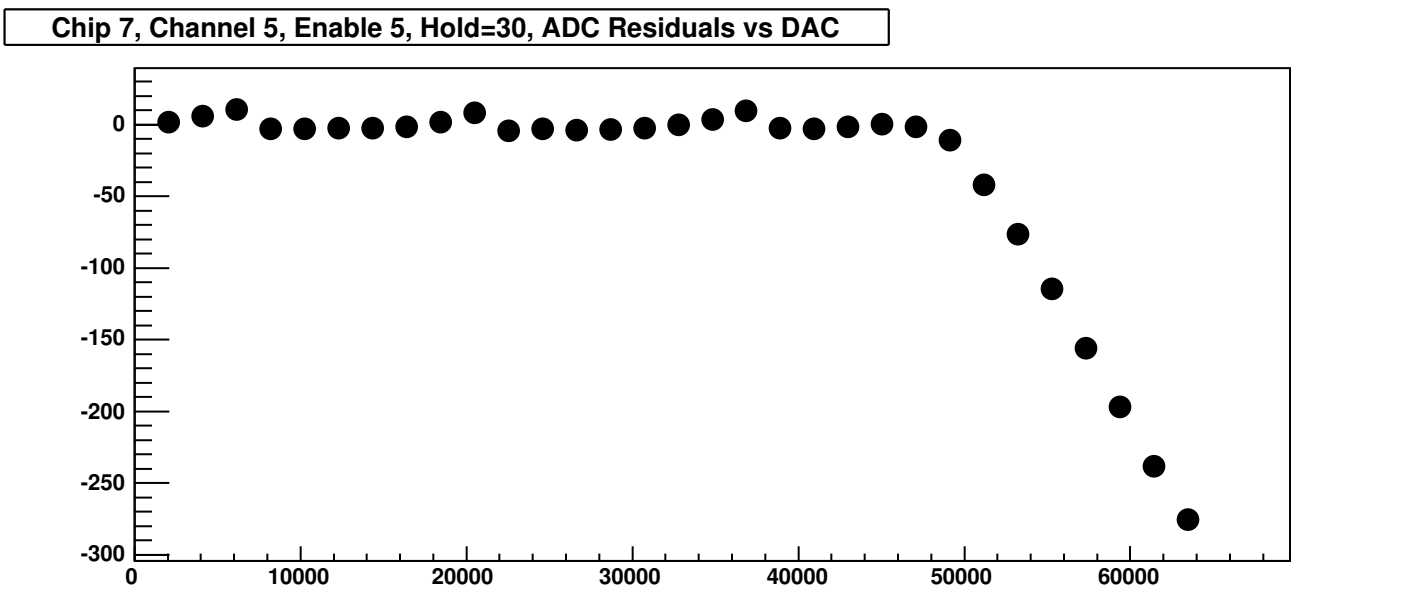

Chip 7, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 9850 / 15
p0 1279 ± 0.2851
p1 0.6717 ± 3.037e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

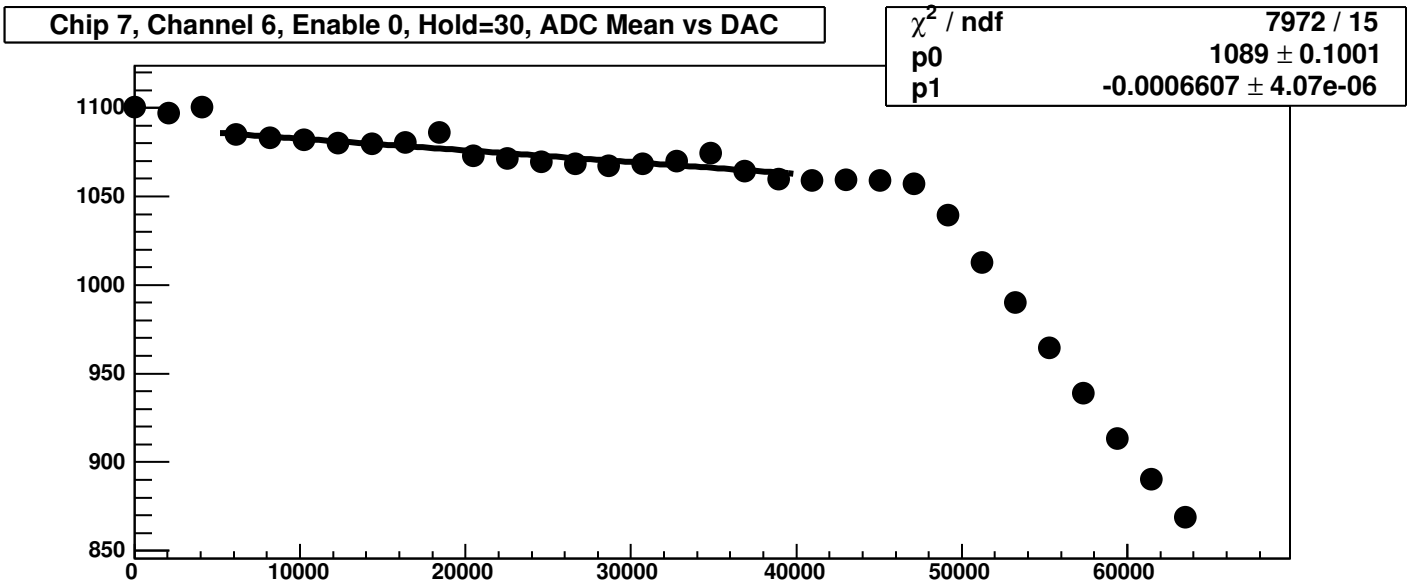

Chip 7, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

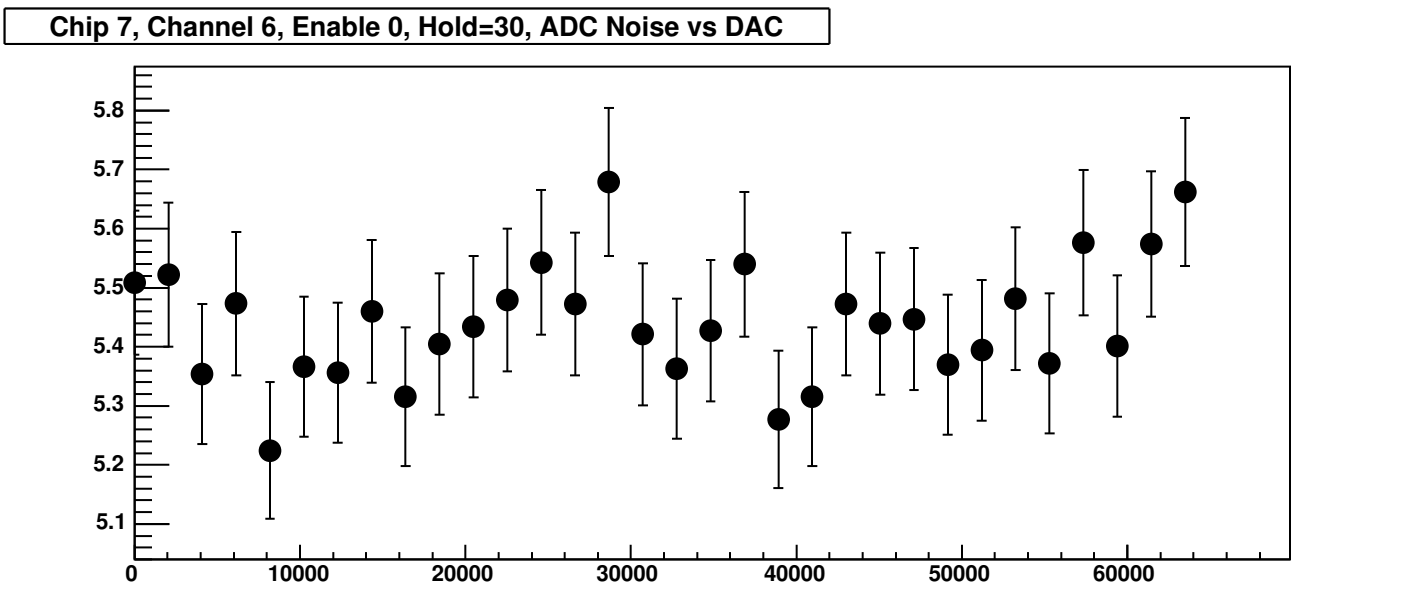

Chip 7, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

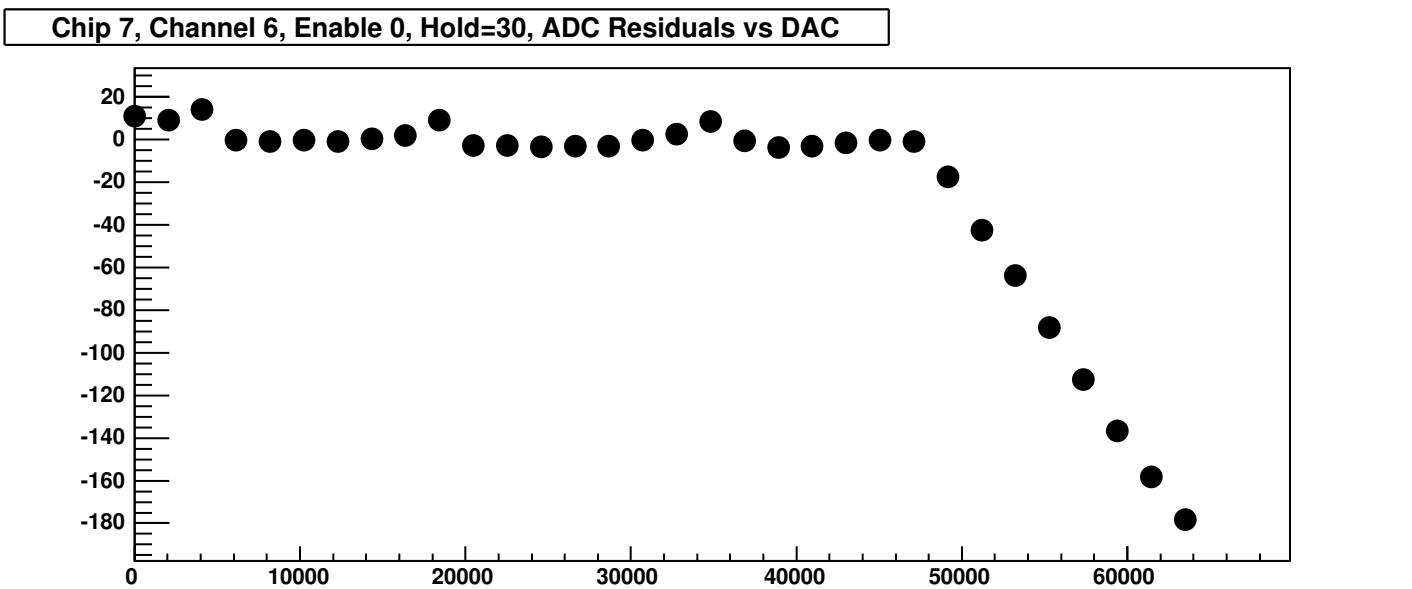

Chip 7, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.281e+04 / 15
p0 1164 ± 0.1016
p1 -0.0001078 ± 4.123e-06
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

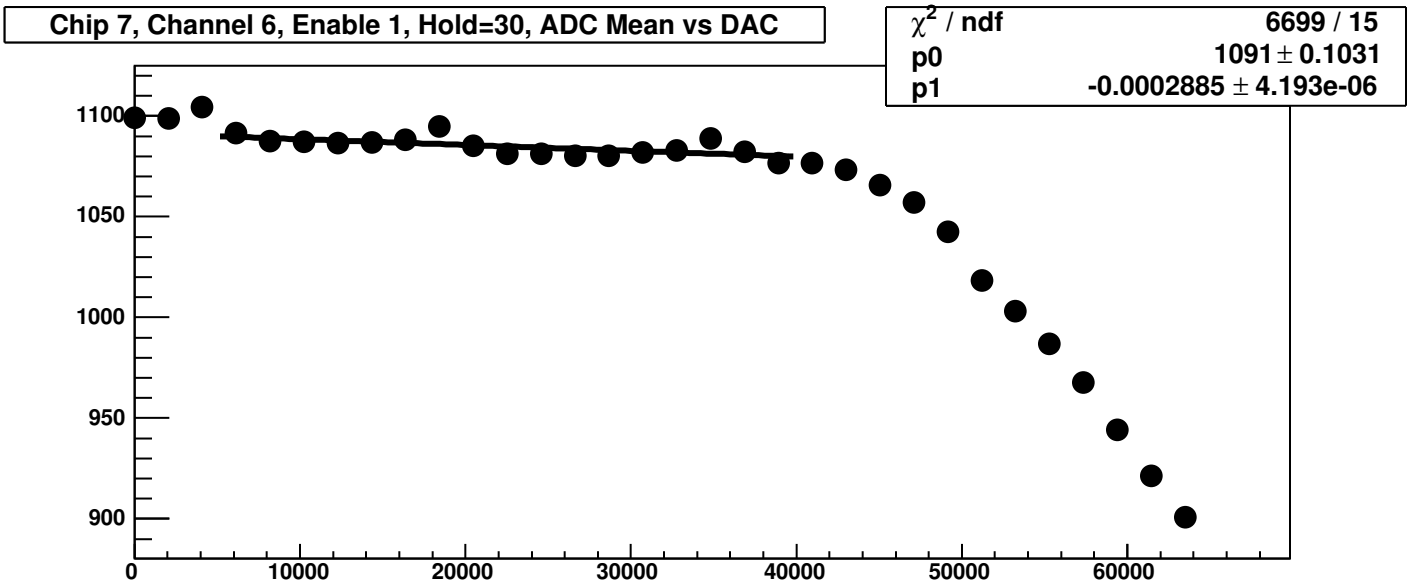

Chip 7, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

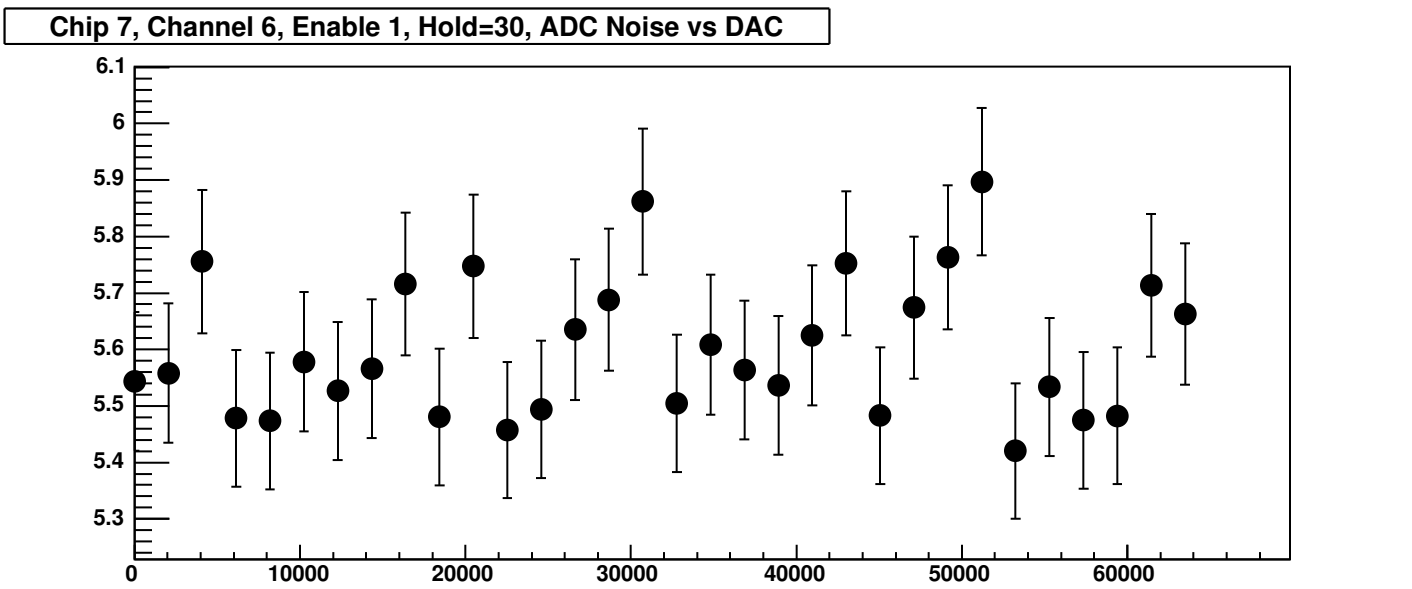

Chip 7, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

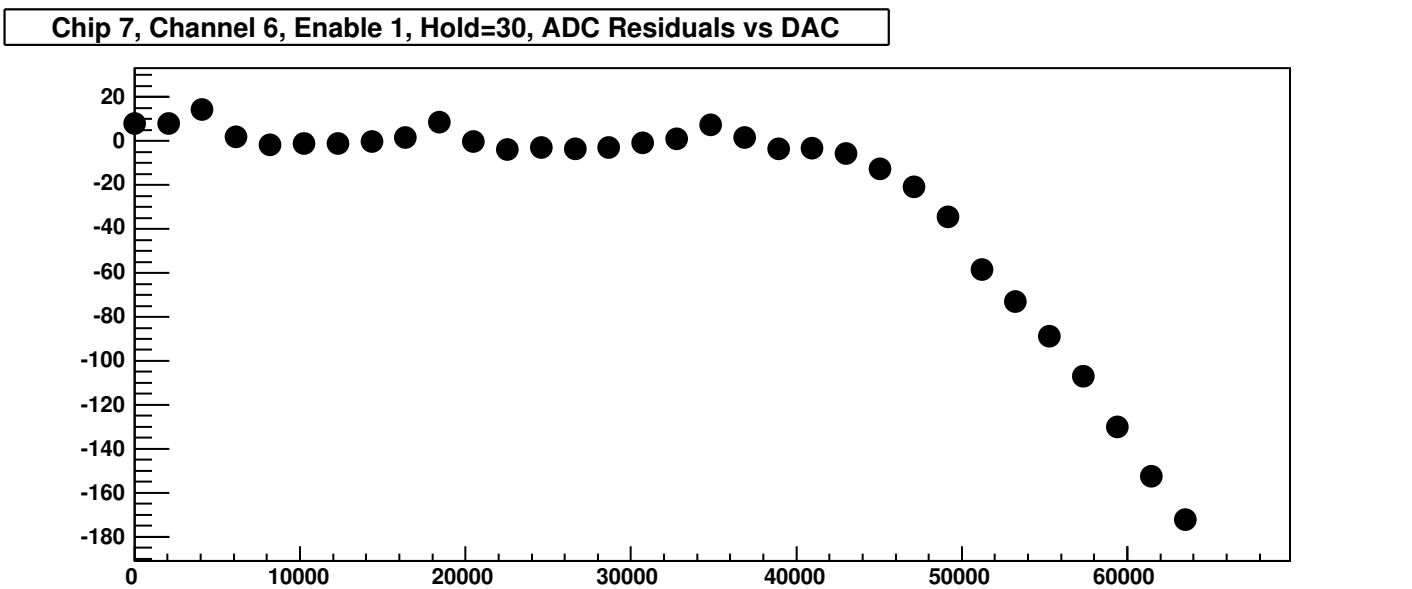

Chip 7, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7972 / 15
p0 1089 ± 0.1001
p1 -0.0006607 ± 4.07e-06
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

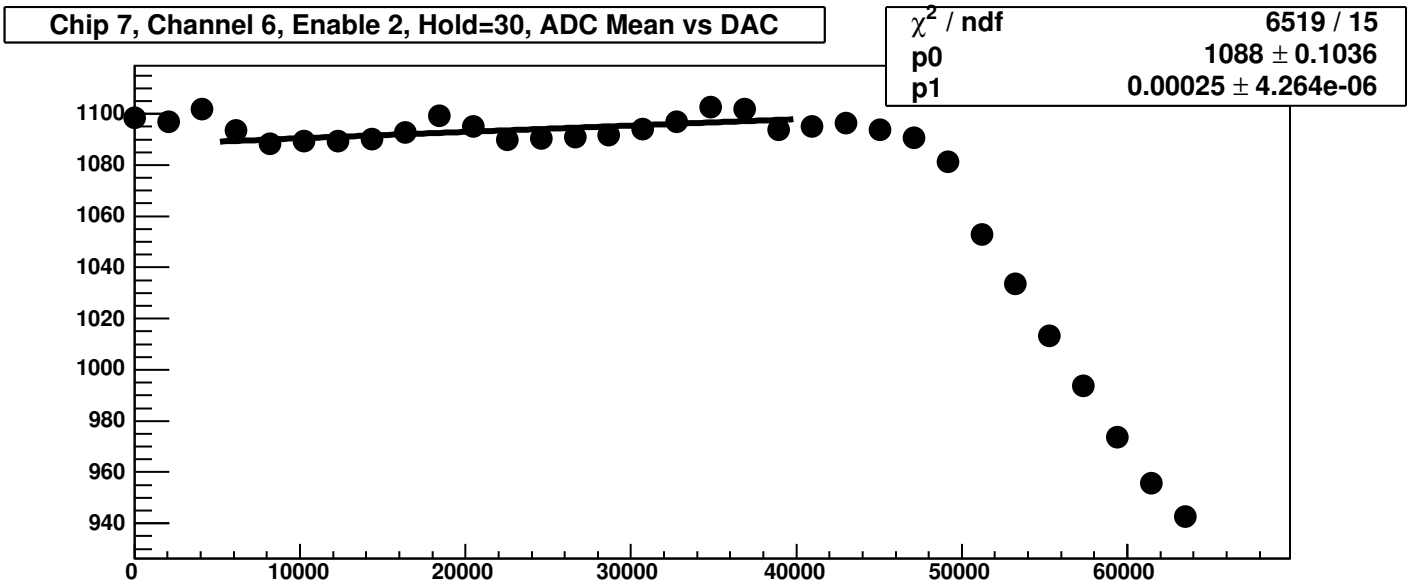

Chip 7, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
0
10000
20000
30000
40000
50000
60000

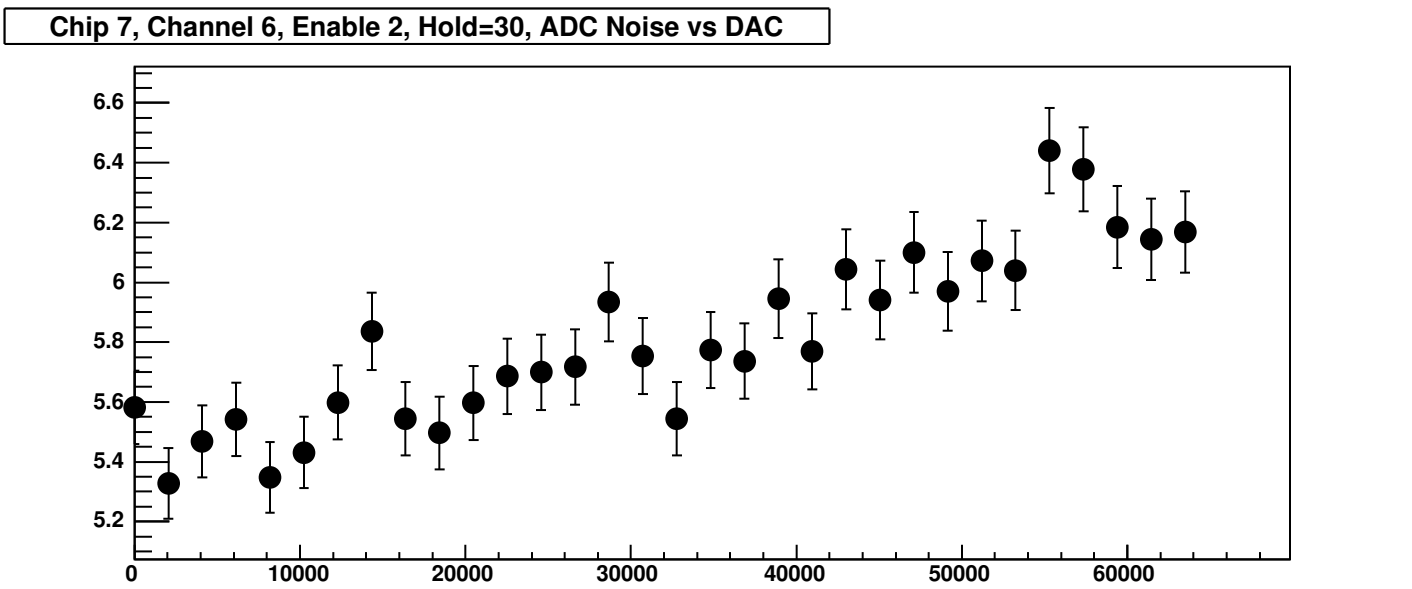

Chip 7, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

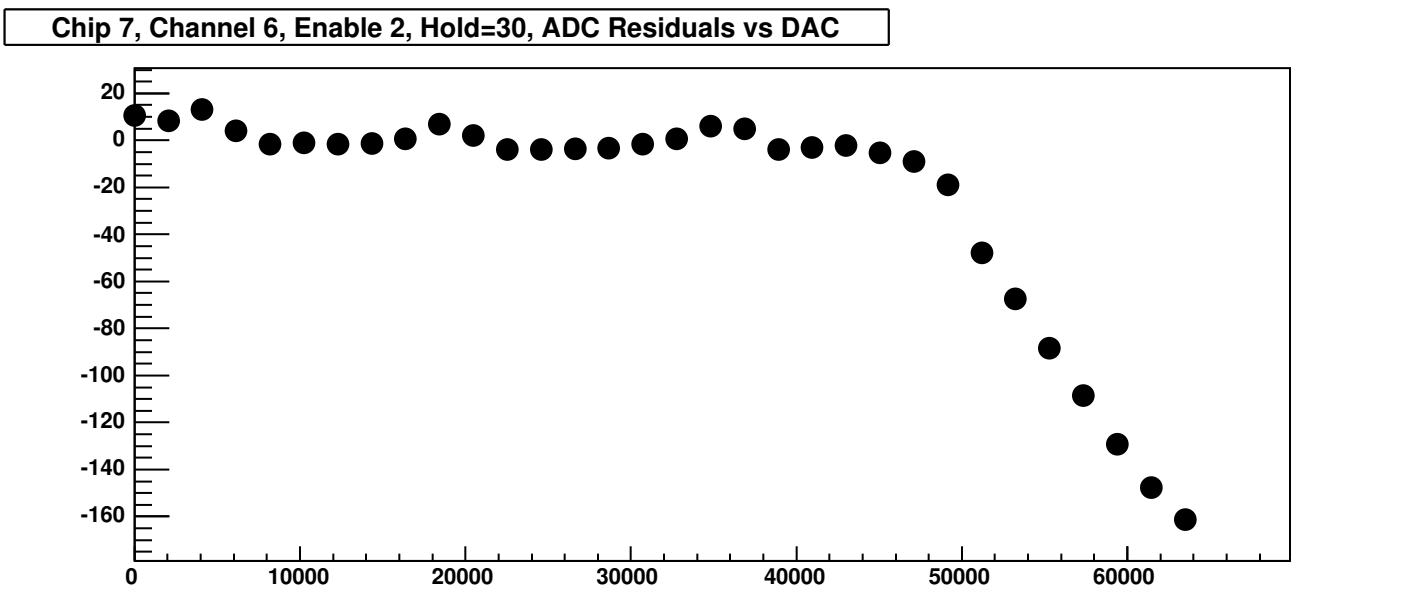

Chip 7, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6699 / 15
p0 1091 ± 0.1031
p1 -0.0002885 ± 4.193e-06
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

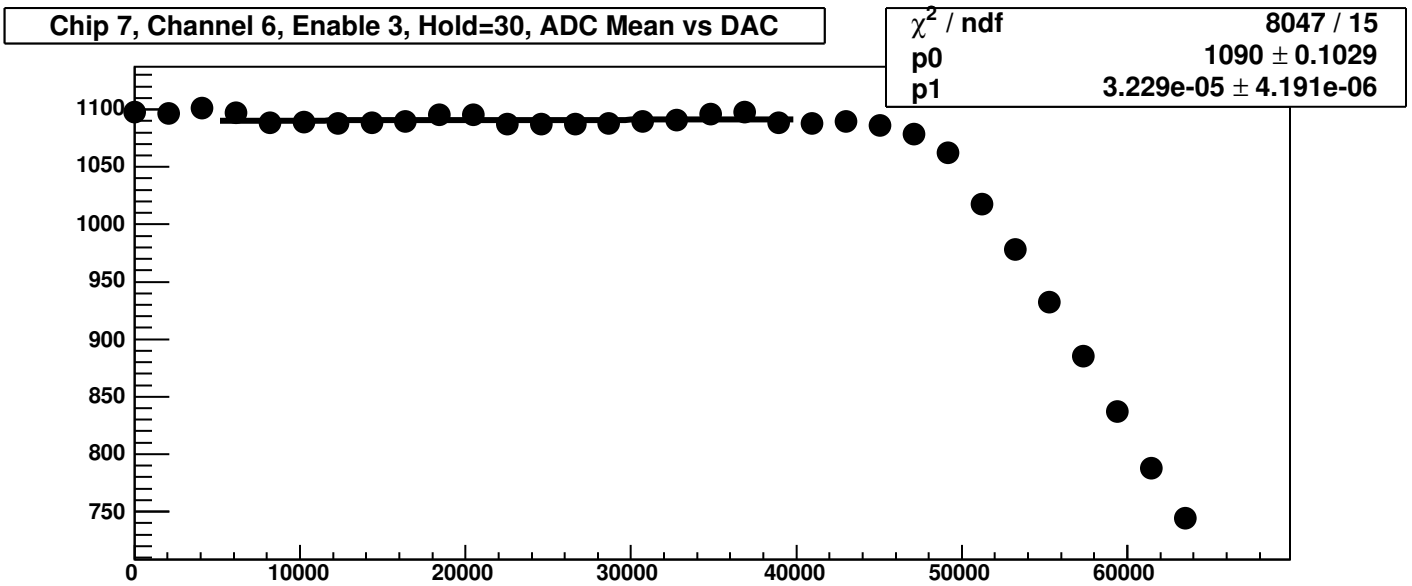

Chip 7, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
0
10000
20000
30000
40000
50000
60000

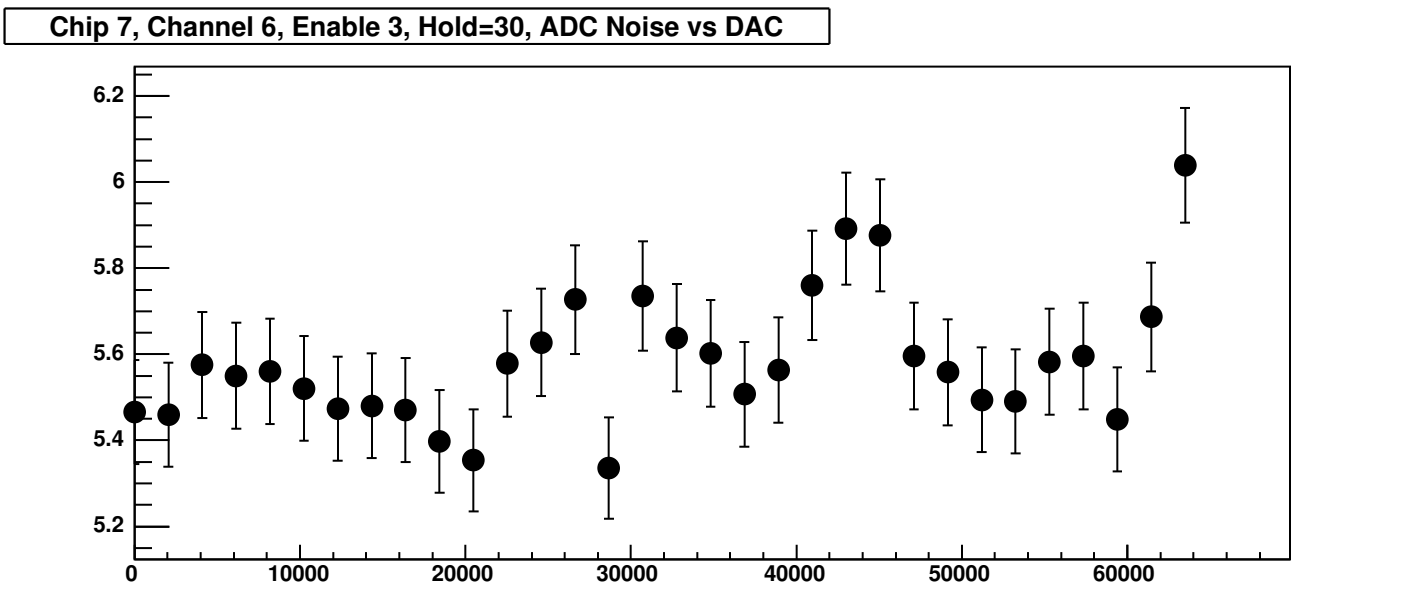

Chip 7, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

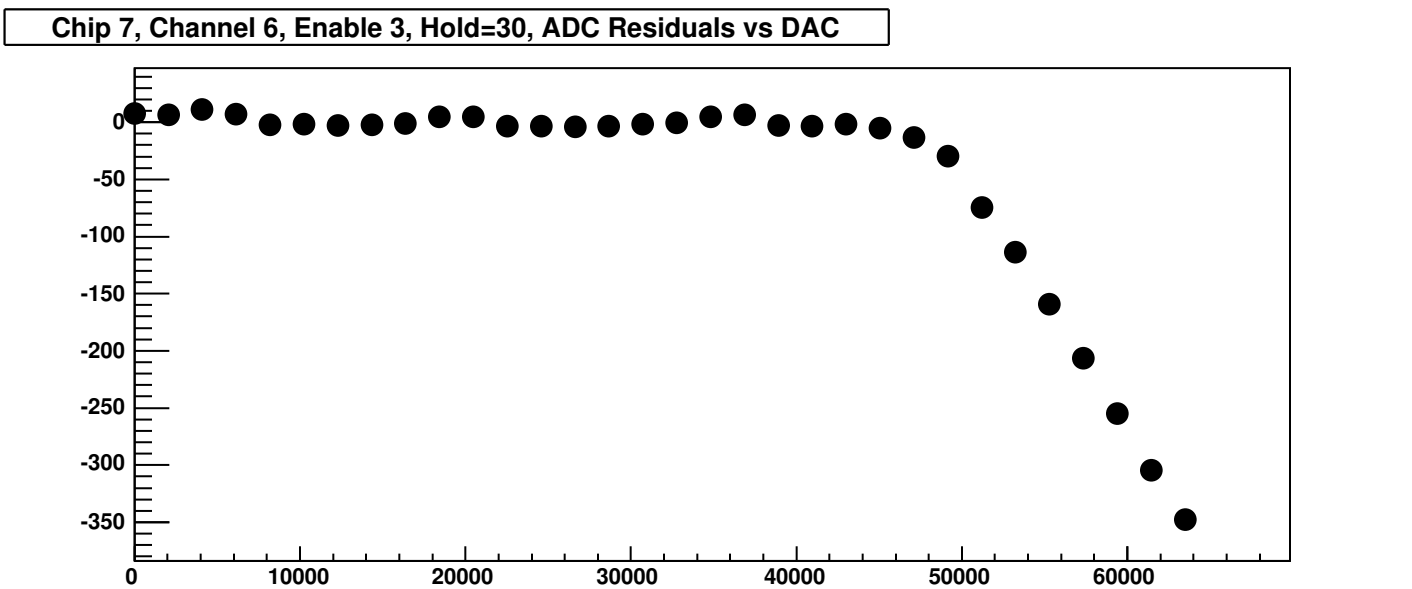

Chip 7, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6519 / 15
p0 1088 ± 0.1036
p1 0.00025 ± 4.264e-06
1100
1080
1060
1040
1020
1000
980
960
940
0
10000
20000
30000
40000
50000
60000

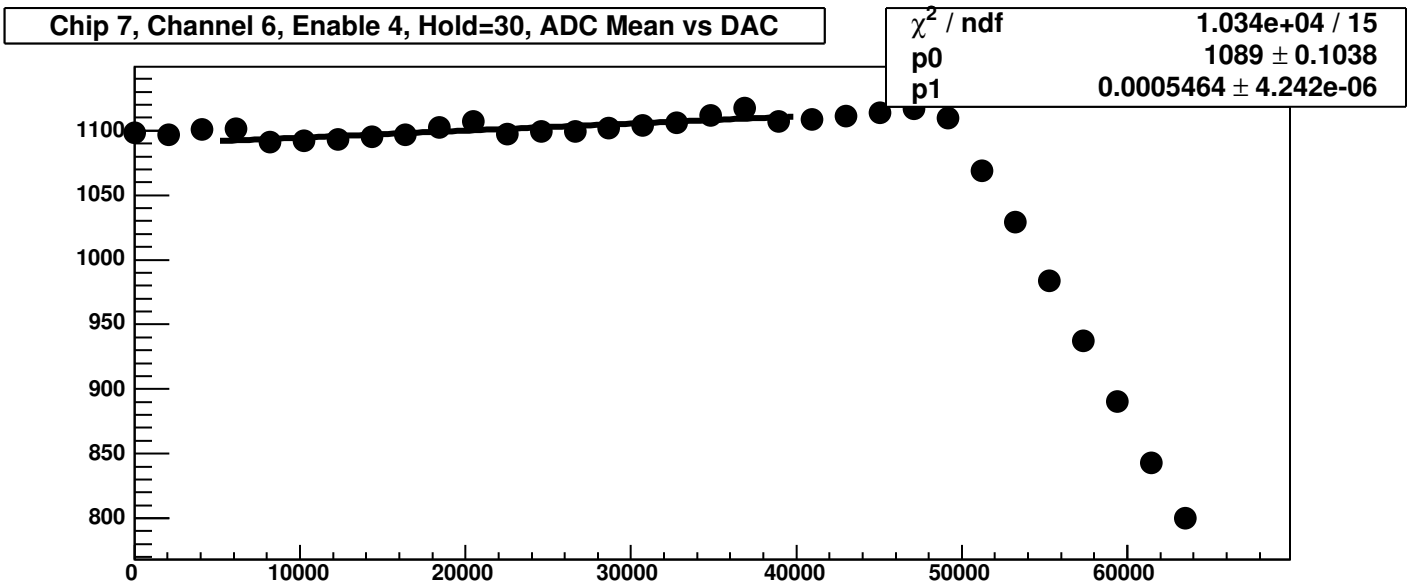

Chip 7, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

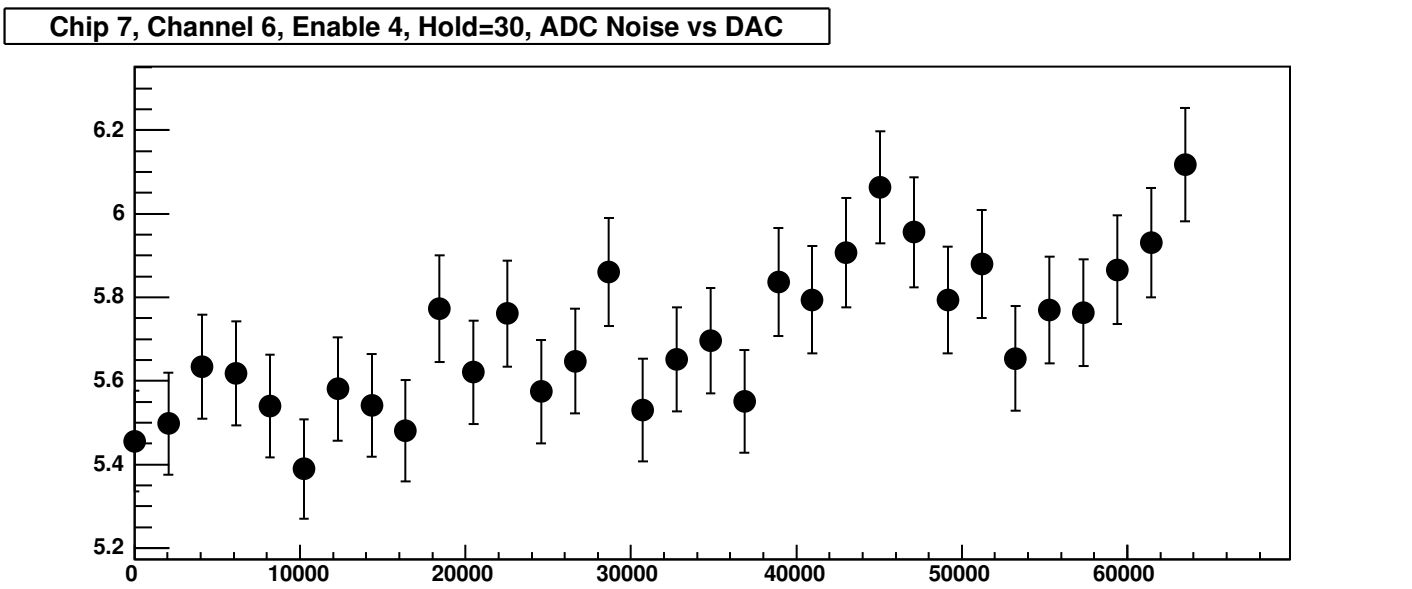

Chip 7, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

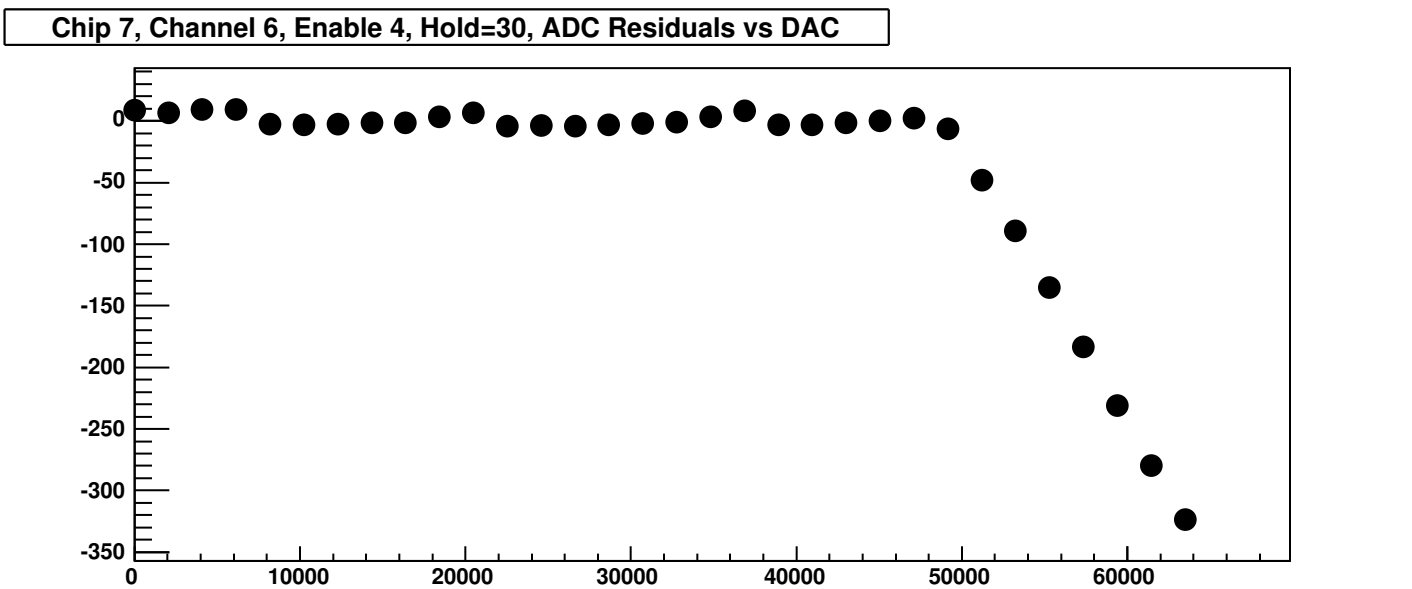

Chip 7, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8047 / 15
p0 1090 ± 0.1029
p1 3.229e-05 ± 4.191e-06
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

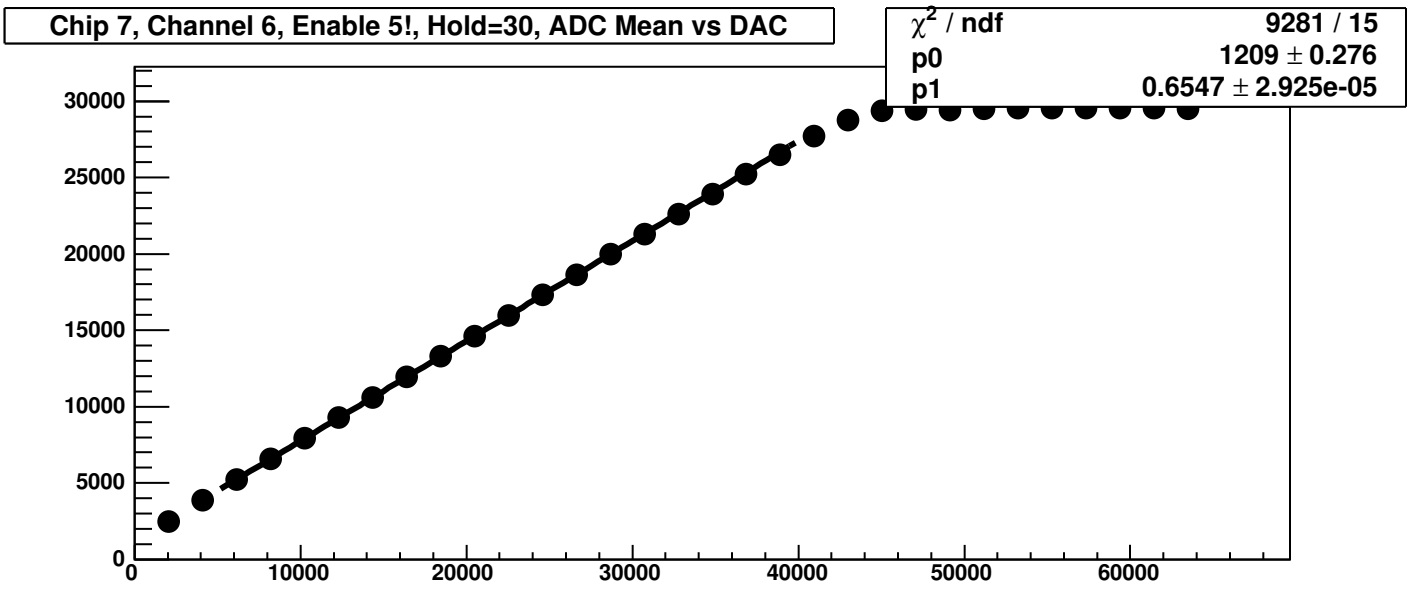

Chip 7, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

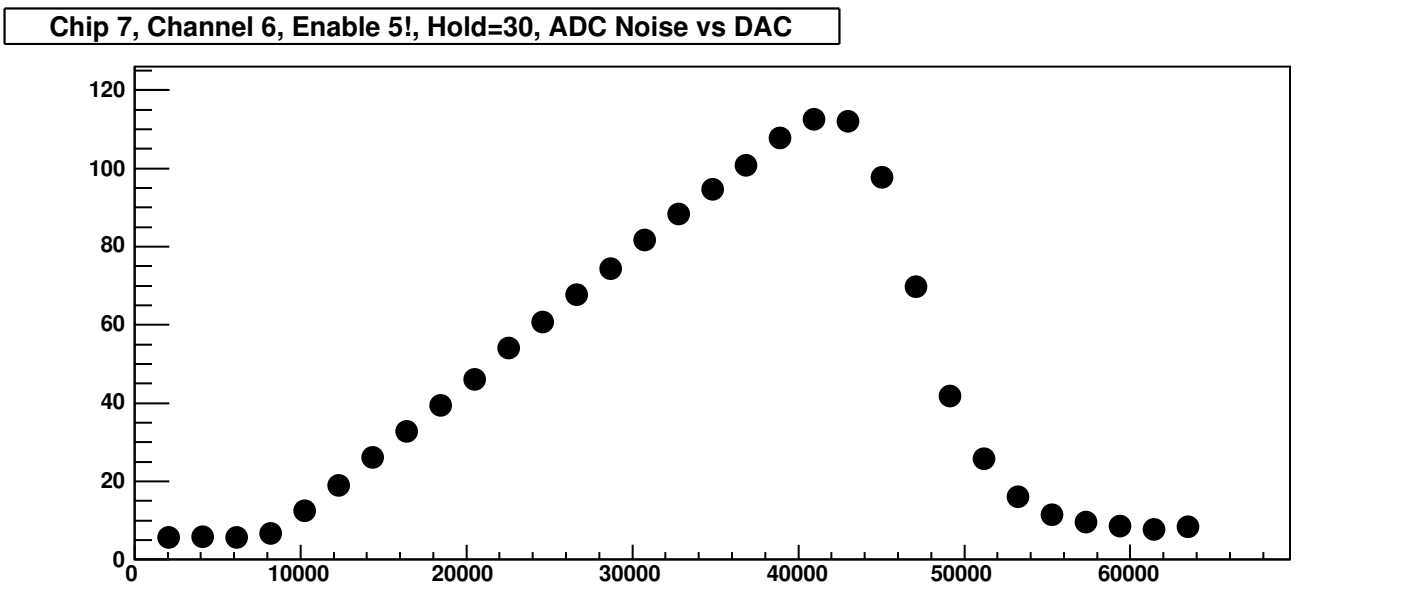

Chip 7, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

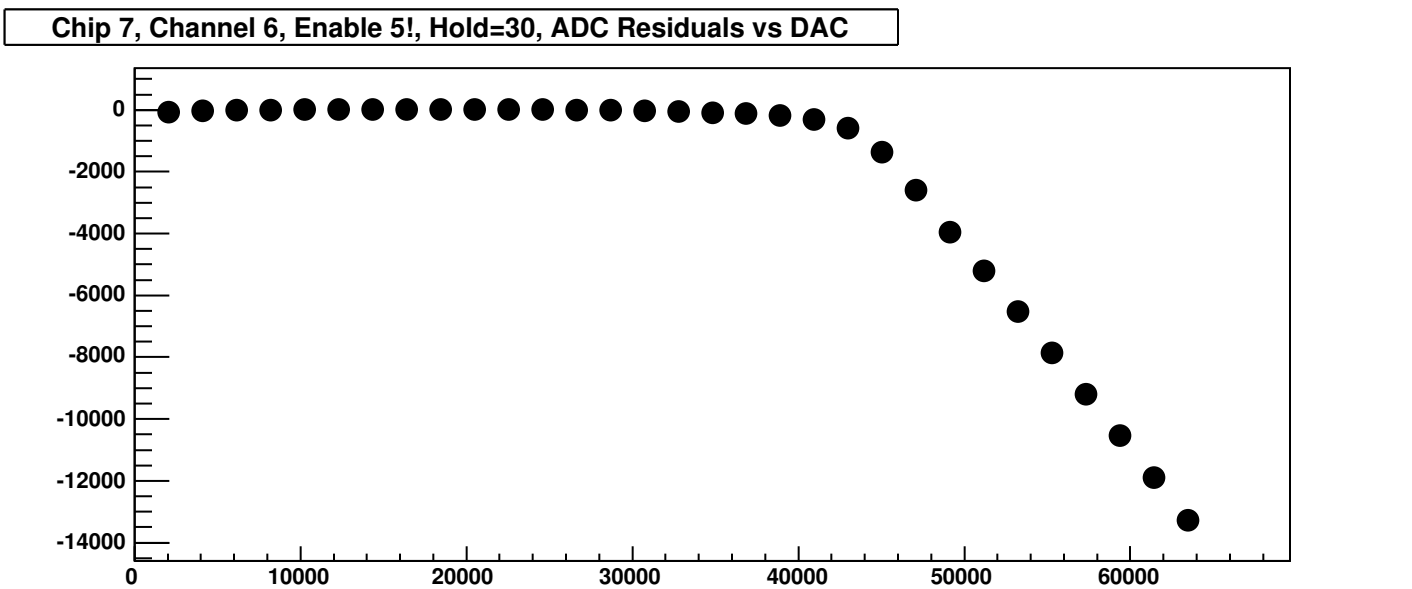

Chip 7, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.034e+04 / 15
p0 1089 ± 0.1038
p1 0.0005464 ± 4.242e-06
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

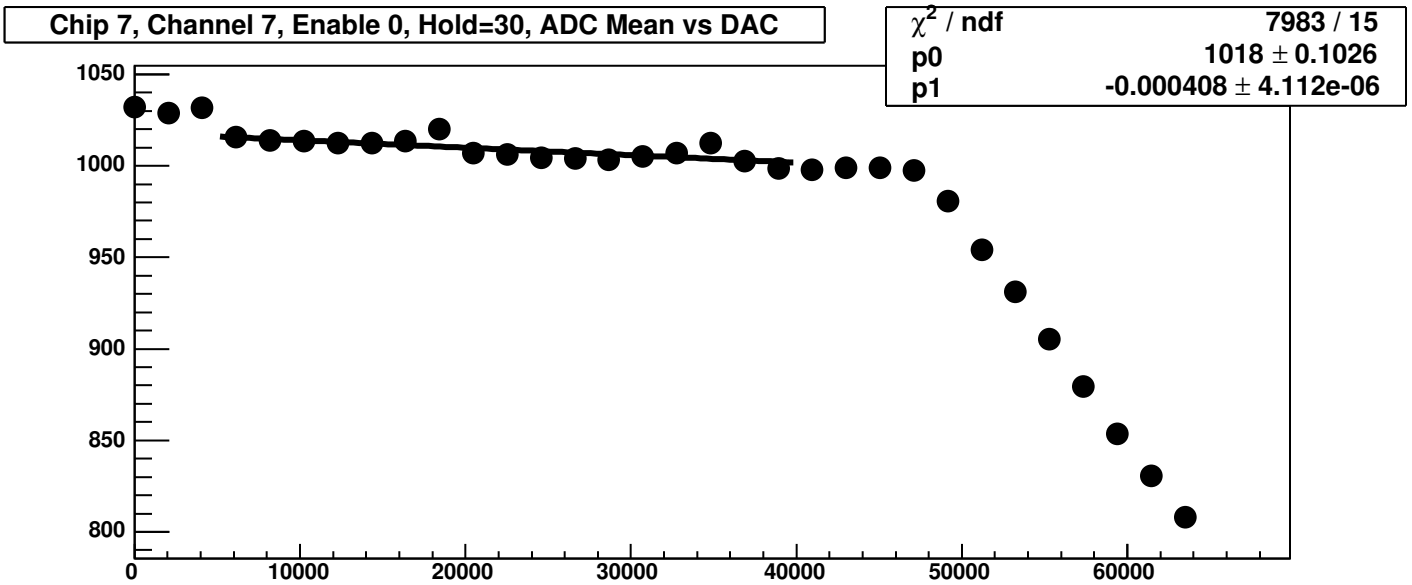

Chip 7, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

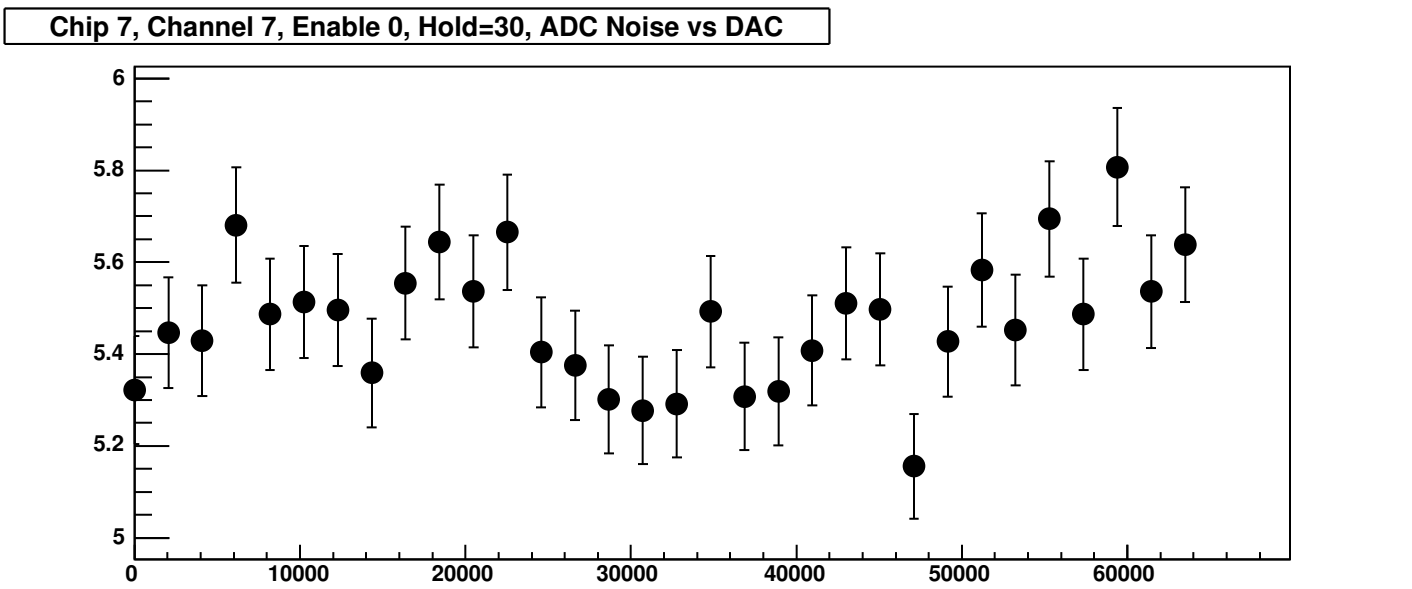

Chip 7, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

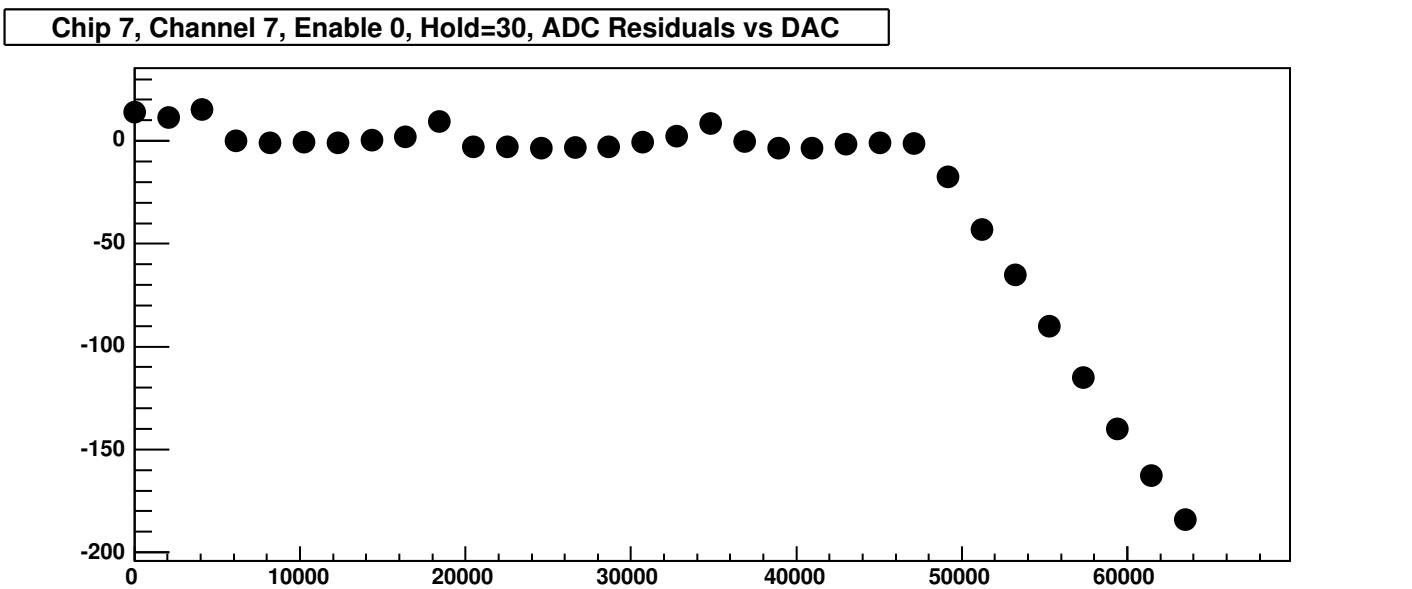

Chip 7, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 9281 / 15
p0 1209 ± 0.276
p1 0.6547 ± 2.925e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

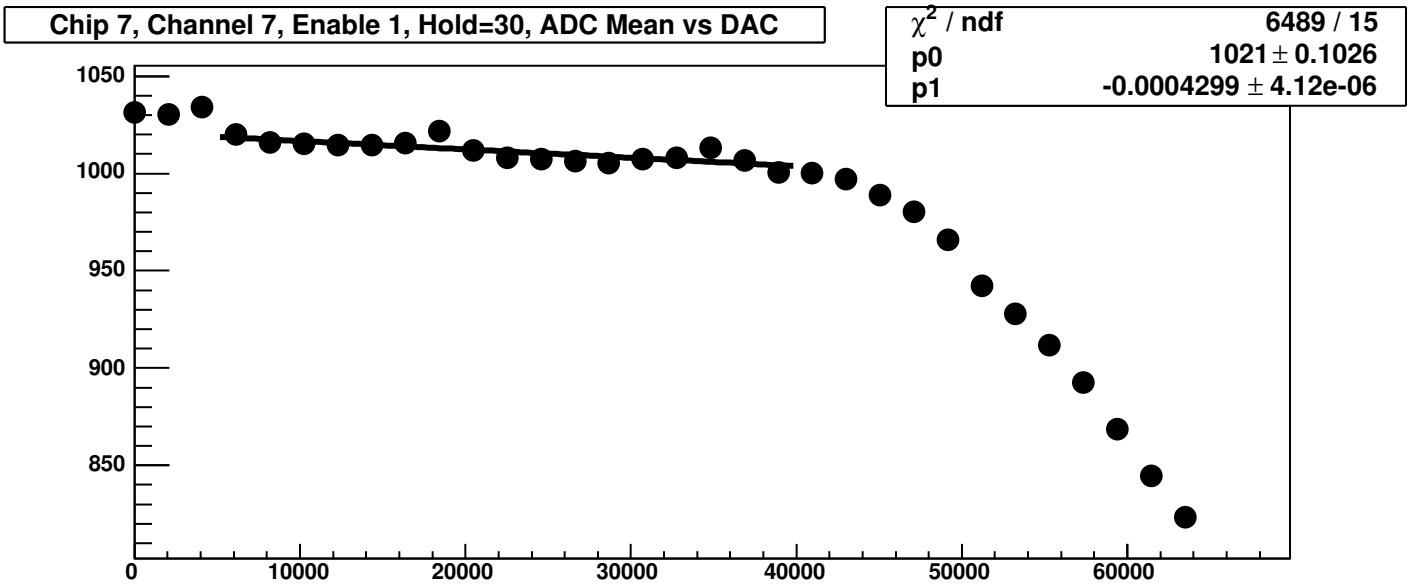

Chip 7, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

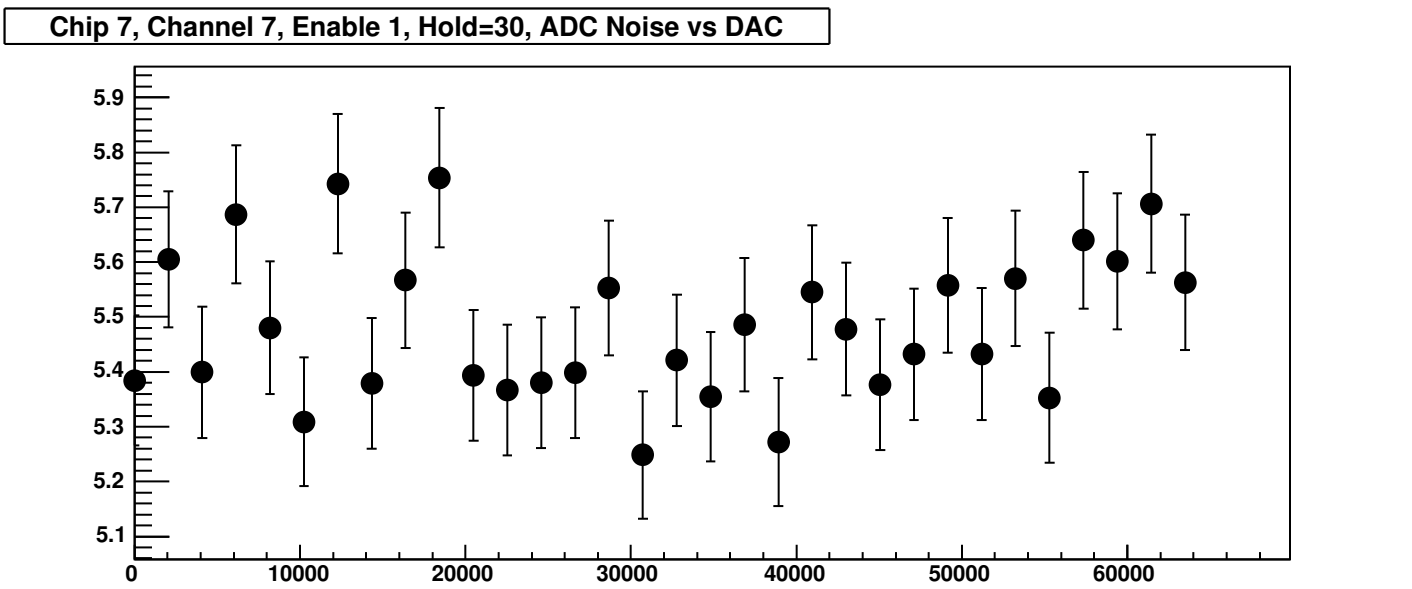

Chip 7, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

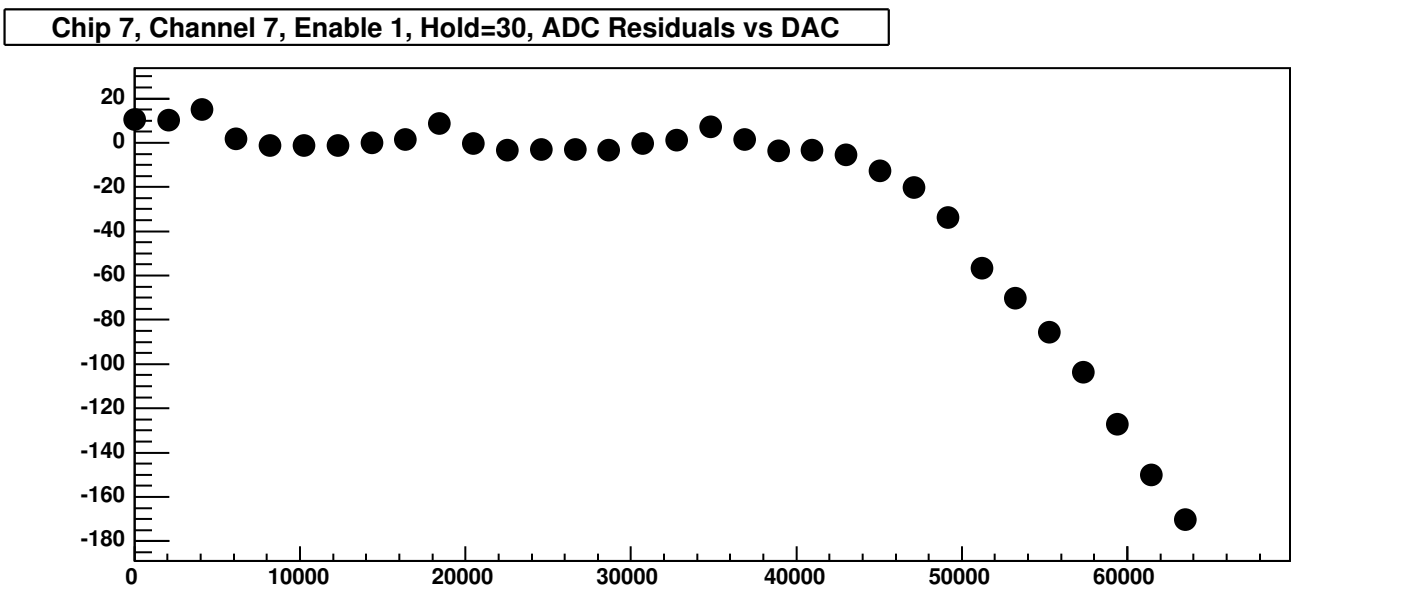

Chip 7, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7983 / 15
p0 1018 ± 0.1026
p1 -0.000408 ± 4.112e-06
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

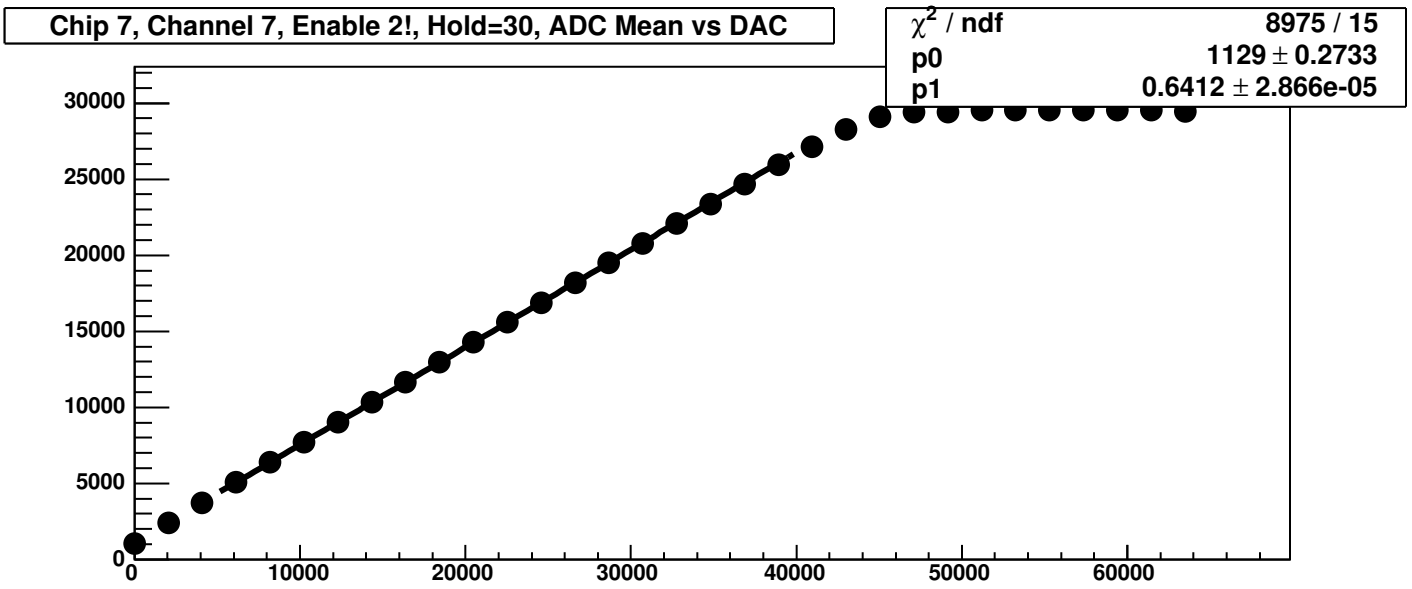

Chip 7, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

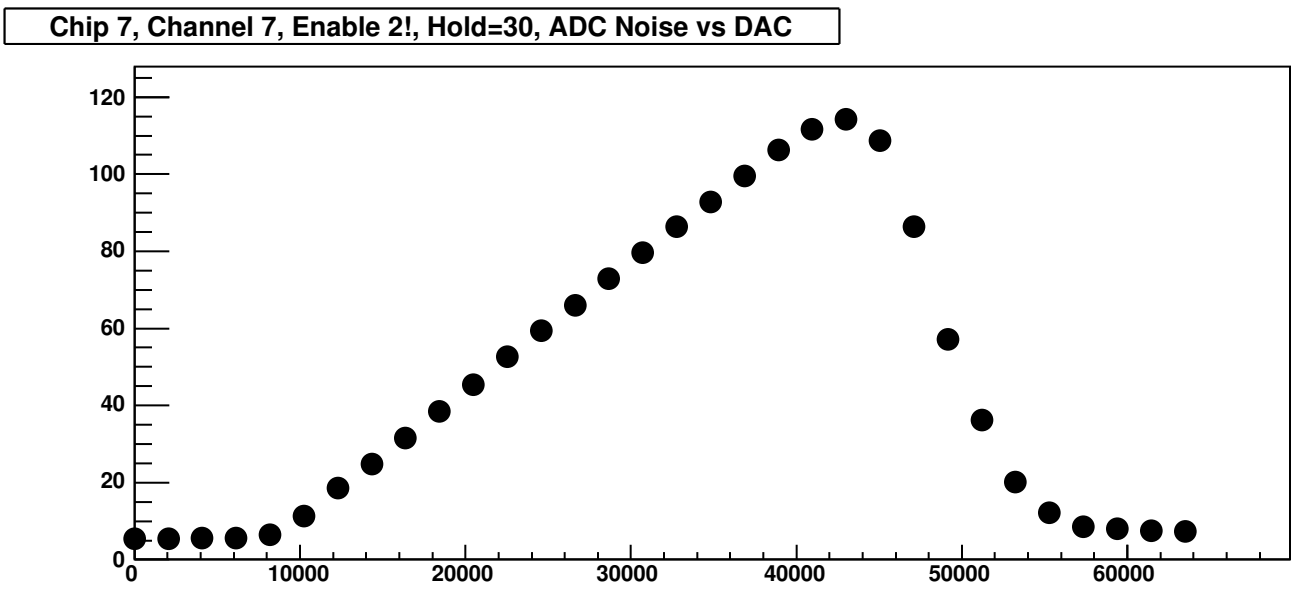

Chip 7, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

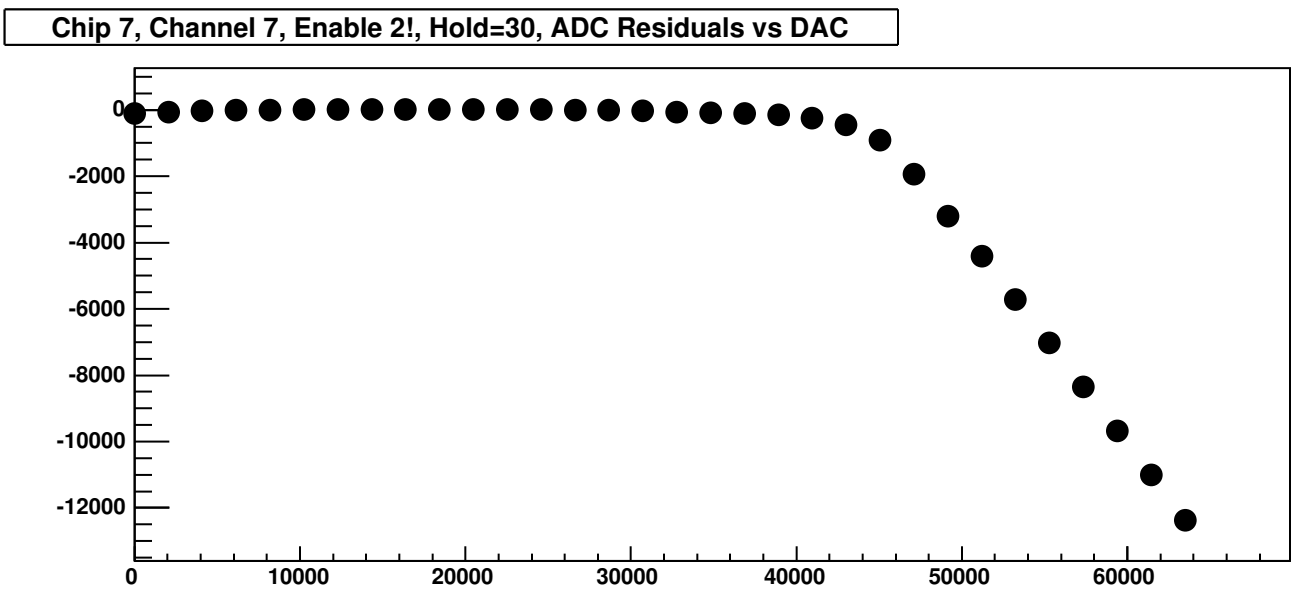

Chip 7, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6489 / 15
p0 1021 ± 0.1026
p1 -0.0004299 ± 4.12e-06
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
0
10000
20000
30000
40000
50000
60000

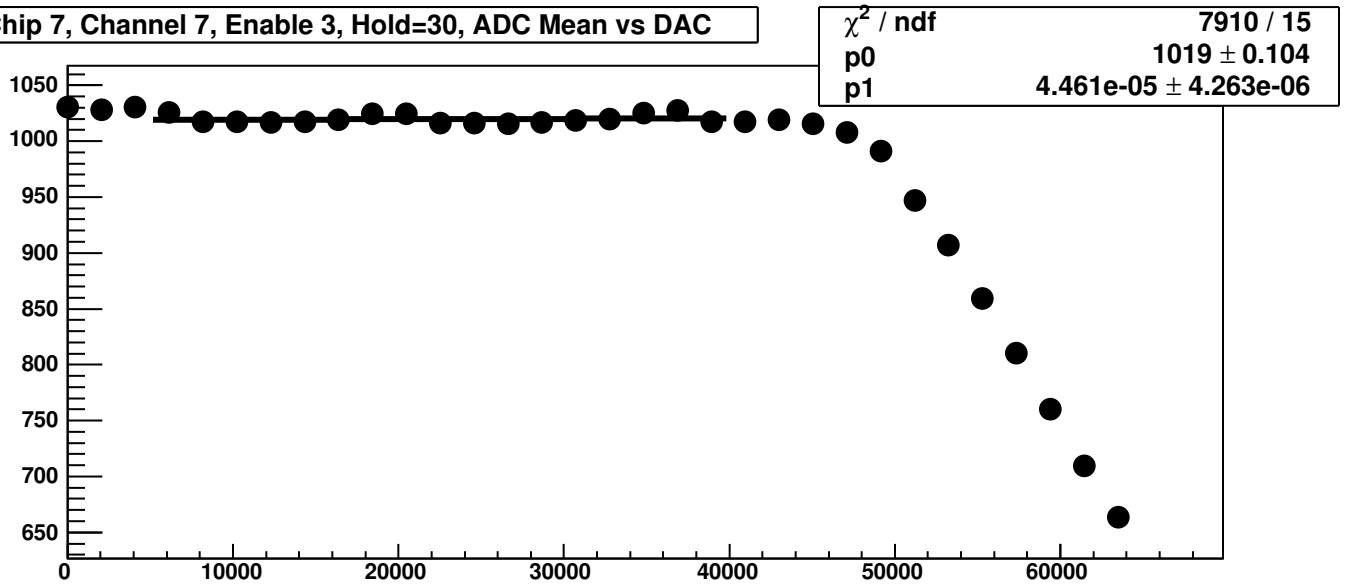

Chip 7, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 8975 / 15
p0 1129 ± 0.2733
p1 0.6412 ± 2.866e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

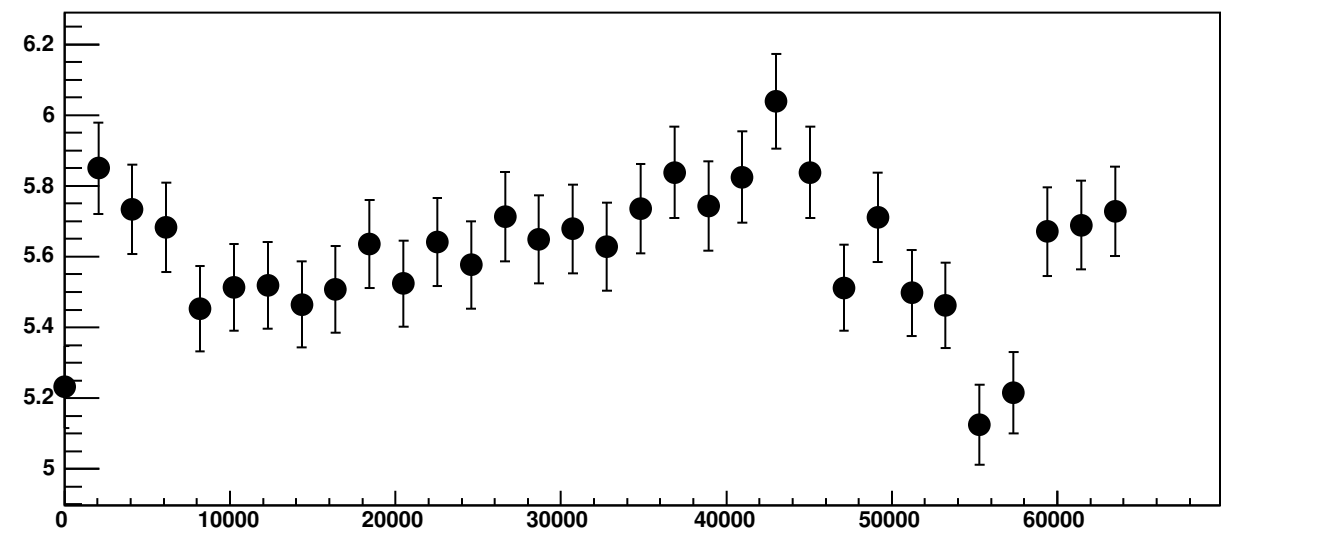

Chip 7, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

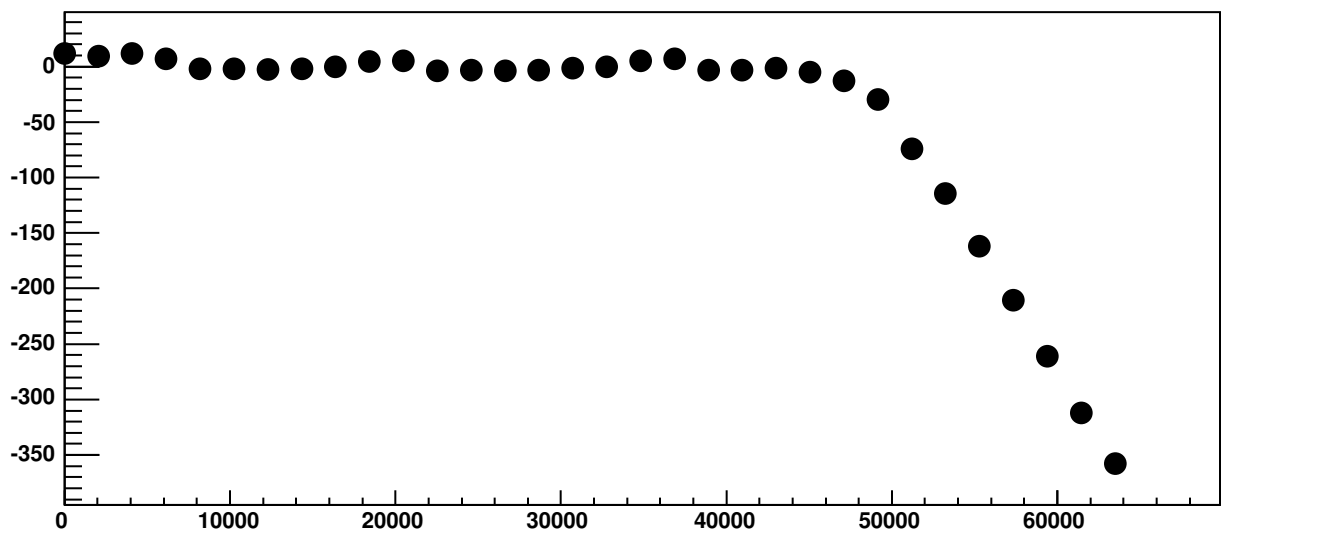
Chip 7, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

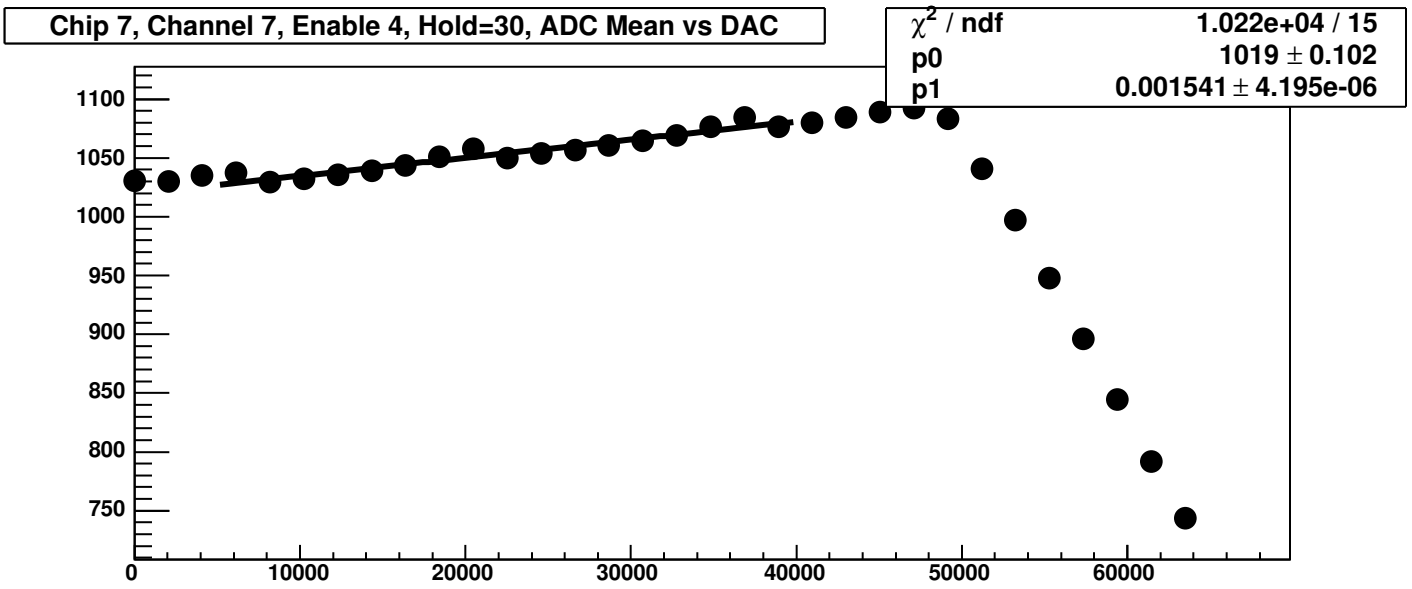


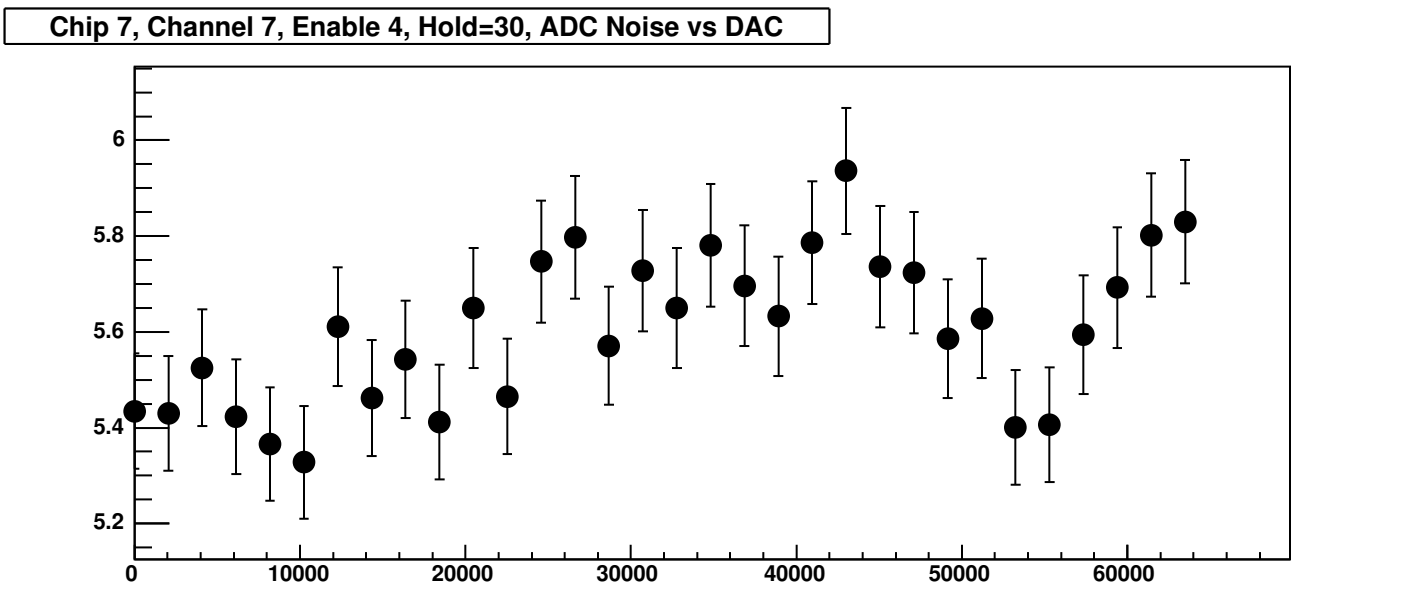
Chip 7, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

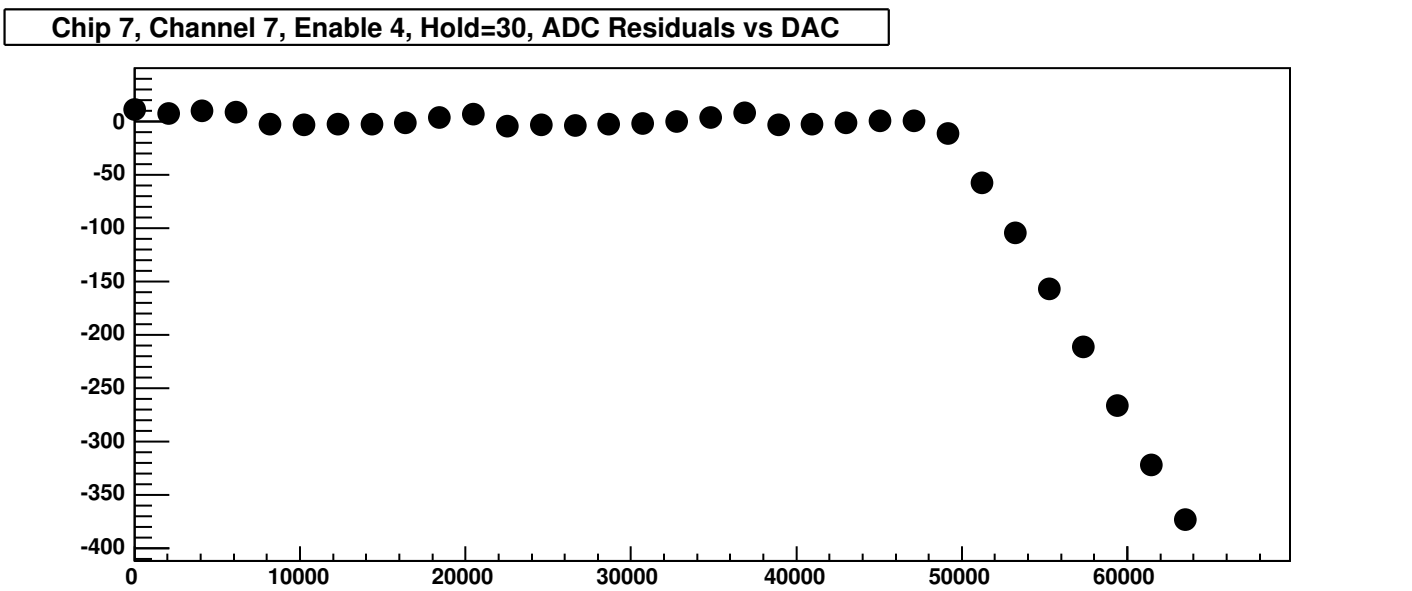


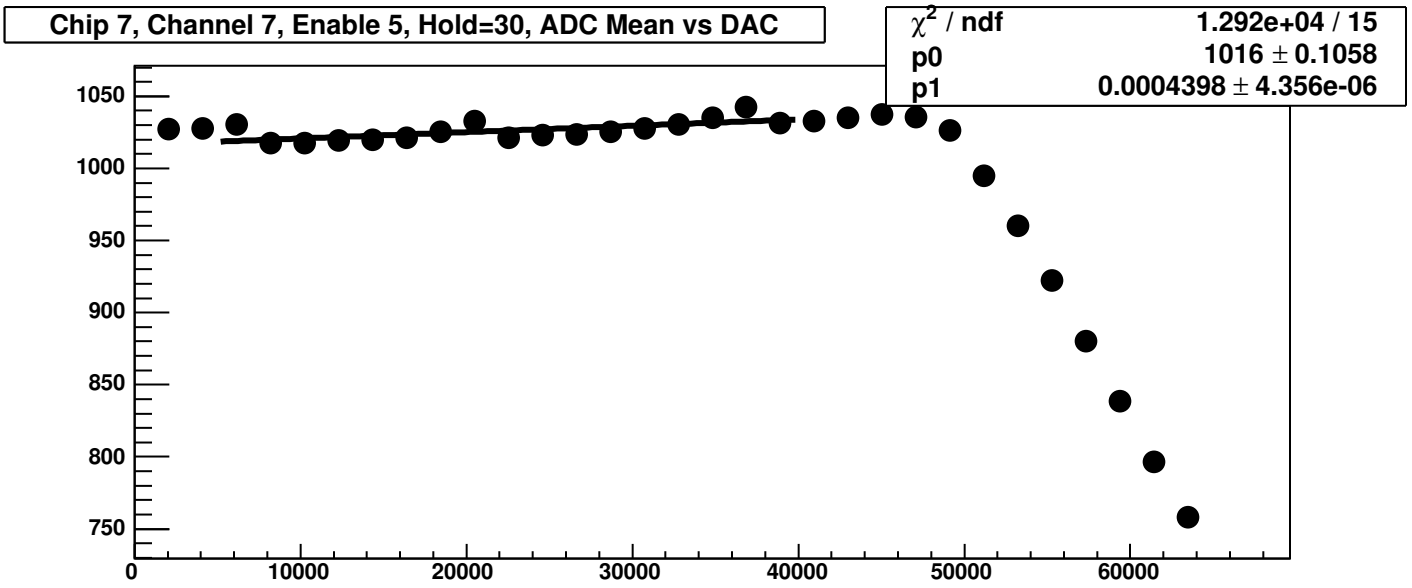
Chip 7, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

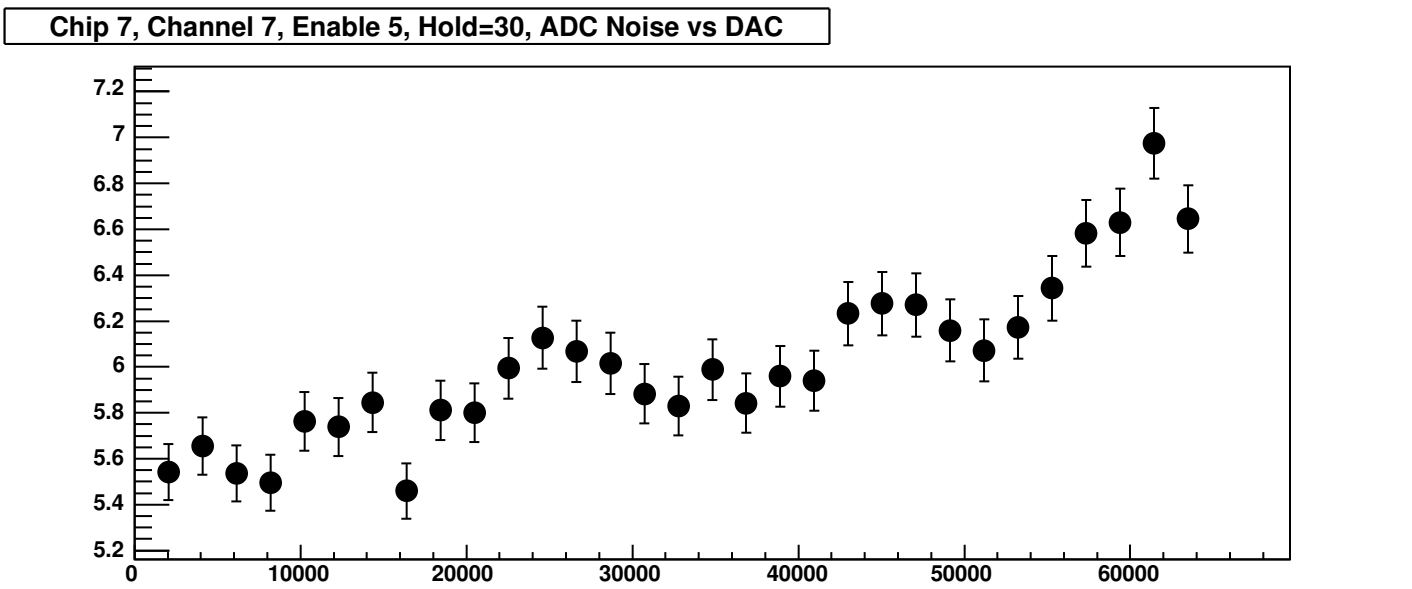


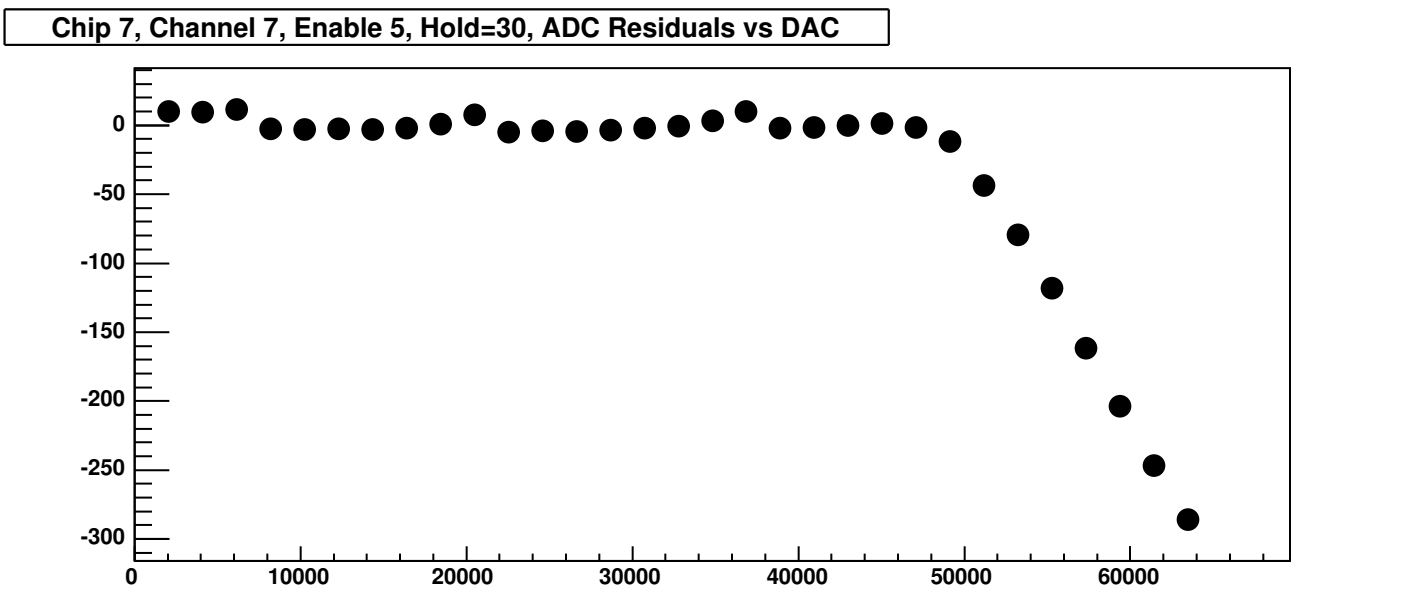

Chip 7, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.022e+04 / 15
p0 1019 ± 0.102
p1 0.001541 ± 4.195e-06
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

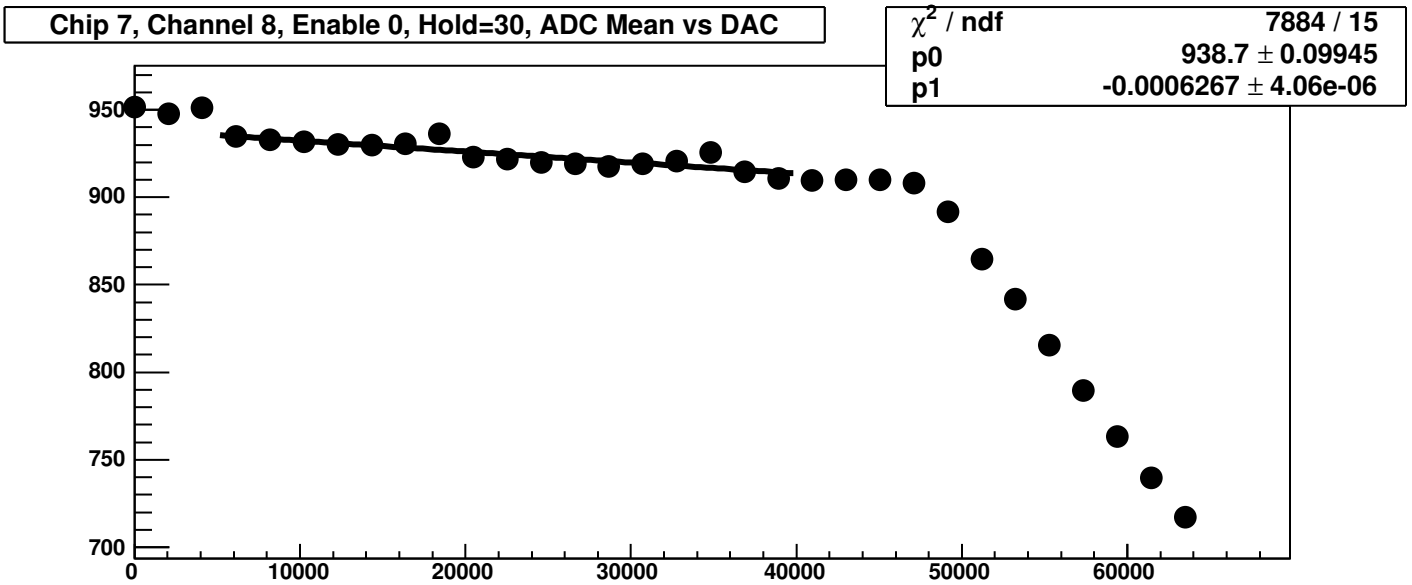

Chip 7, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

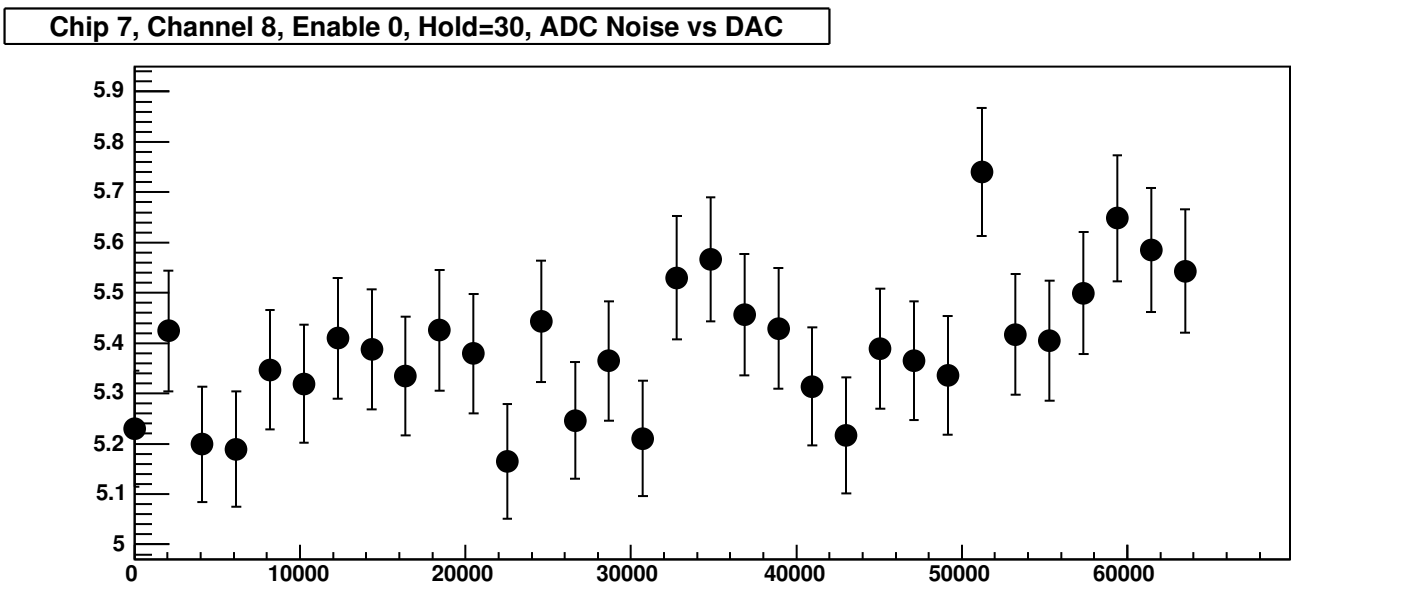

Chip 7, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

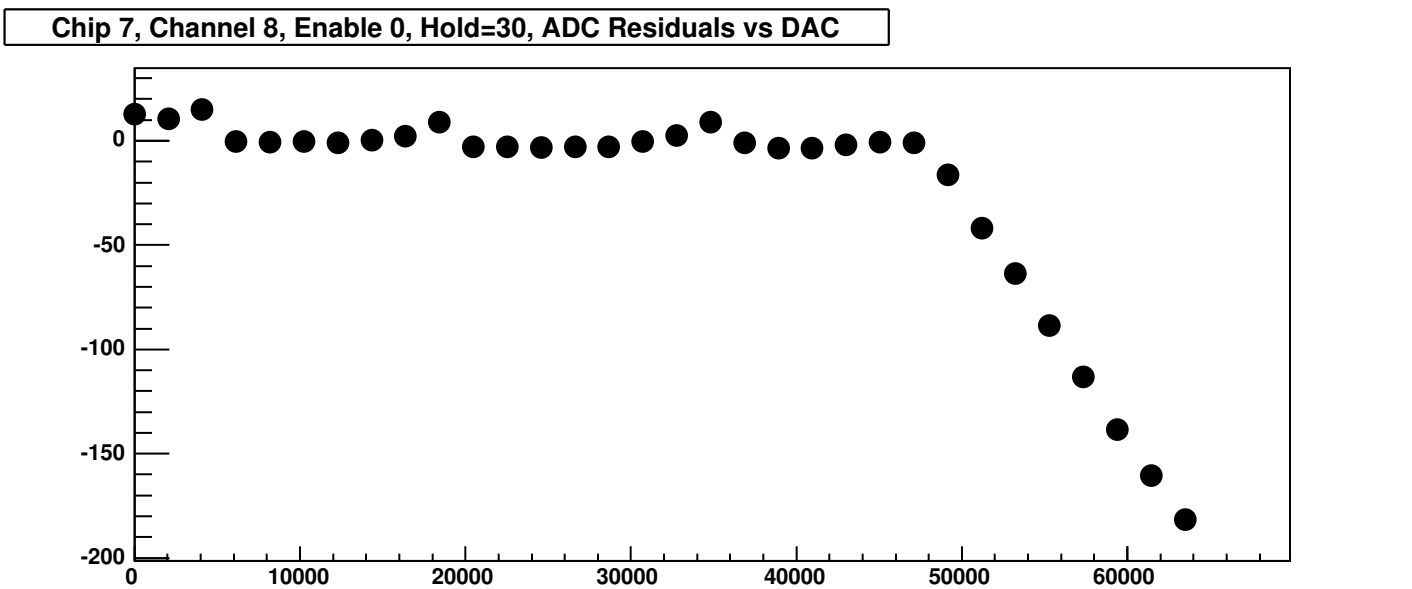

Chip 7, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.292e+04 / 15
p0 1016 ± 0.1058
p1 0.0004398 ± 4.356e-06
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

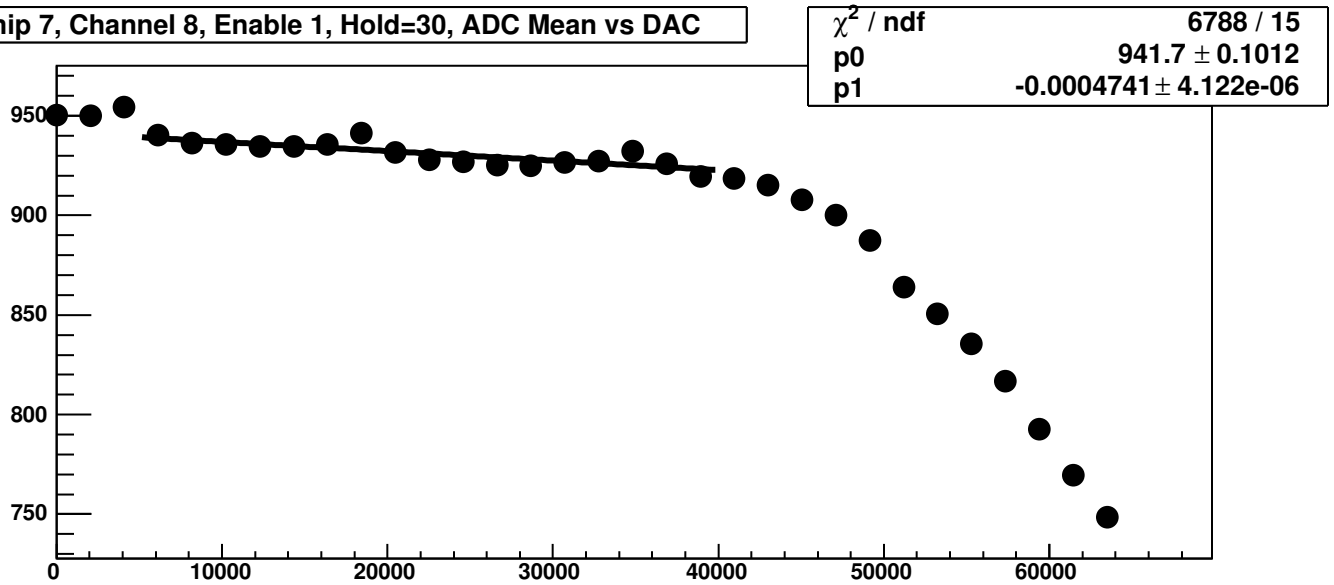

Chip 7, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7884 / 15
p0 938.7 ± 0.09945
p1 -0.0006267 ± 4.06e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

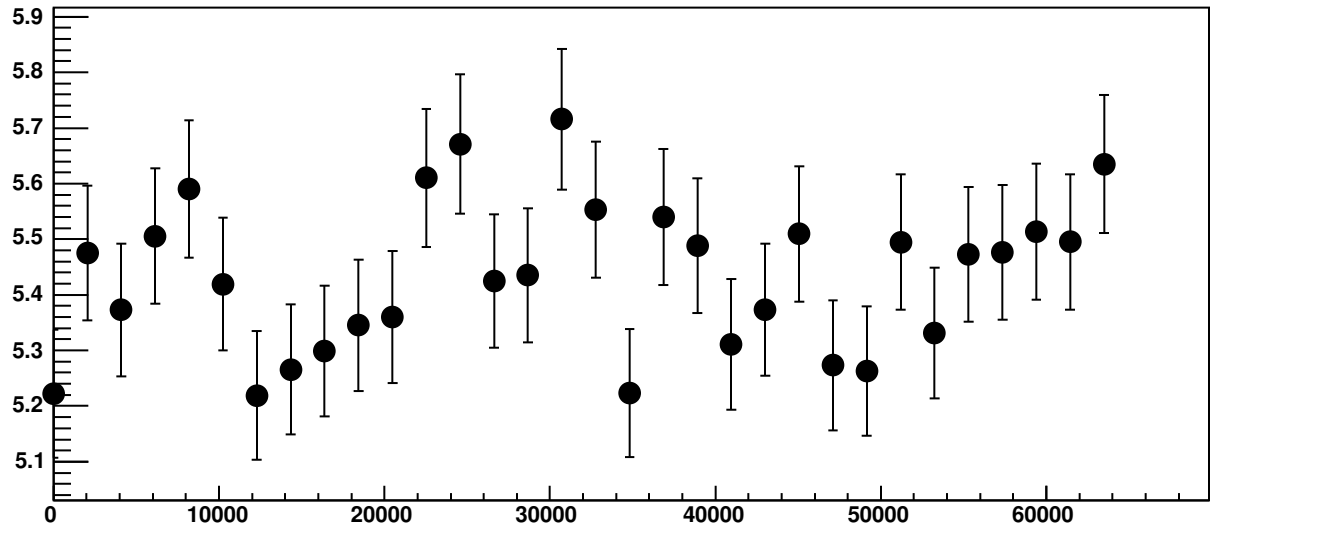

Chip 7, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
5
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

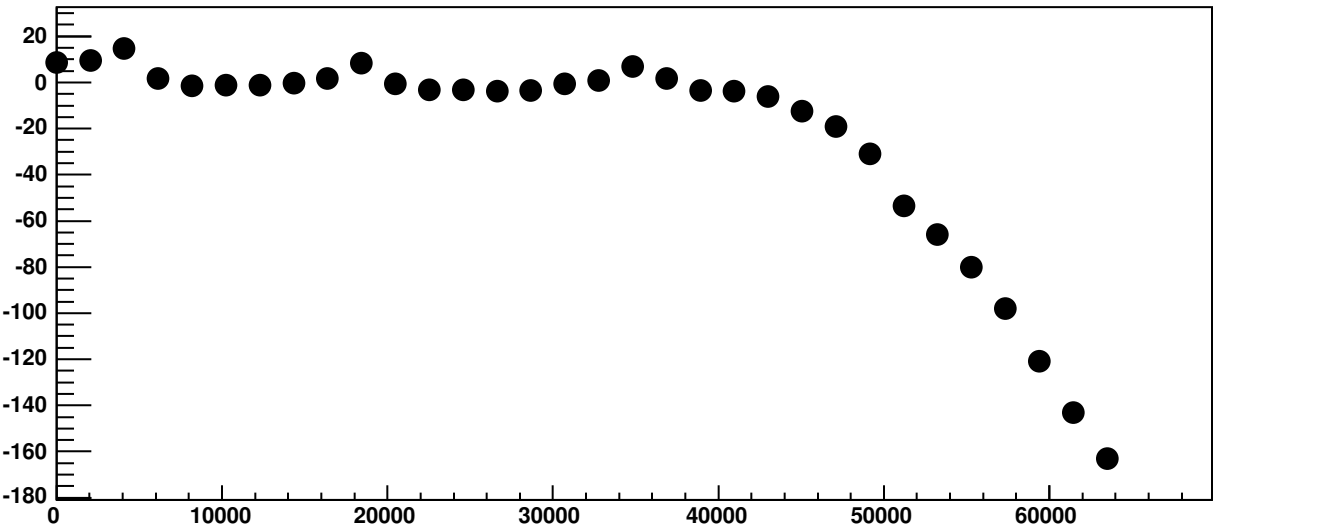
Chip 7, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC

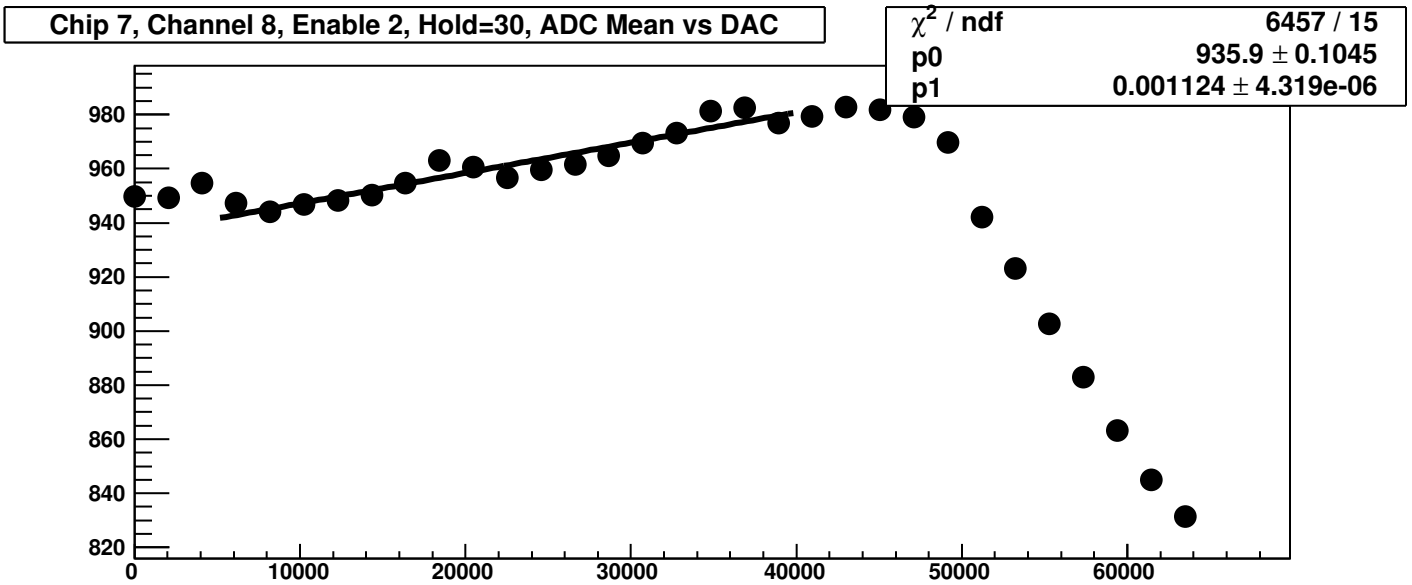


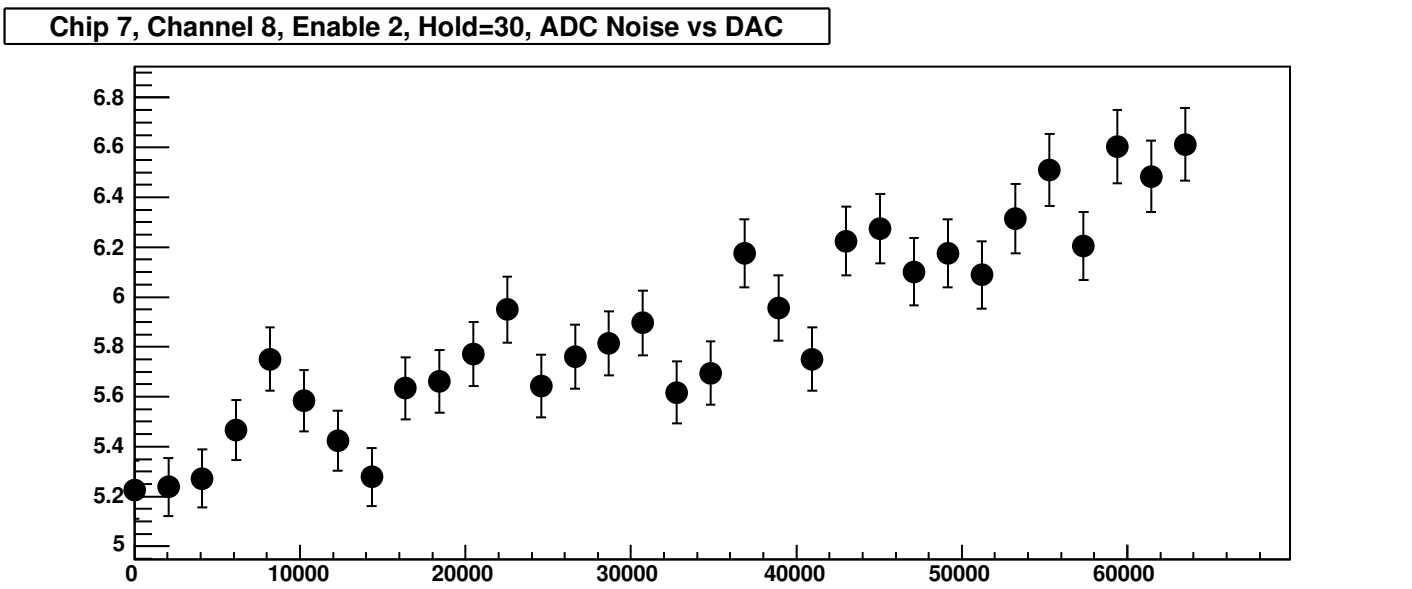
Chip 7, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC

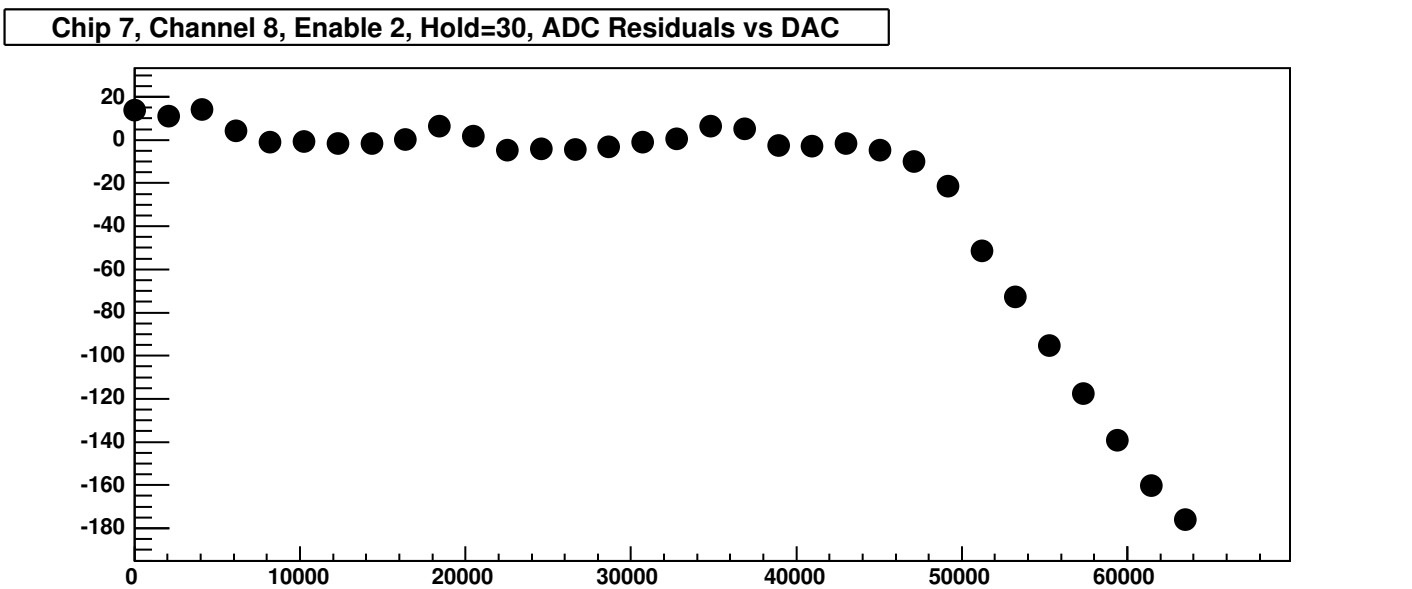


Chip 7, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC

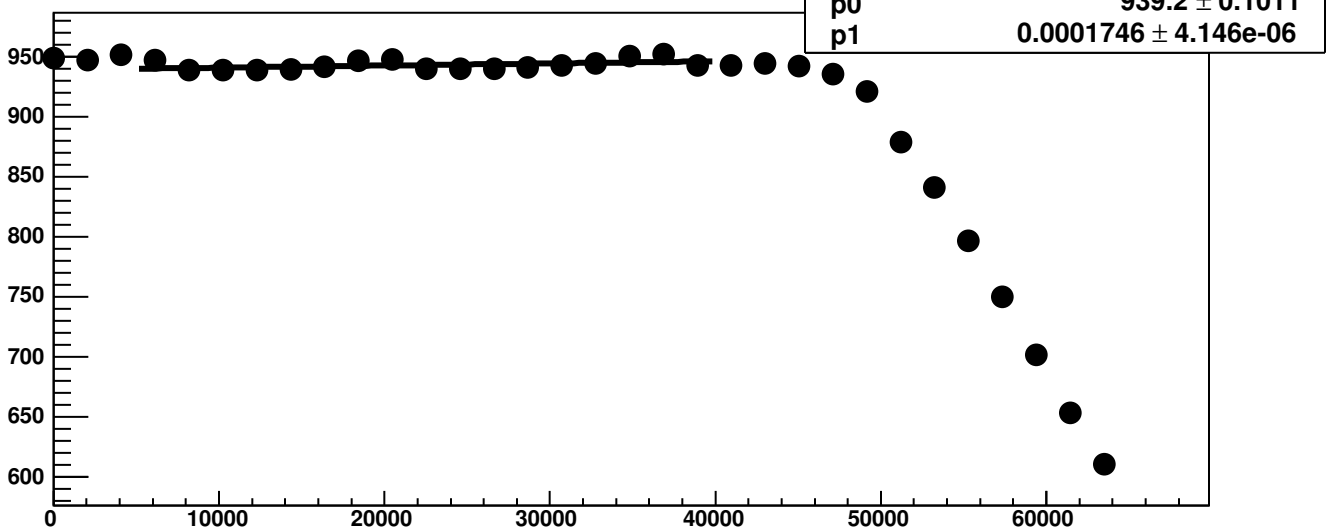



Chip 7, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6457 / 15
p0 935.9 ± 0.1045
p1 0.001124 ± 4.319e-06
980
960
940
920
900
880
860
840
820
0
10000
20000
30000
40000
50000
60000

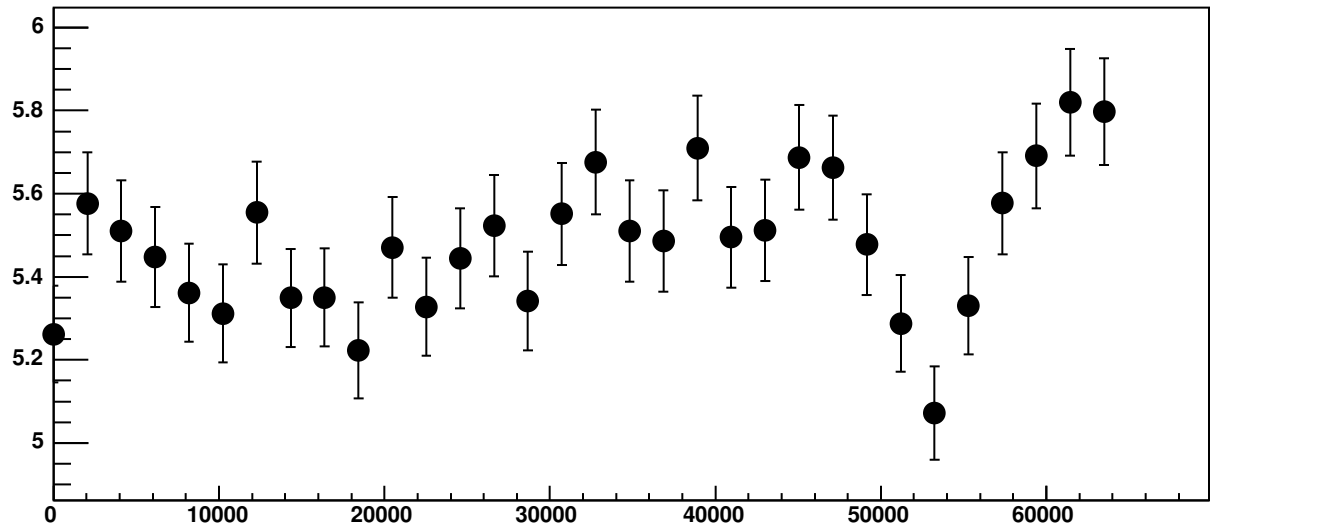

Chip 7, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

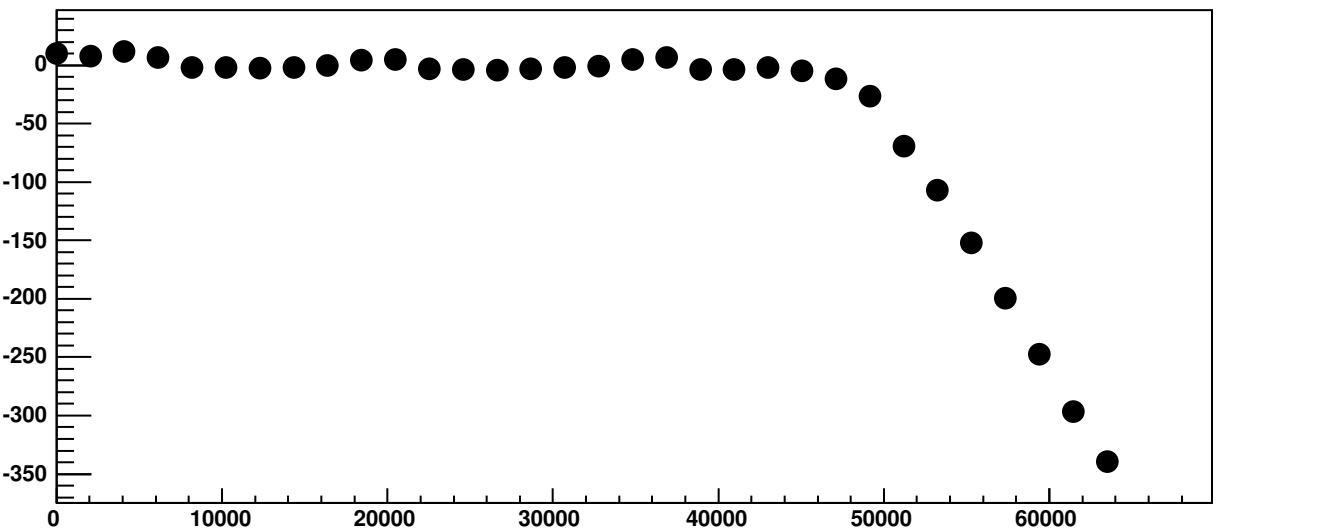
Chip 7, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC



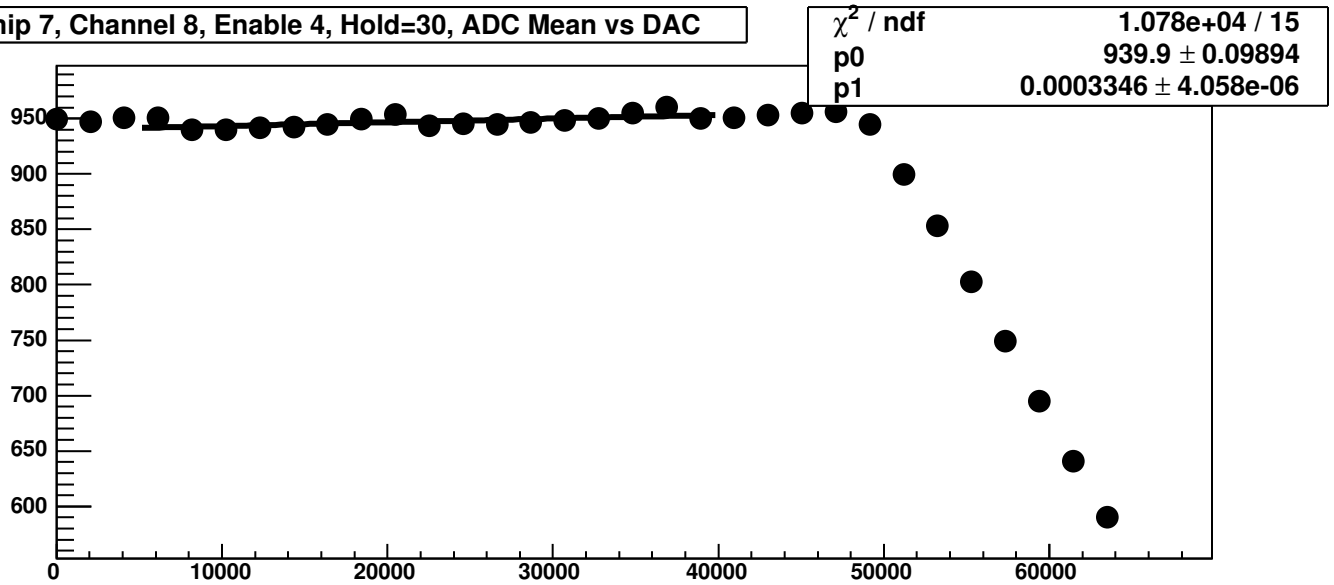
Chip 7, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



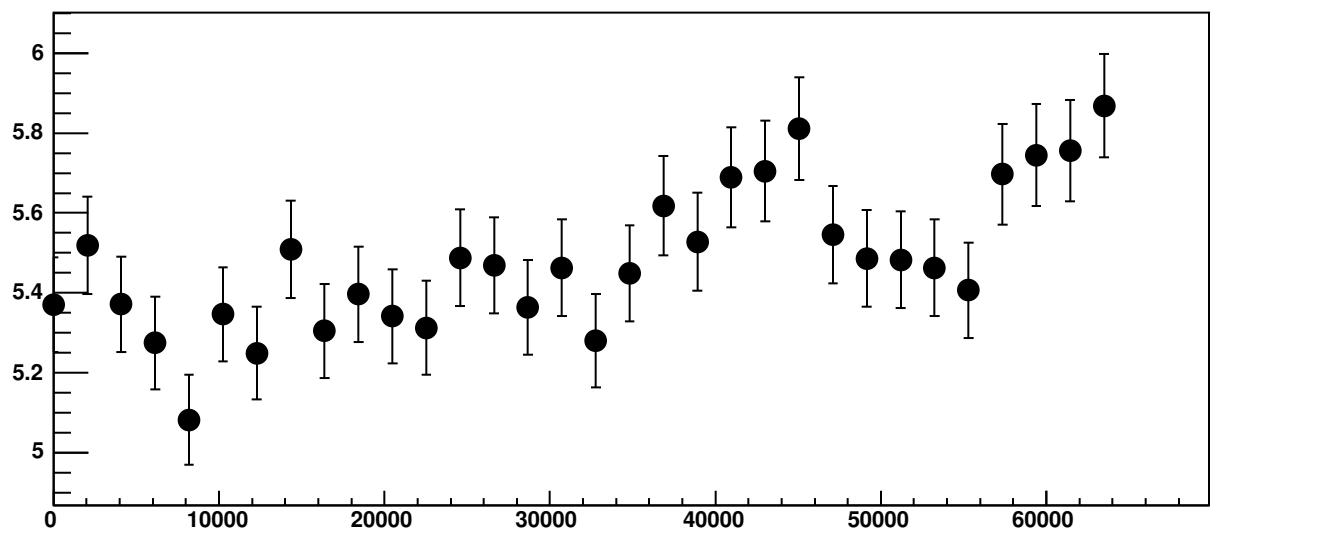
Chip 7, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC



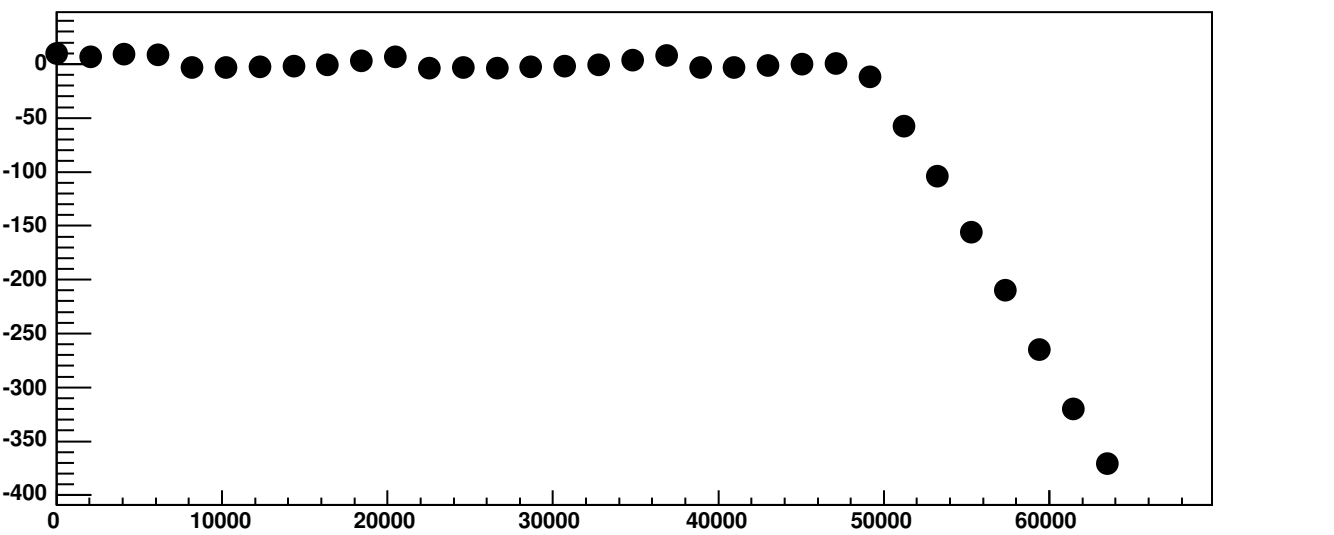
Chip 7, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

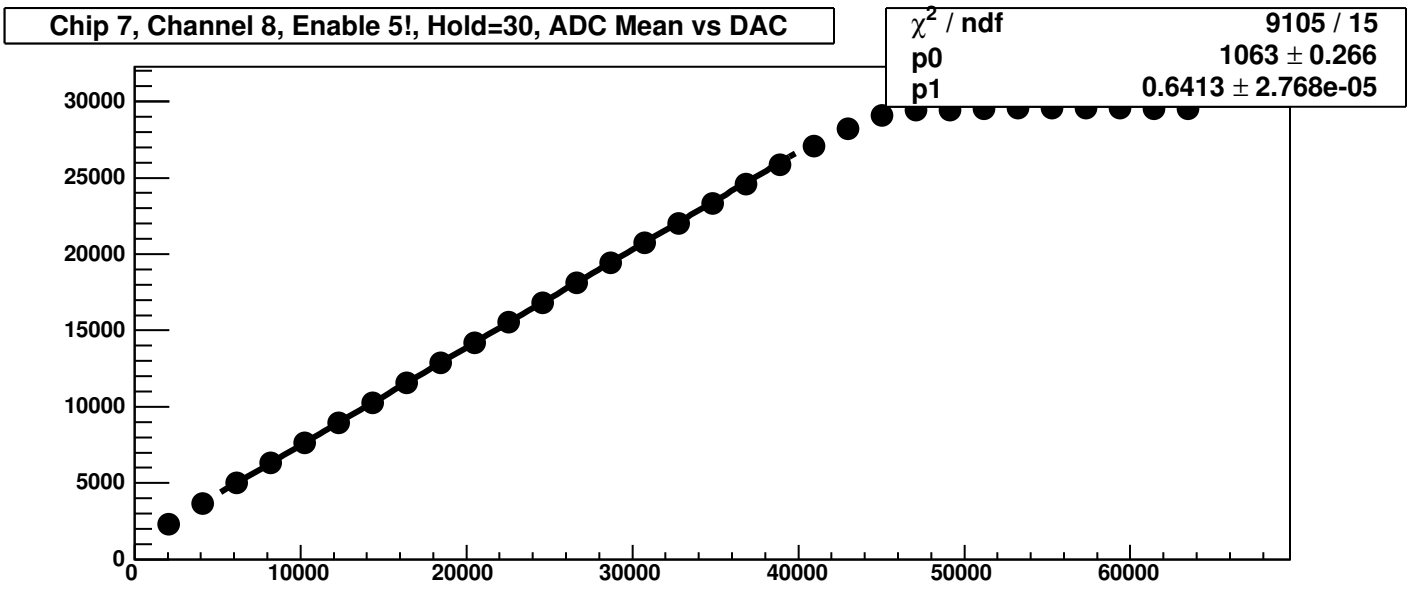


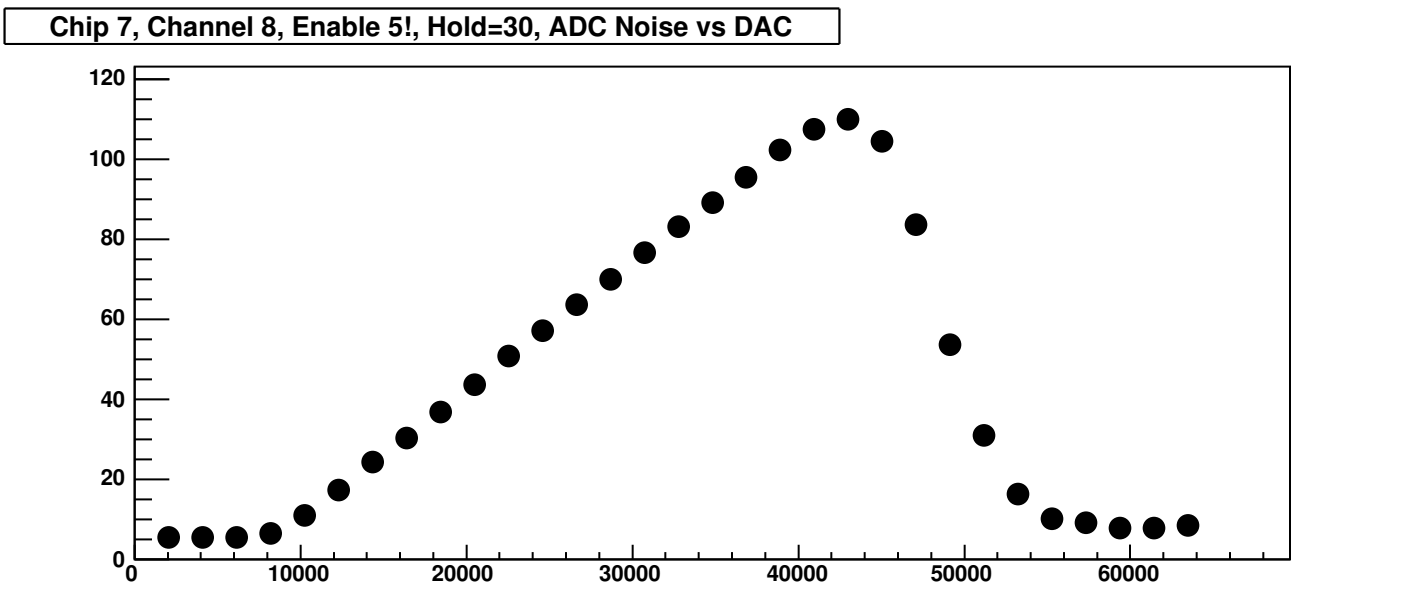
Chip 7, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC

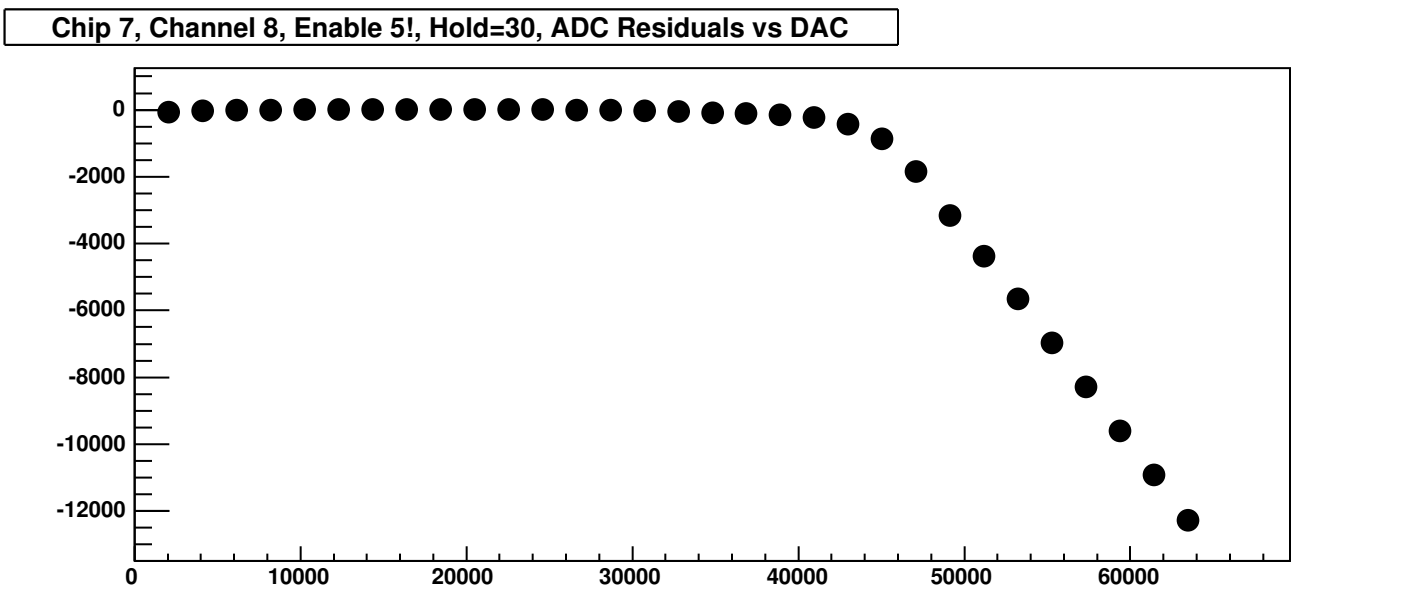


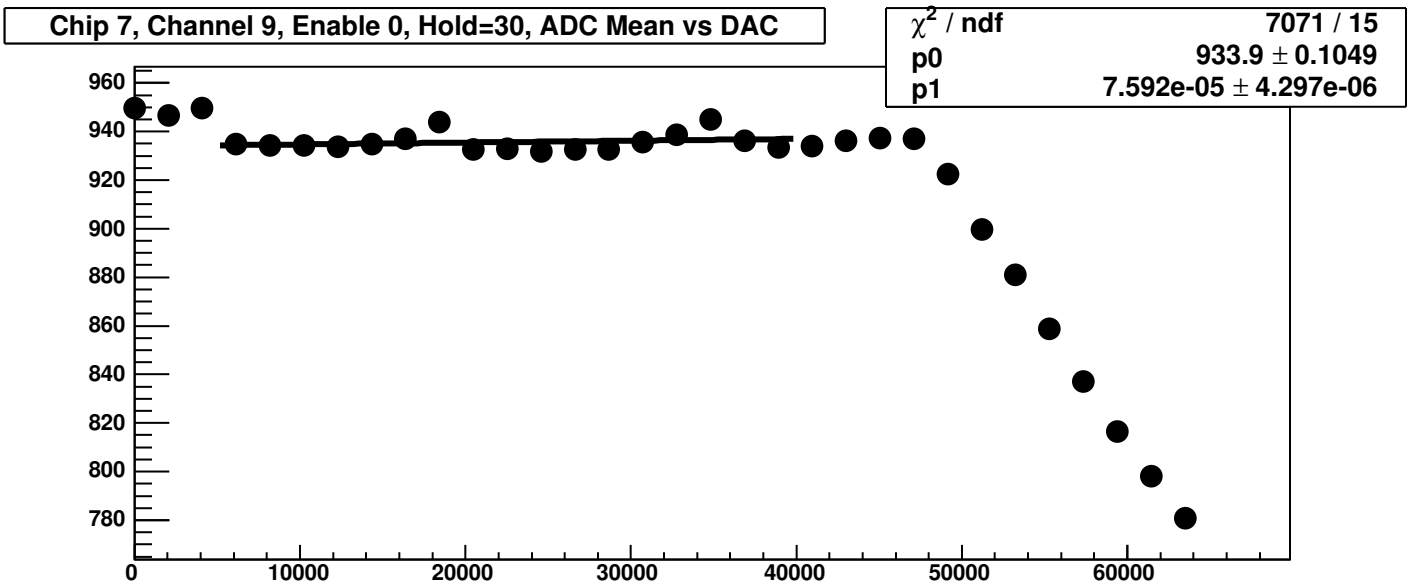
Chip 7, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC

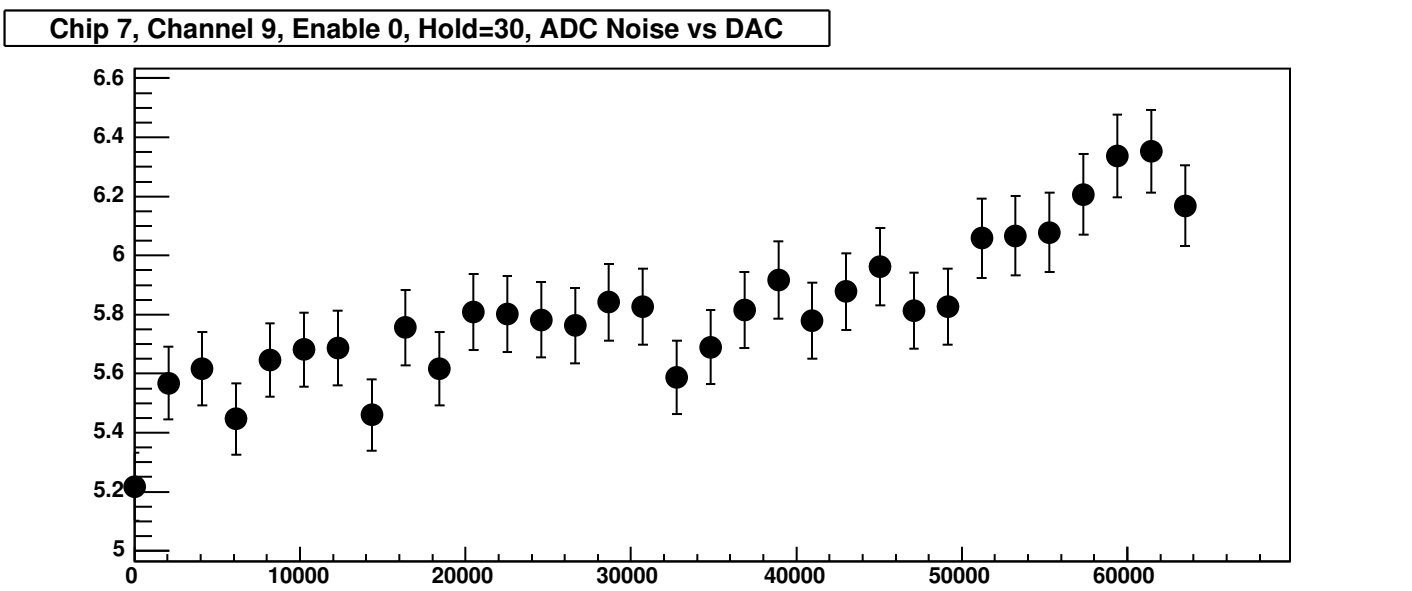


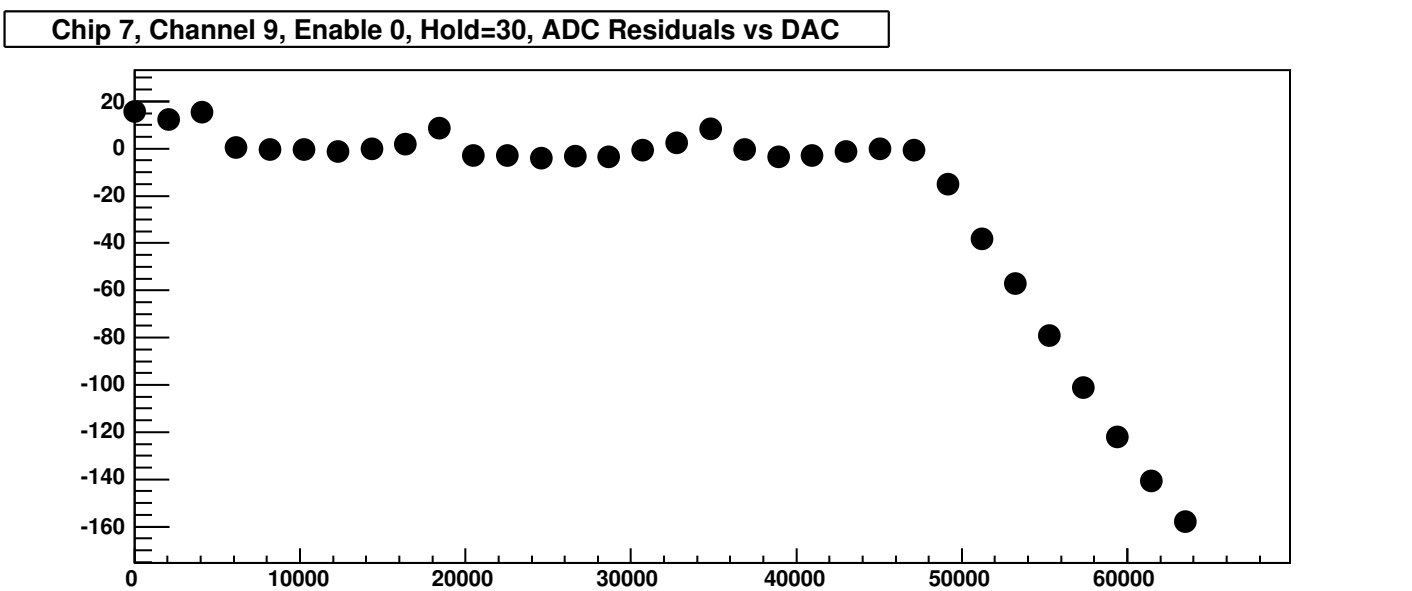

Chip 7, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 9105 / 15
p0 1063 ± 0.266
p1 0.6413 ± 2.768e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

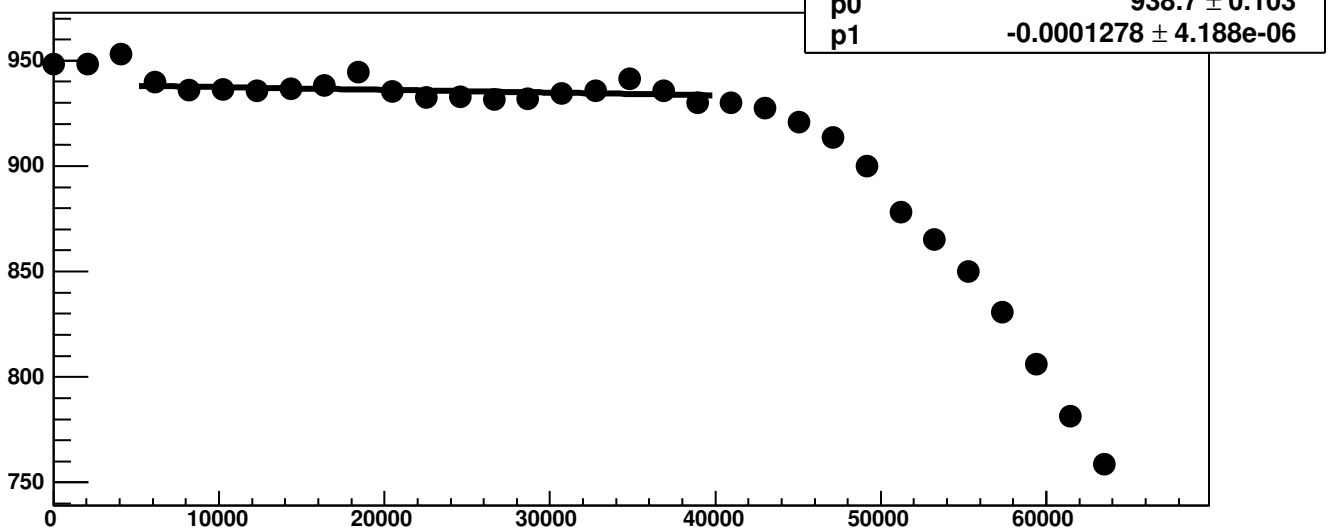

Chip 7, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7071 / 15
p0 933.9 ± 0.1049
p1 7.592e-05 ± 4.297e-06
960
940
920
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

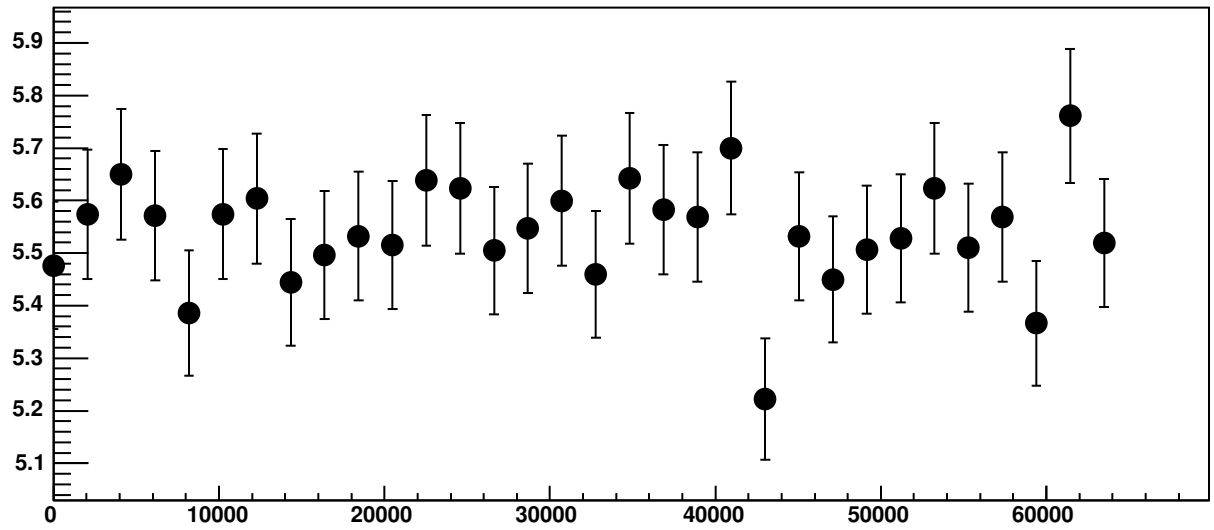

Chip 7, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

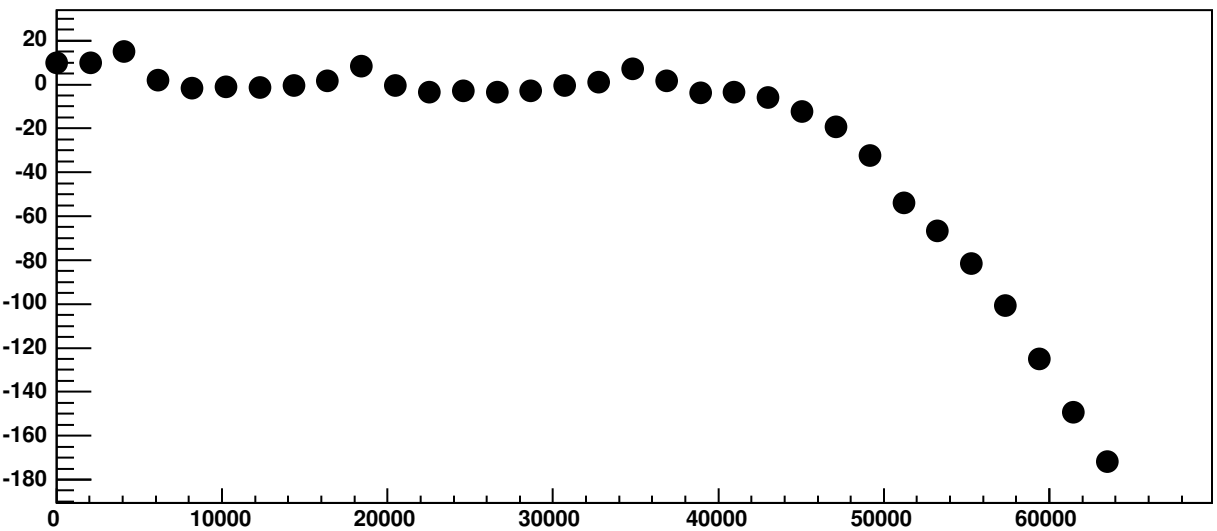
Chip 7, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

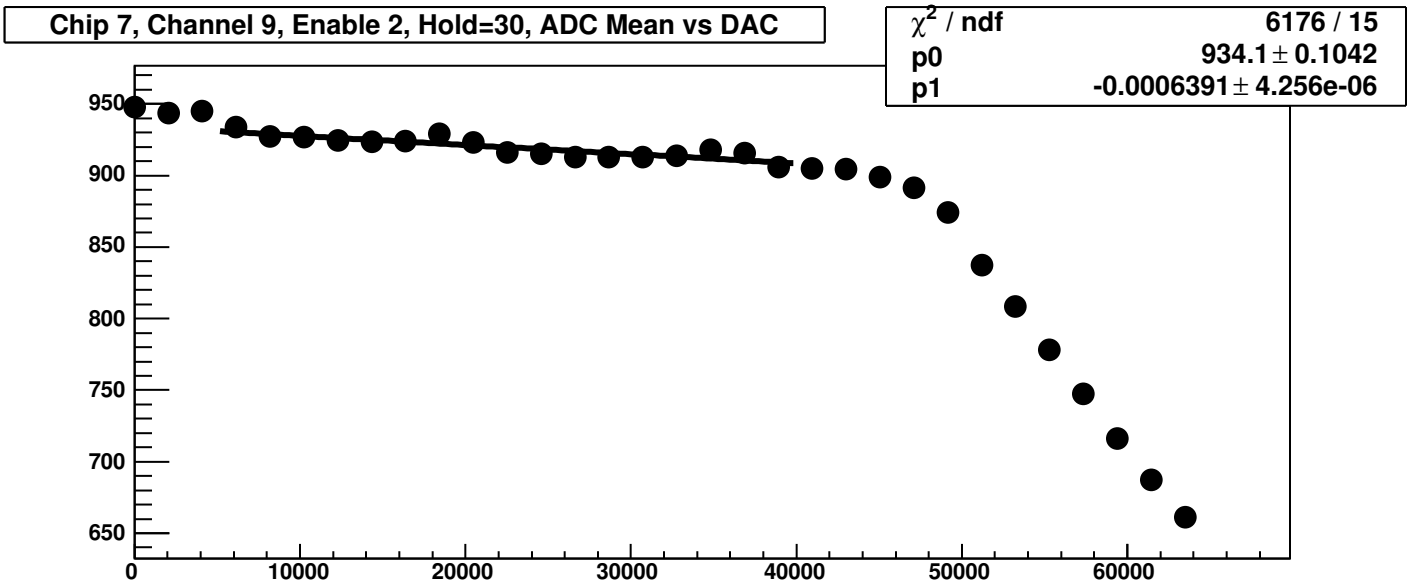


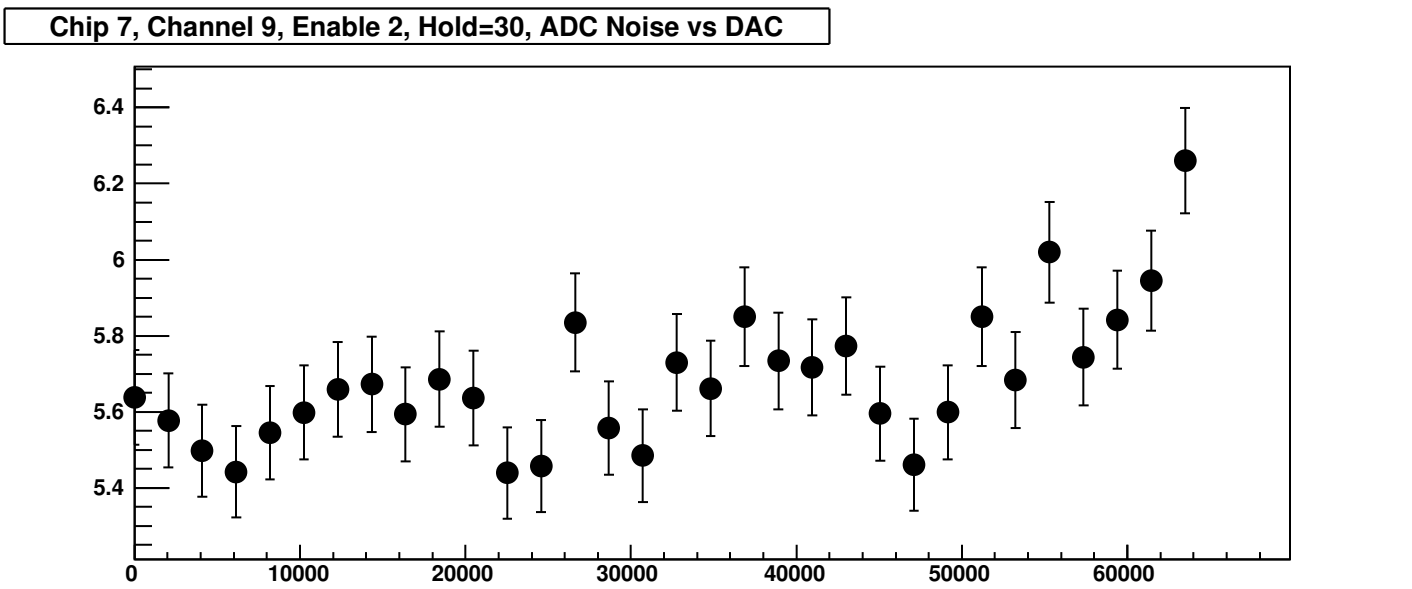
Chip 7, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC

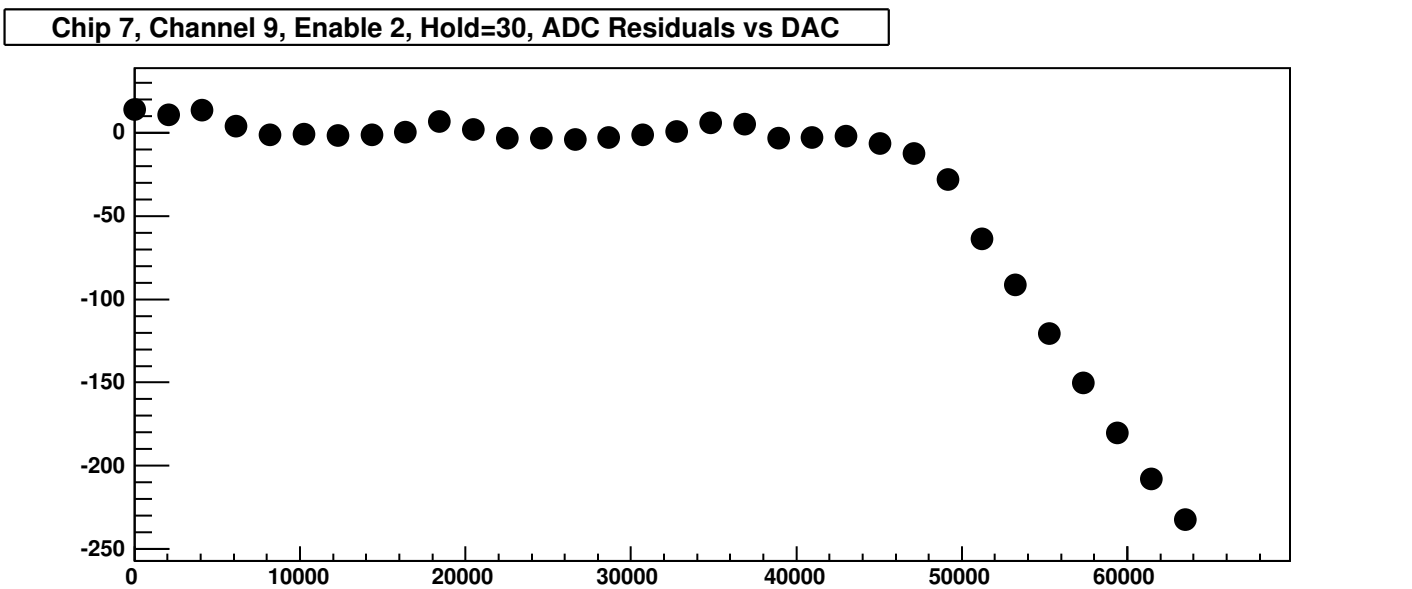


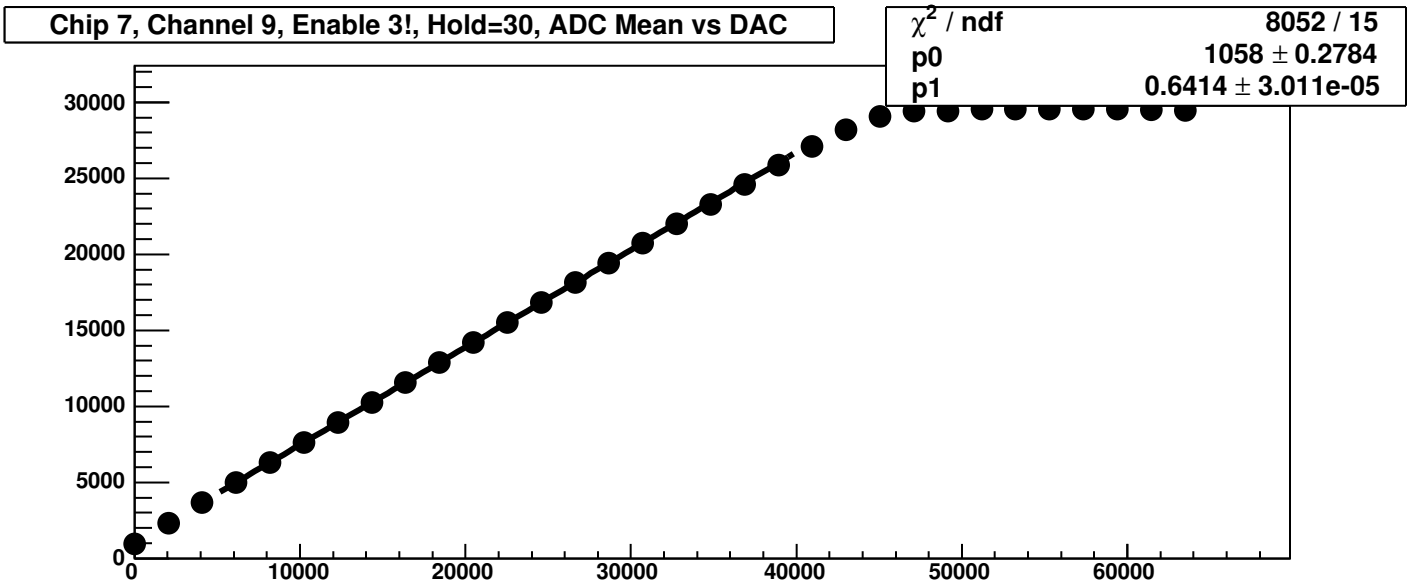
Chip 7, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

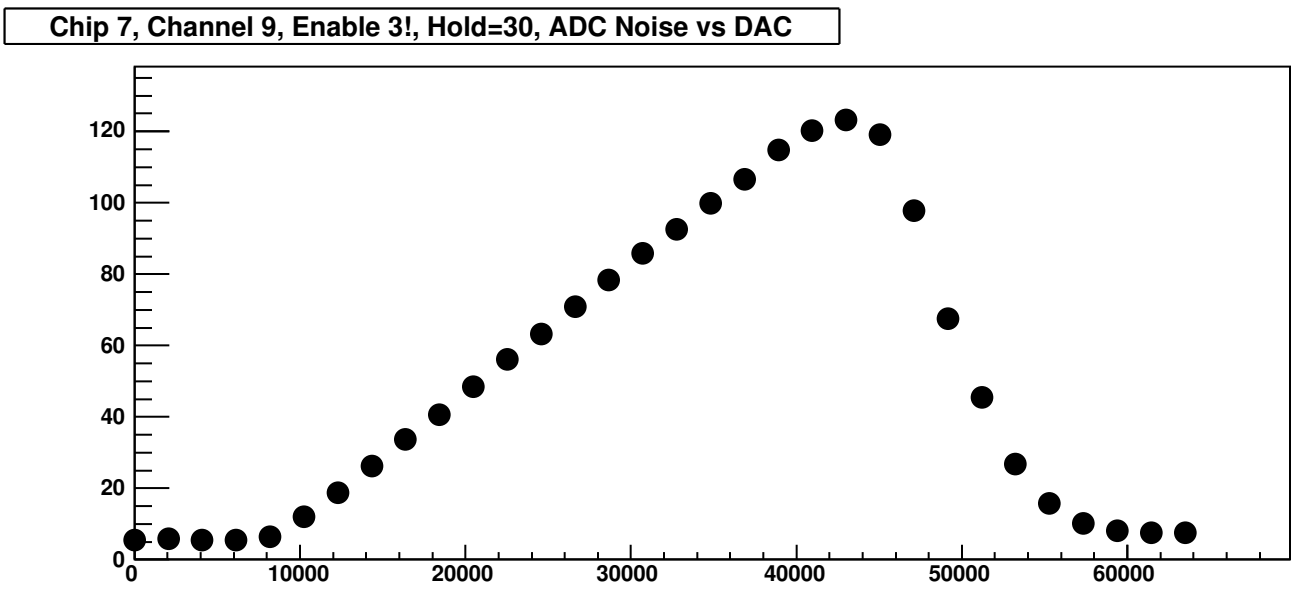


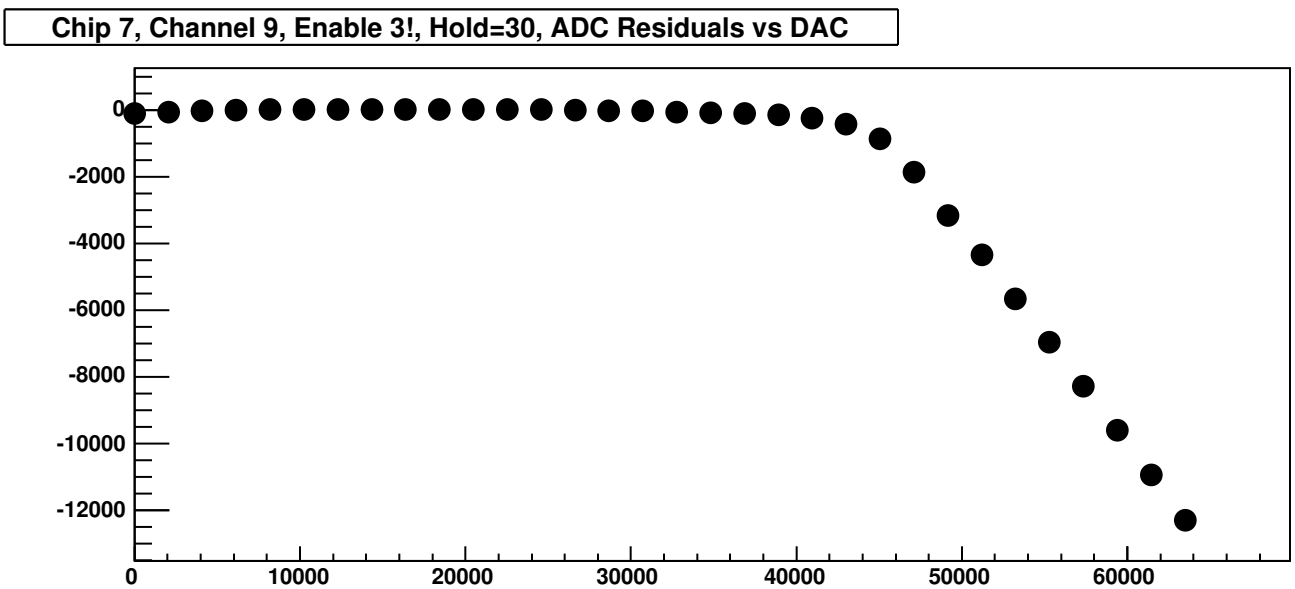

Chip 7, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6176 / 15
p0 934.1 ± 0.1042
p1 -0.0006391 ± 4.256e-06
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

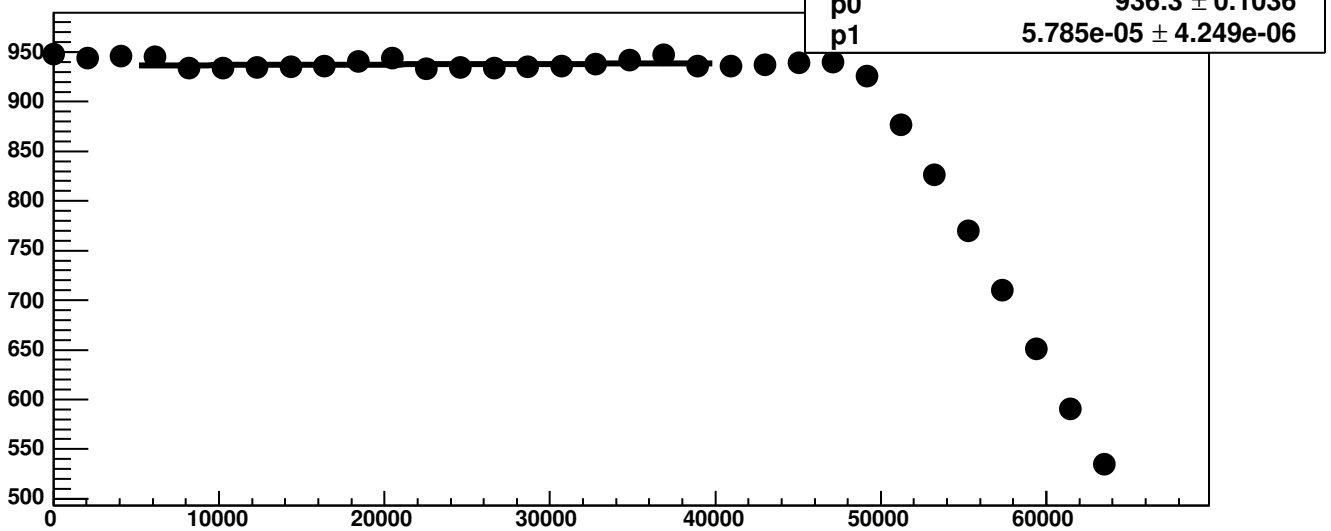

Chip 7, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 8052 / 15
p0 1058 ± 0.2784
p1 0.6414 ± 3.011e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

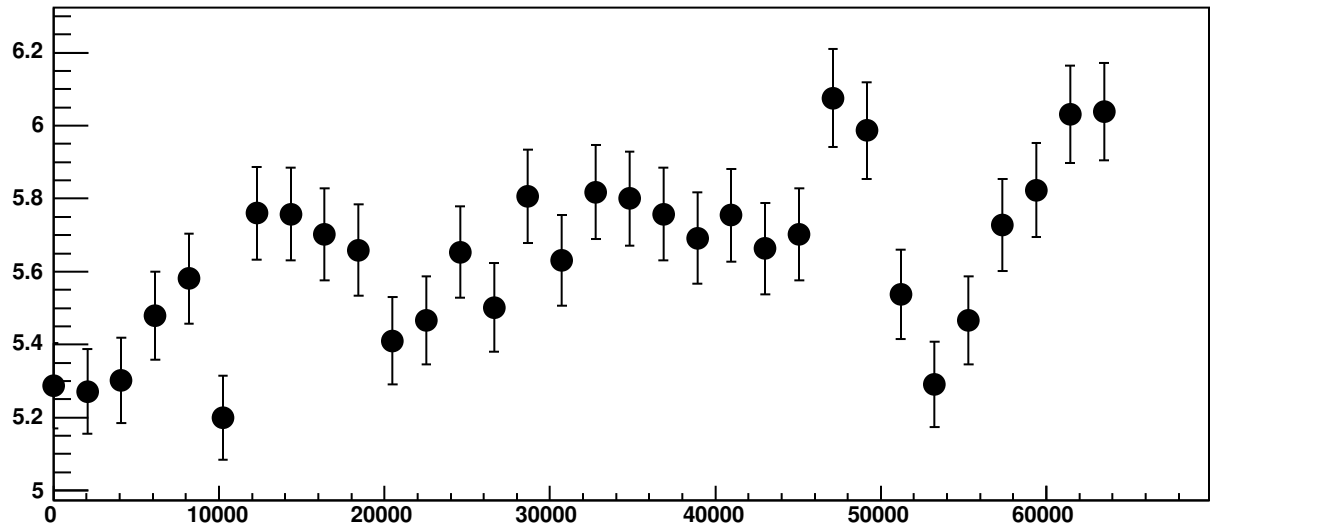

Chip 7, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

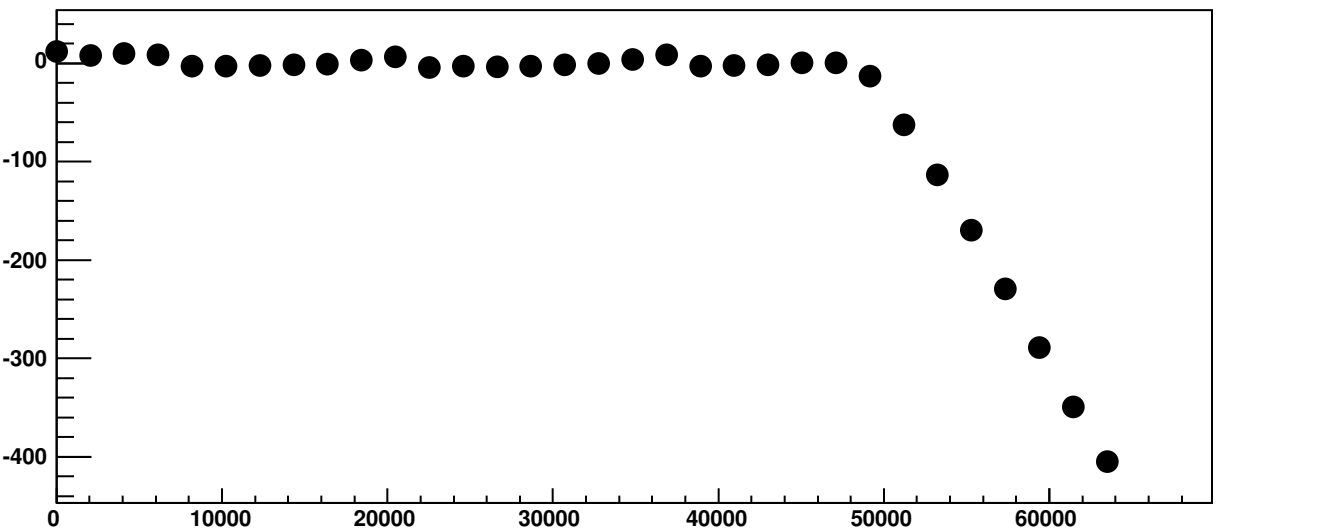
Chip 7, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC

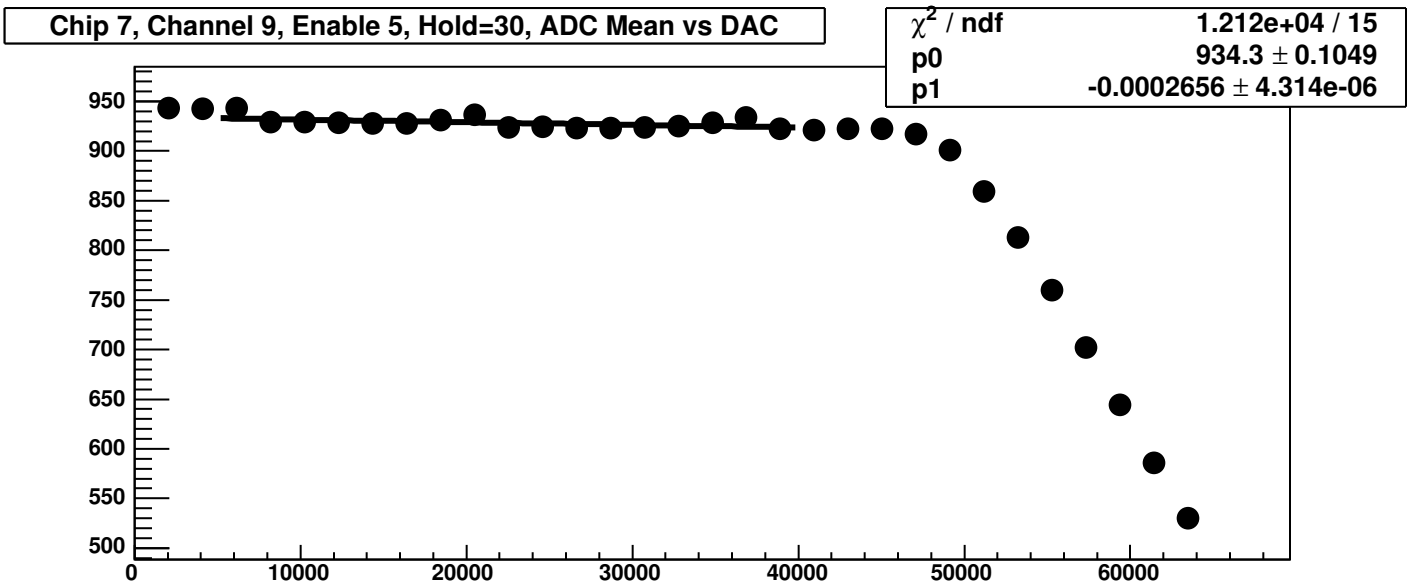


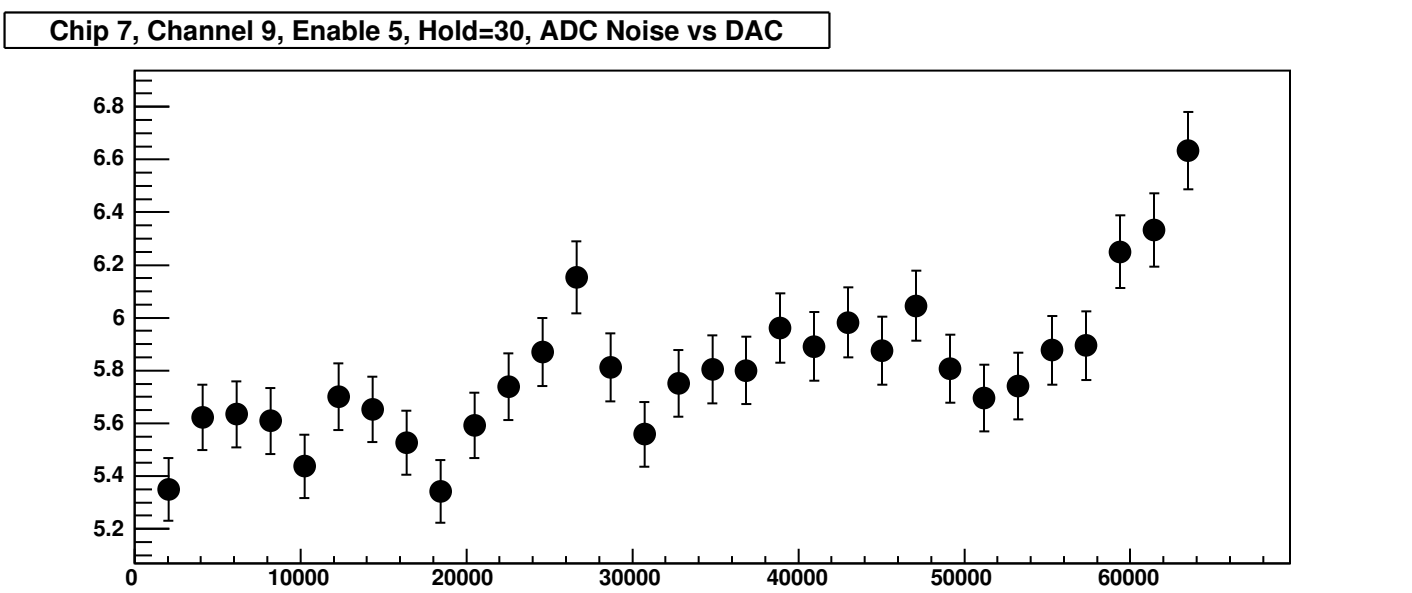
Chip 7, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC

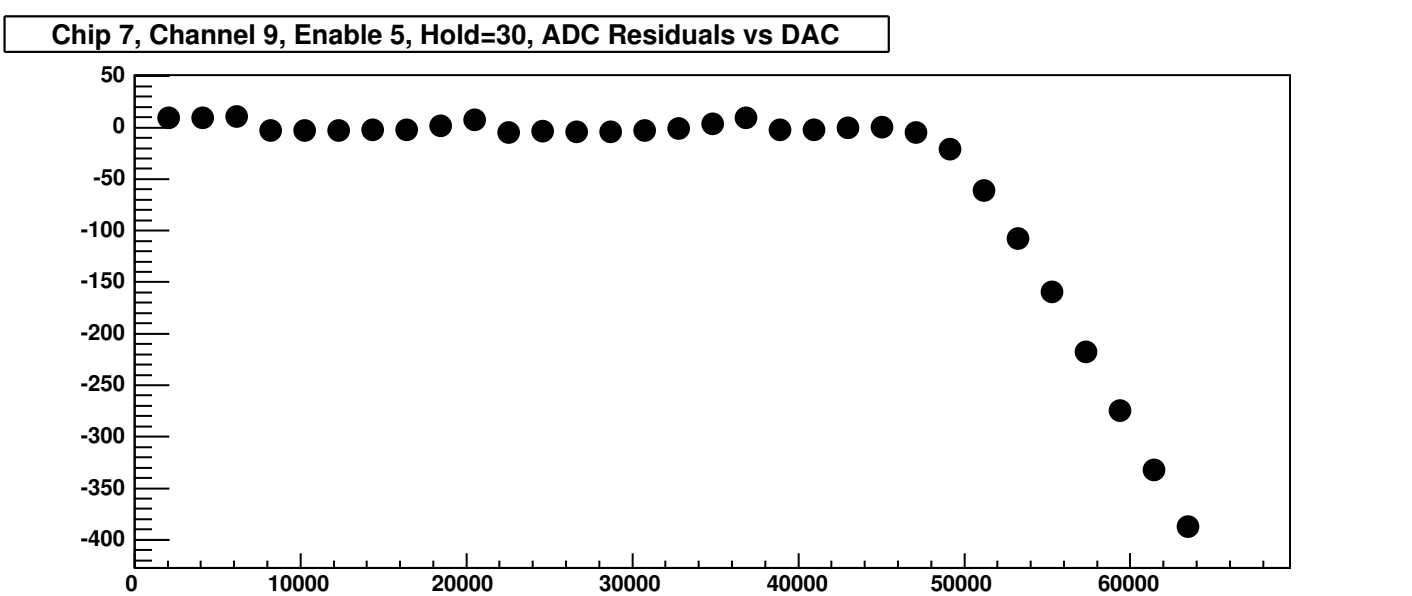


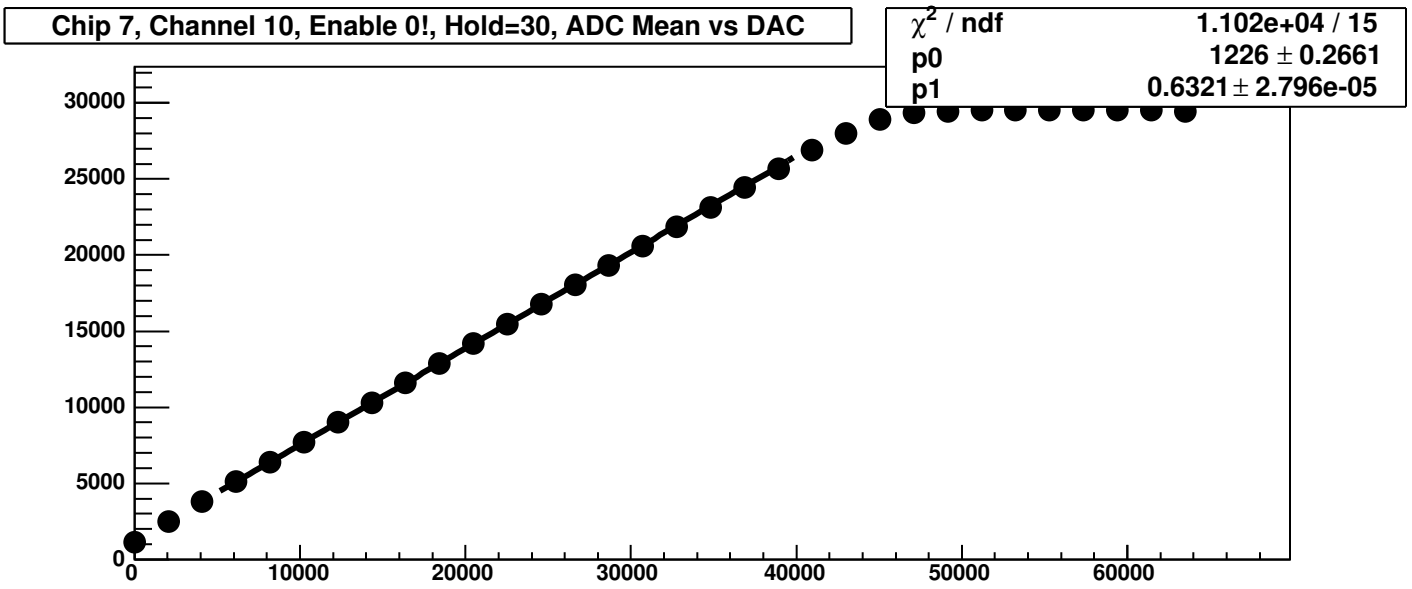
Chip 7, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC

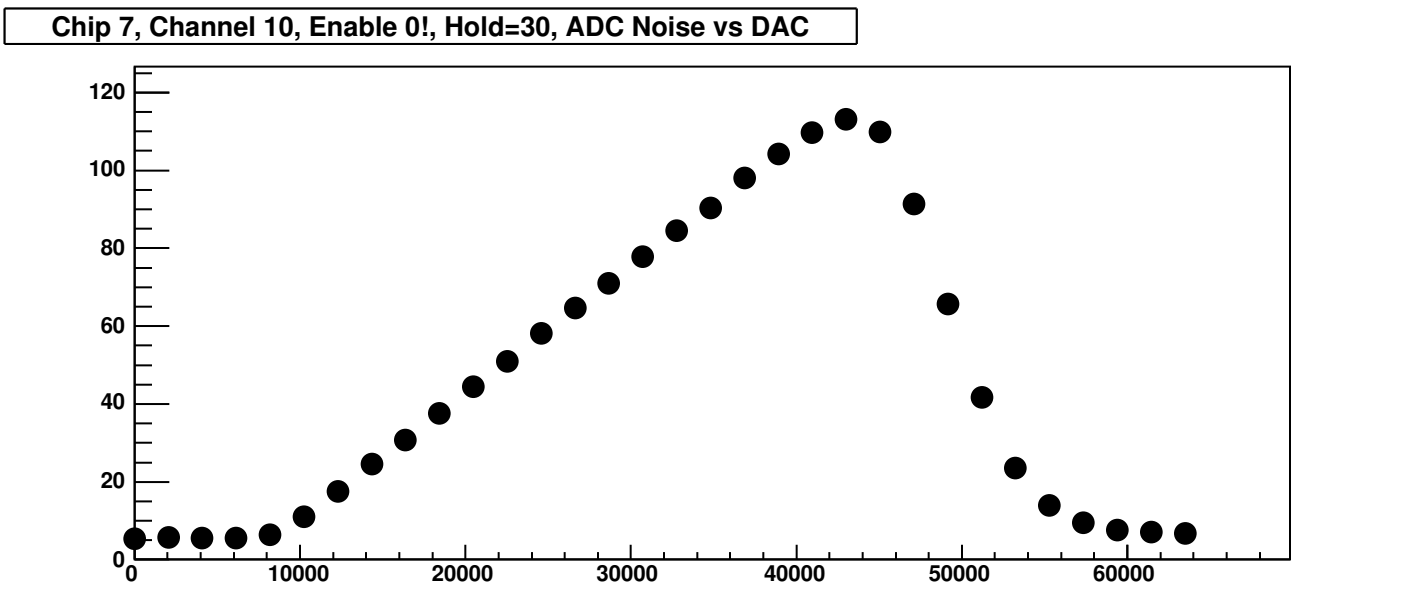


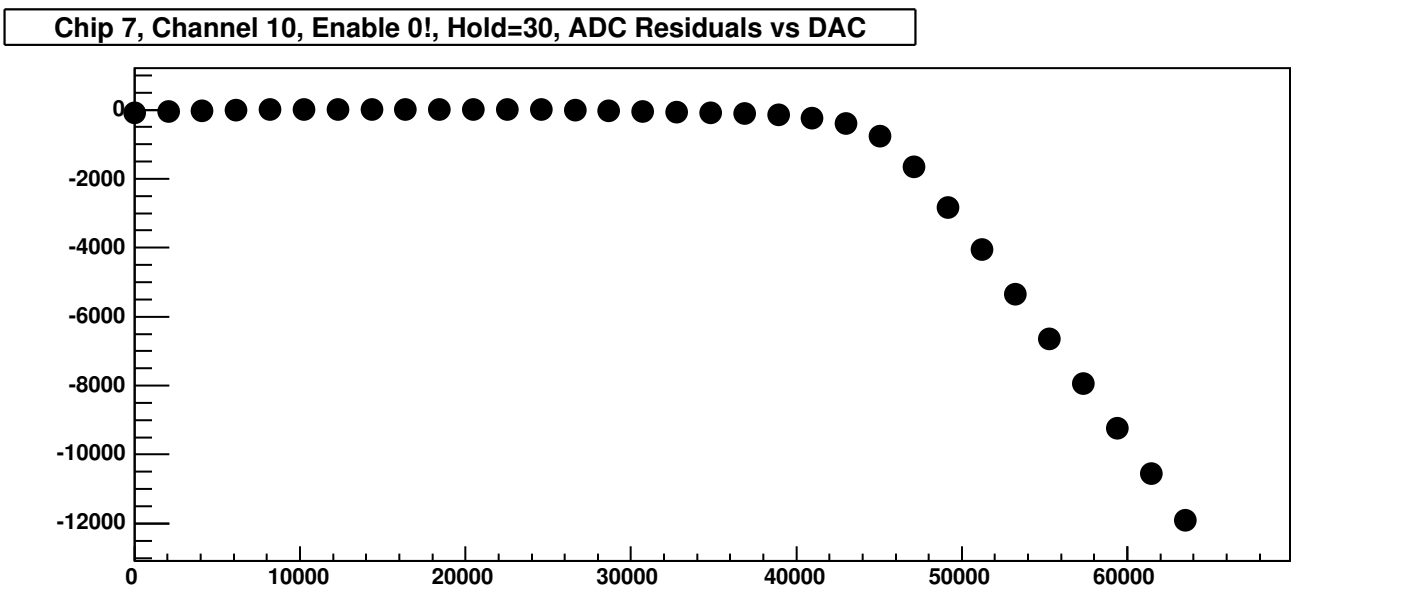

Chip 7, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.212e+04 / 15
p0 934.3 ± 0.1049
p1 -0.0002656 ± 4.314e-06
950
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

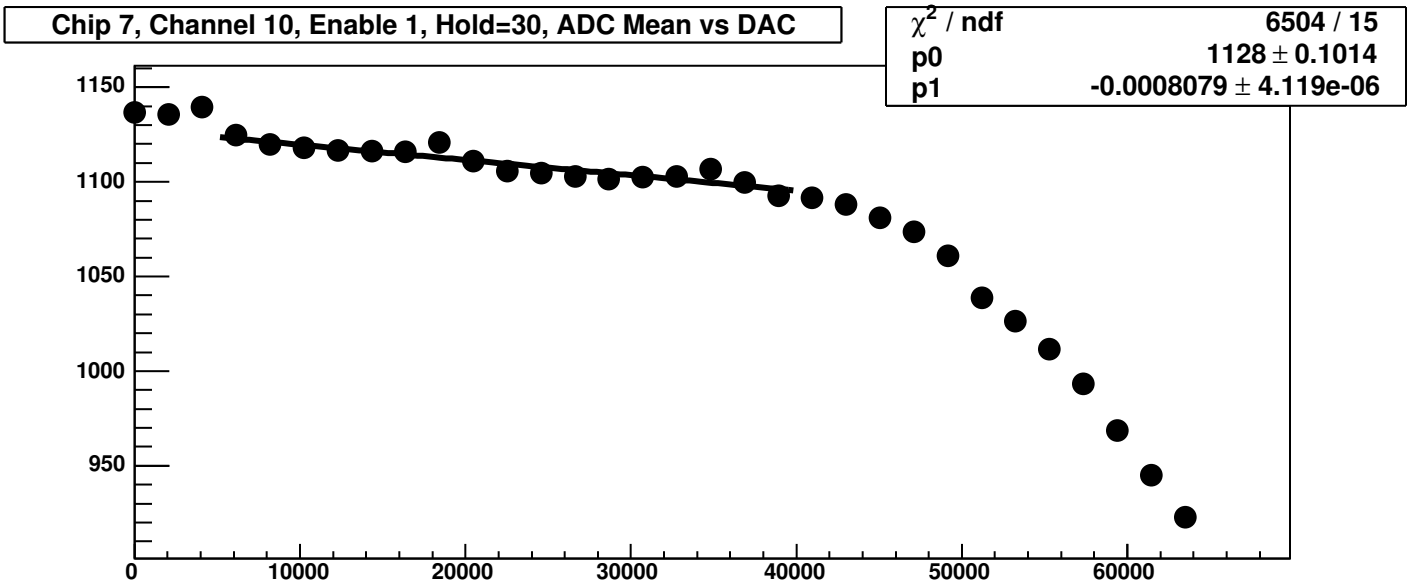

Chip 7, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

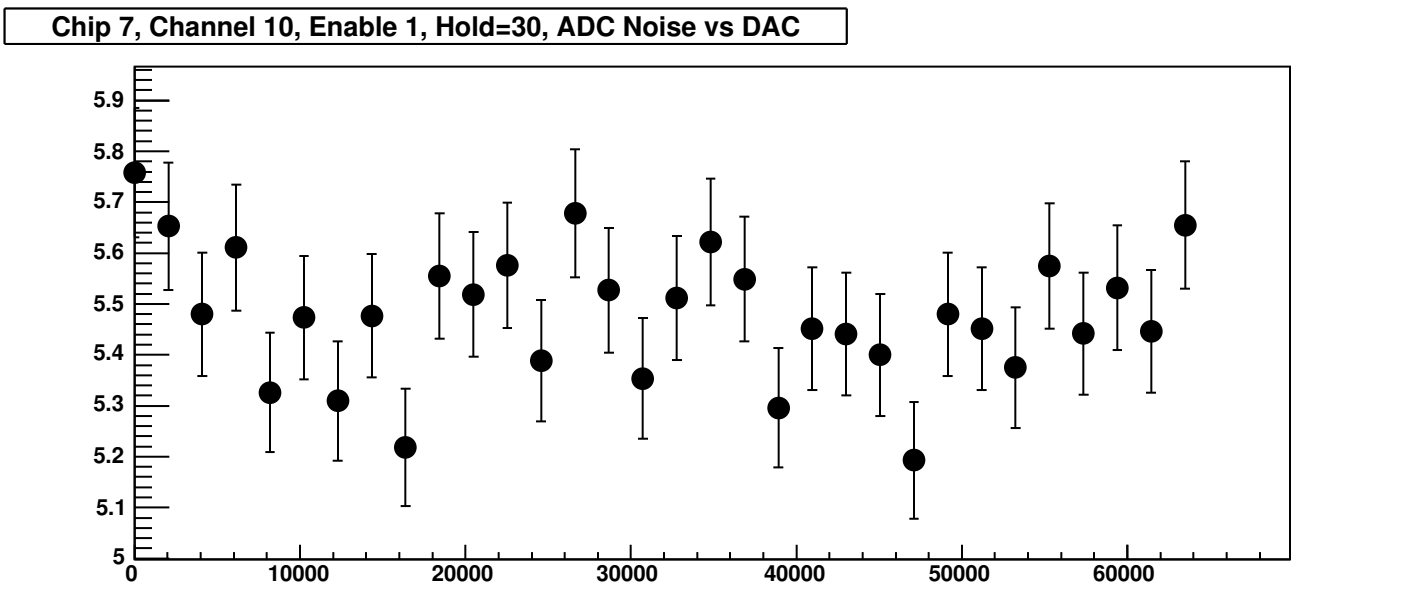

Chip 7, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

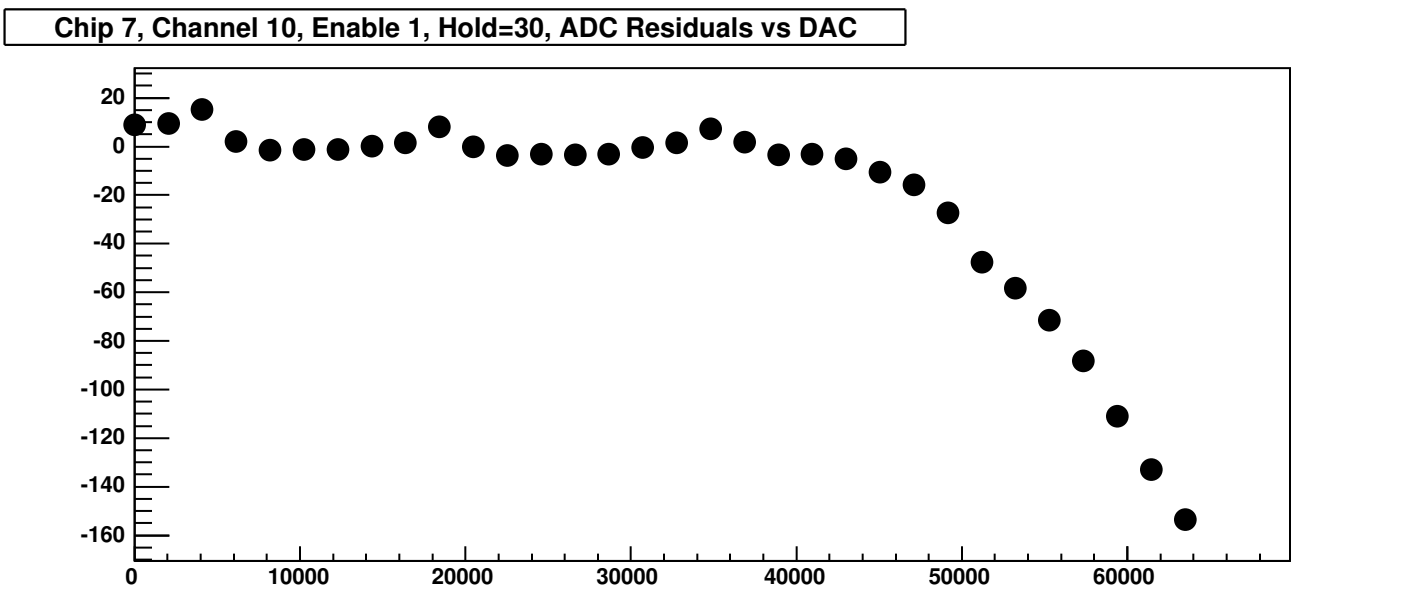

Chip 7, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.102e+04 / 15
p0 1226 ± 0.2661
p1 0.6321 ± 2.796e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

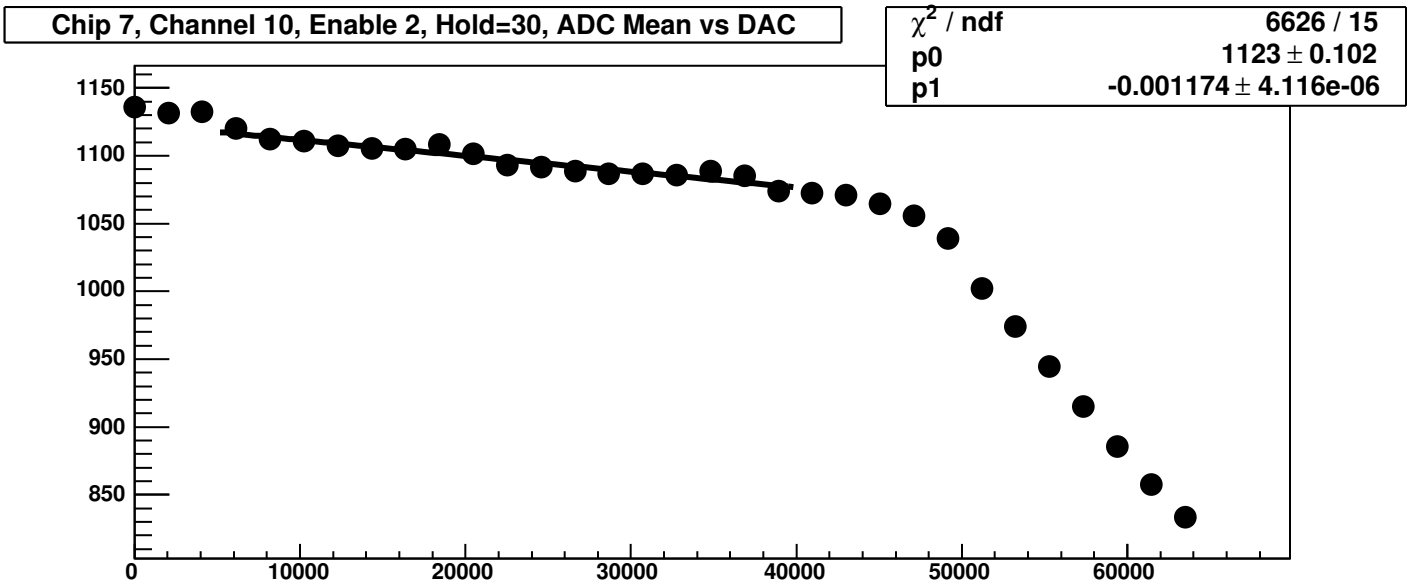

Chip 7, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

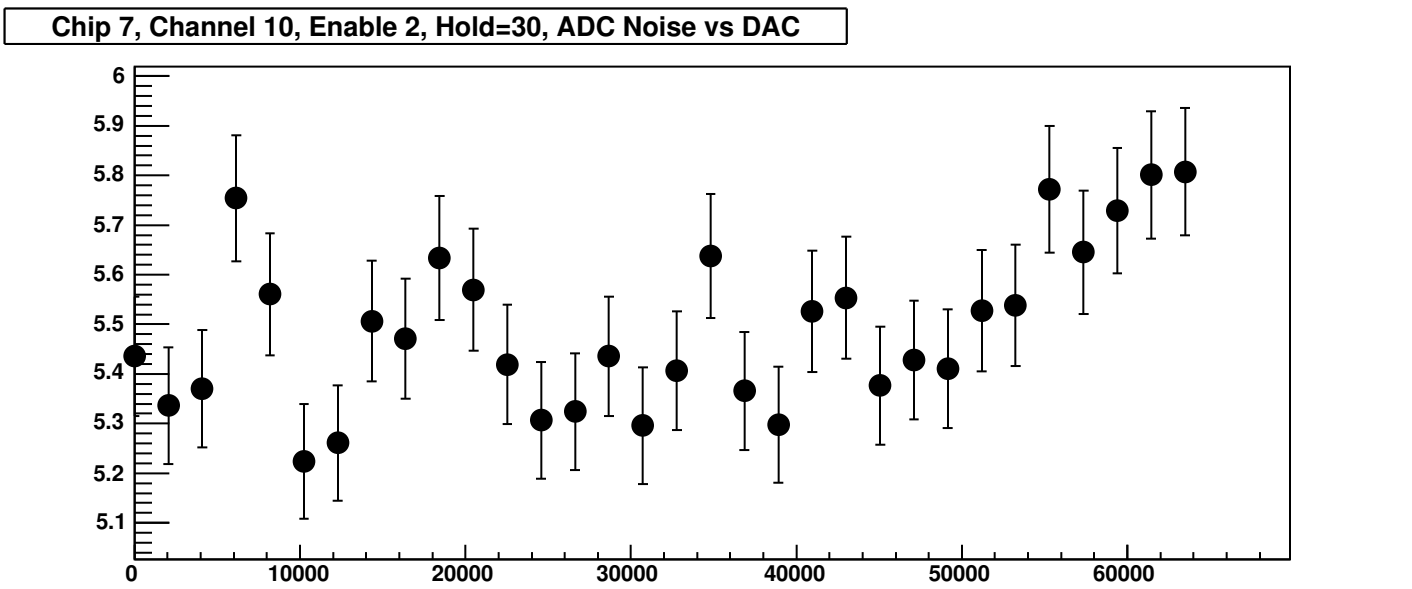

Chip 7, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

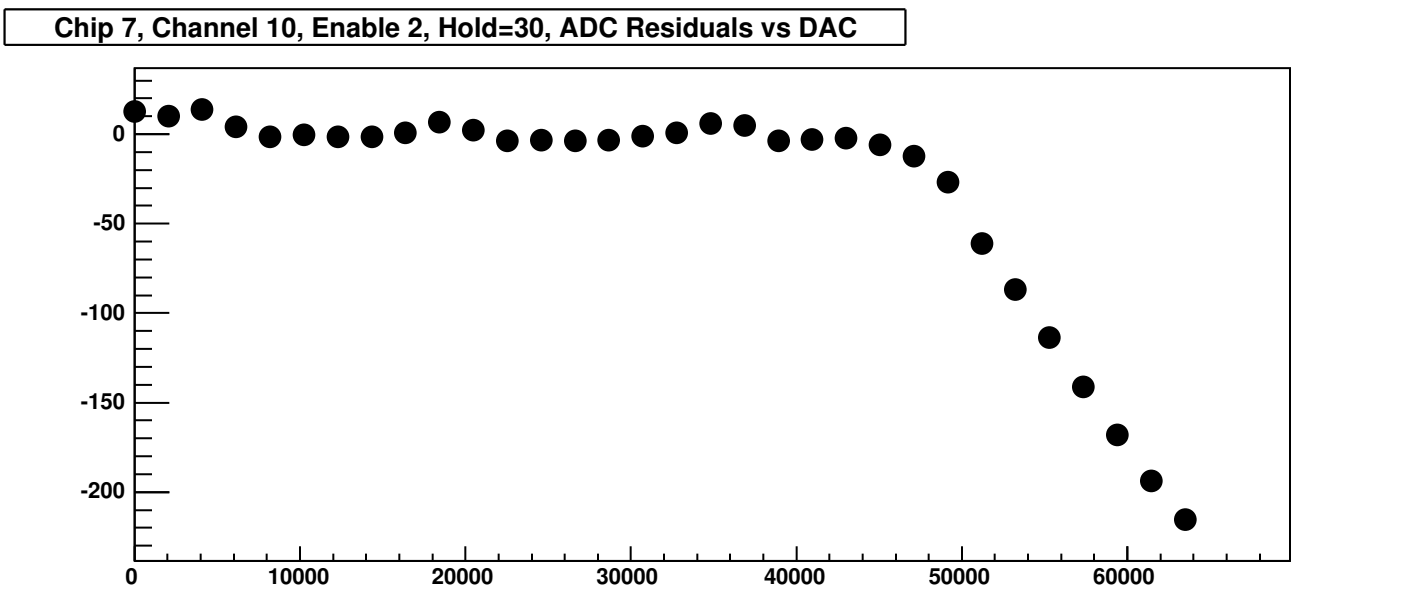

Chip 7, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6504 / 15
p0 1128 ± 0.1014
p1 -0.0008079 ± 4.119e-06
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

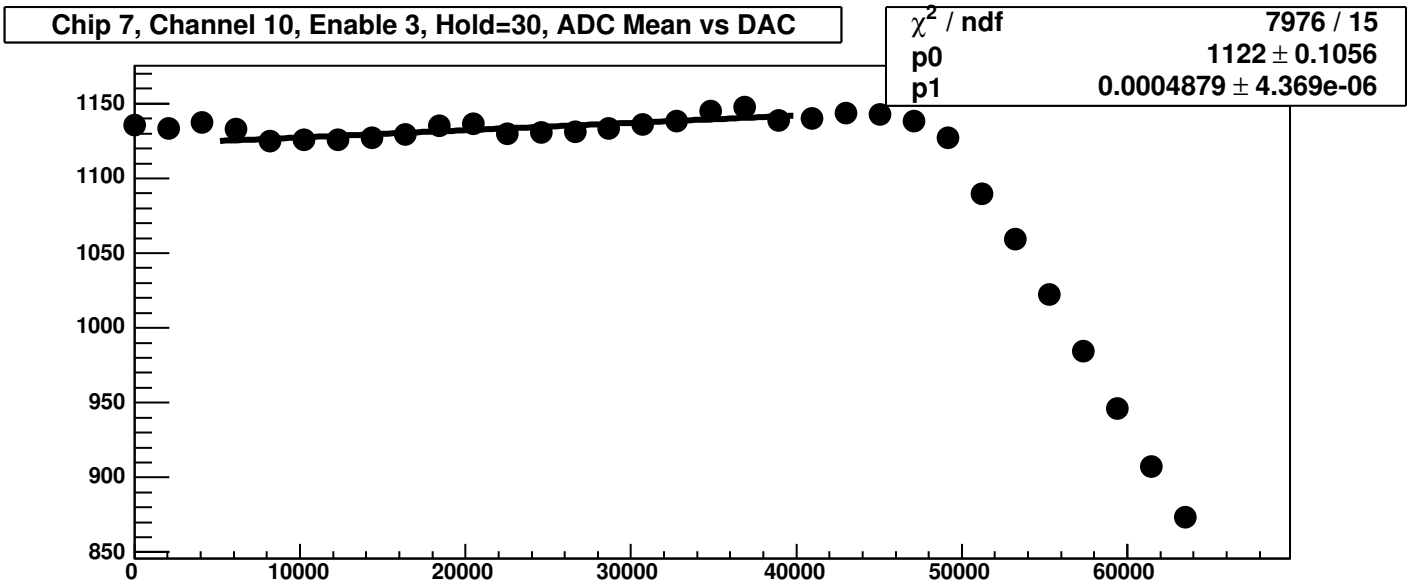

Chip 7, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
5
0
10000
20000
30000
40000
50000
60000

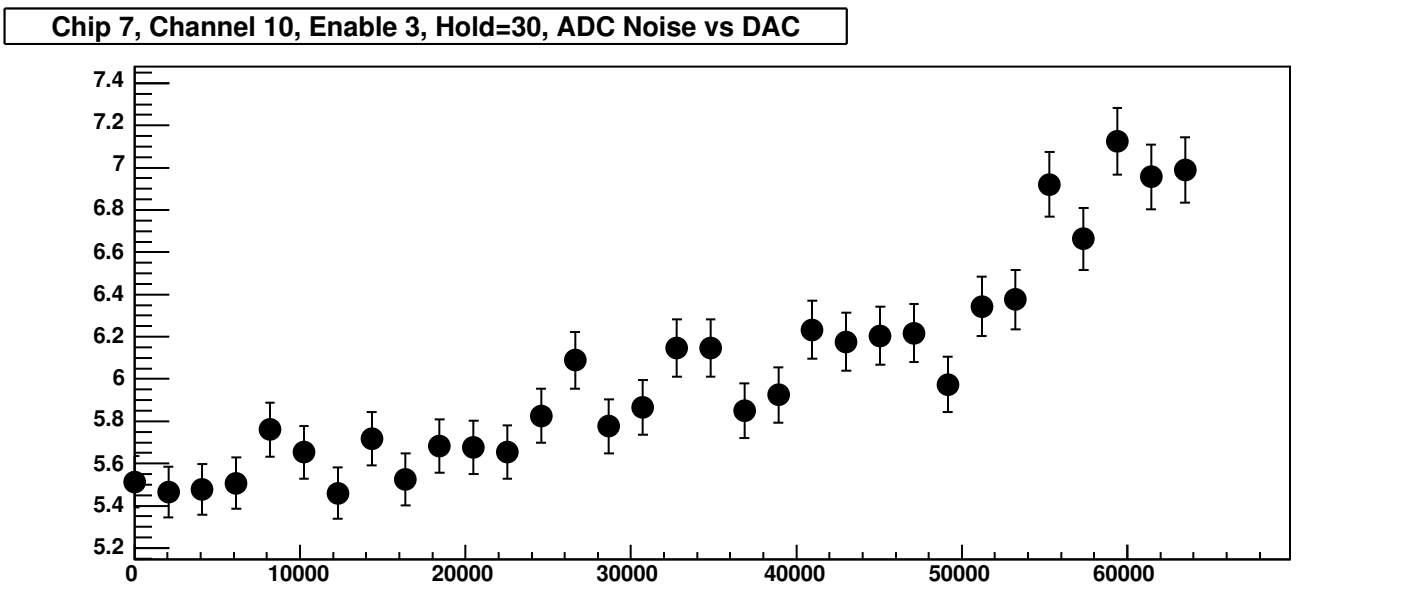

Chip 7, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

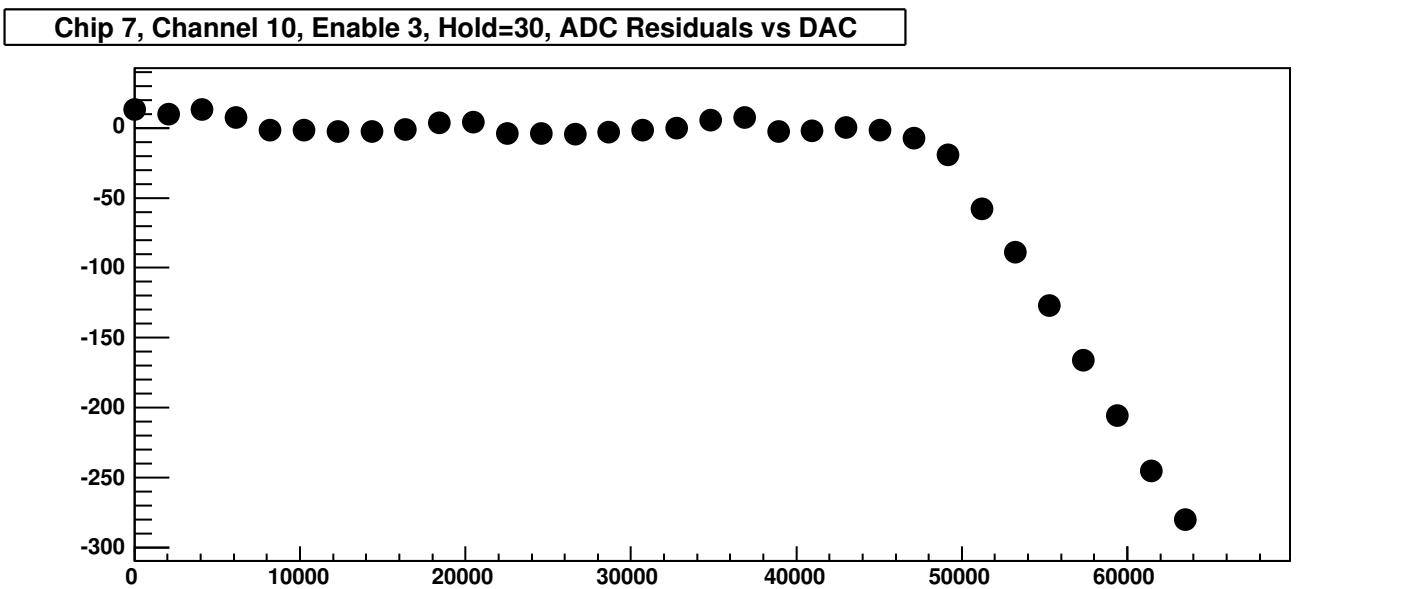

Chip 7, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6626 / 15
p0 1123 ± 0.102
p1 -0.001174 ± 4.116e-06
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

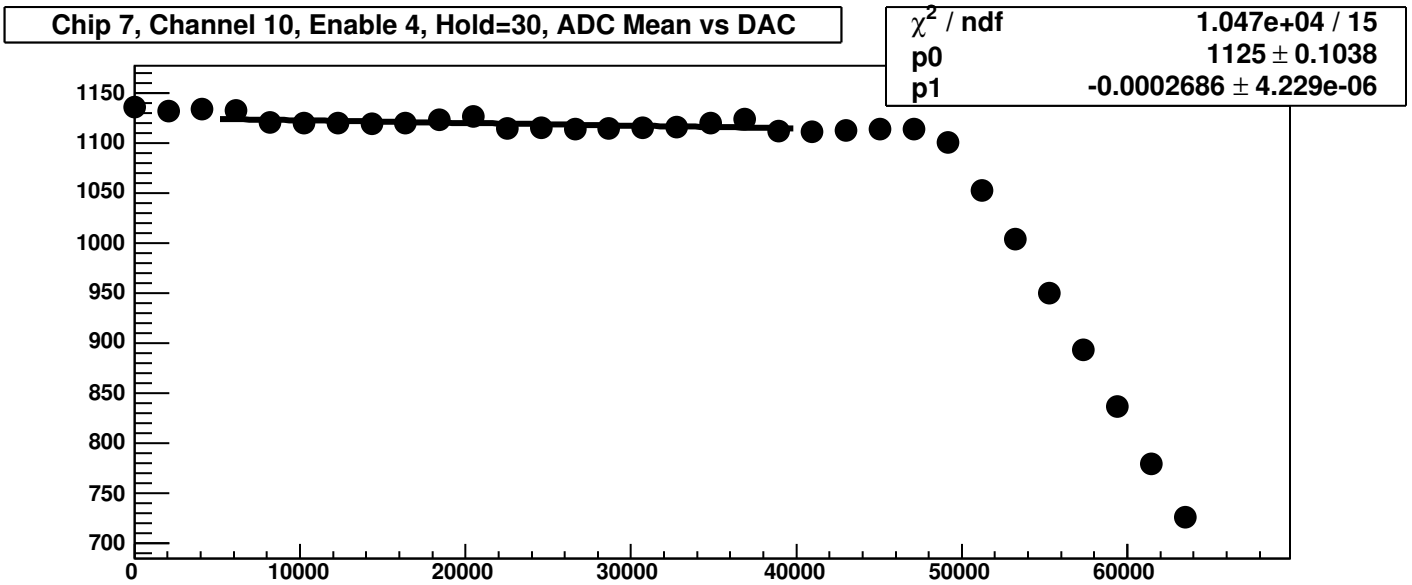

Chip 7, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
0
10000
20000
30000
40000
50000
60000

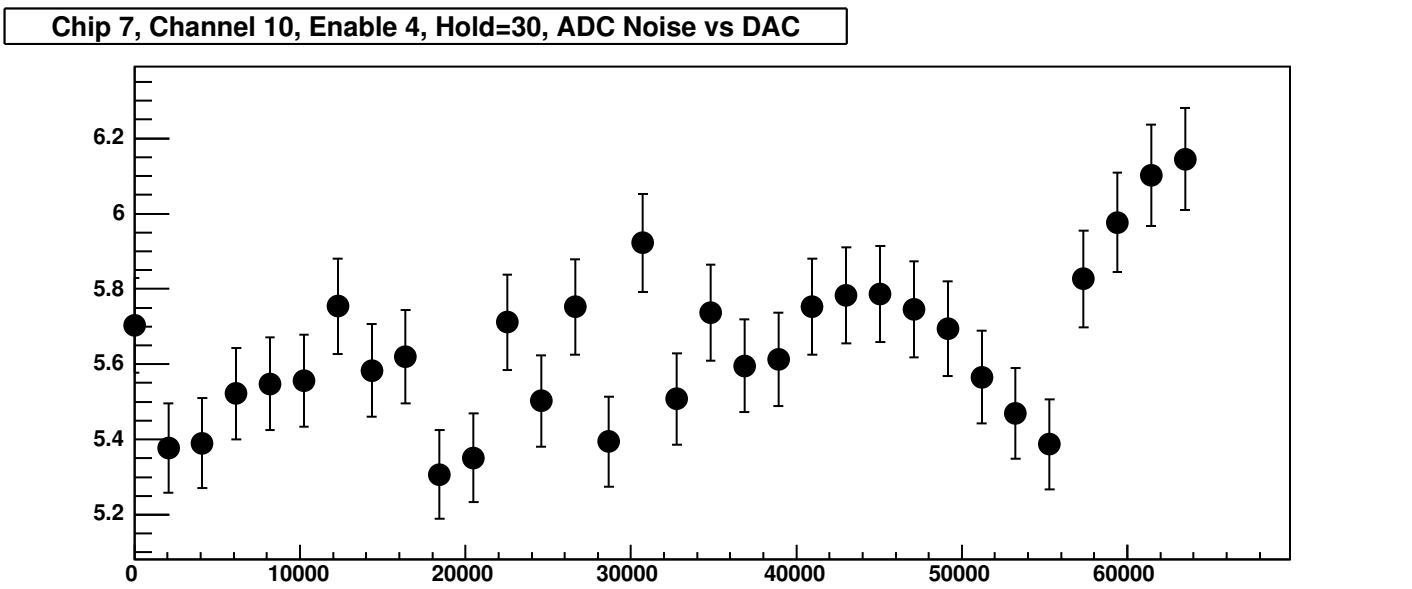

Chip 7, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

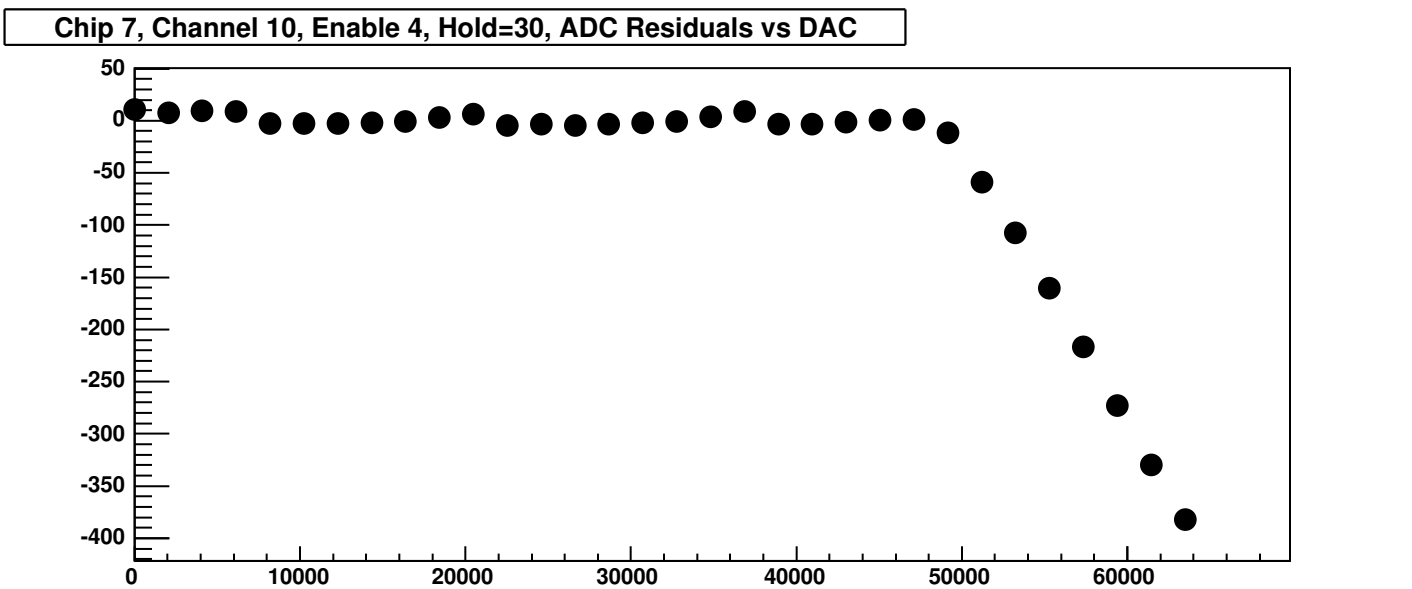

Chip 7, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7976 / 15
p0 1122 ± 0.1056
p1 0.0004879 ± 4.369e-06
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

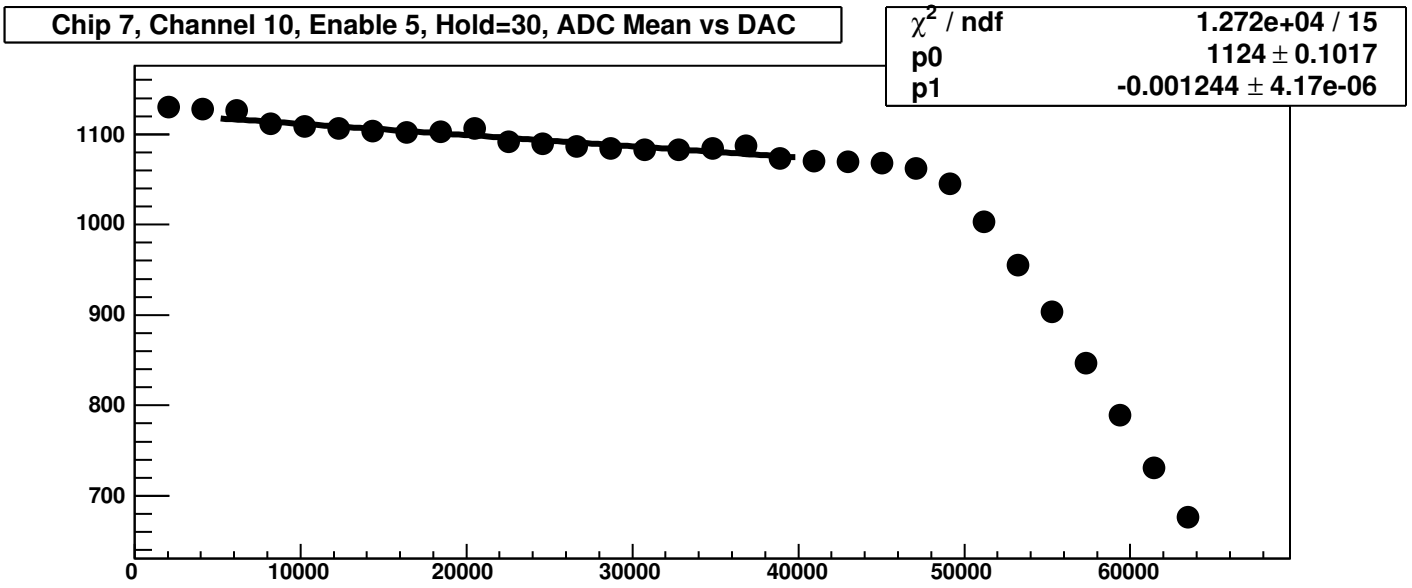

Chip 7, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

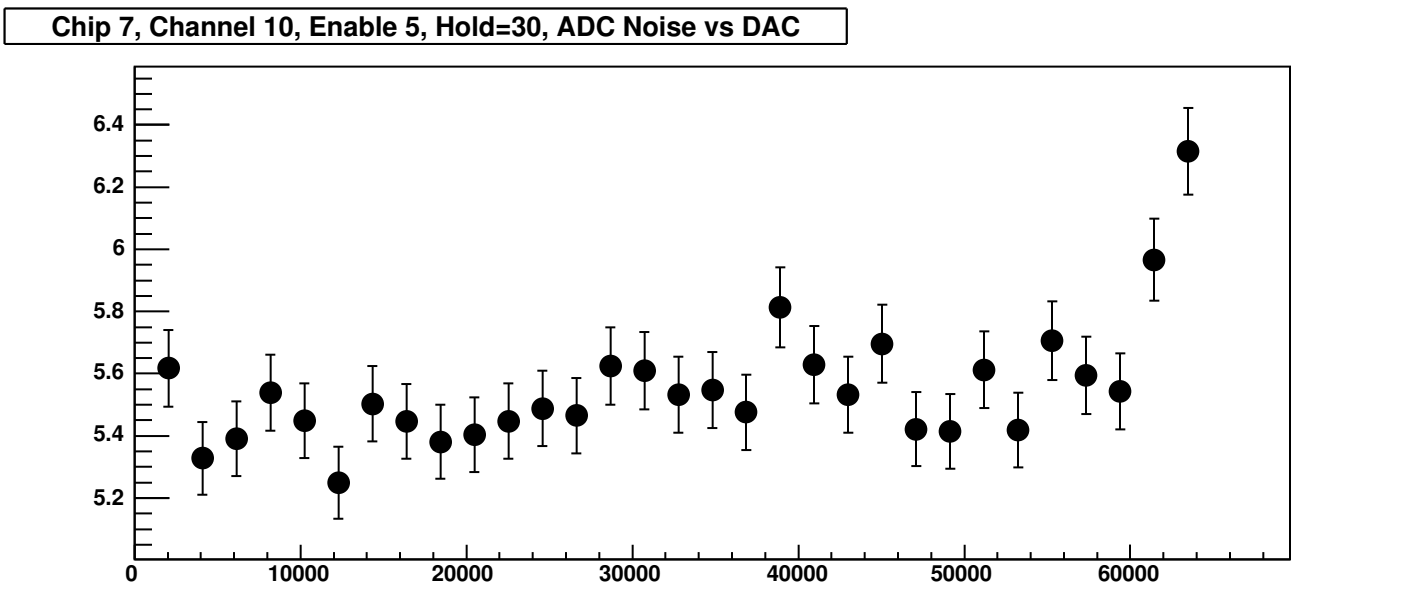

Chip 7, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

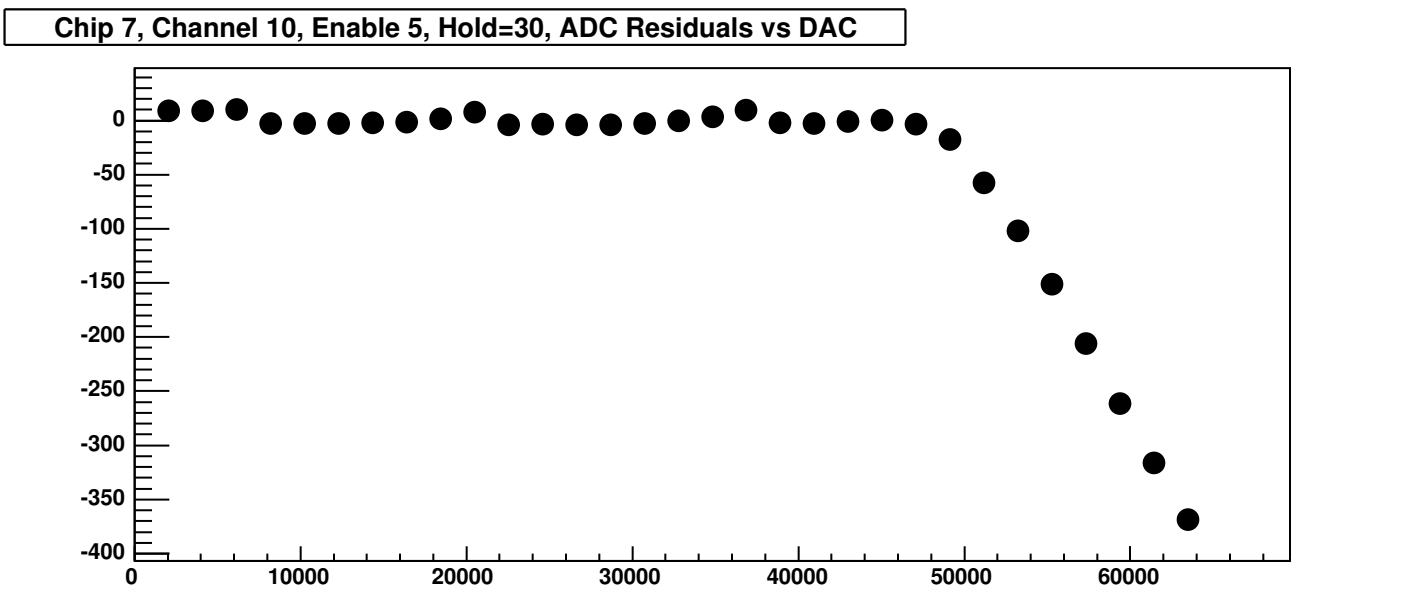

Chip 7, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.047e+04 / 15
p0 1125 ± 0.1038
p1 -0.0002686 ± 4.229e-06
1150
1100
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

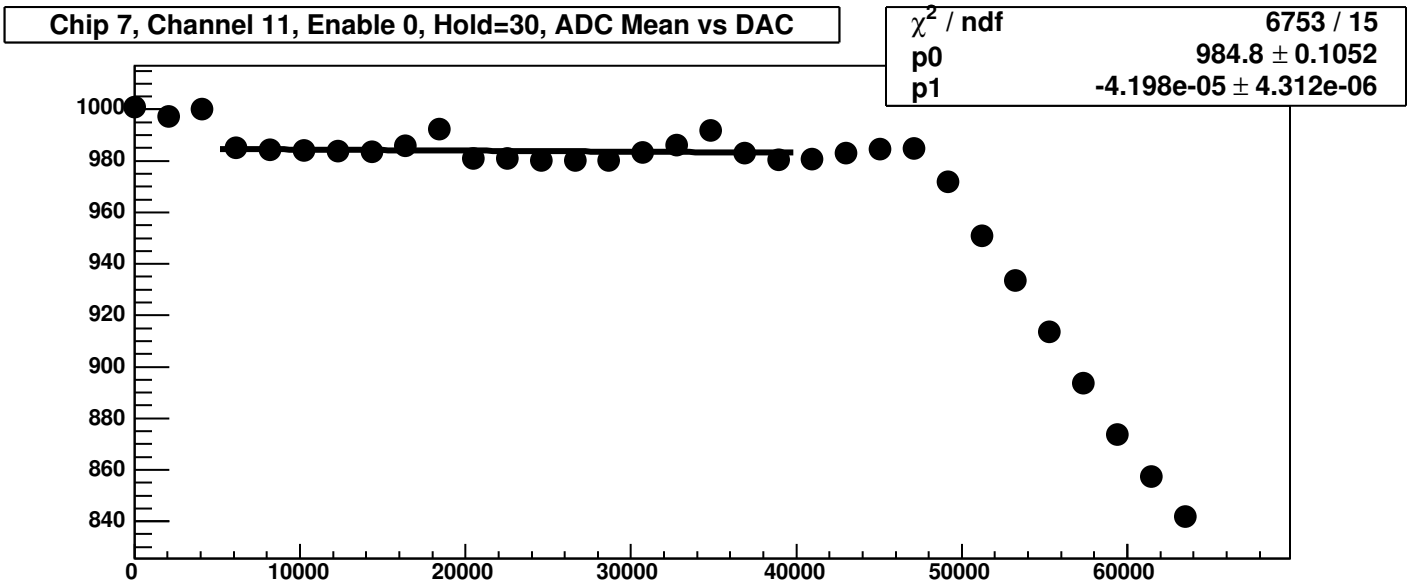

Chip 7, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

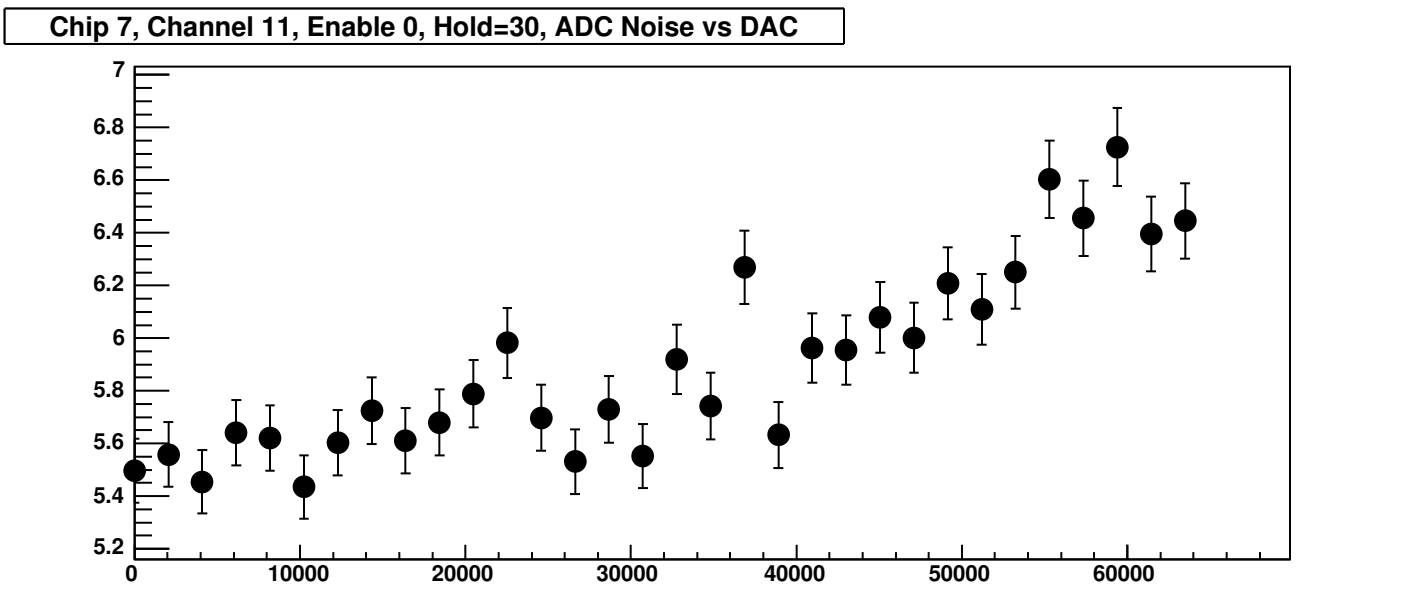

Chip 7, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

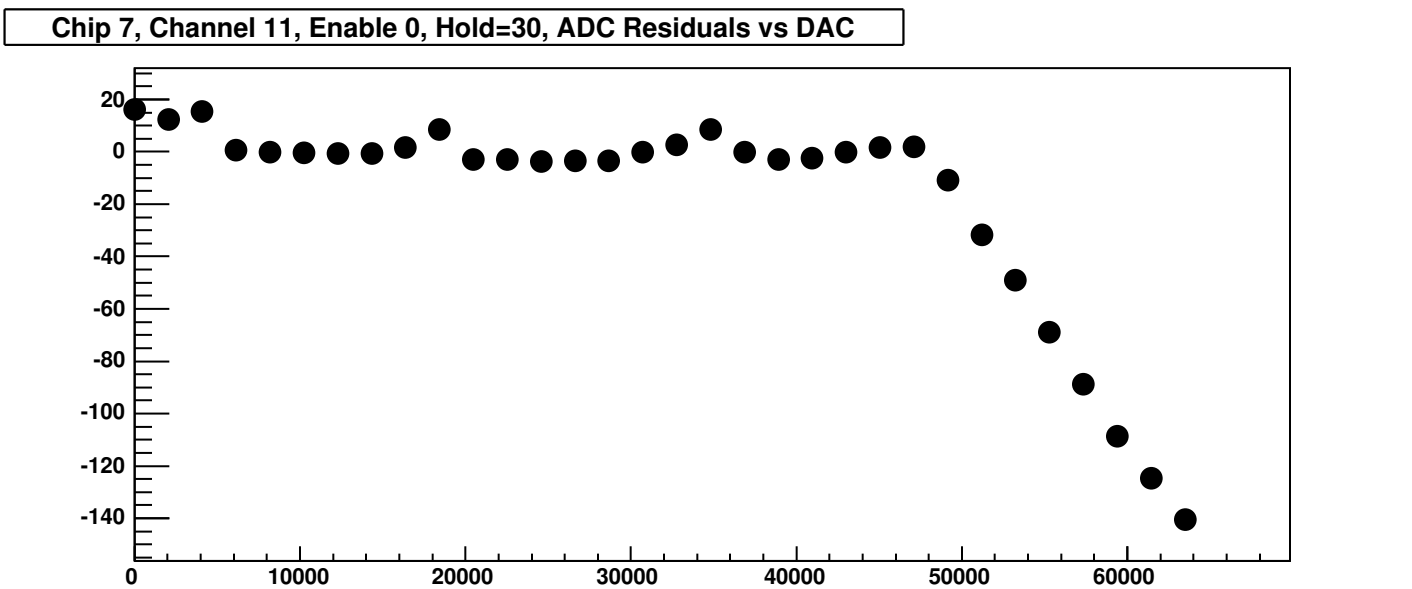

Chip 7, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.272e+04 / 15
p0 1124 ± 0.1017
p1 -0.001244 ± 4.17e-06
1100
1000
900
800
700
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

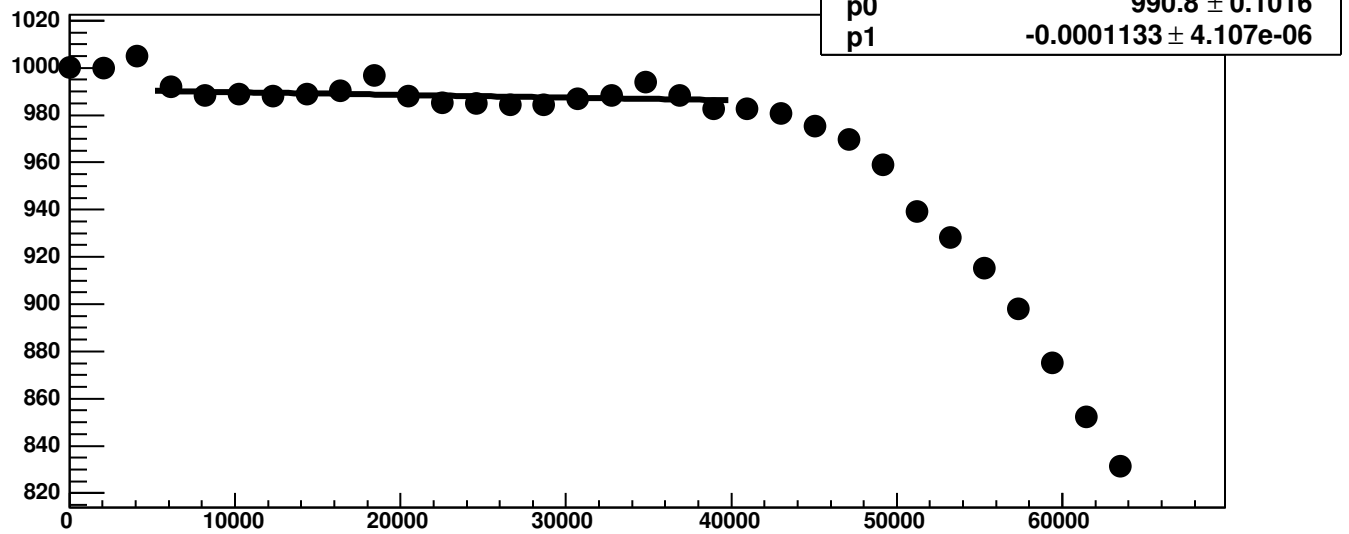

Chip 7, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6753 / 15
p0 984.8 ± 0.1052
p1 -4.198e-05 ± 4.312e-06
1000
980
960
940
920
900
880
860
840
0
10000
20000
30000
40000
50000
60000

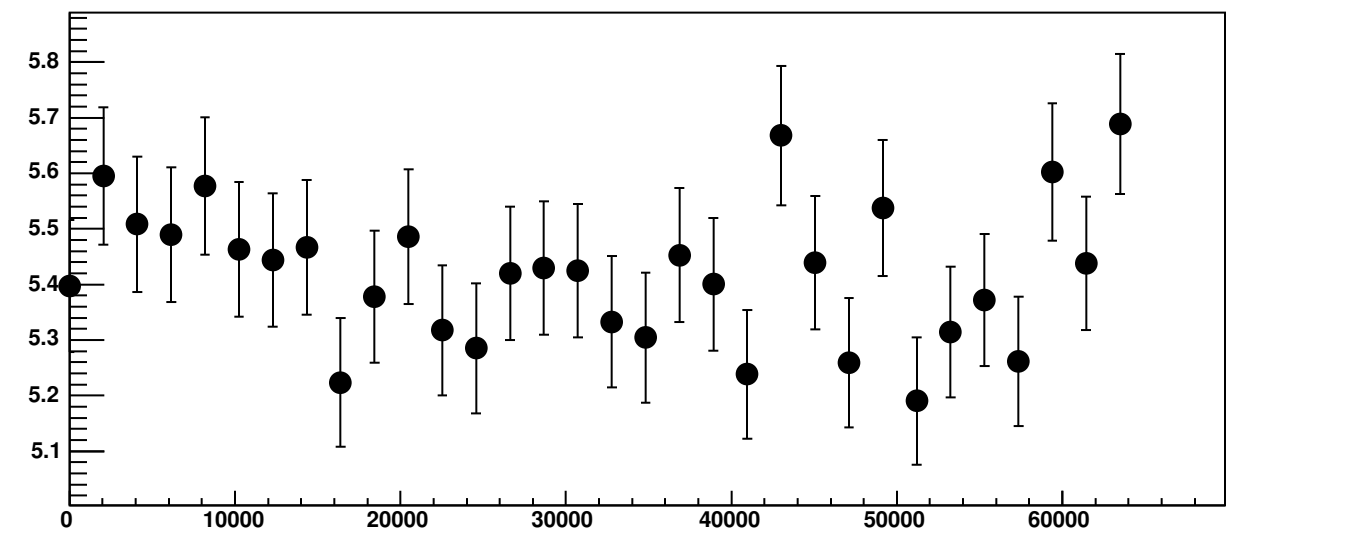

Chip 7, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

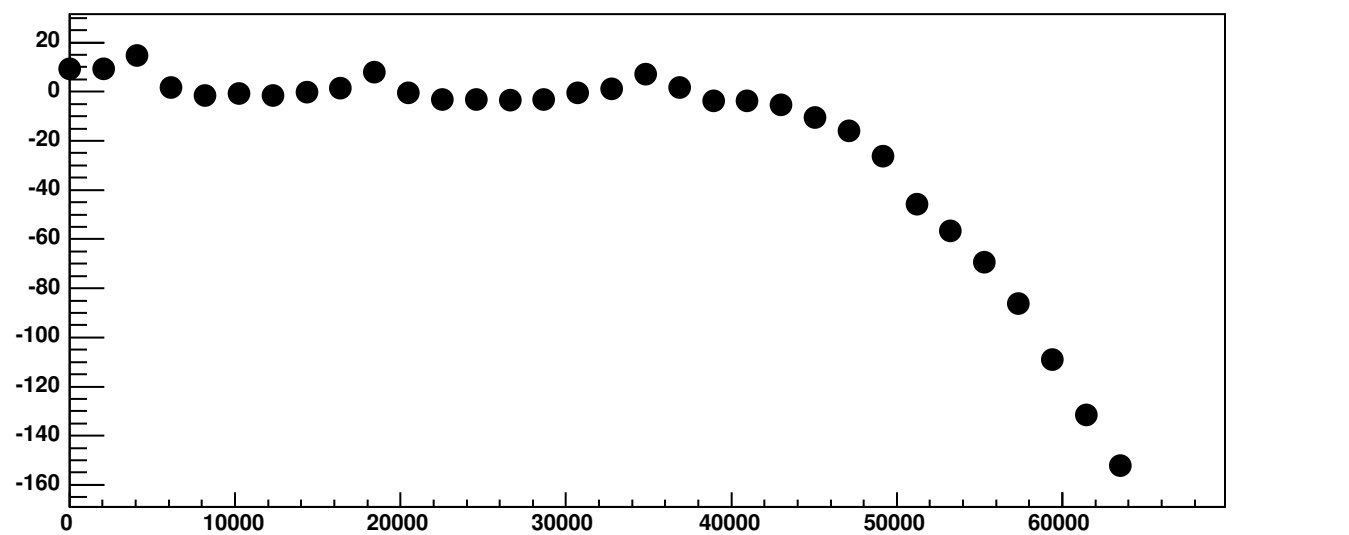
Chip 7, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

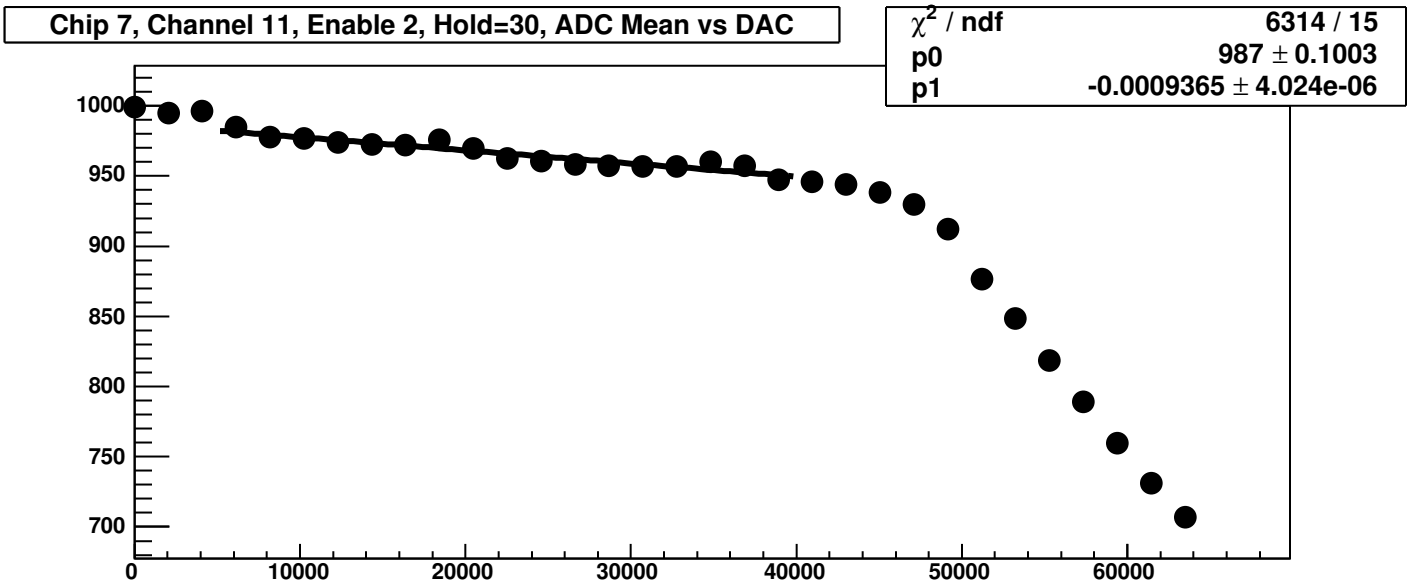


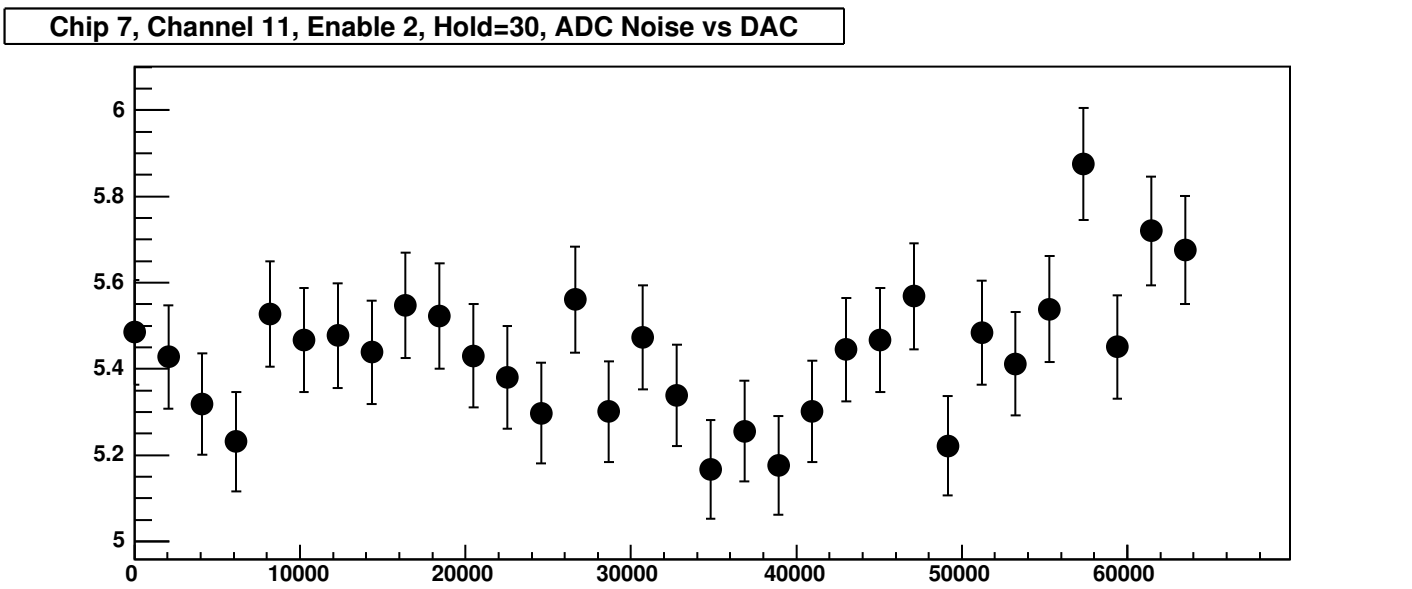
Chip 7, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC

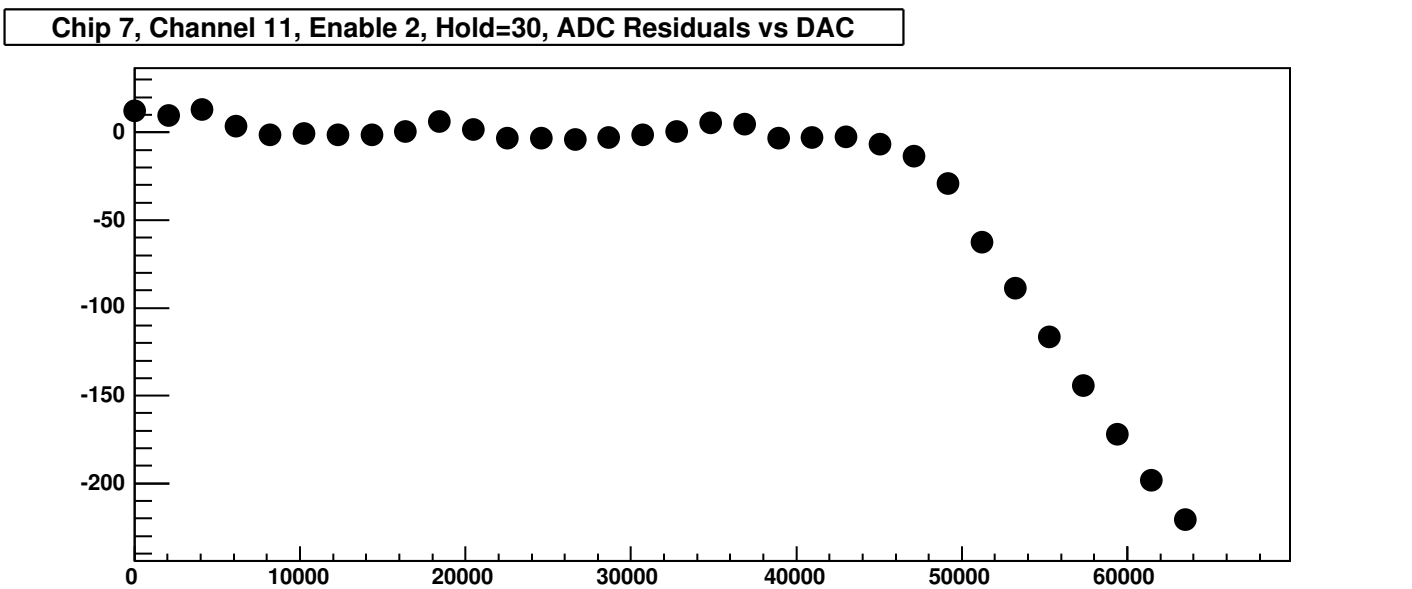


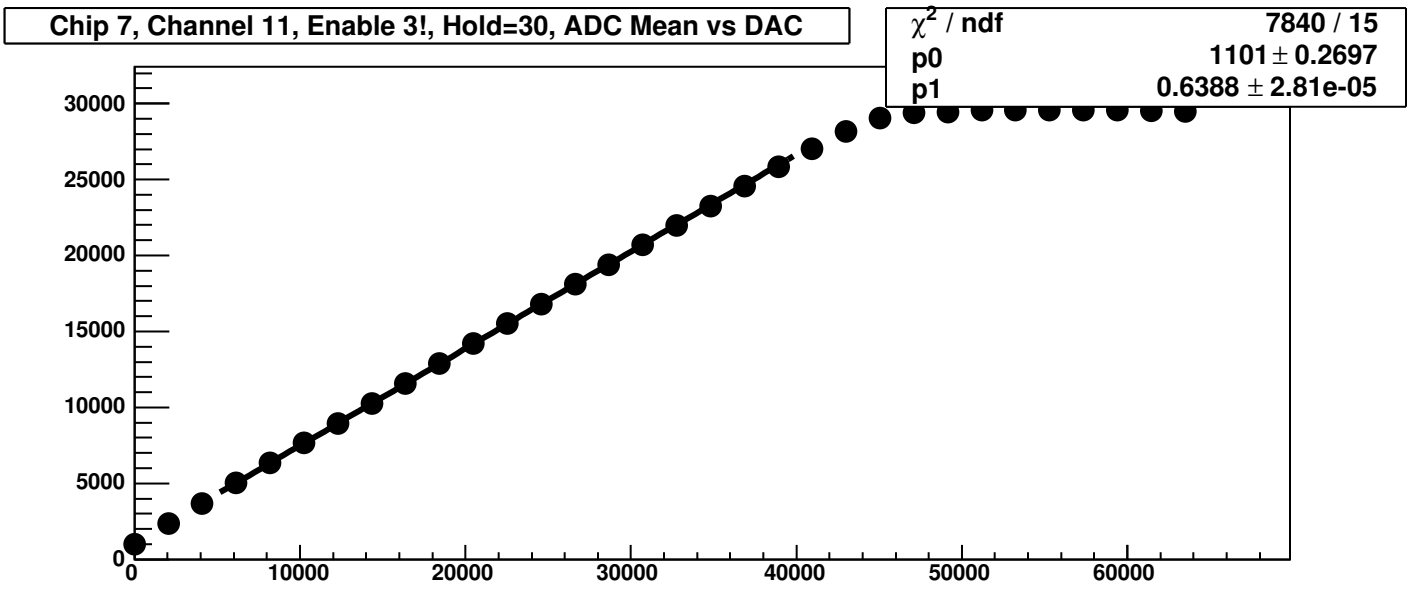
Chip 7, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

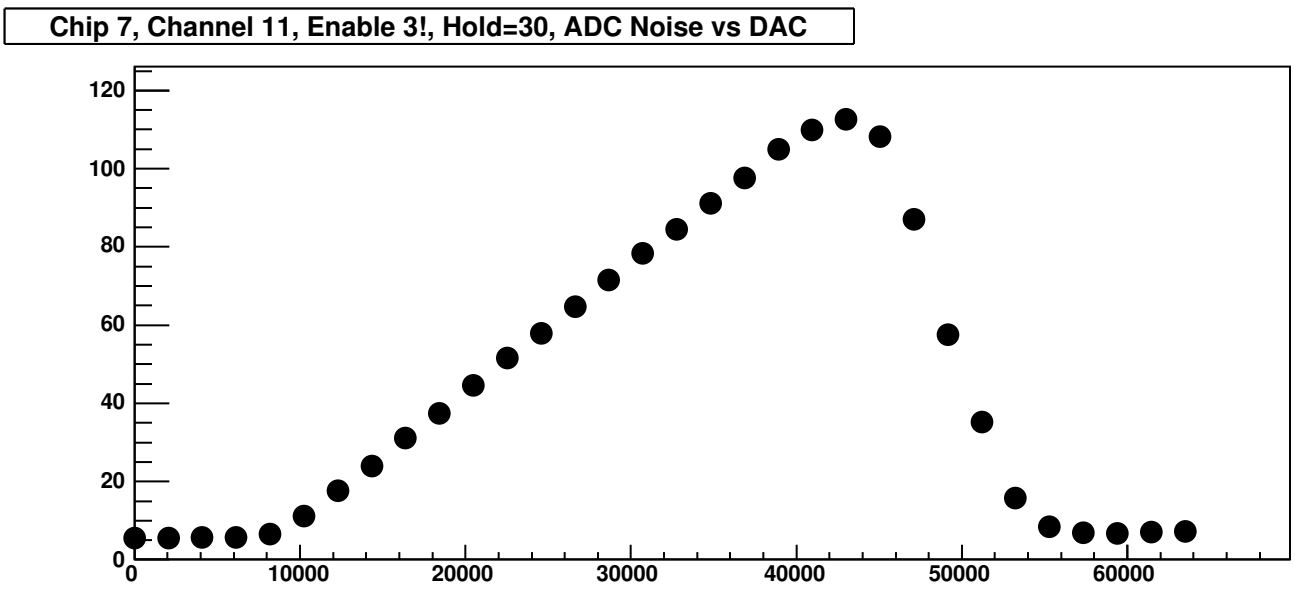


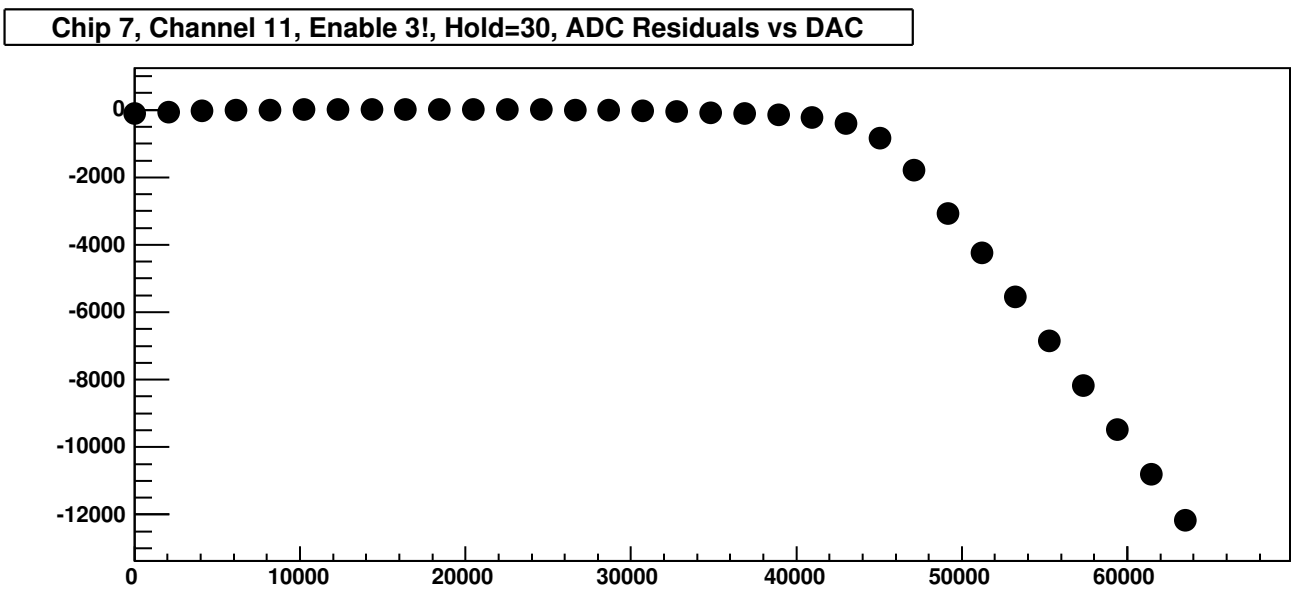

Chip 7, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6314 / 15
p0 987 ± 0.1003
p1 -0.0009365 ± 4.024e-06
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

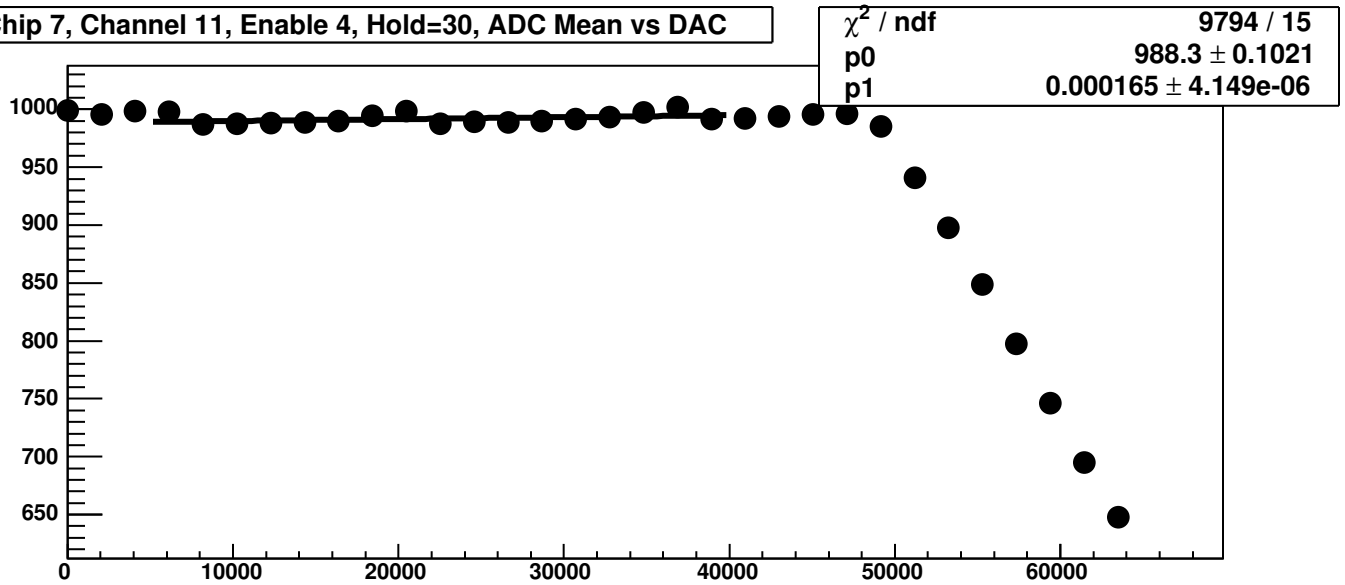

Chip 7, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 7840 / 15
p0 1101 ± 0.2697
p1 0.6388 ± 2.81e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

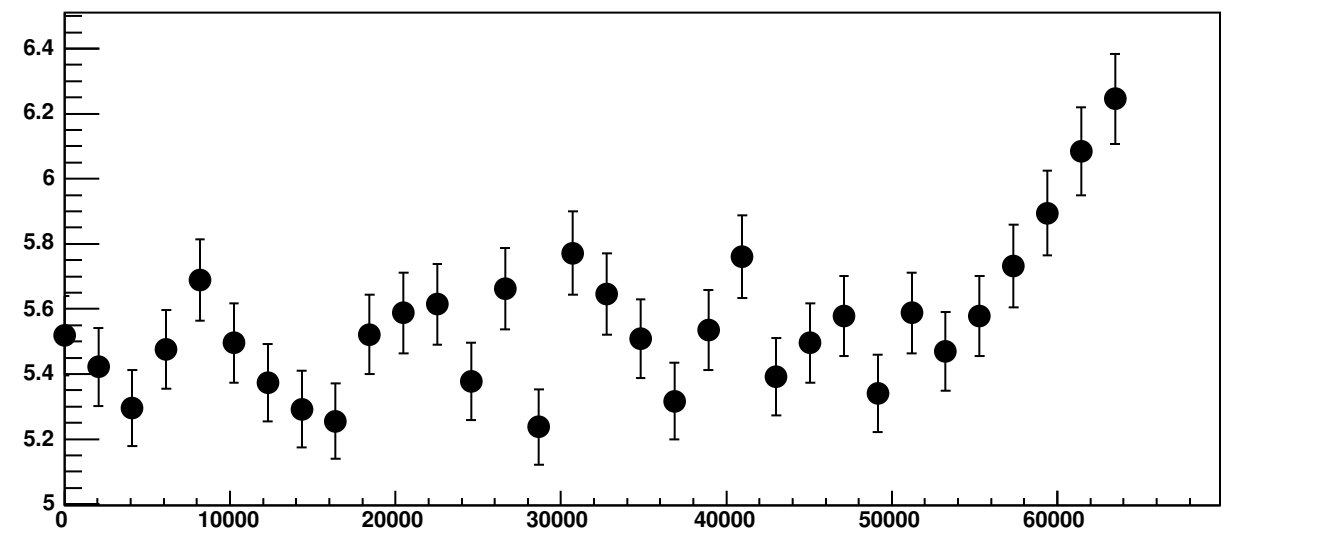

Chip 7, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

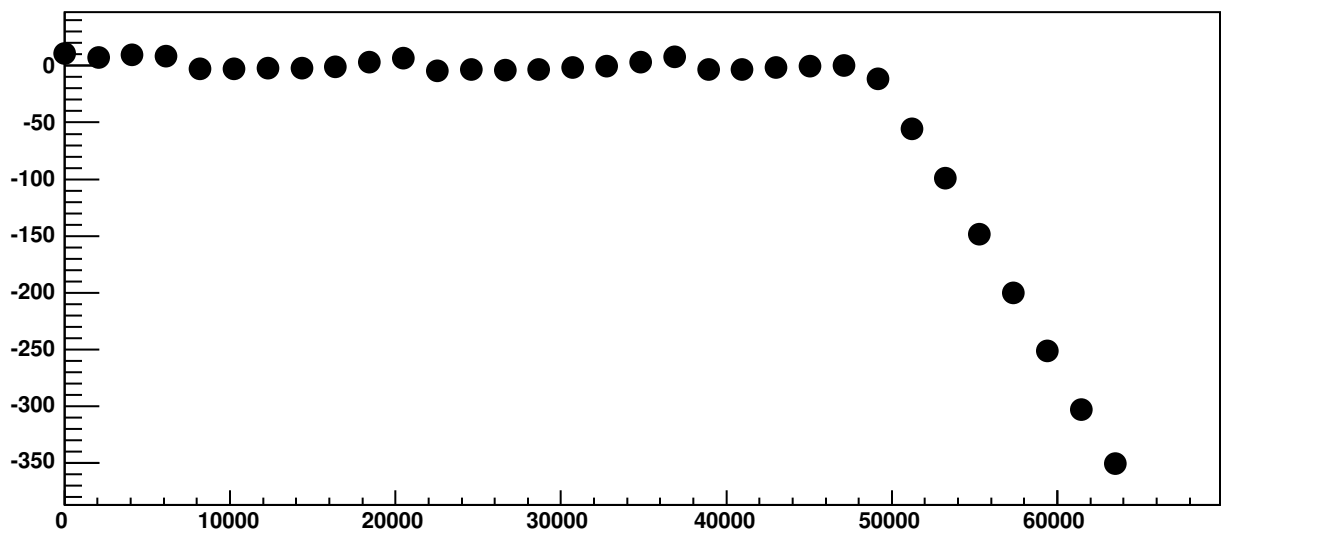
Chip 7, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

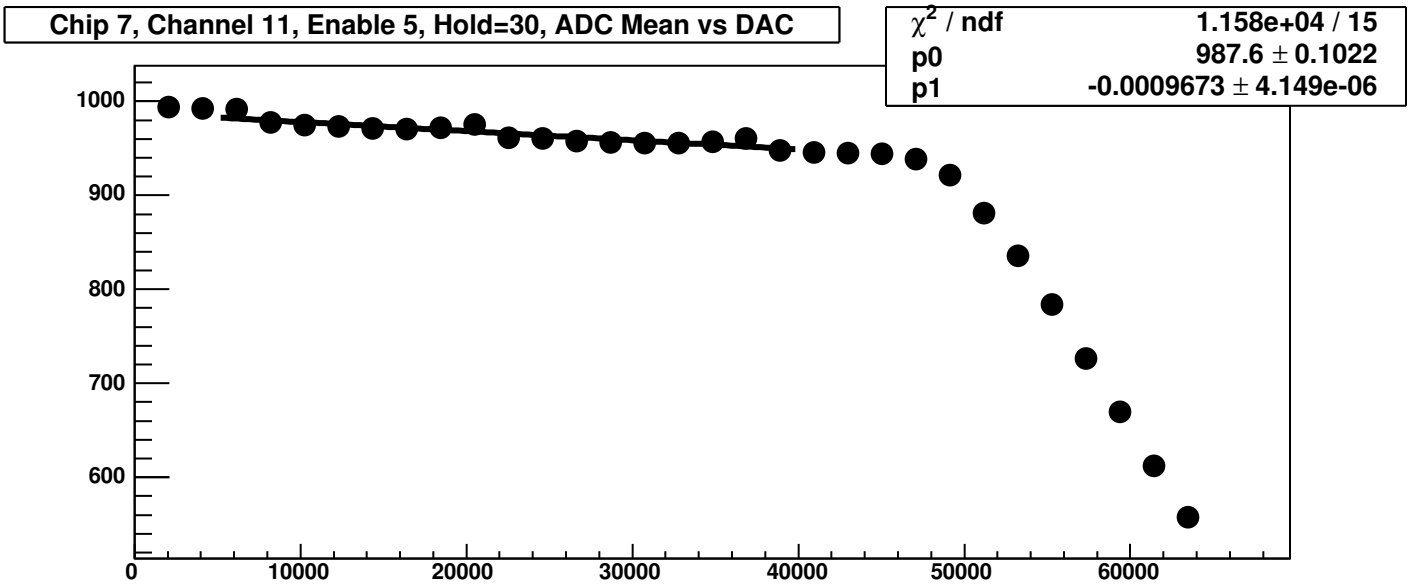


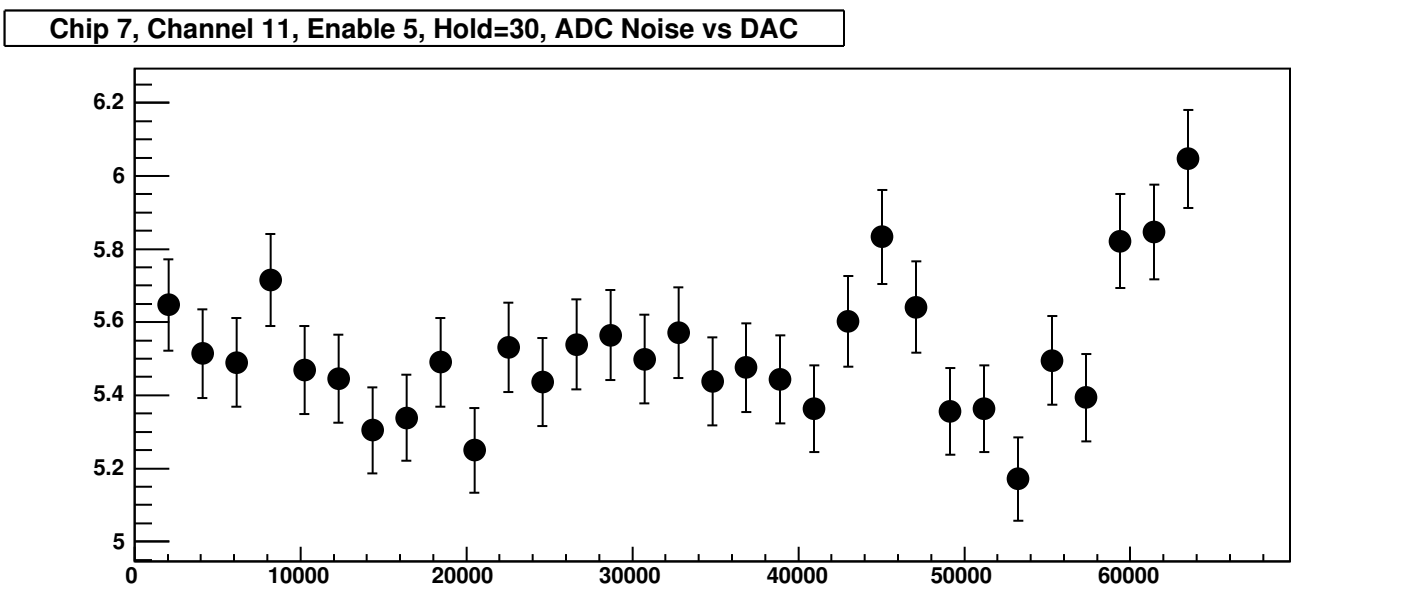
Chip 7, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC

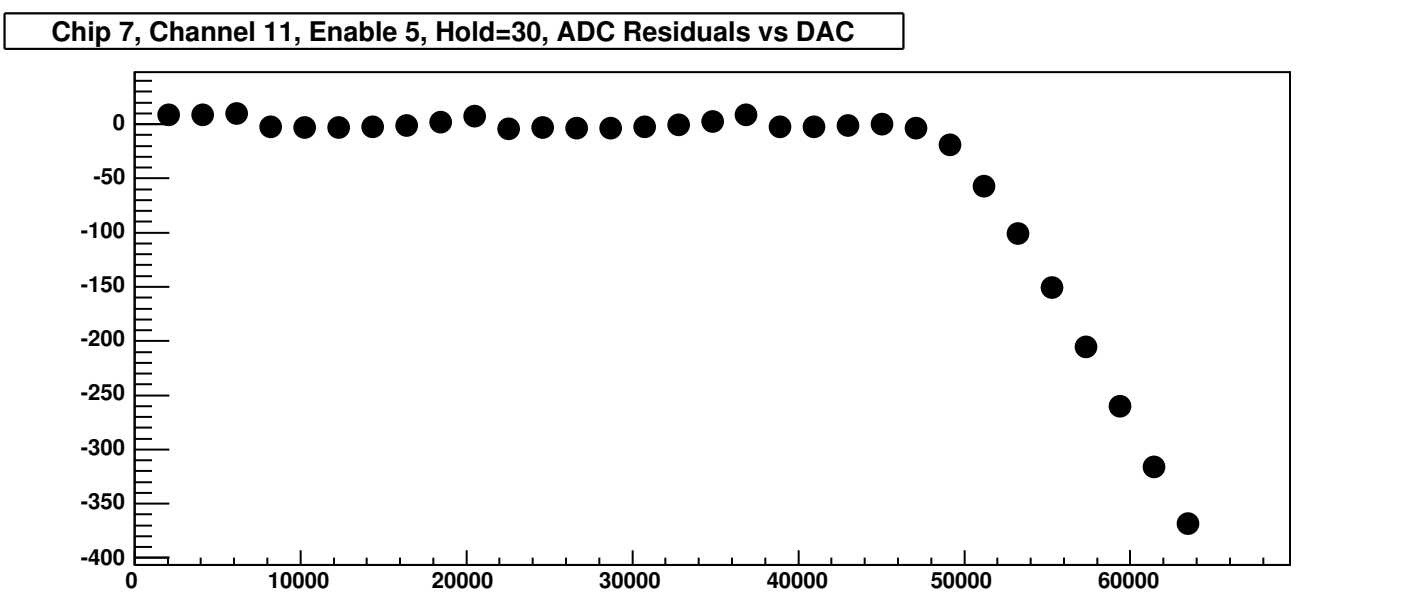


Chip 7, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC

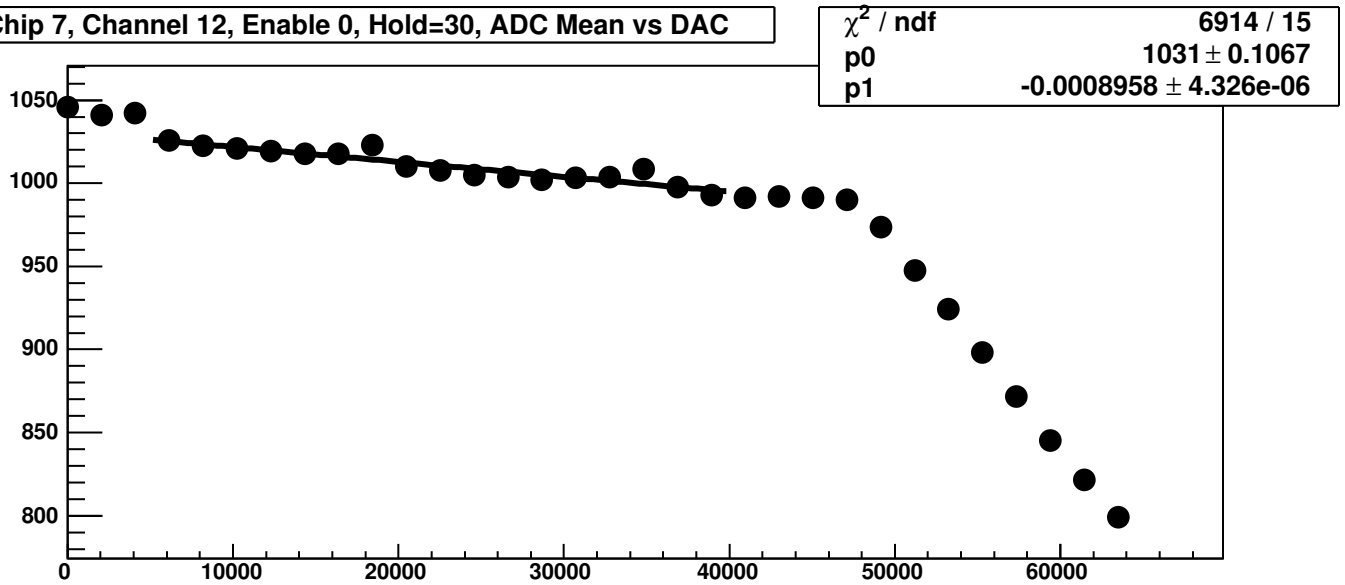



Chip 7, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.158e+04 / 15
p0 987.6 ± 0.1022
p1 -0.0009673 ± 4.149e-06
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000

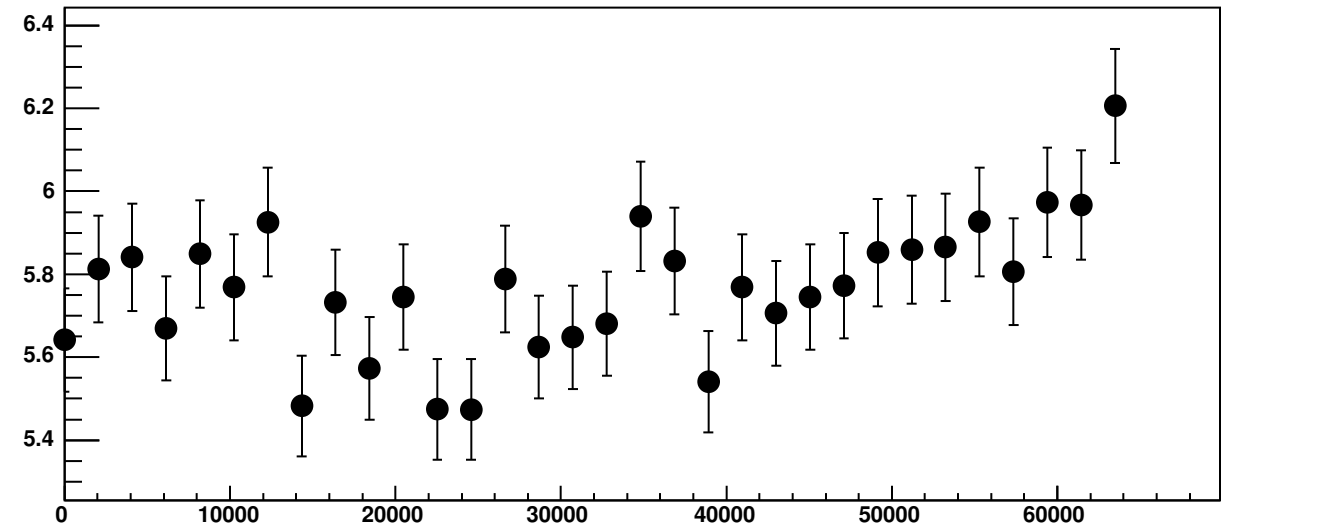

Chip 7, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

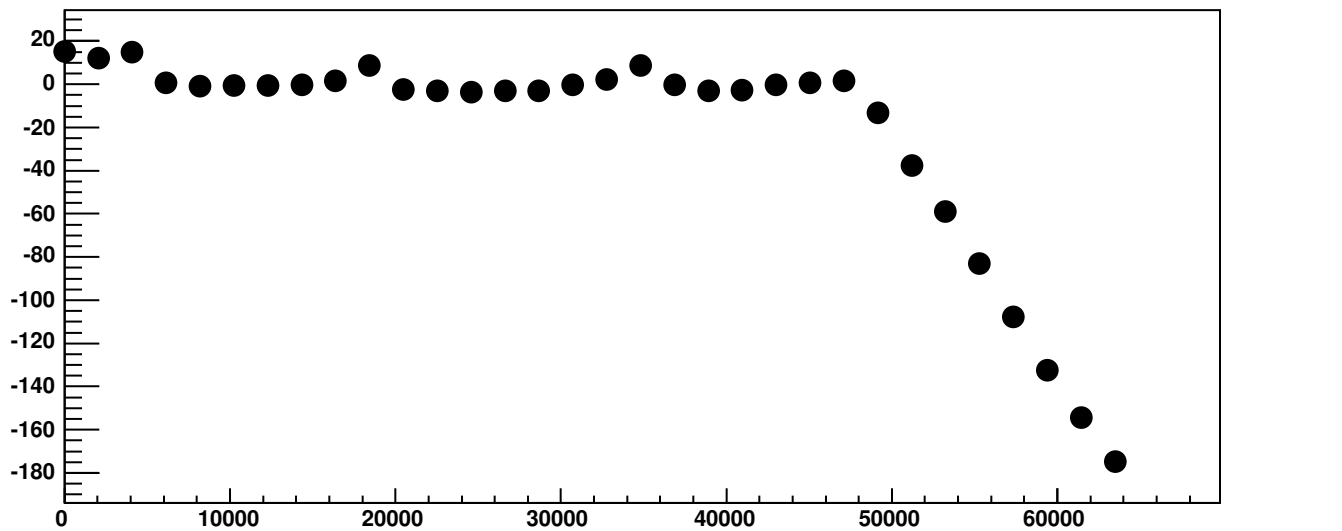
Chip 7, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC



Chip 7, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC

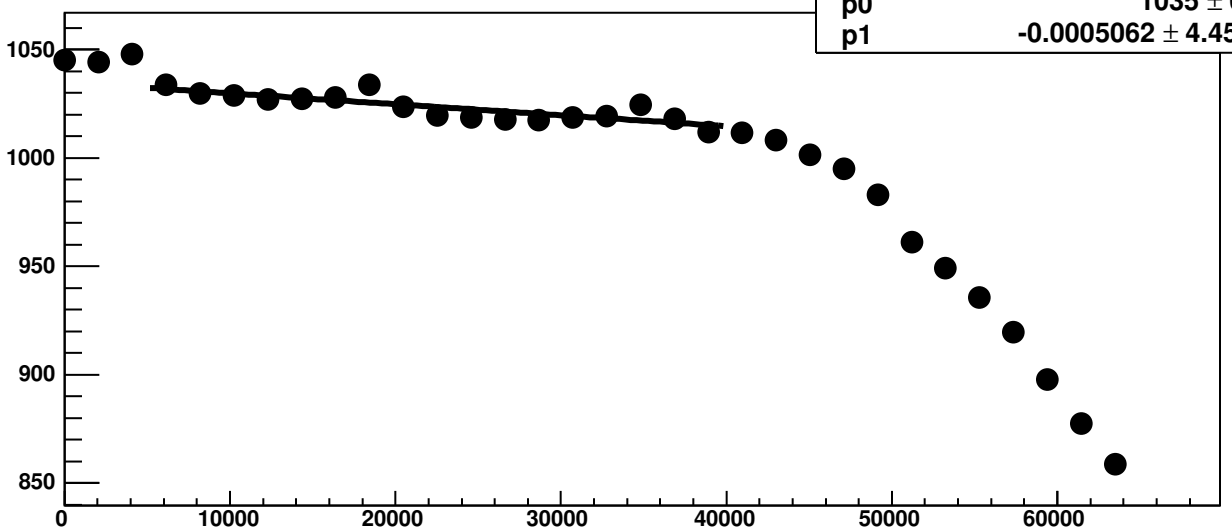


Chip 7, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

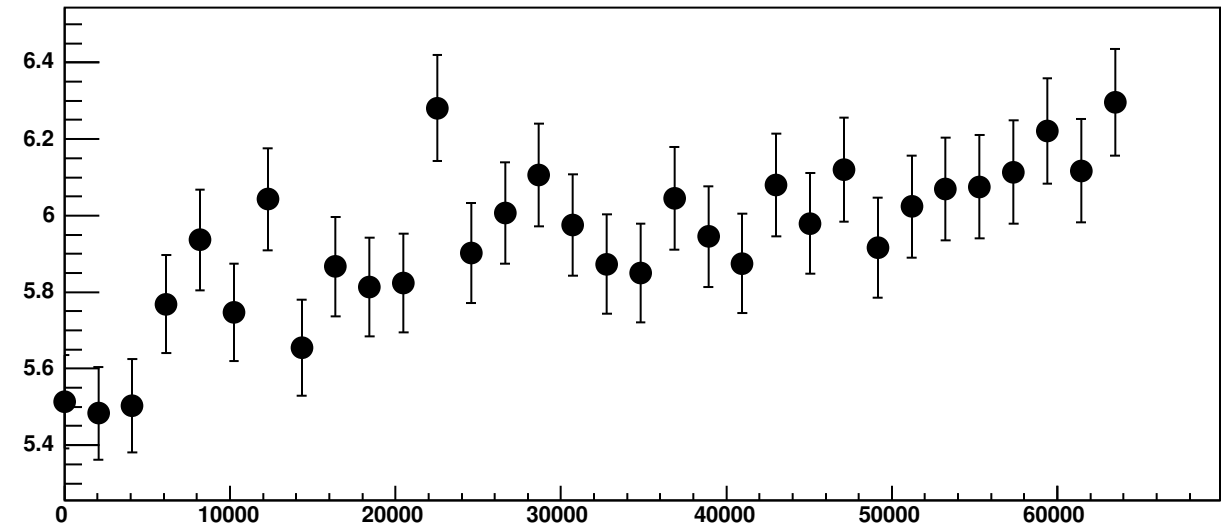


Chip 7, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC

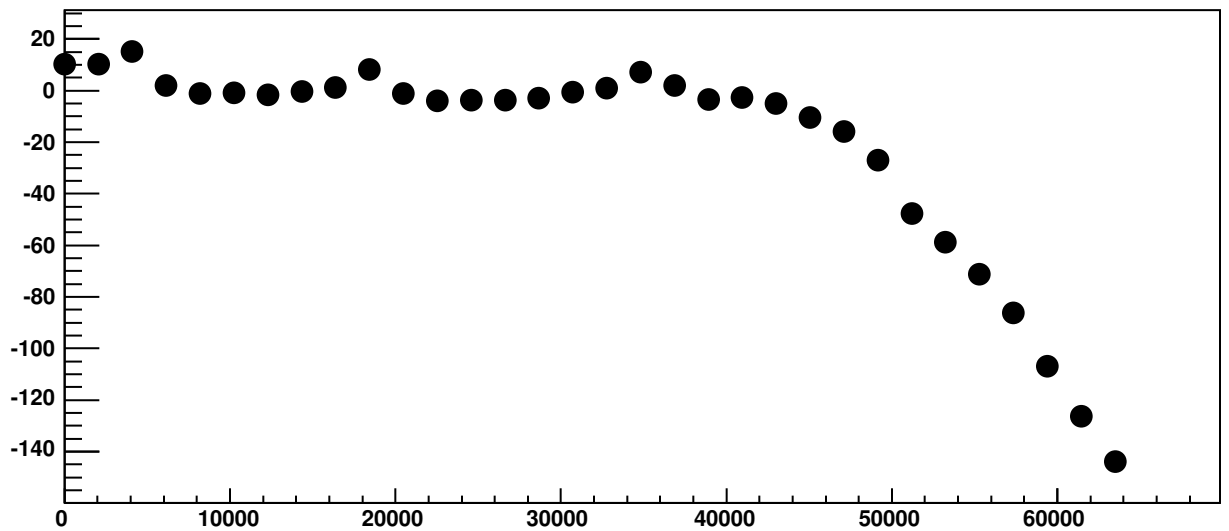
χ^2 / ndf	5841 / 15
p0	1035 ± 0.1091
p1	-0.0005062 ± 4.451e-06



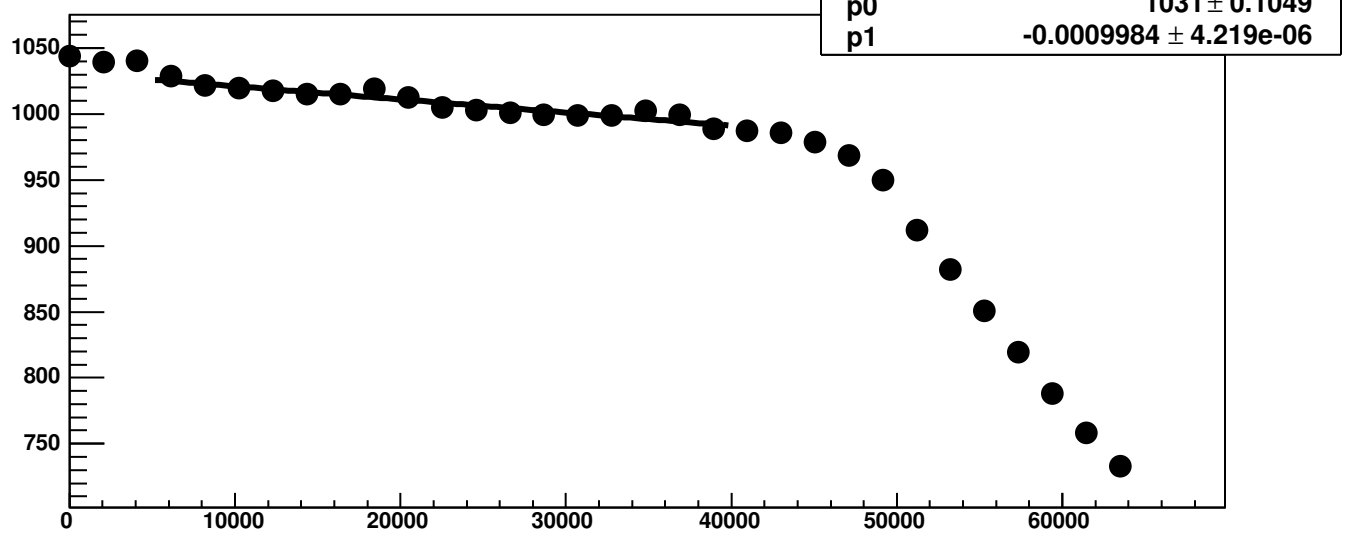
Chip 7, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC



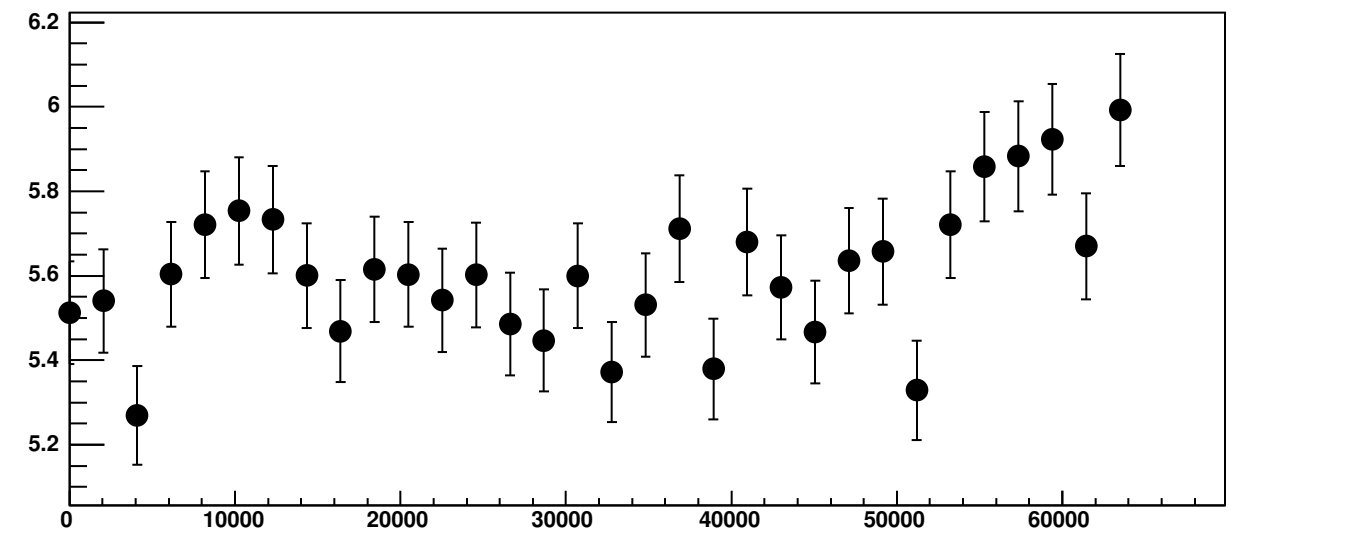
Chip 7, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC



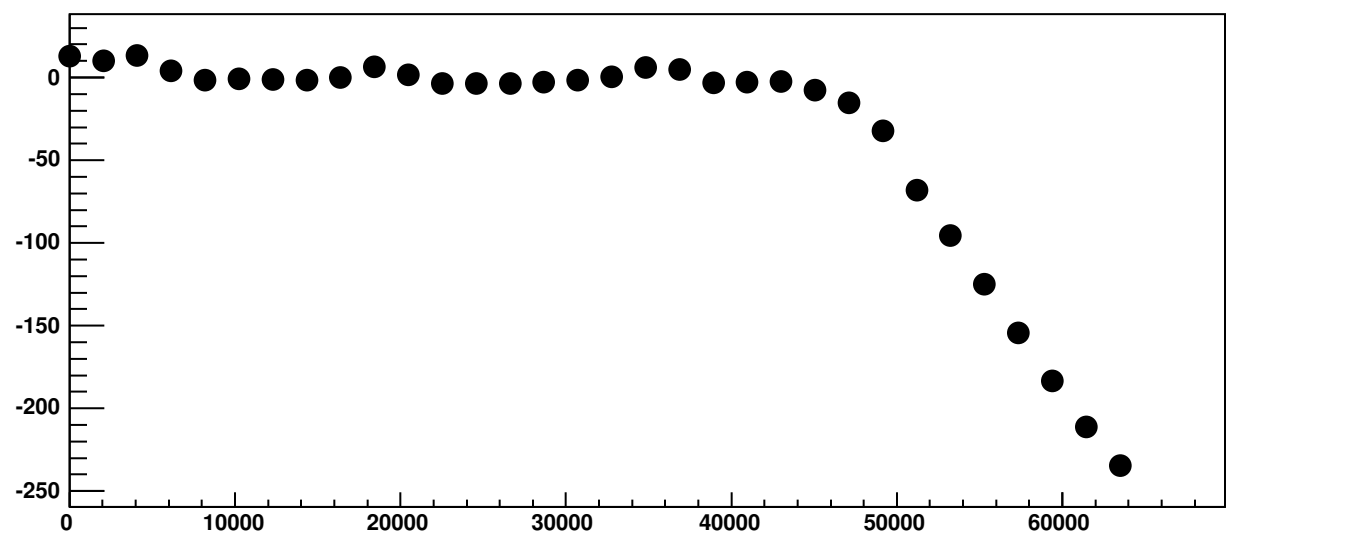
Chip 7, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC

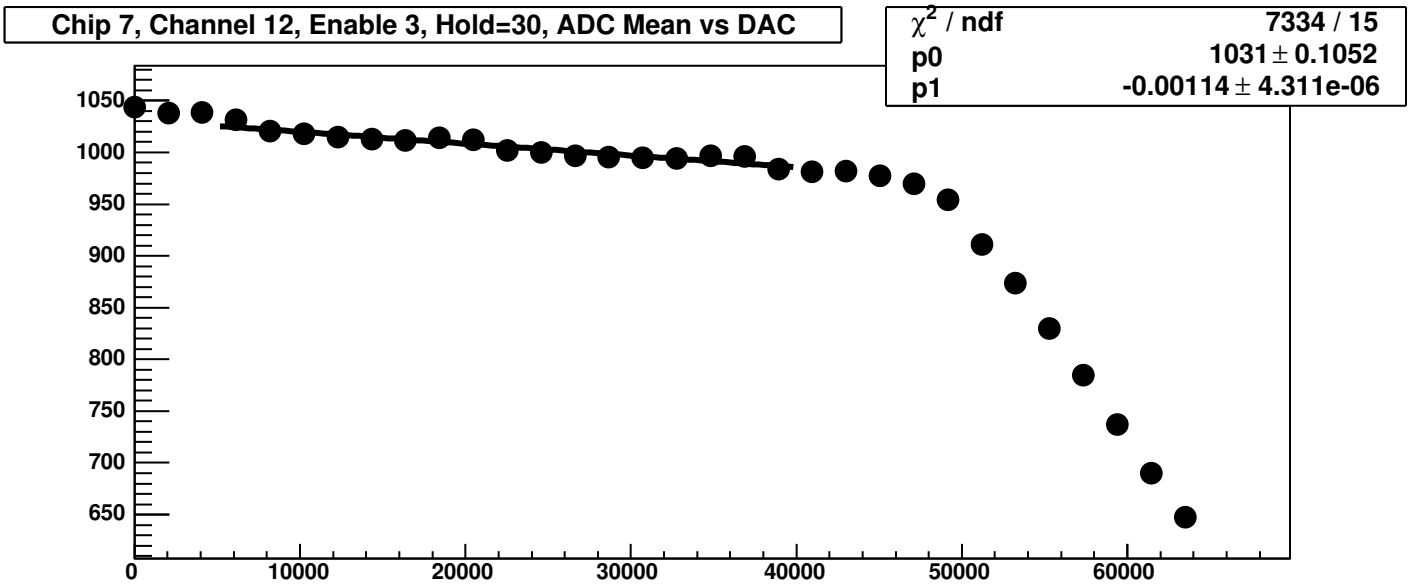


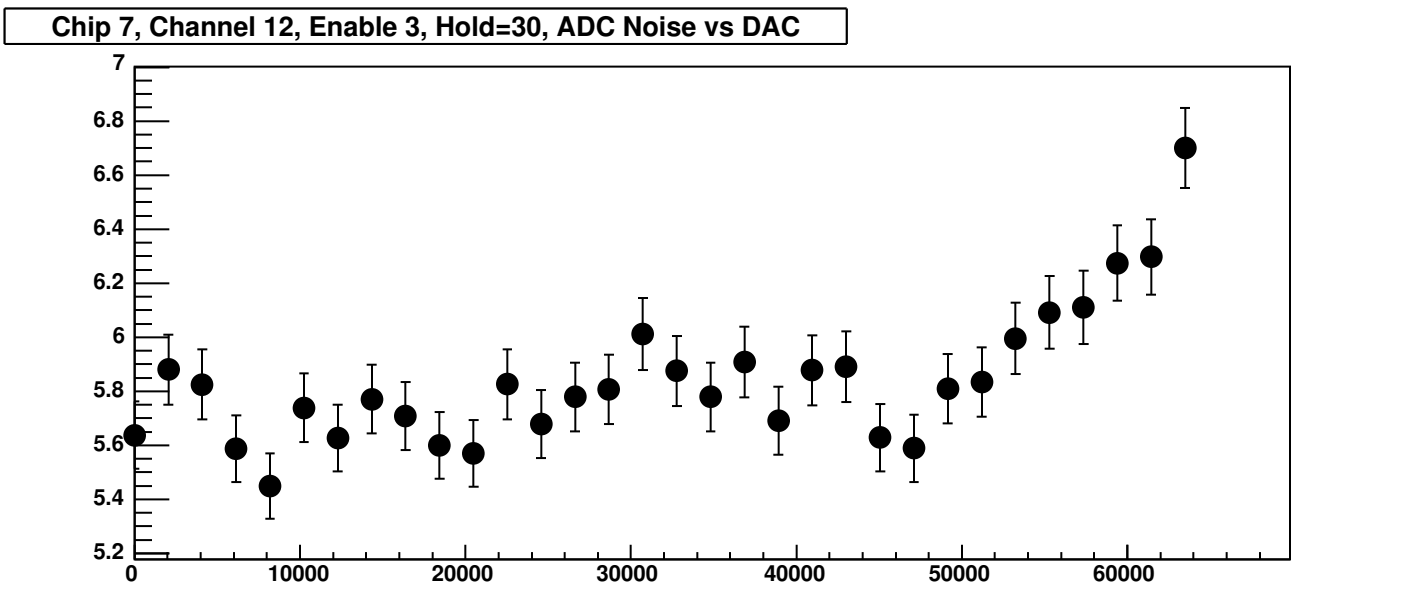
Chip 7, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC

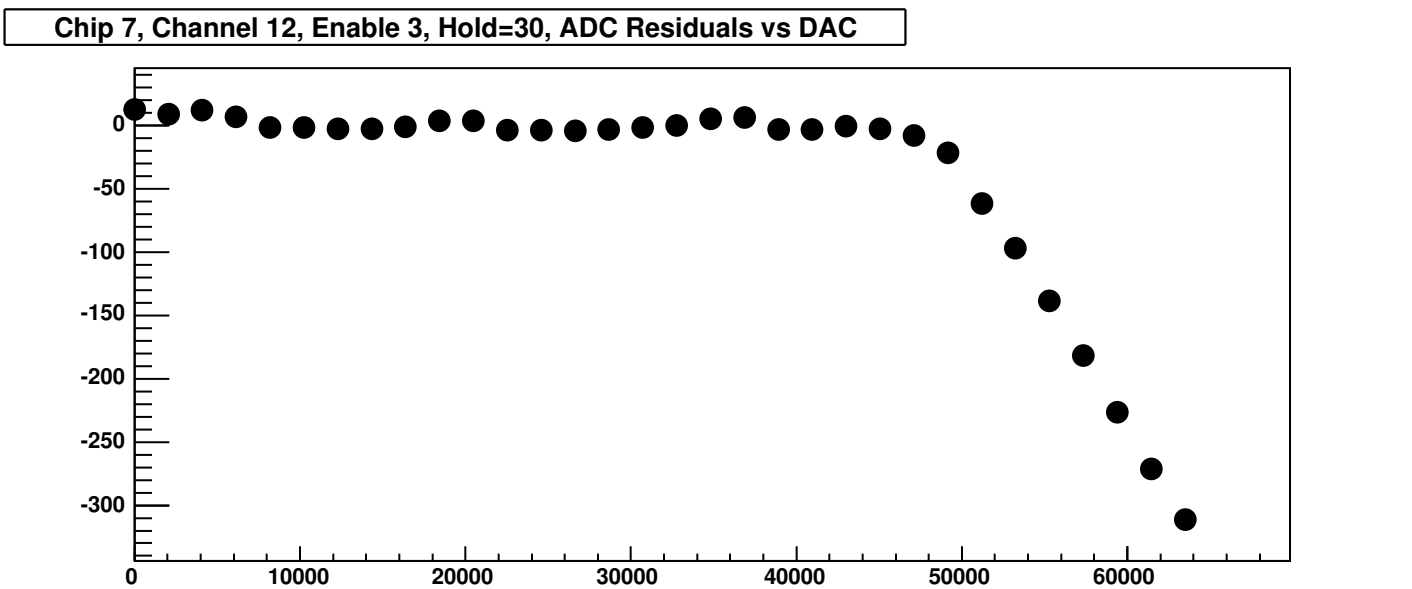


Chip 7, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC

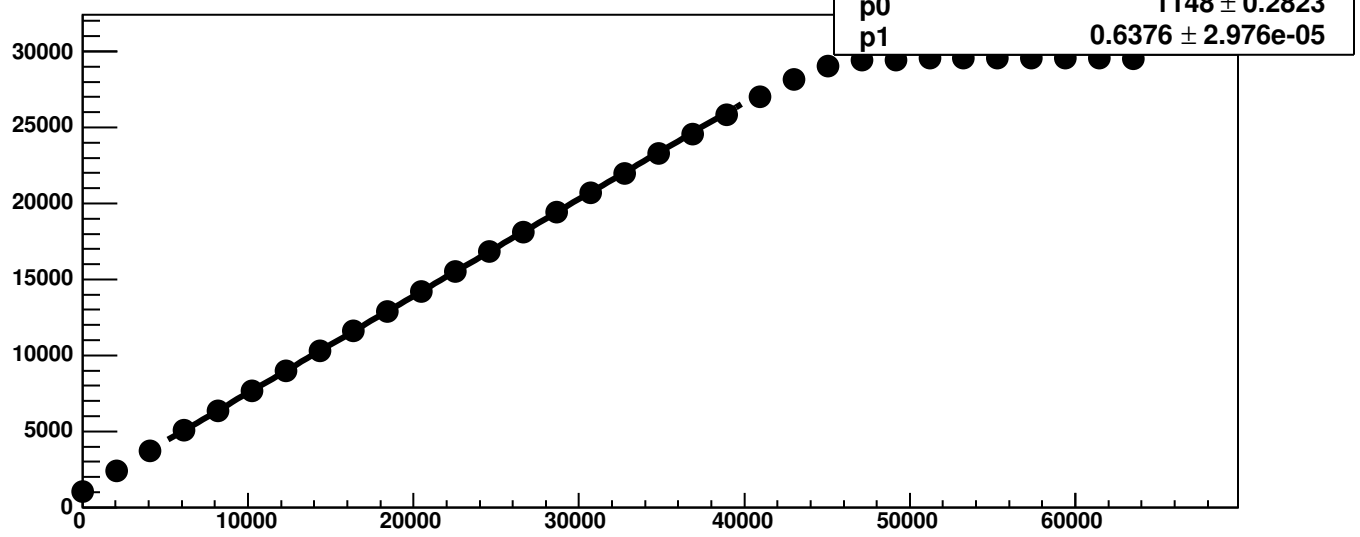



Chip 7, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7334 / 15
p0 1031 ± 0.1052
p1 -0.00114 ± 4.311e-06
1050
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

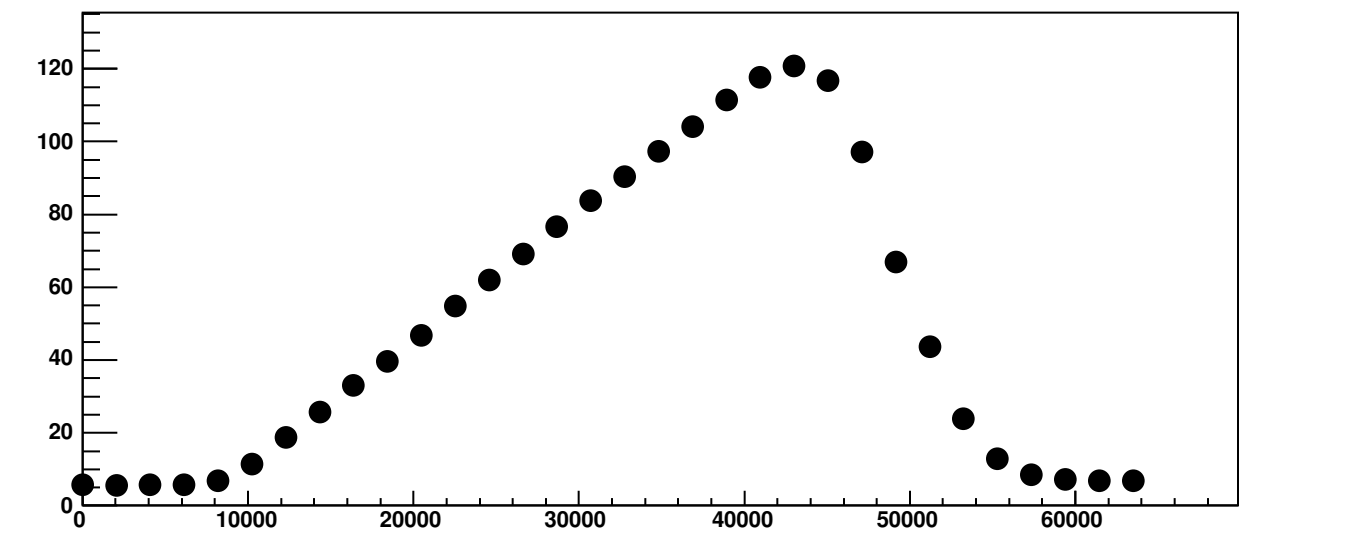

Chip 7, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

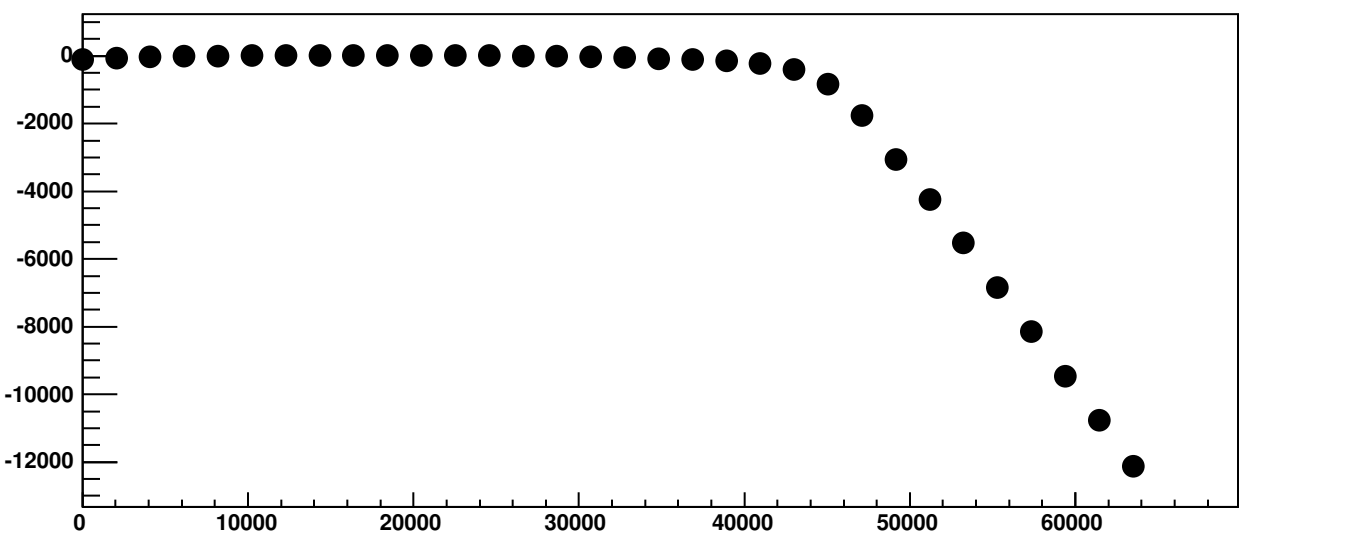
Chip 7, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC

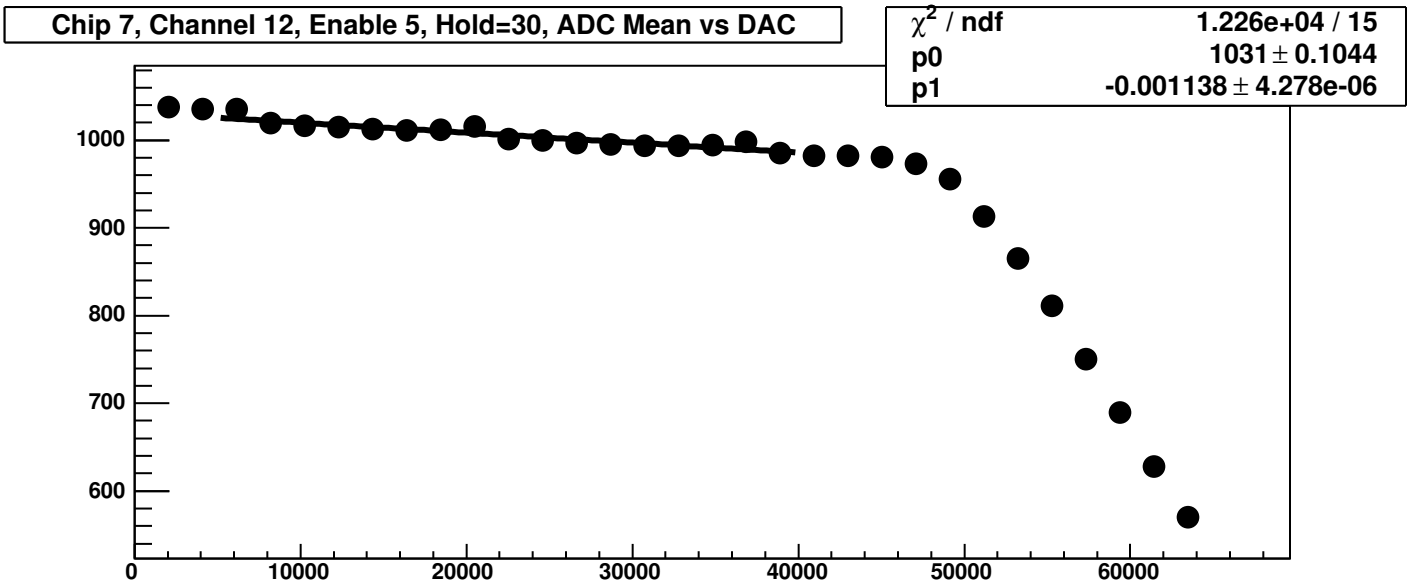


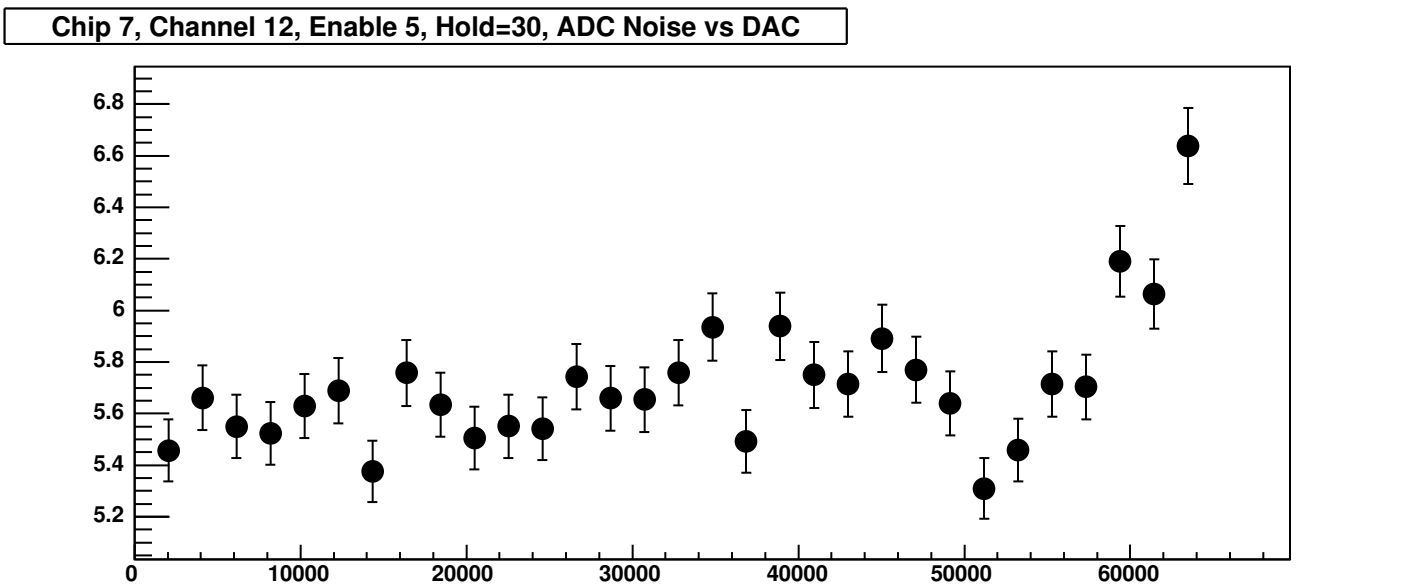
Chip 7, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC

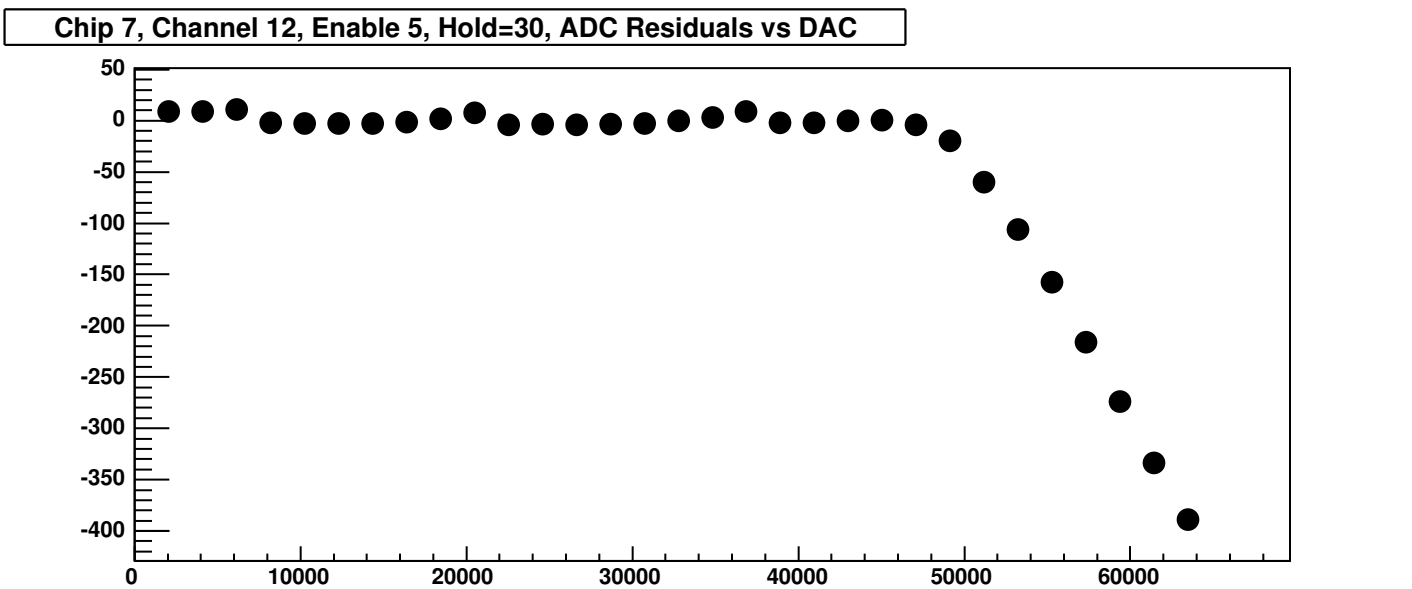


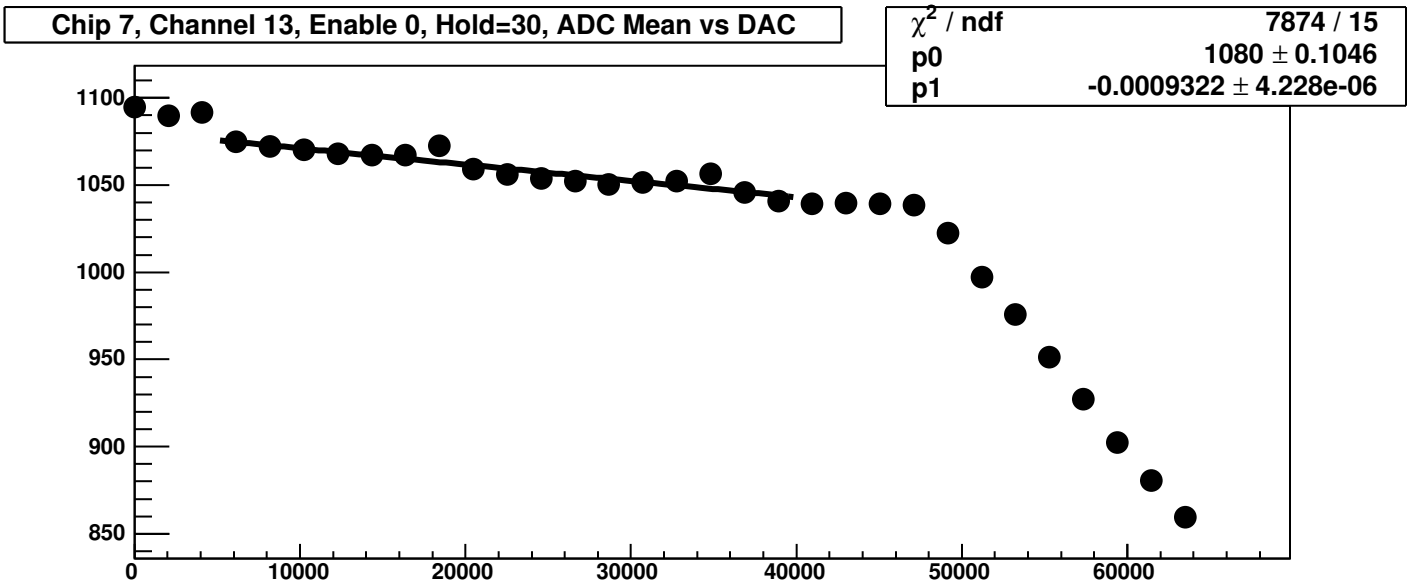
Chip 7, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC

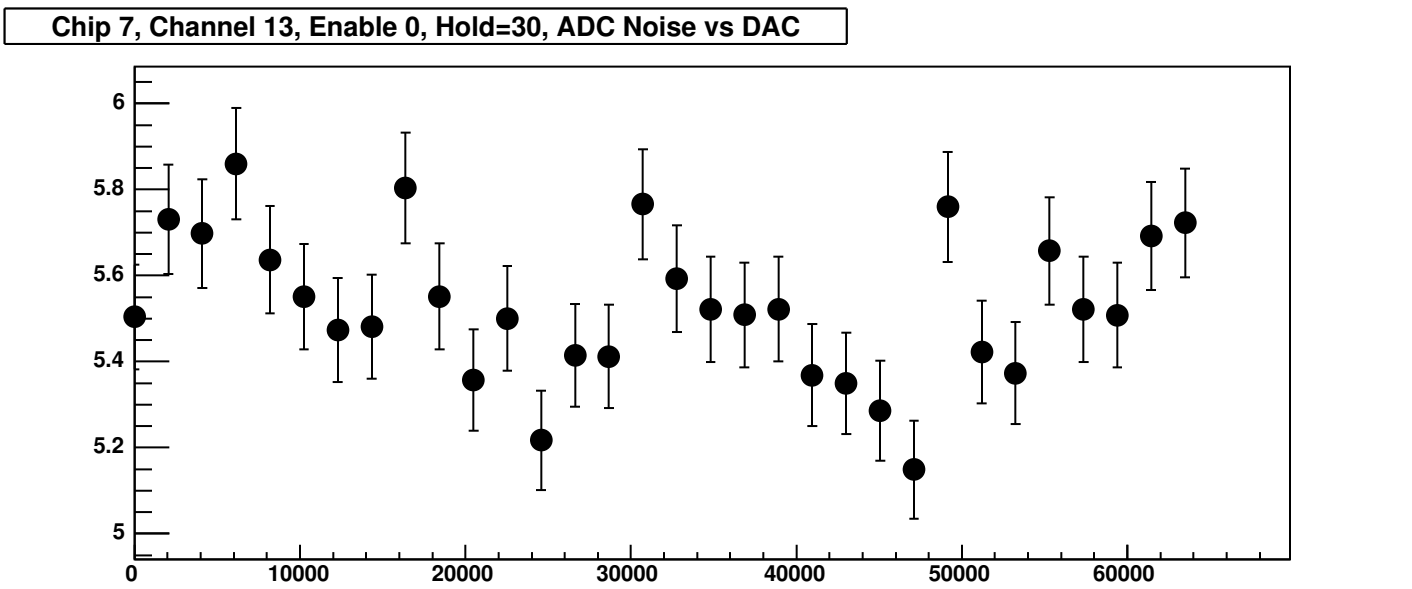


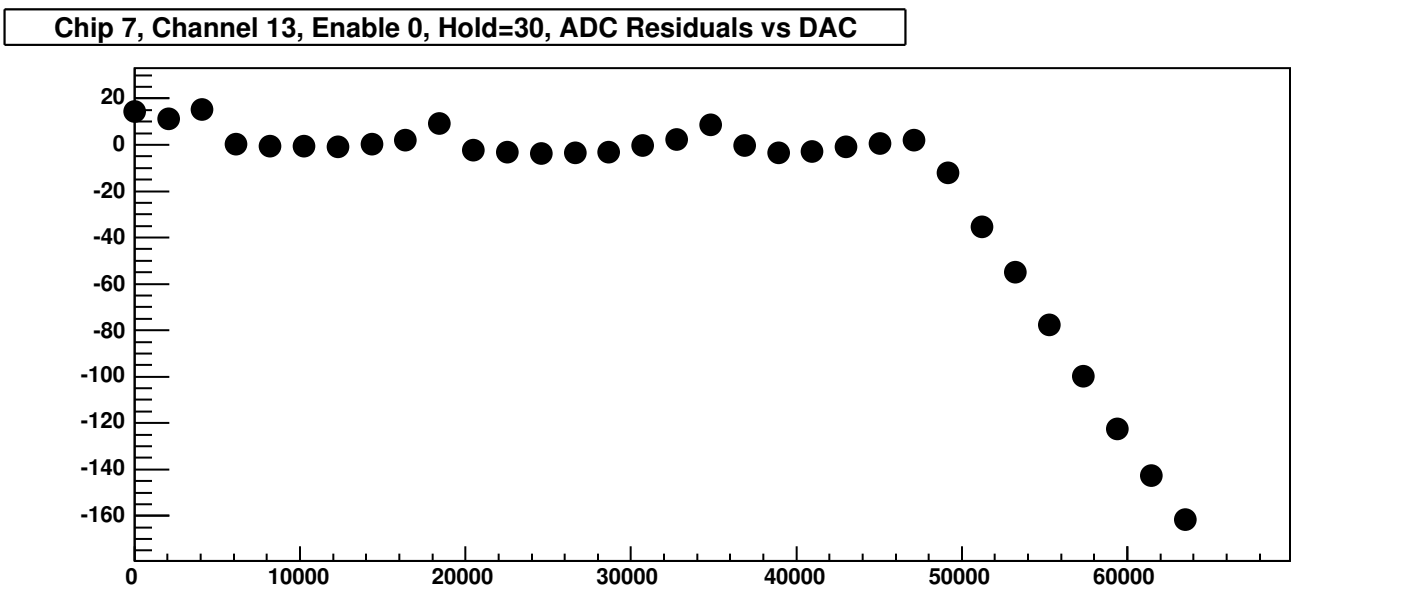

Chip 7, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.226e+04 / 15
p0 1031 ± 0.1044
p1 -0.001138 ± 4.278e-06
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000

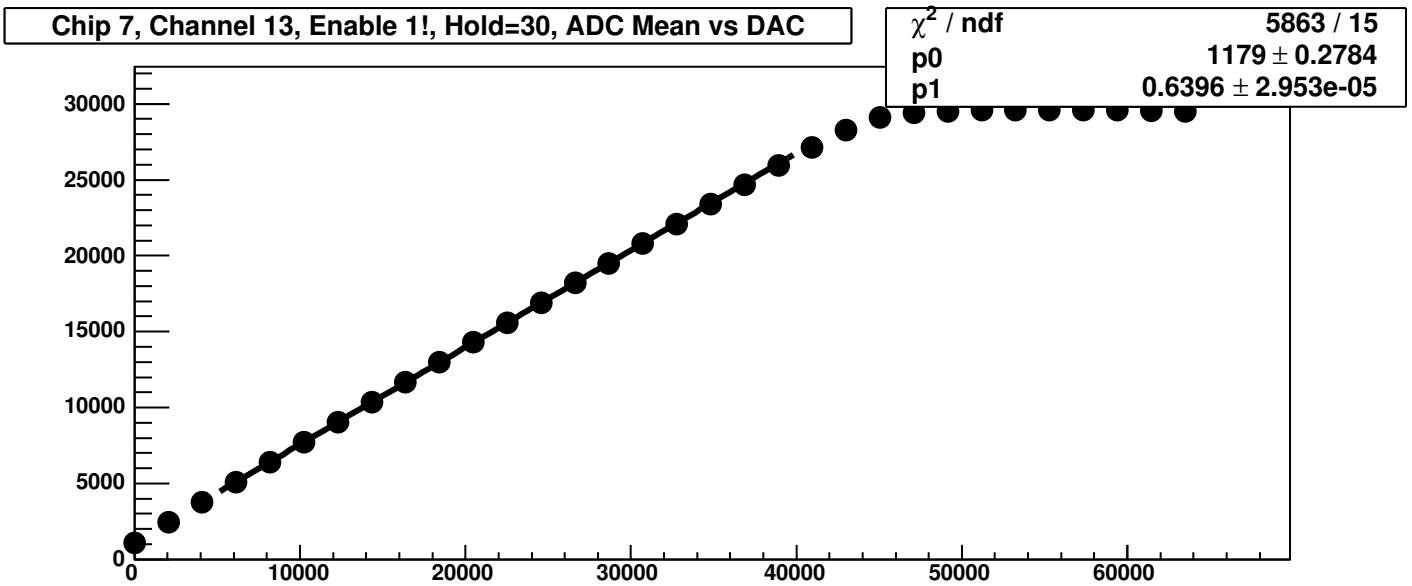

Chip 7, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

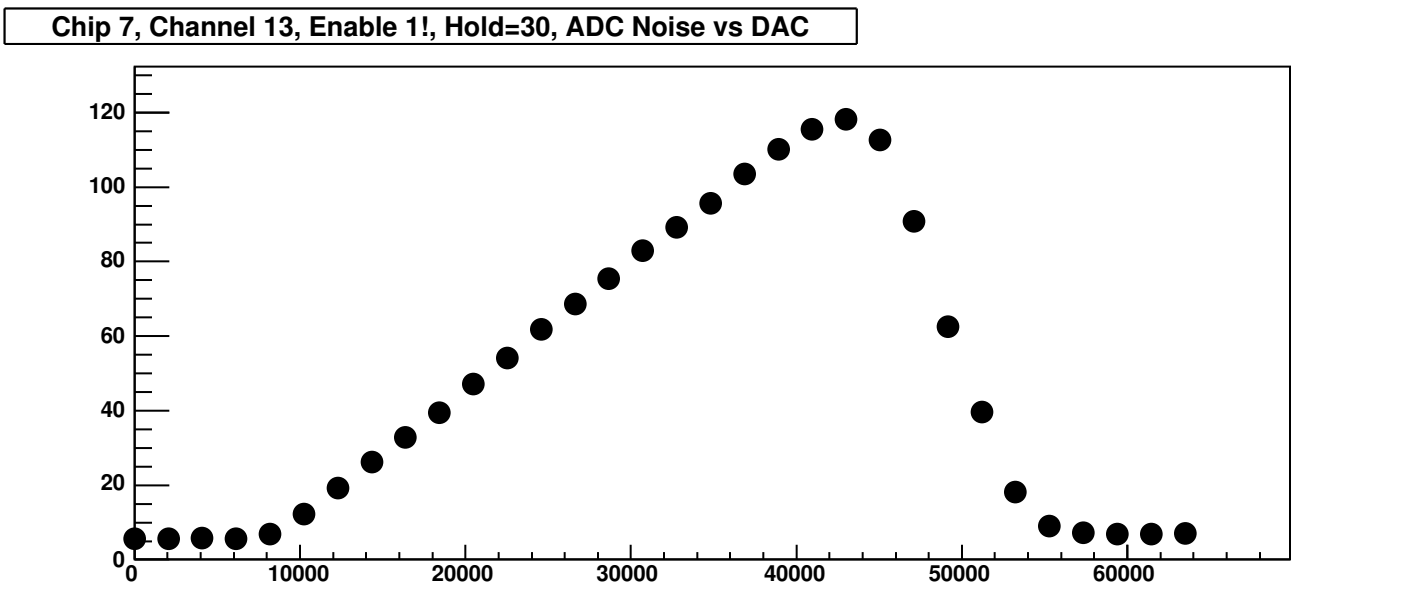

Chip 7, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

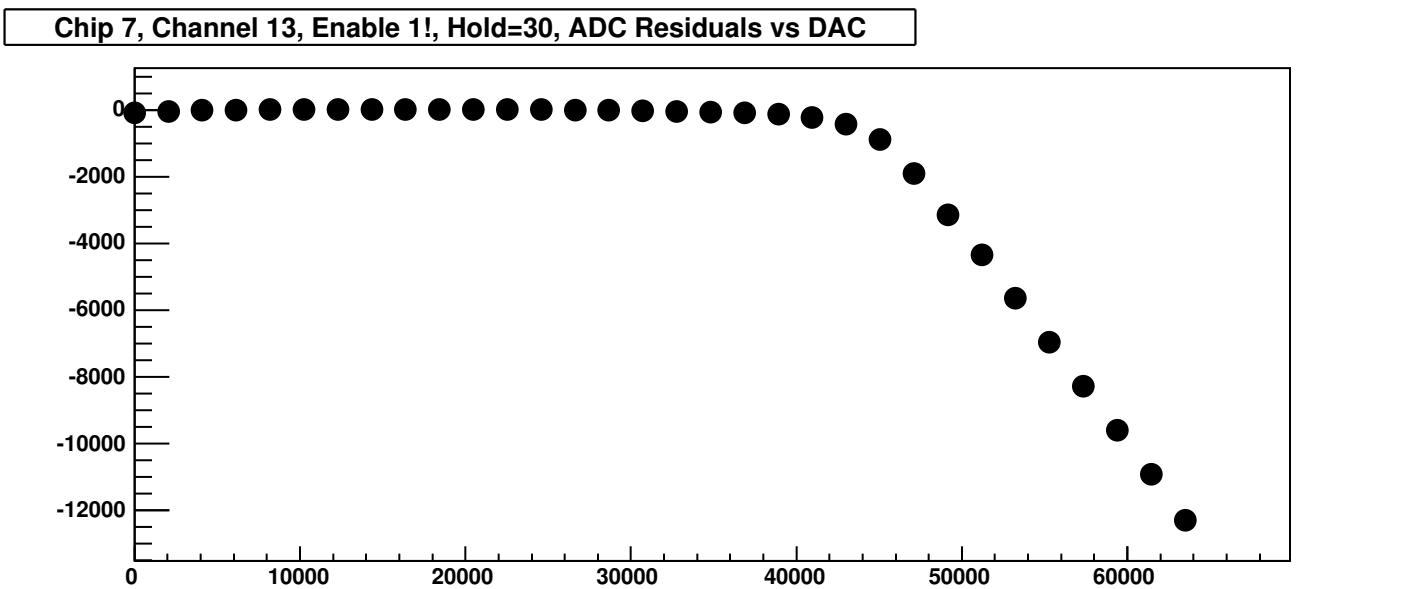

Chip 7, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7874 / 15
p0 1080 ± 0.1046
p1 -0.0009322 ± 4.228e-06
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

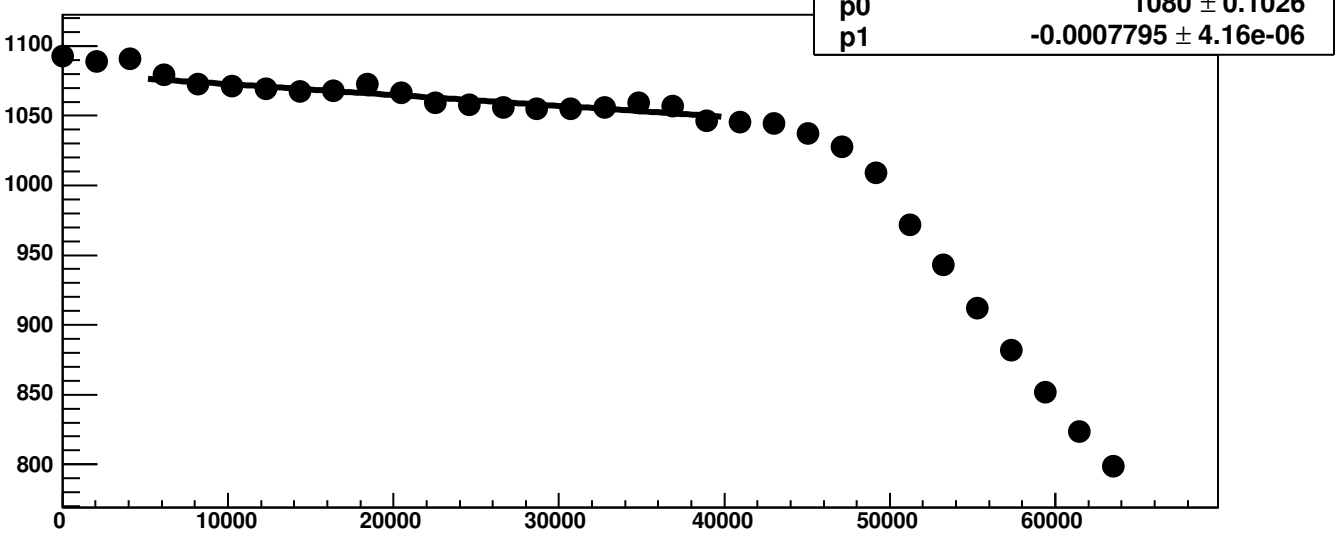

Chip 7, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 5863 / 15
p0 1179 ± 0.2784
p1 0.6396 ± 2.953e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

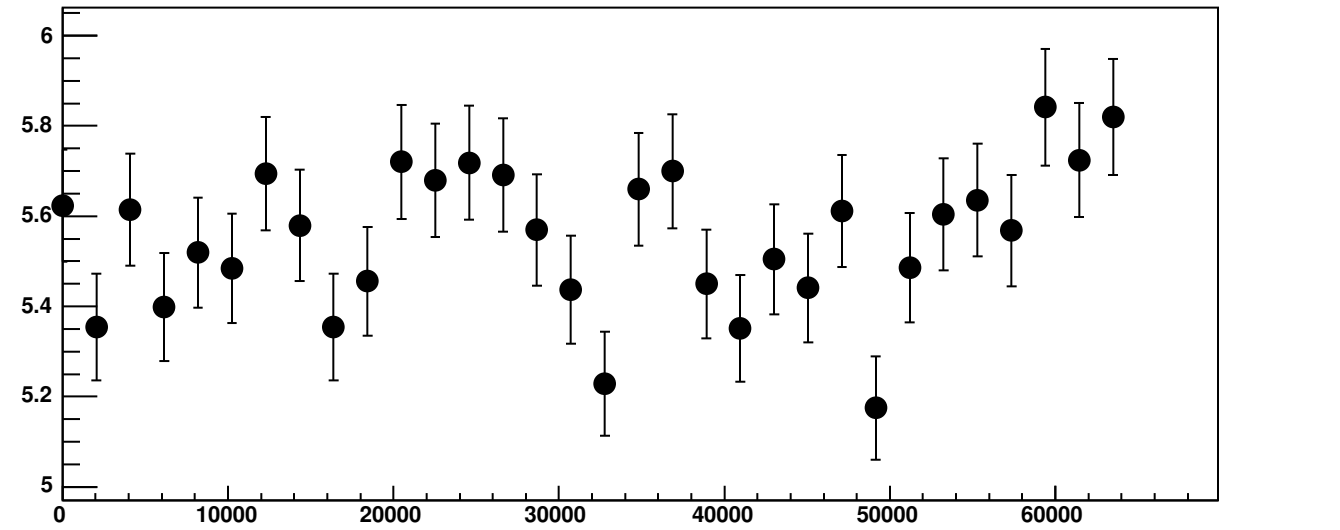

Chip 7, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

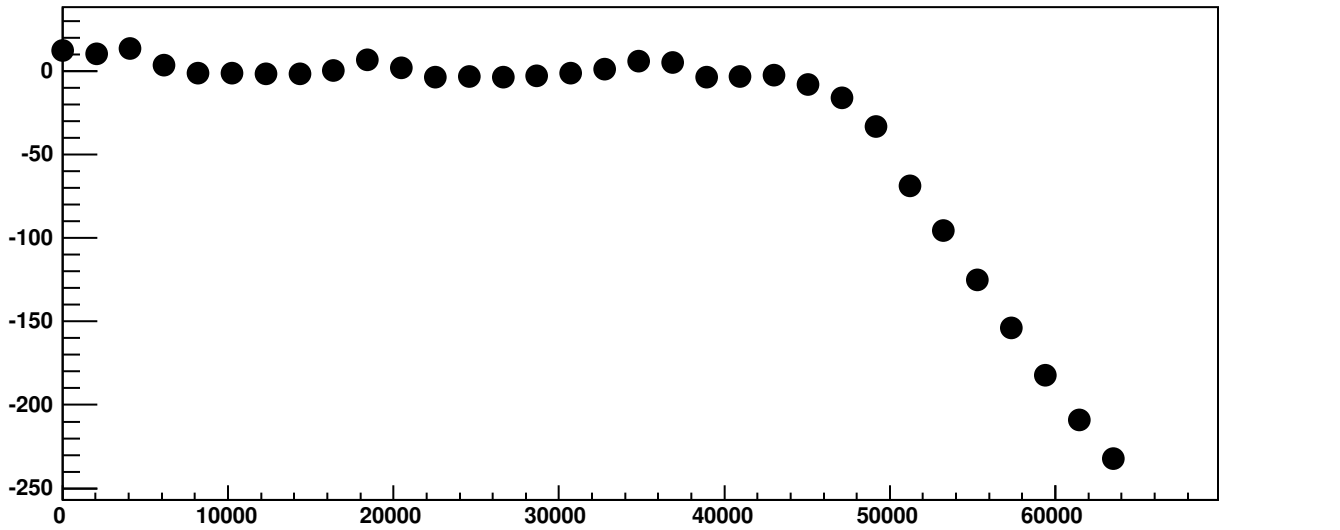
Chip 7, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC

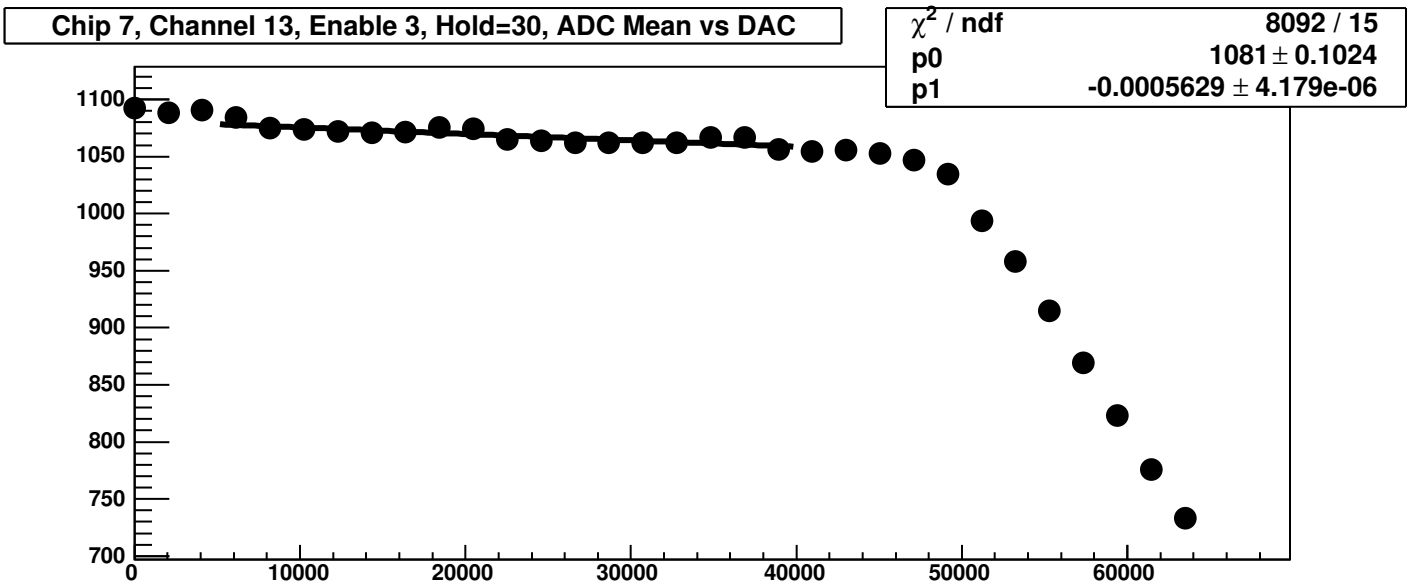


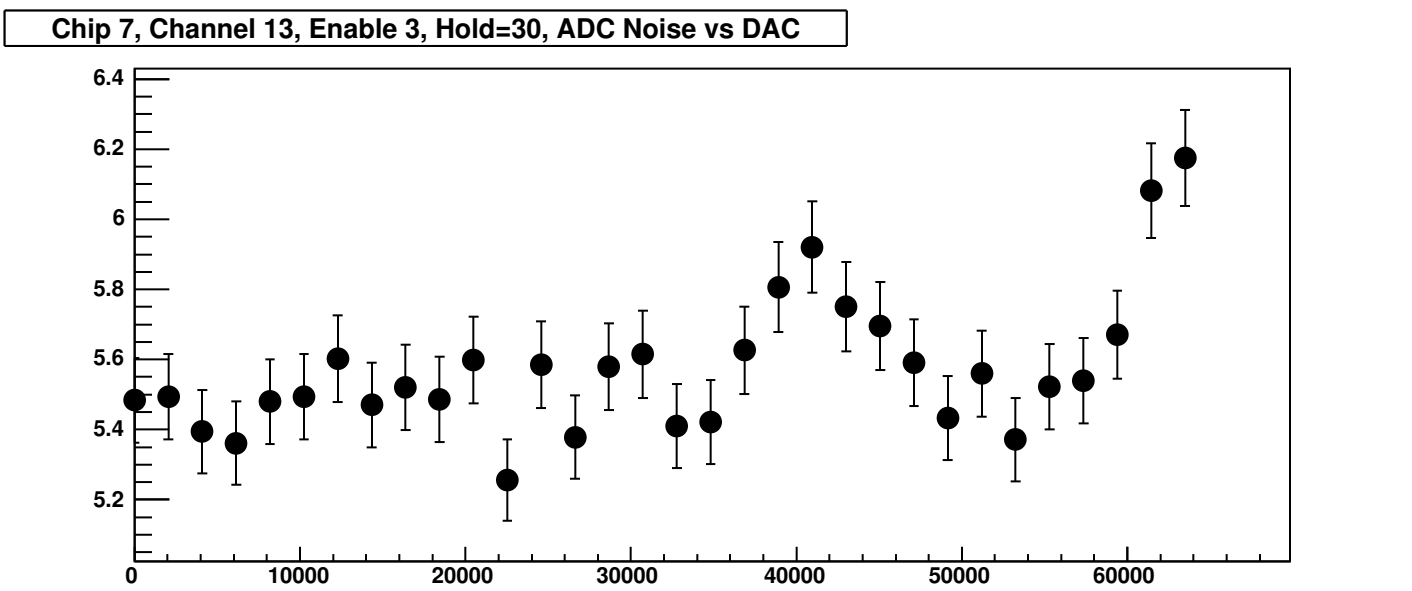
Chip 7, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC

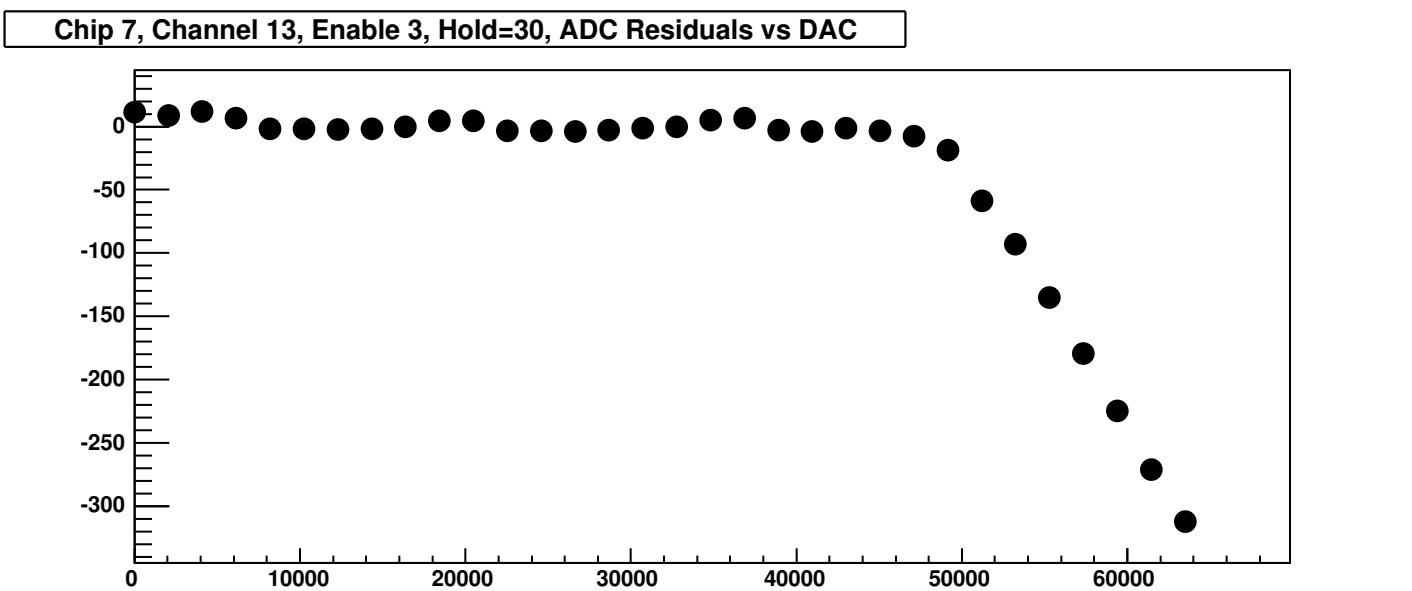


Chip 7, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC

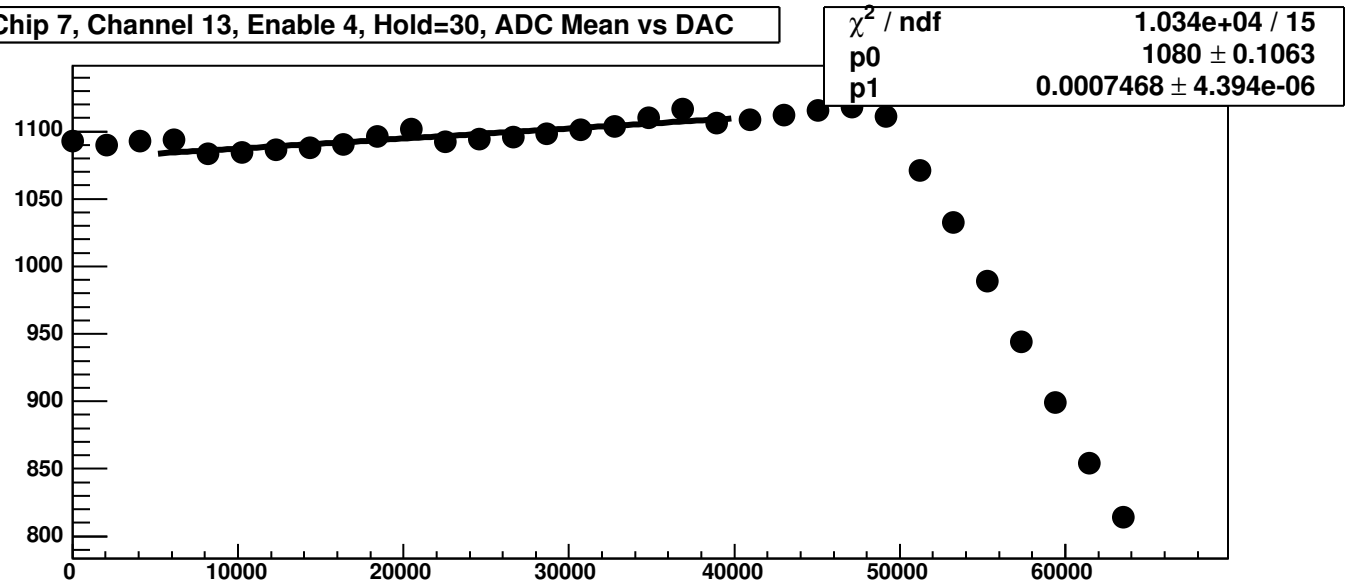



Chip 7, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8092 / 15
p0 1081 ± 0.1024
p1 -0.0005629 ± 4.179e-06
1100
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

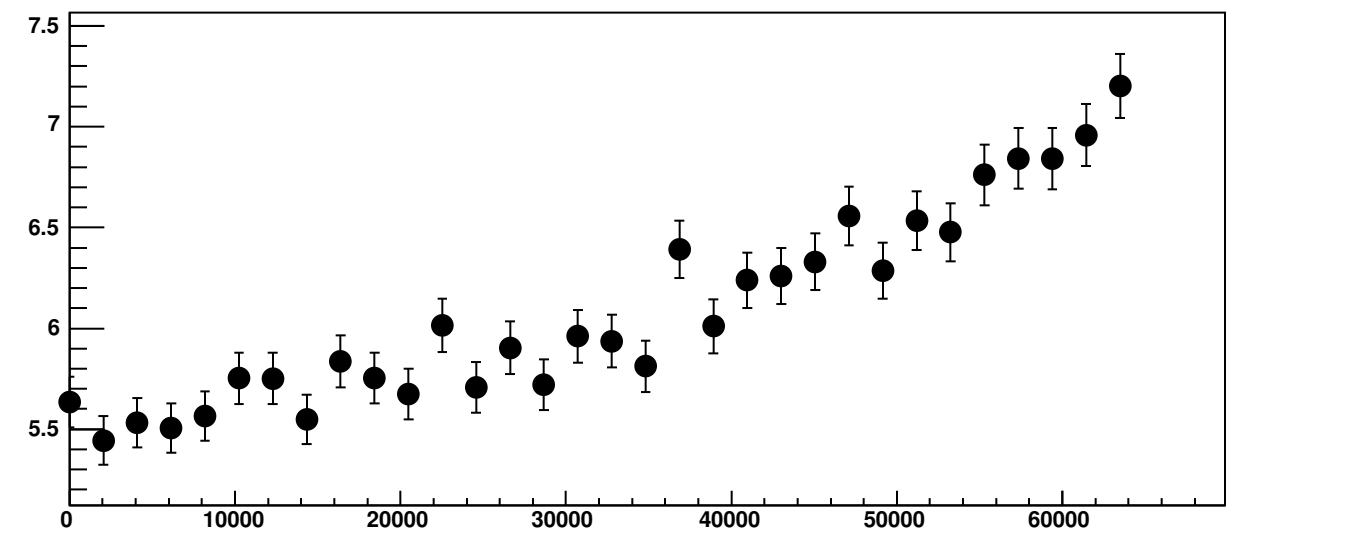

Chip 7, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

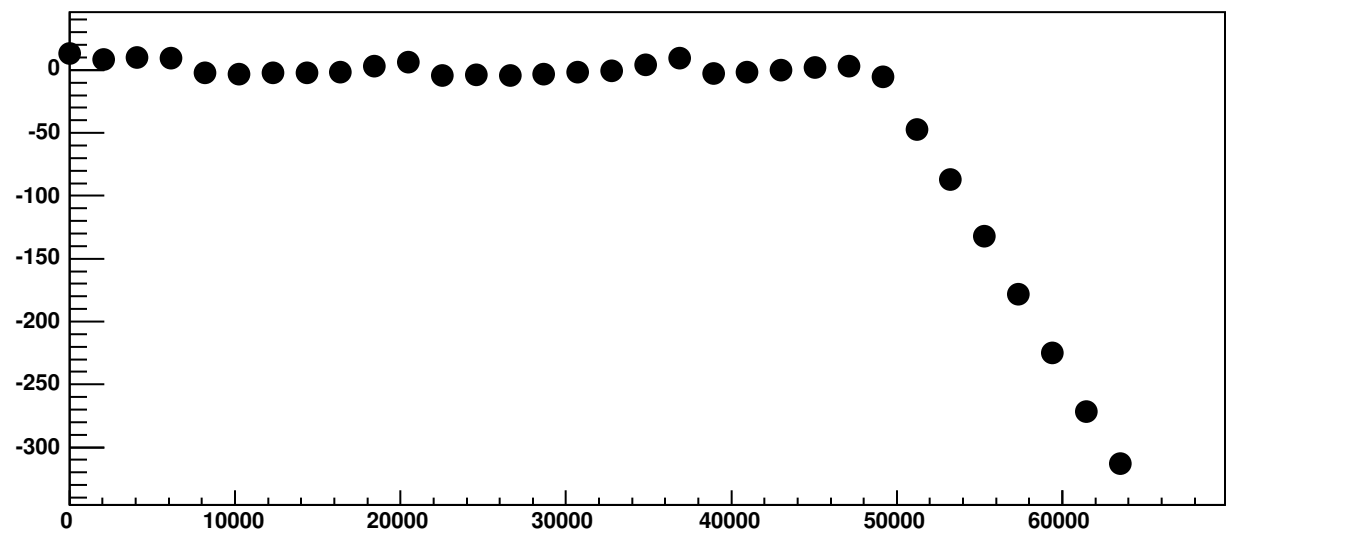
Chip 7, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC

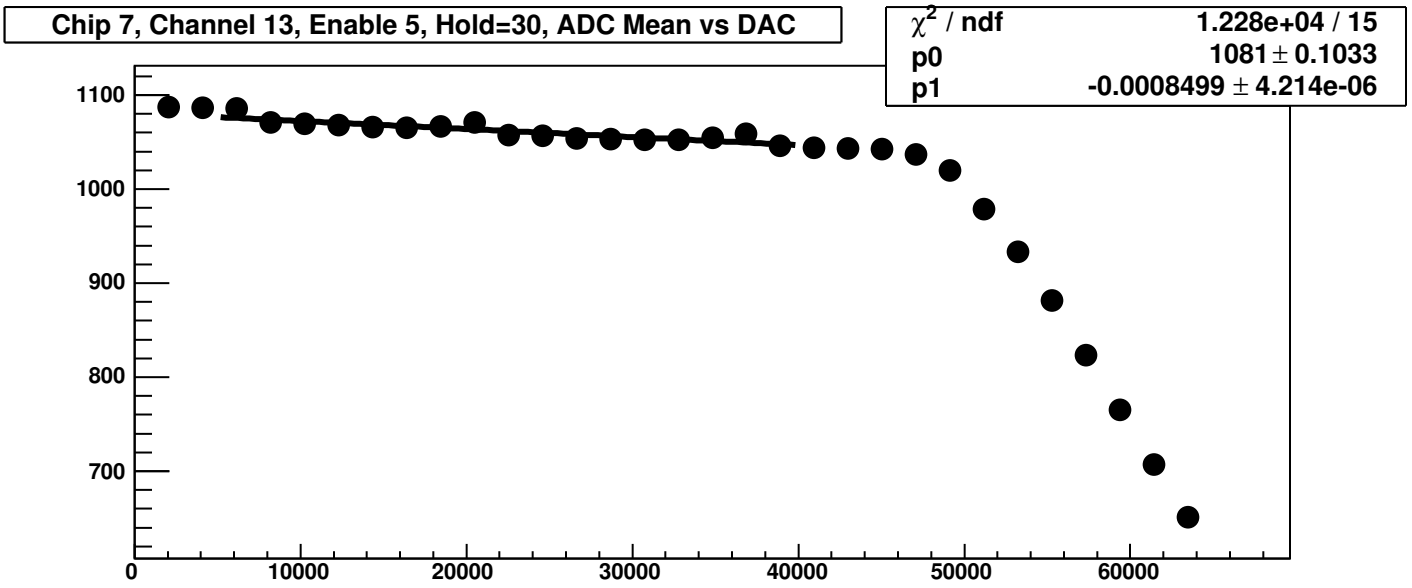


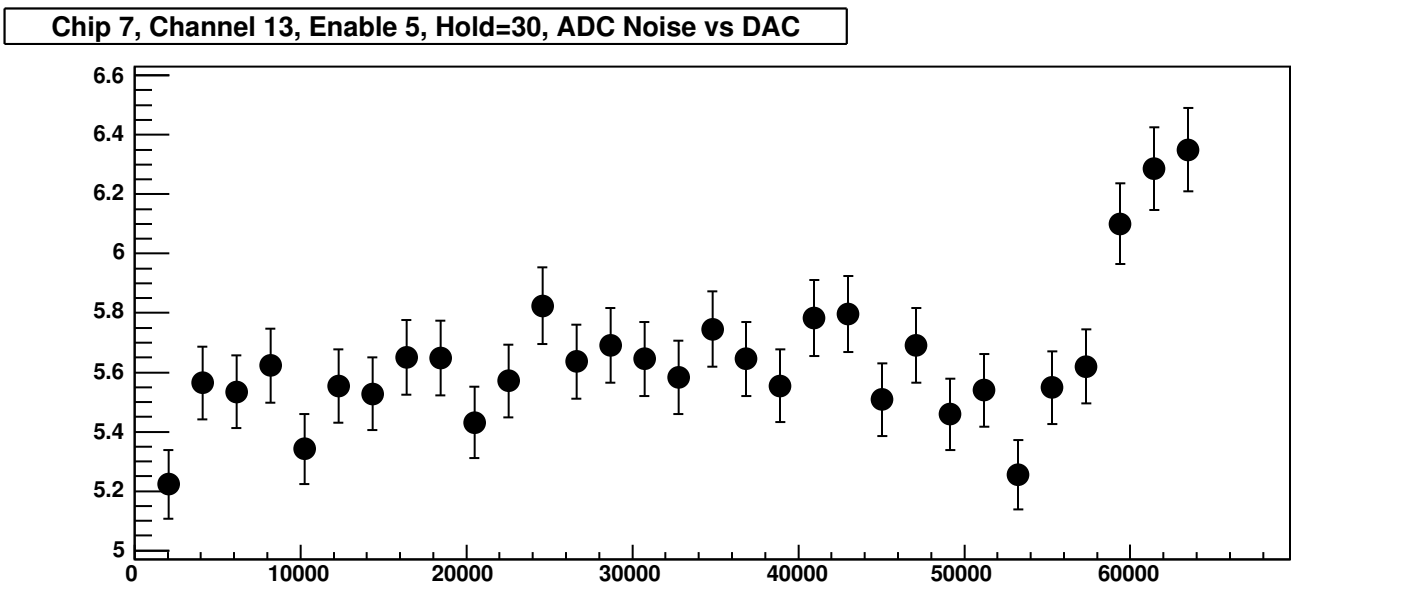
Chip 7, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC

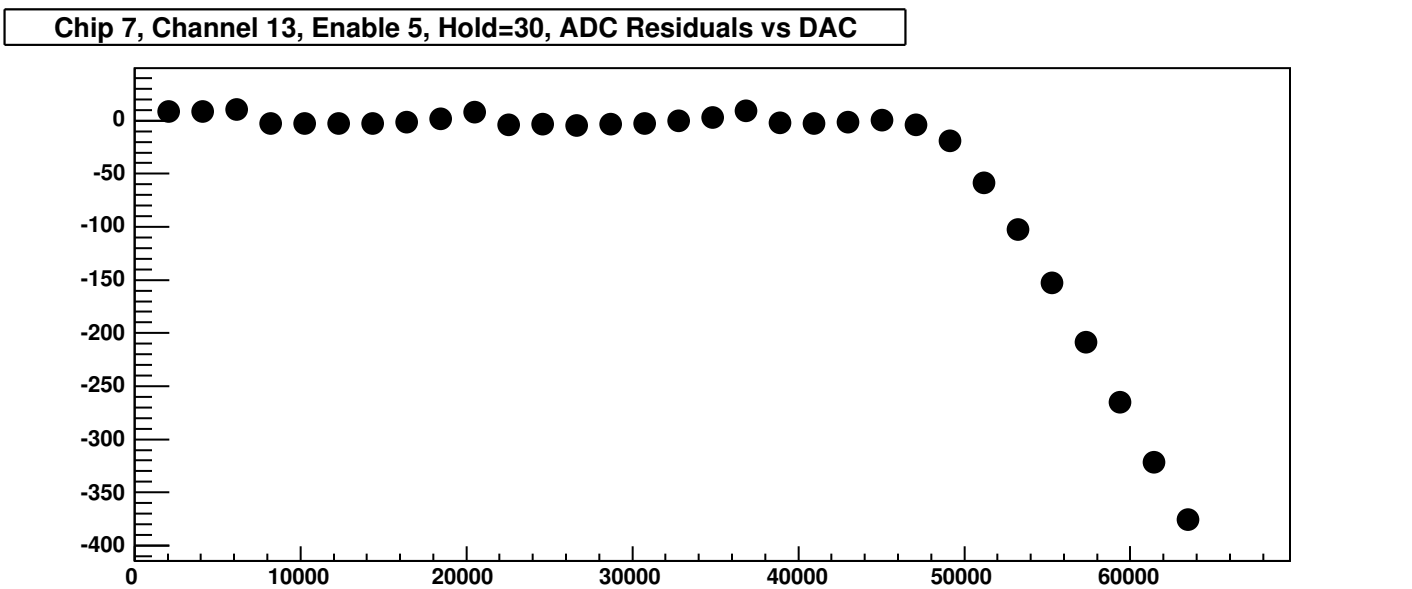


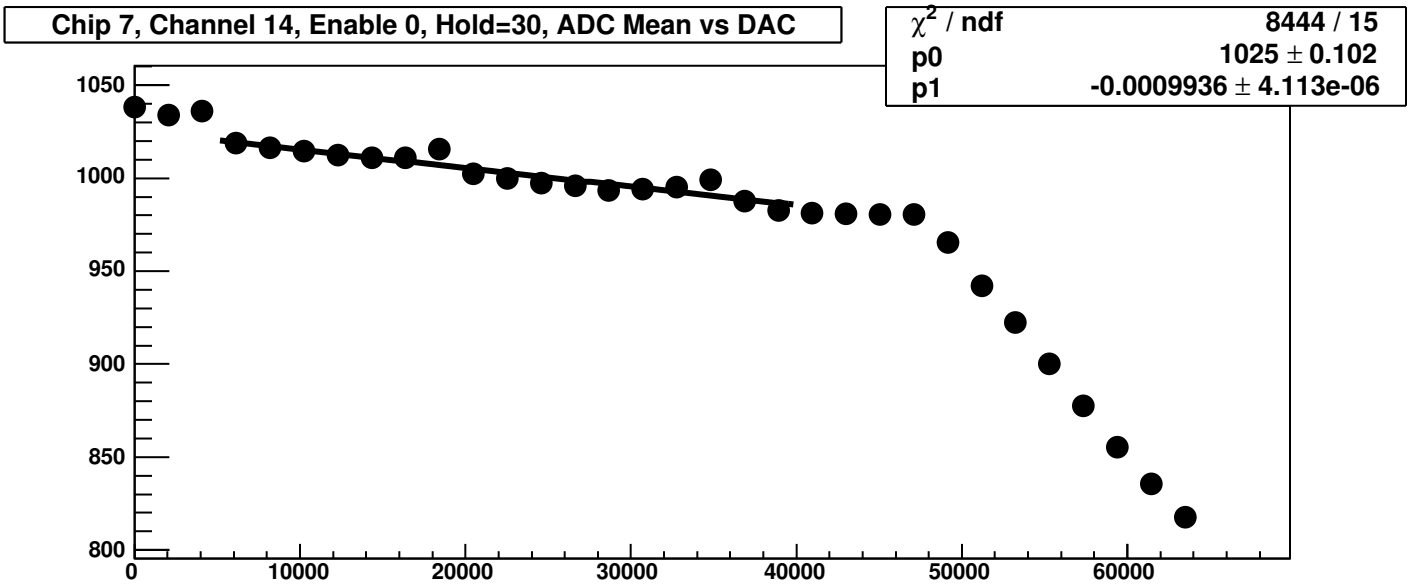
Chip 7, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC

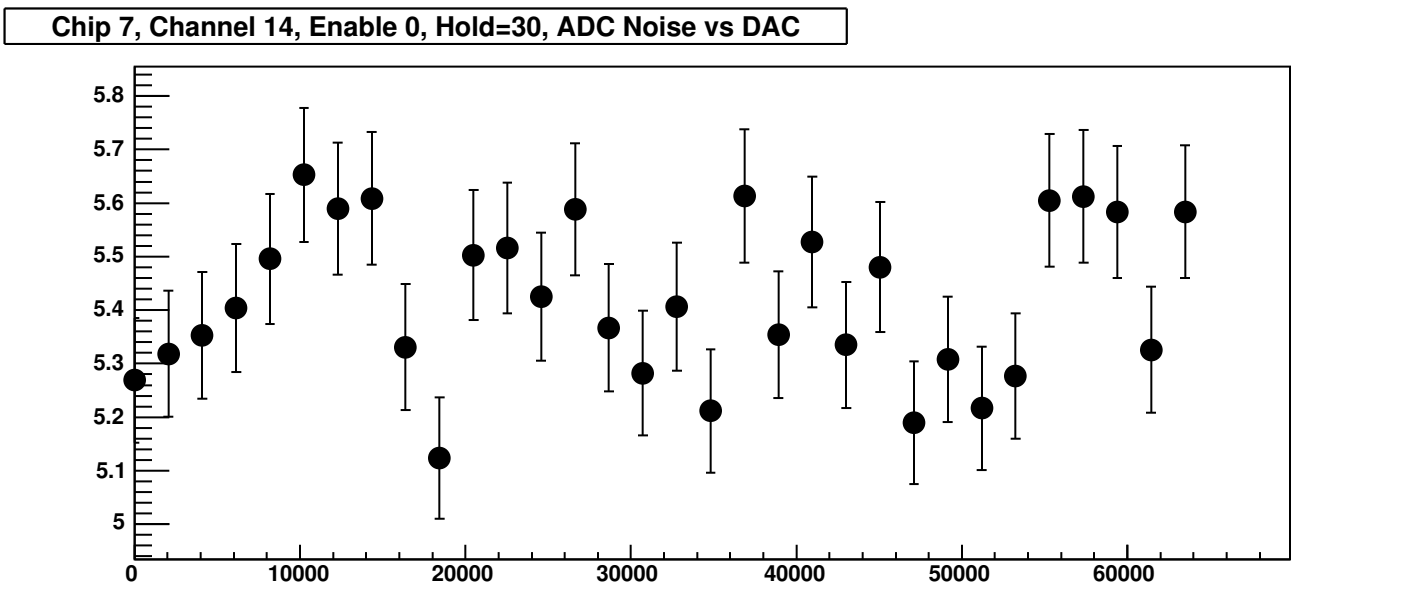


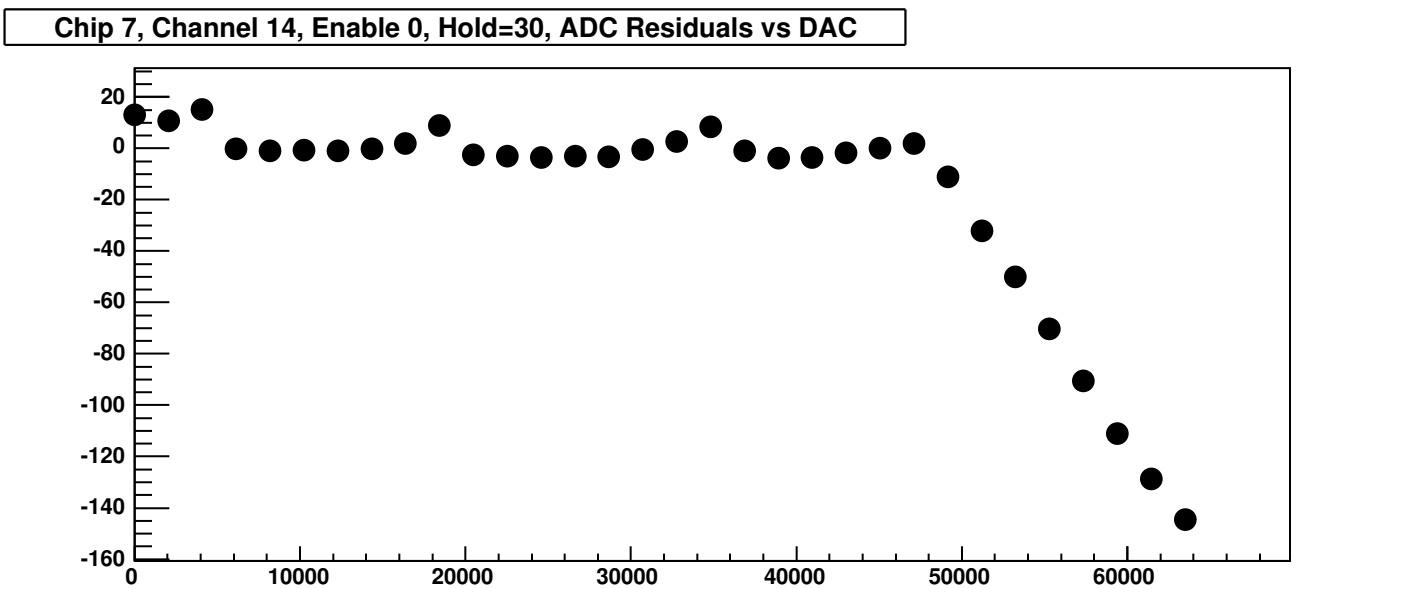

Chip 7, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.228e+04 / 15
p0 1081 ± 0.1033
p1 -0.0008499 ± 4.214e-06
1100
1000
900
800
700
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

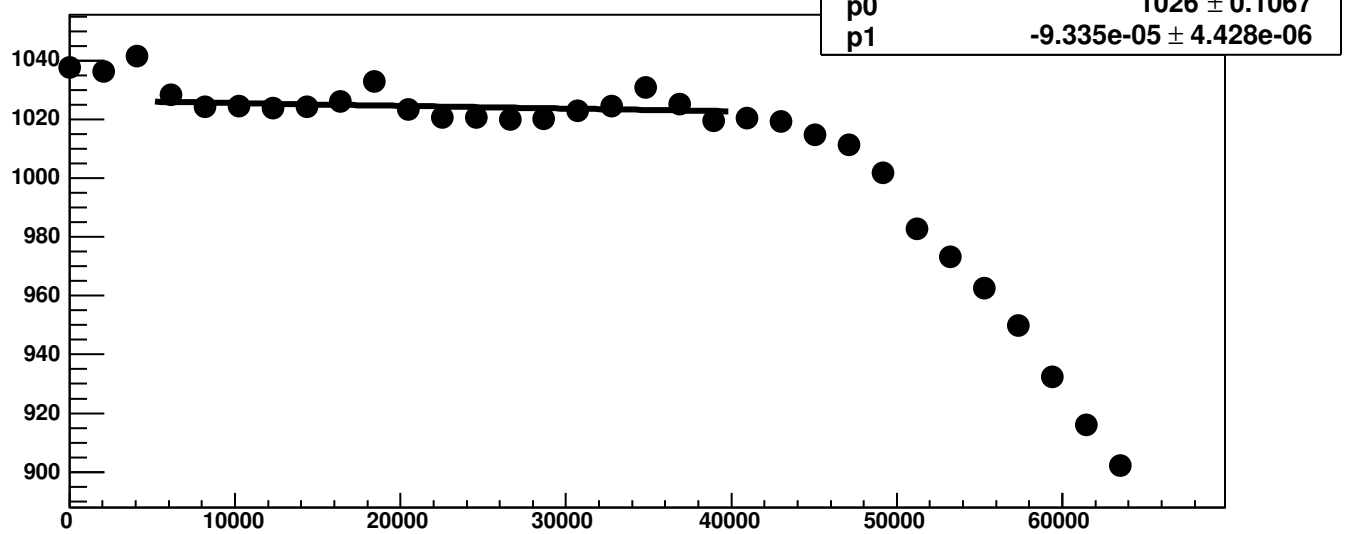

Chip 7, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8444 / 15
p0 1025 ± 0.102
p1 -0.0009936 ± 4.113e-06
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

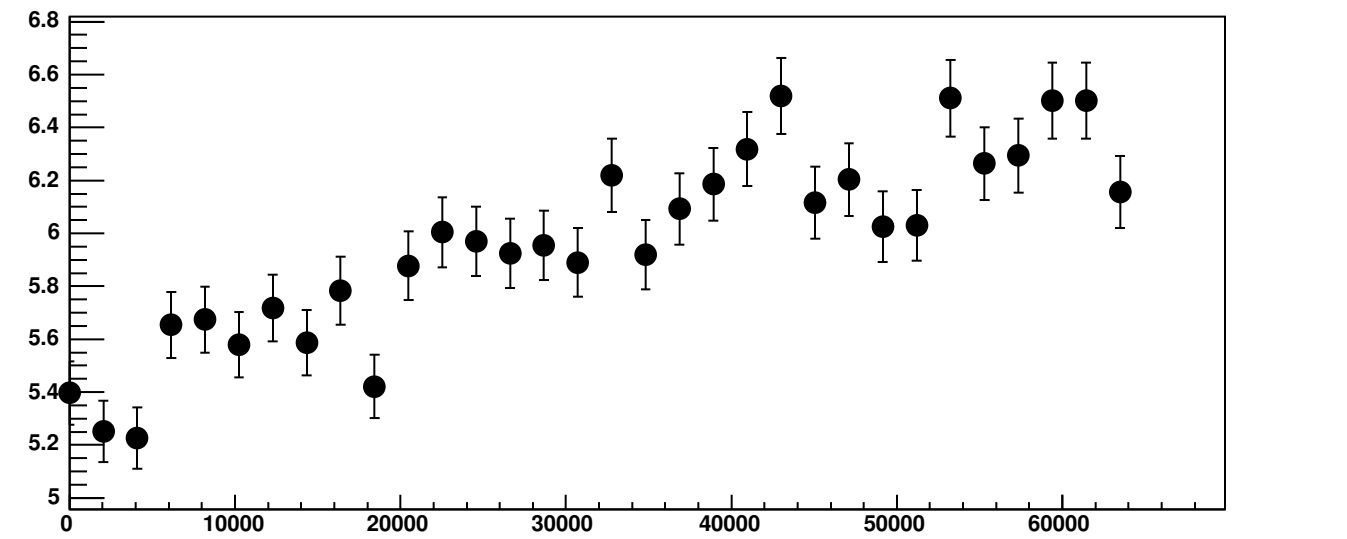

Chip 7, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
5
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

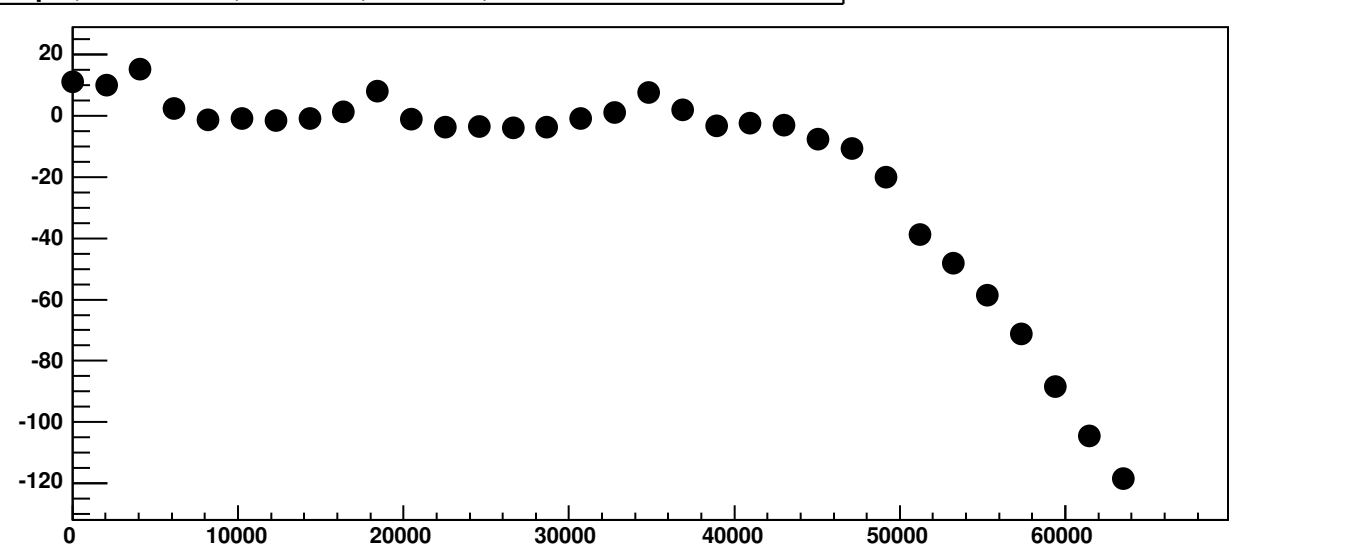
Chip 7, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC

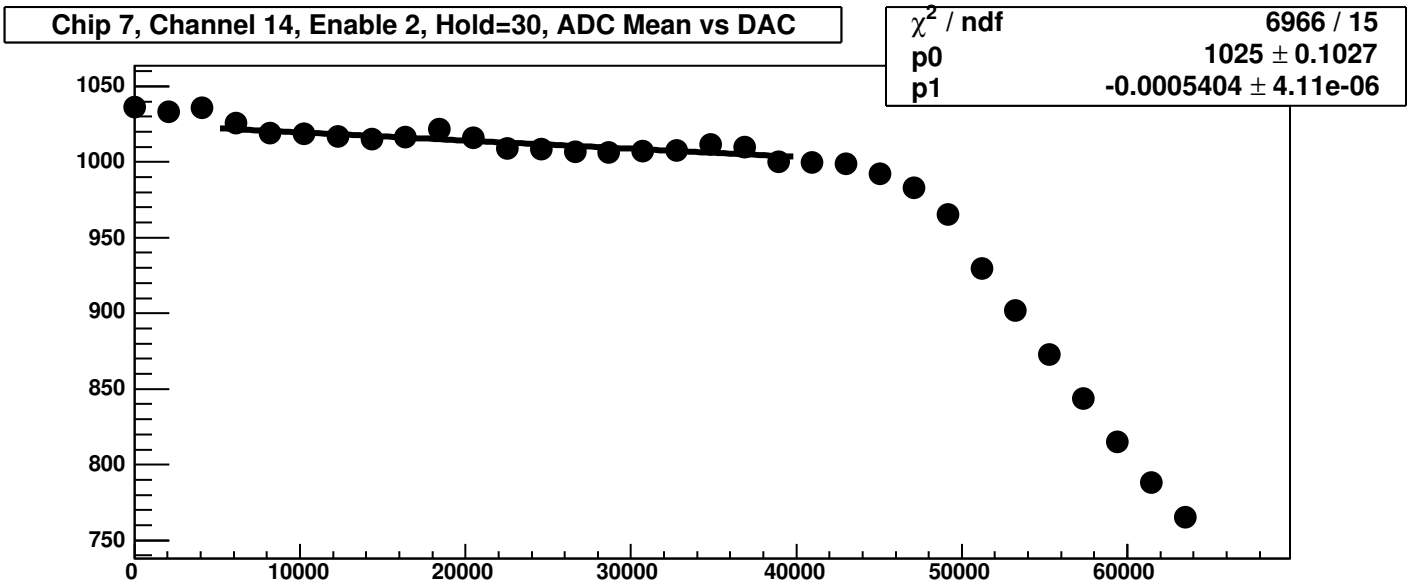


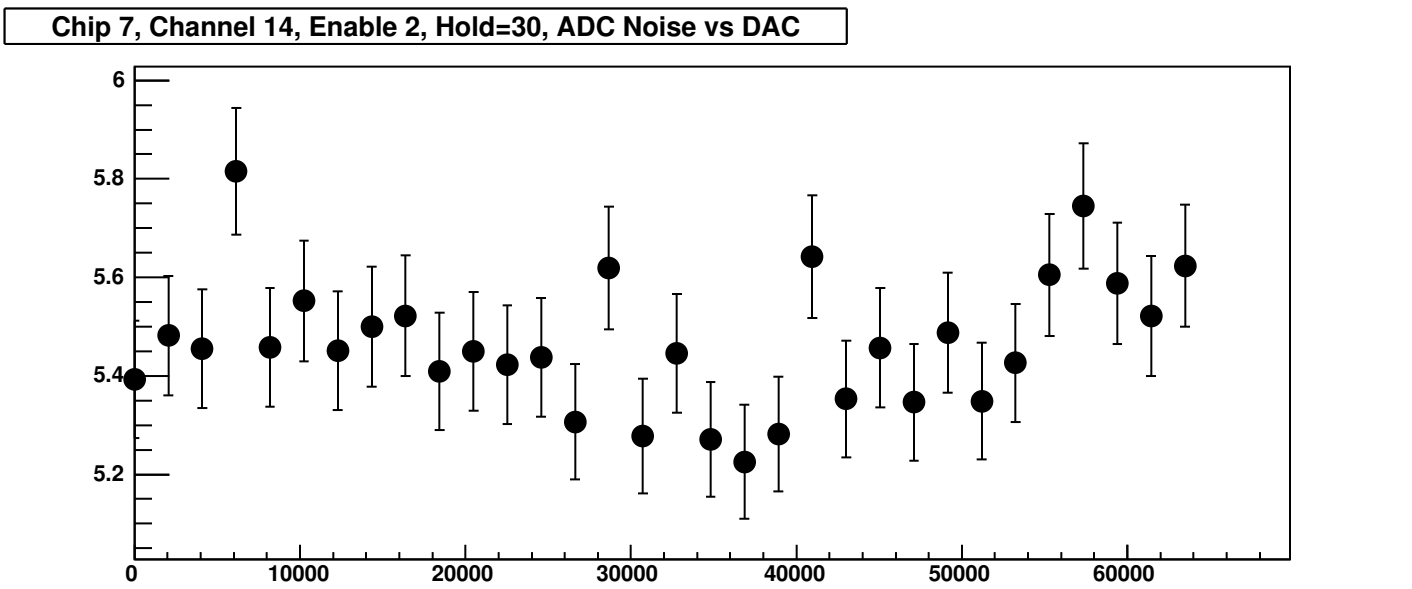
Chip 7, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC

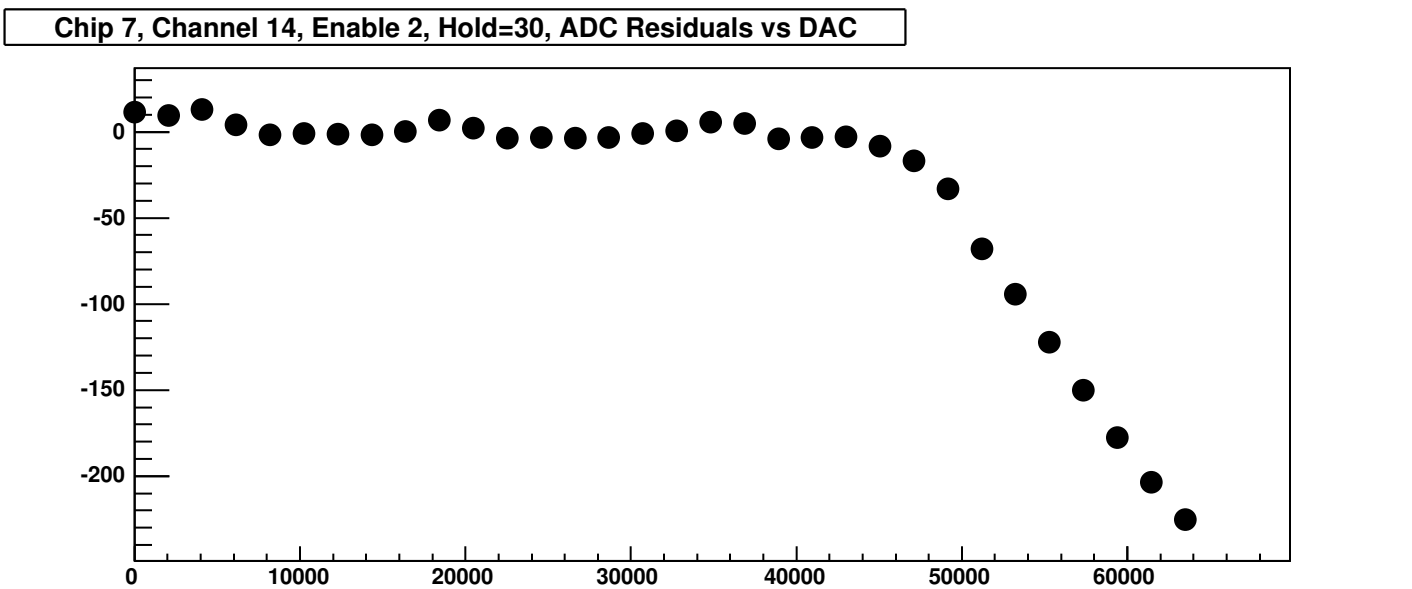


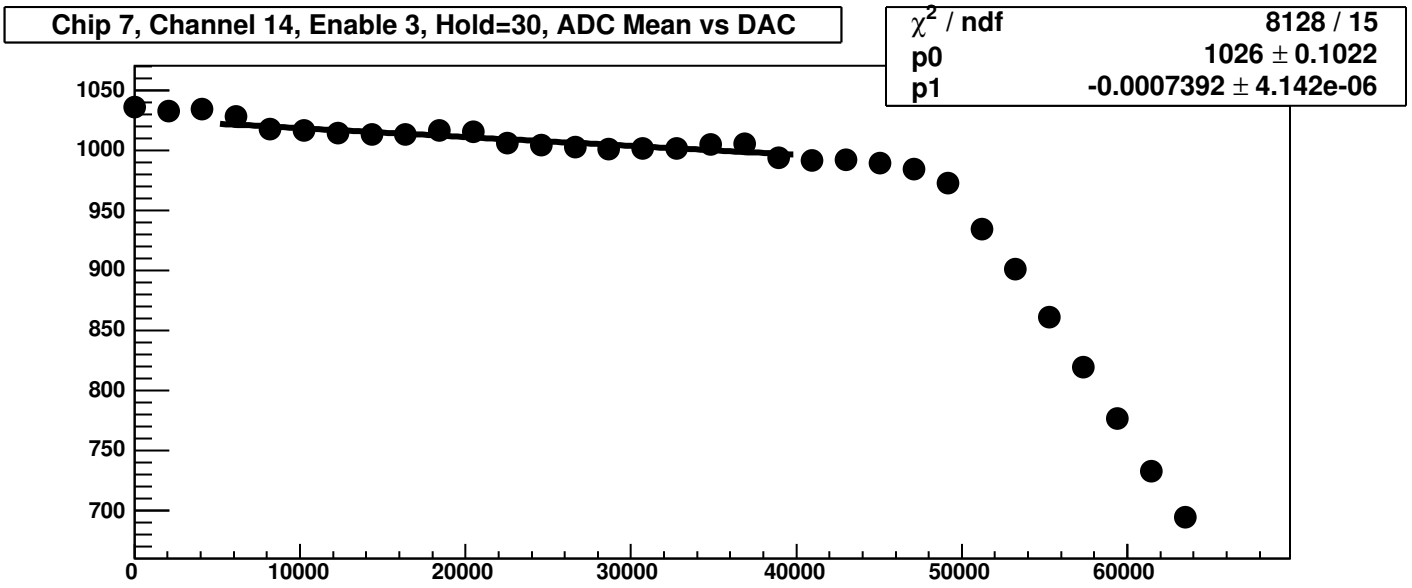
Chip 7, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC

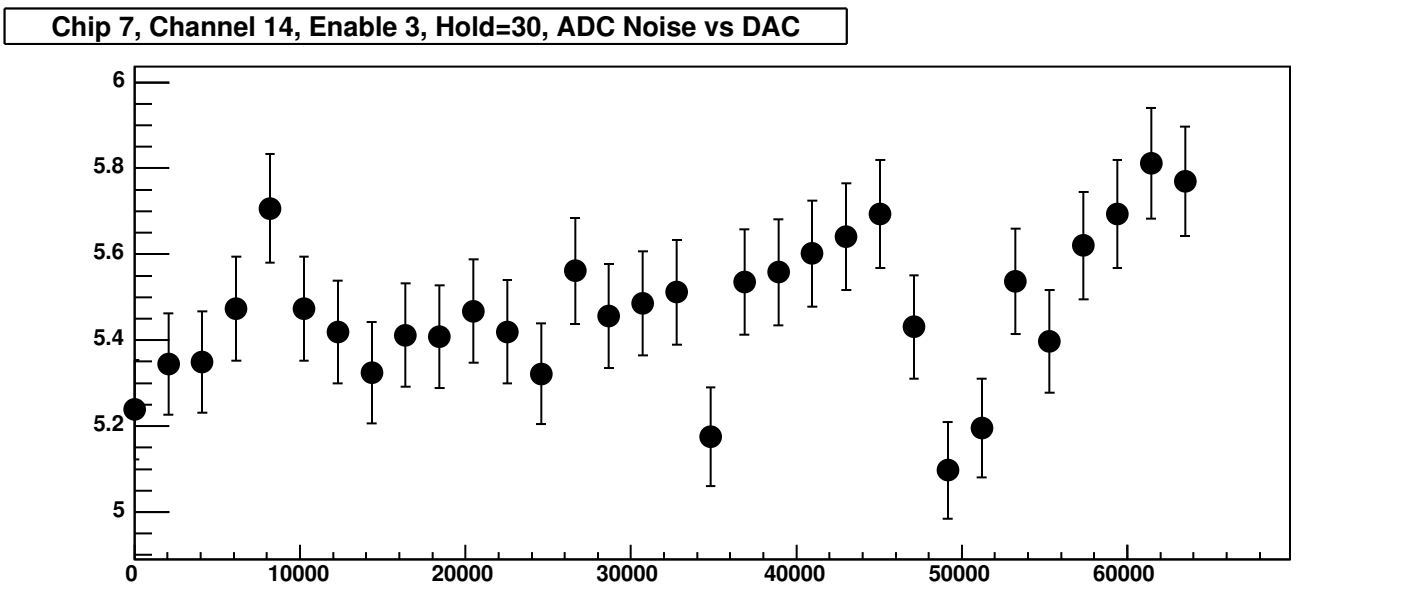


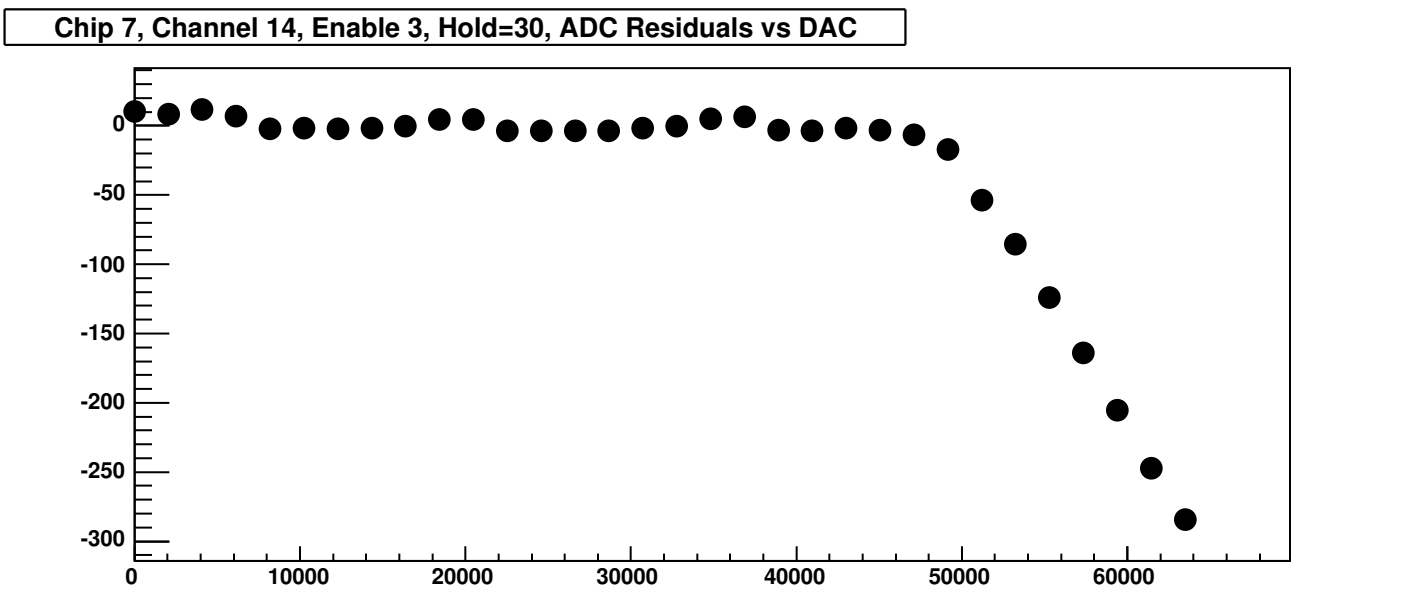

Chip 7, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6966 / 15
p0 1025 ± 0.1027
p1 -0.0005404 ± 4.11e-06
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

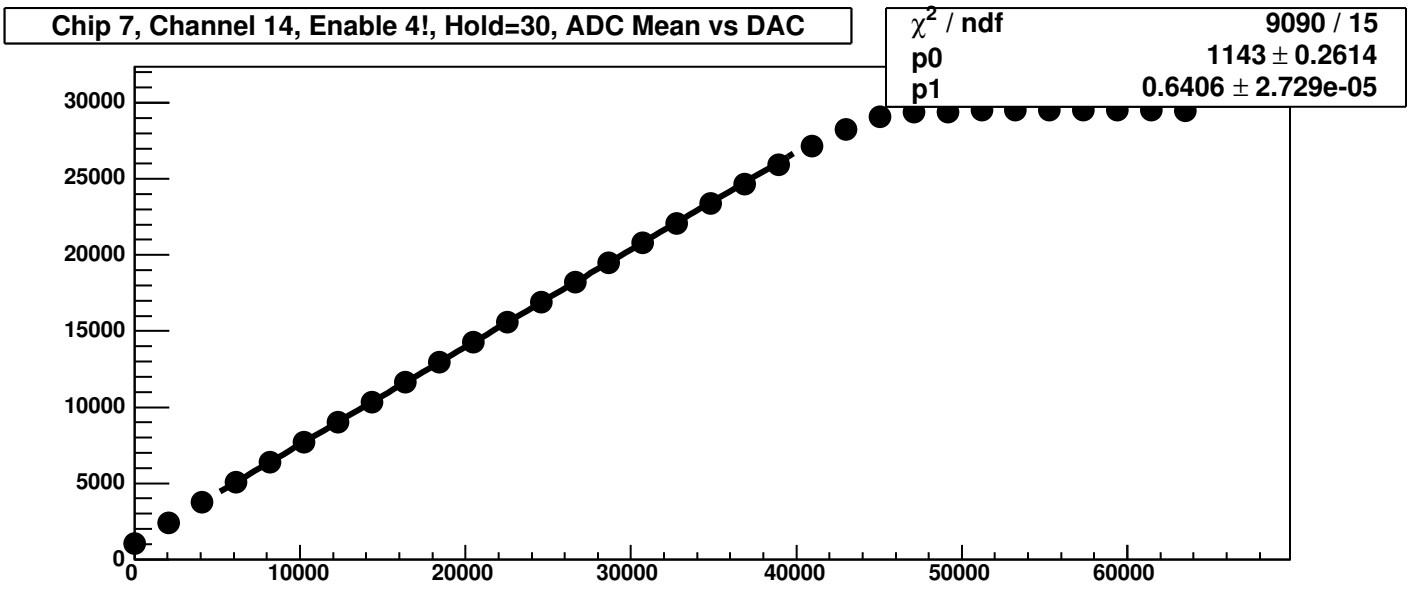

Chip 7, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

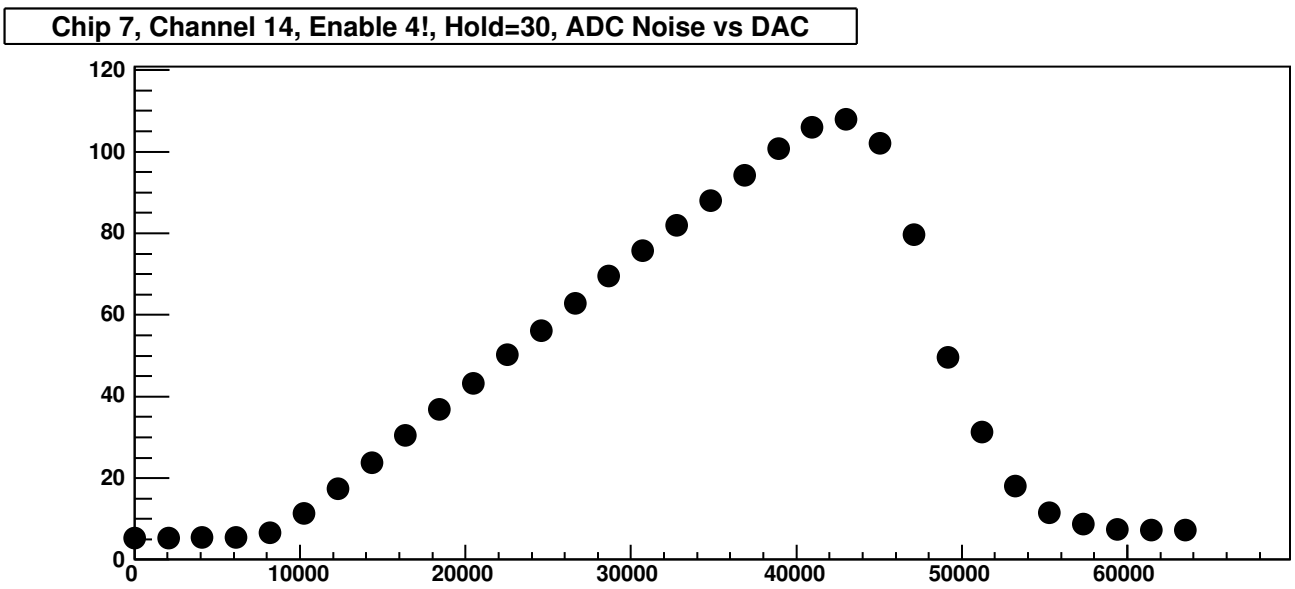

Chip 7, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

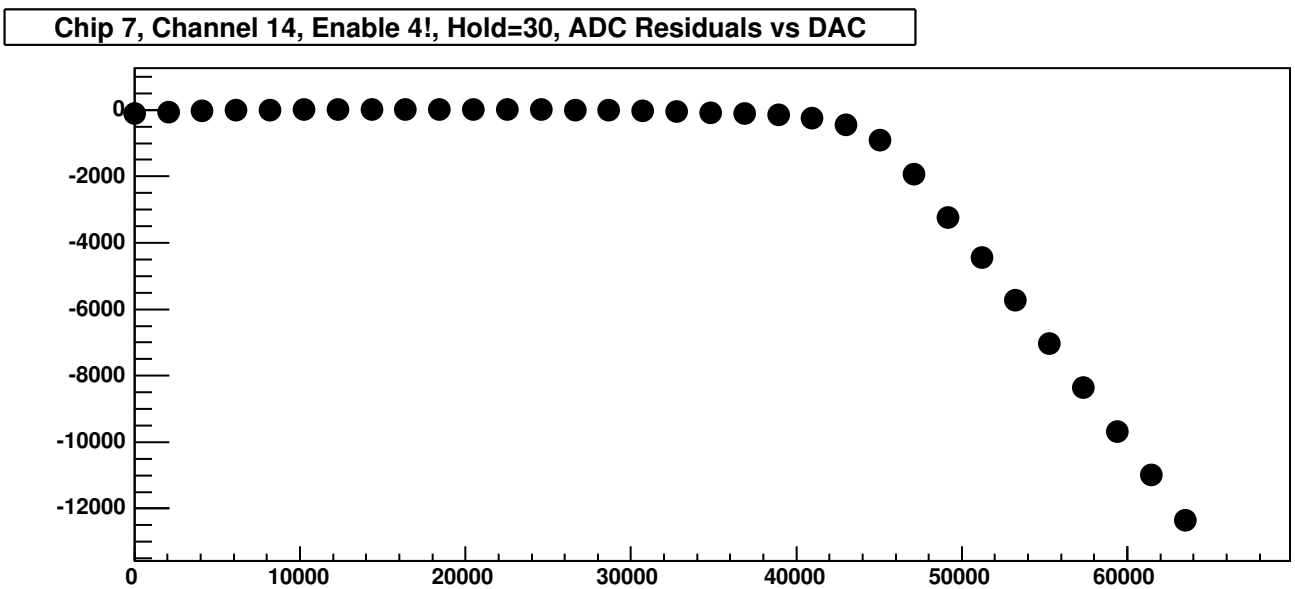

Chip 7, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8128 / 15
p0 1026 ± 0.1022
p1 -0.0007392 ± 4.142e-06
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

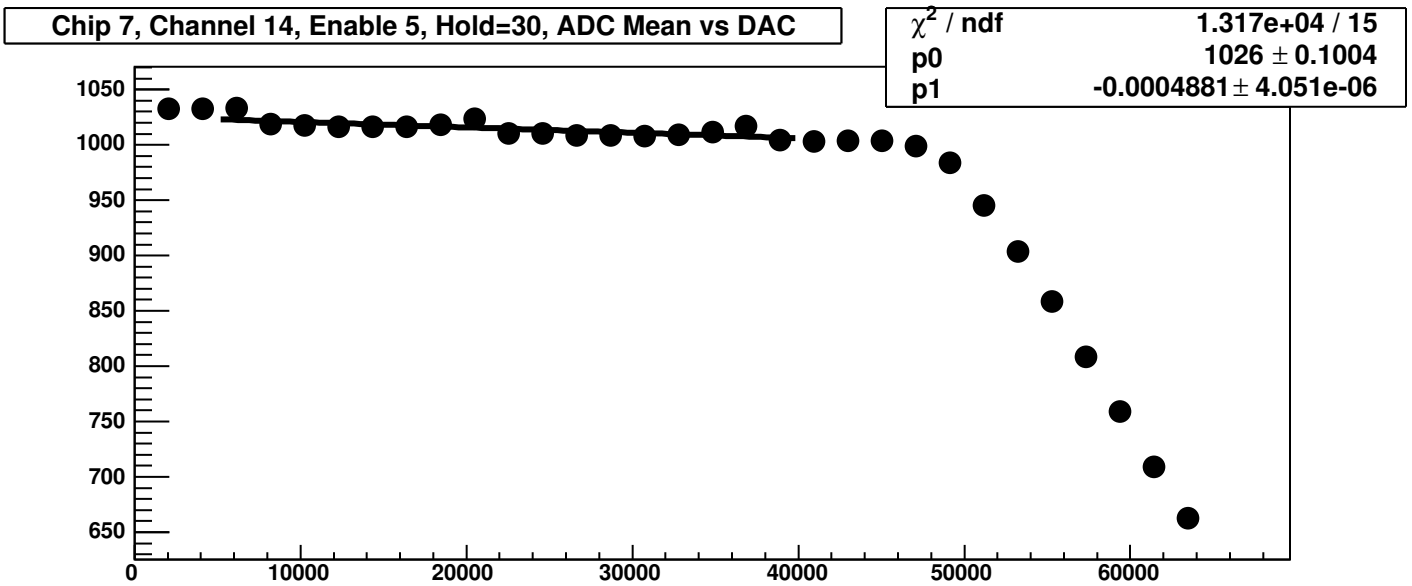

Chip 7, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

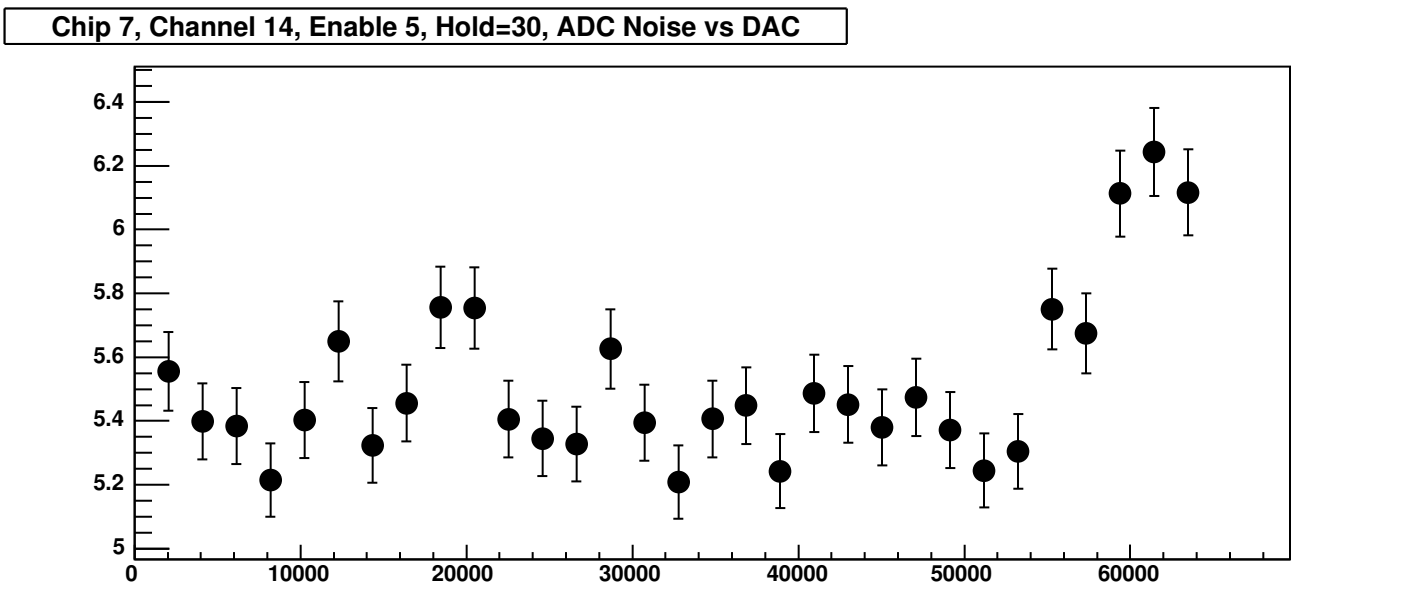

Chip 7, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

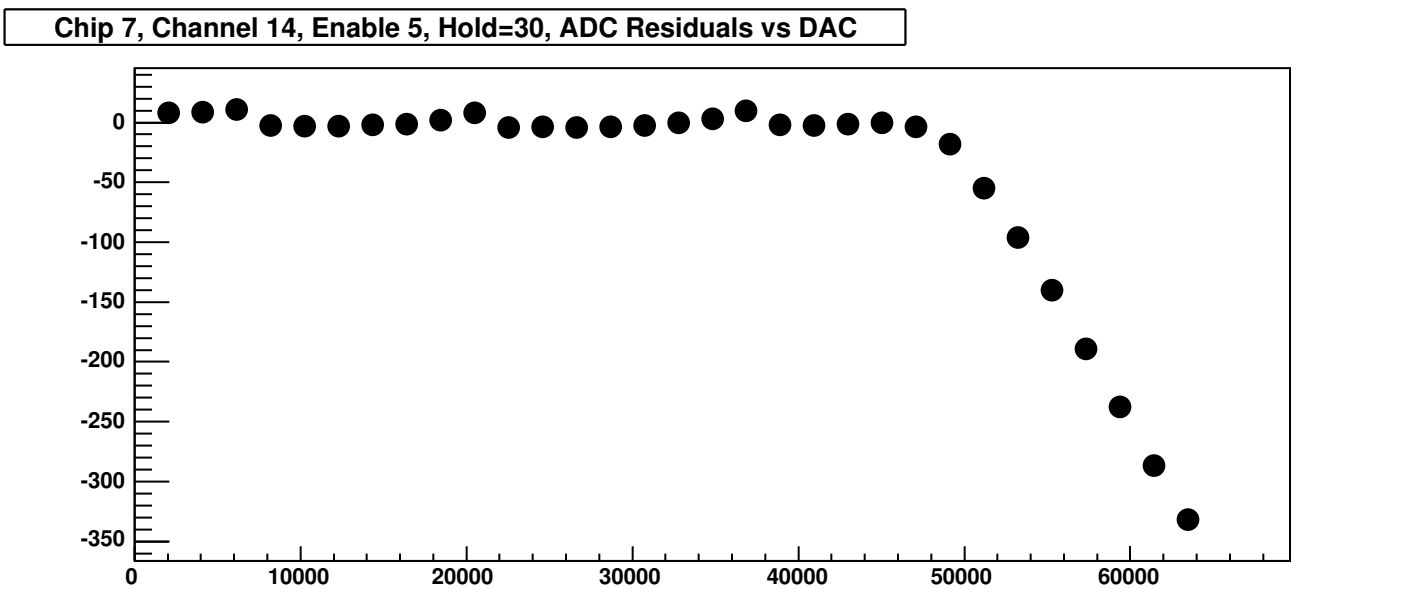

Chip 7, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 9090 / 15
p0 1143 ± 0.2614
p1 0.6406 ± 2.729e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

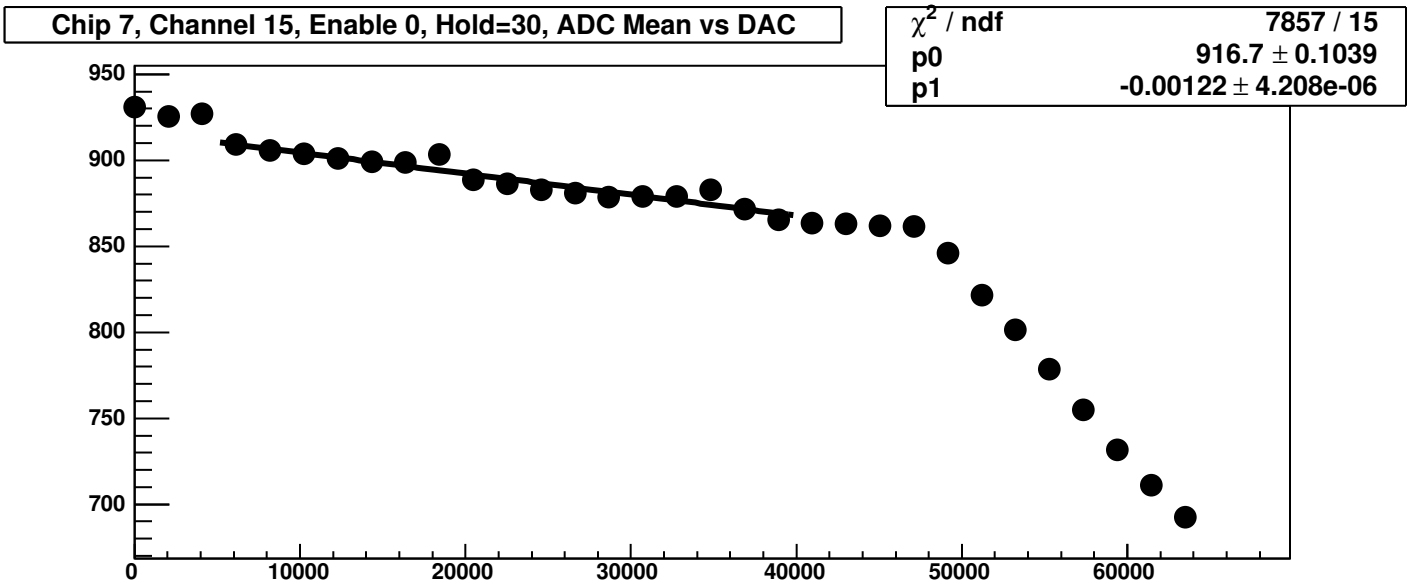

Chip 7, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

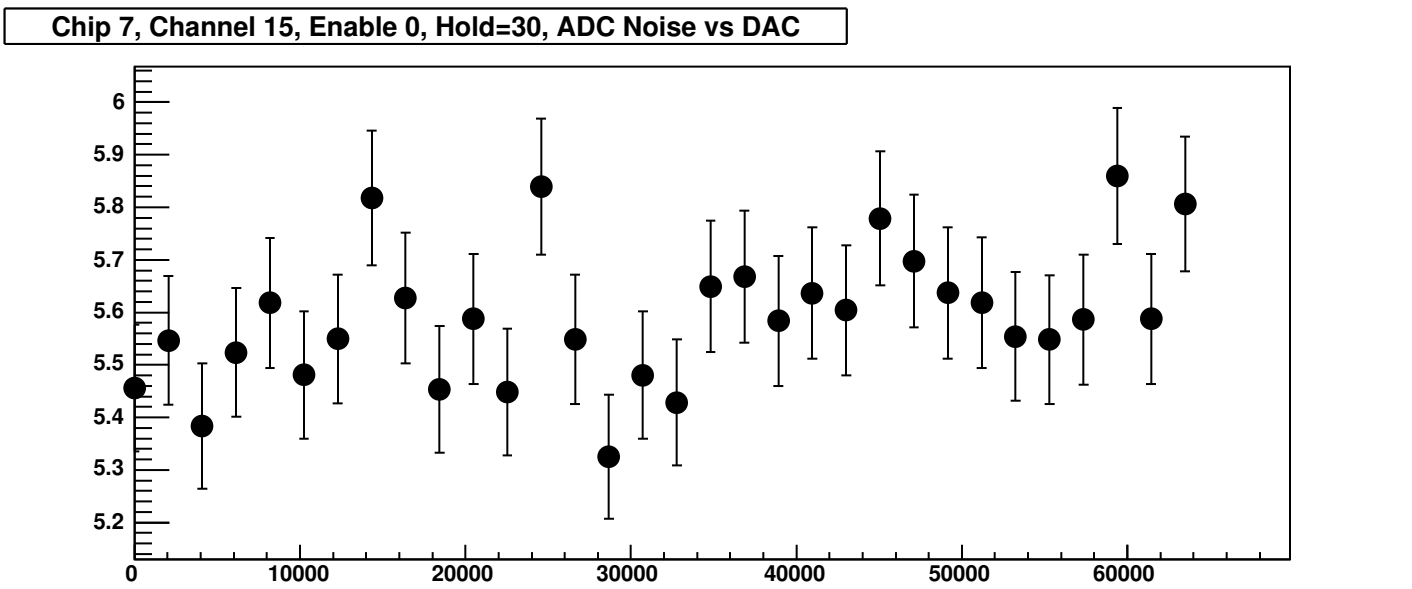

Chip 7, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

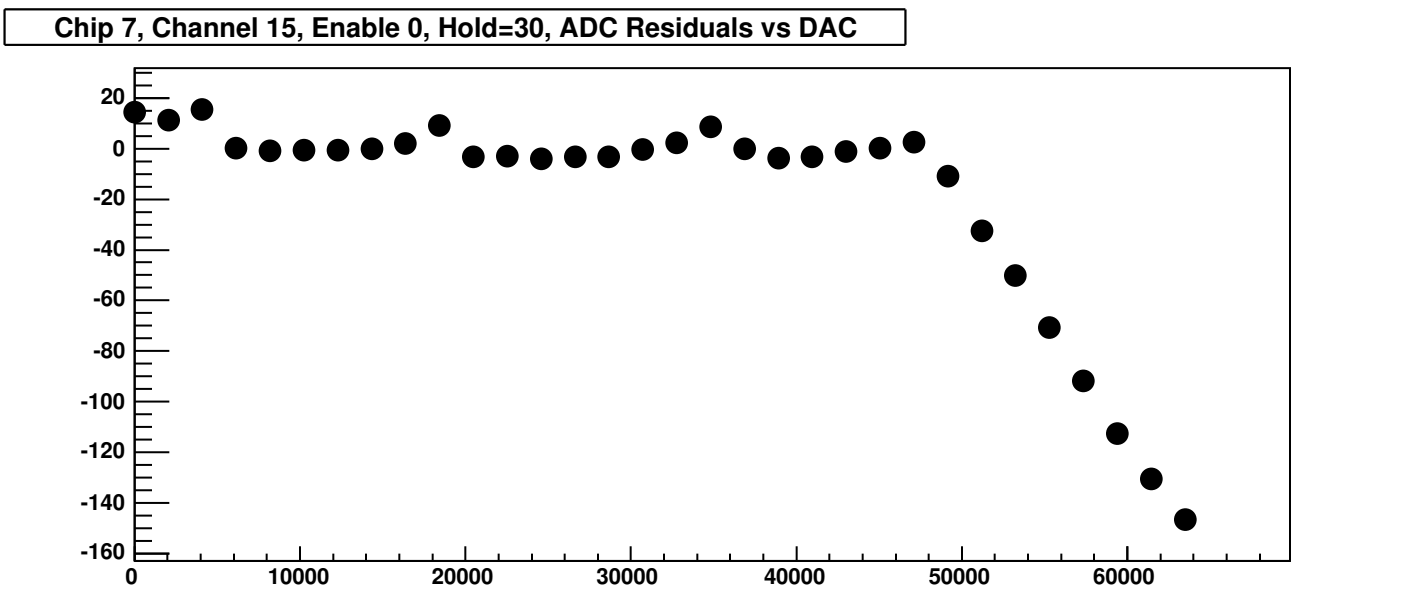

Chip 7, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.317e+04 / 15
p0 1026 ± 0.1004
p1 -0.0004881 ± 4.051e-06
1050
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

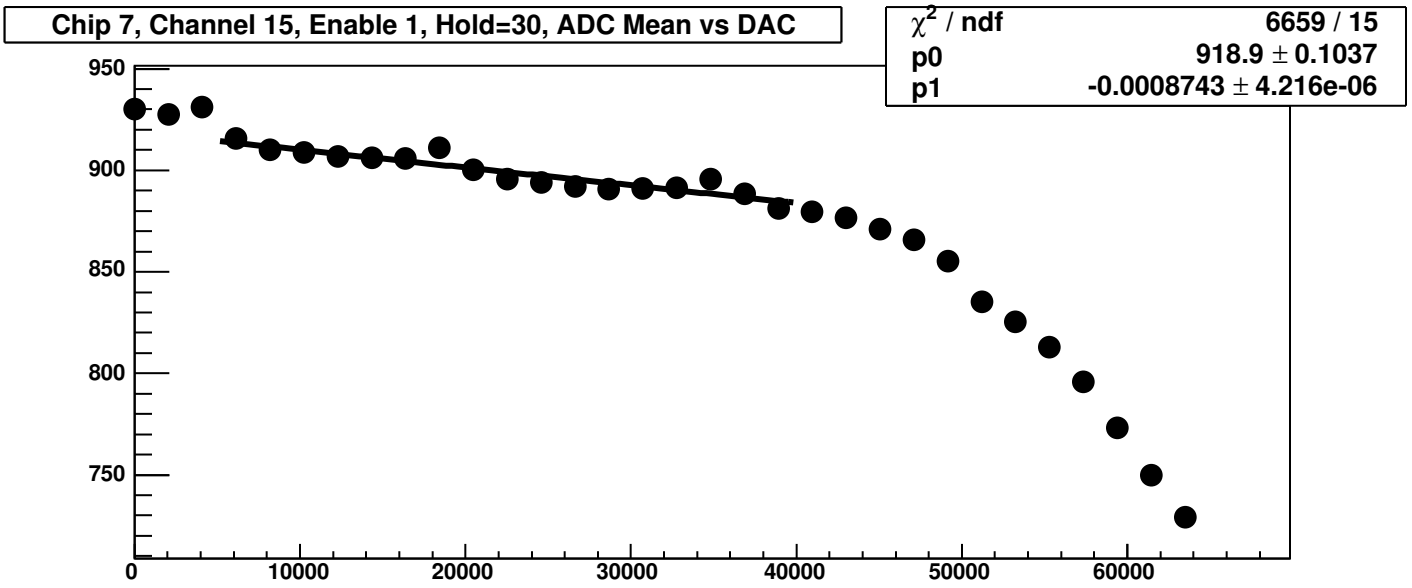

Chip 7, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

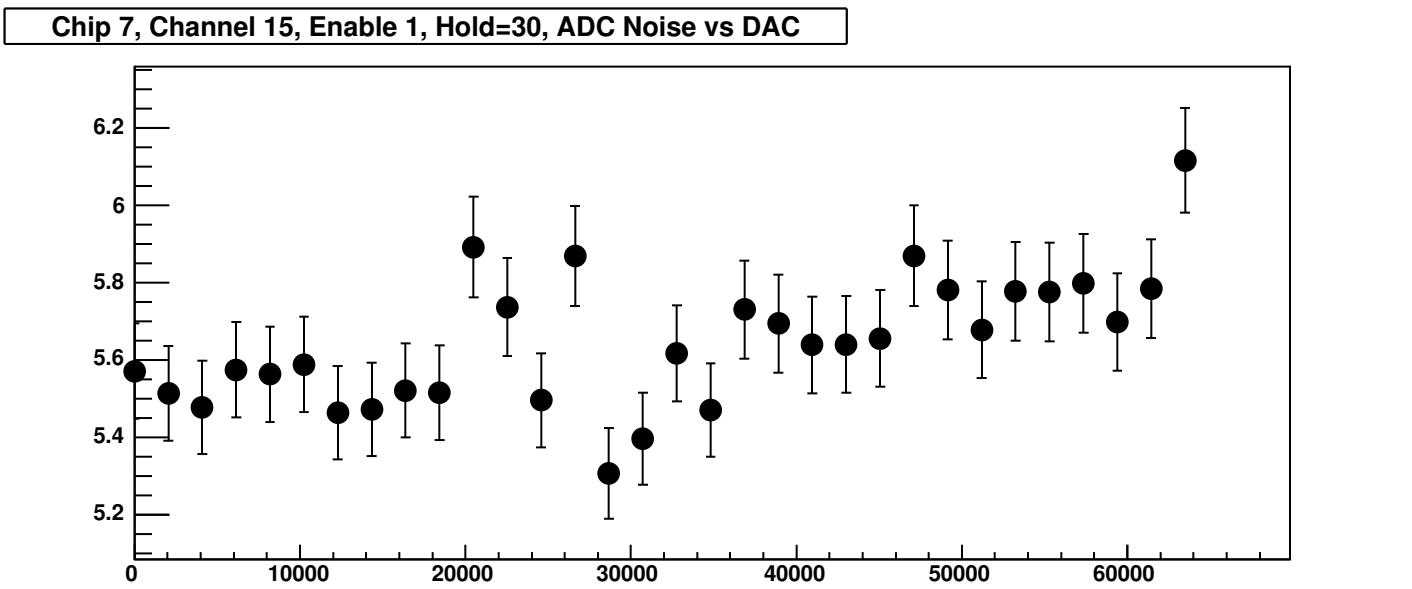

Chip 7, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

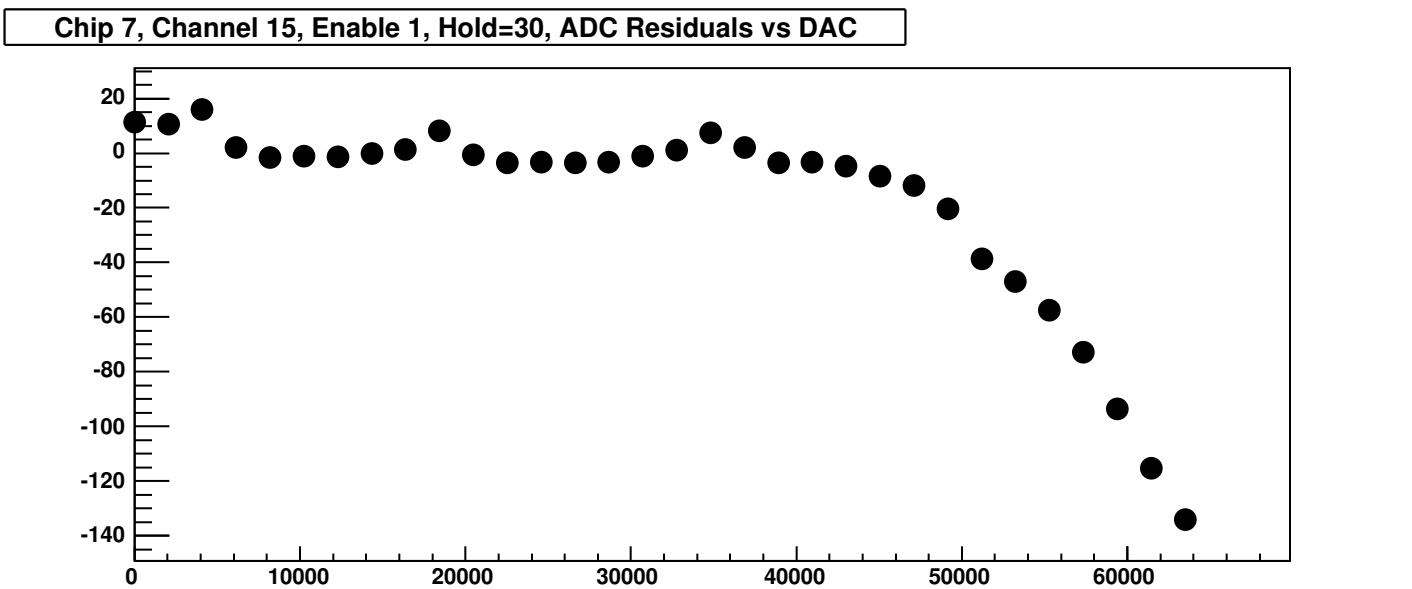

Chip 7, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7857 / 15
p0 916.7 ± 0.1039
p1 -0.00122 ± 4.208e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

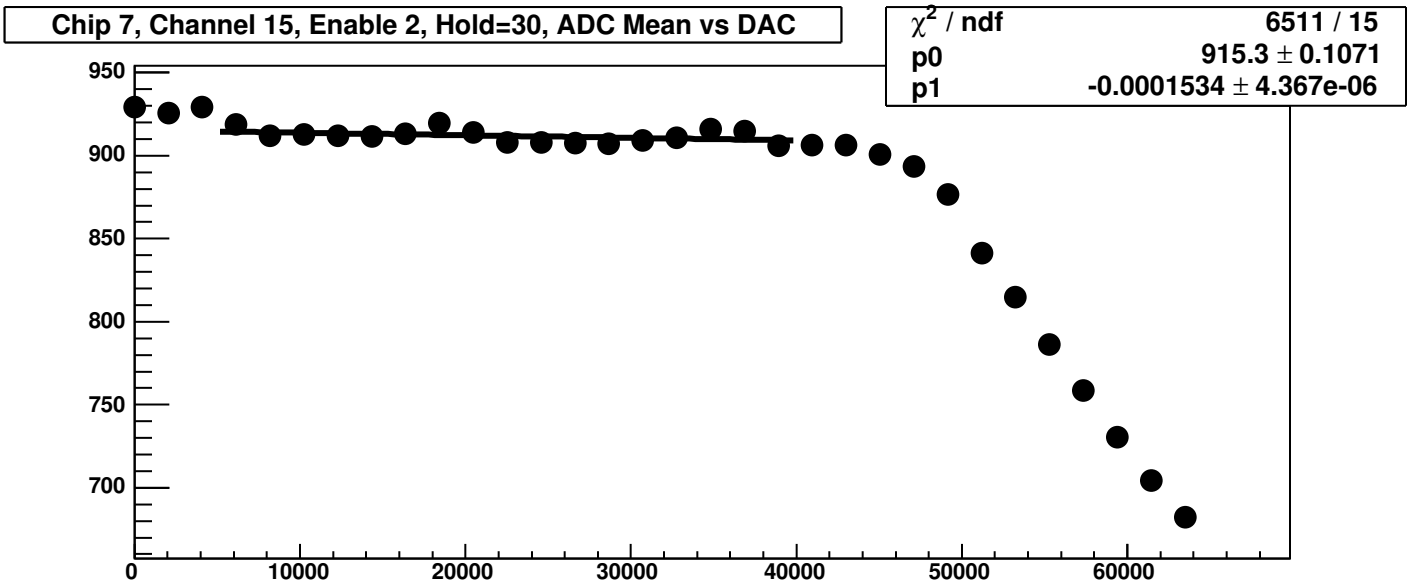

Chip 7, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
0
10000
20000
30000
40000
50000
60000

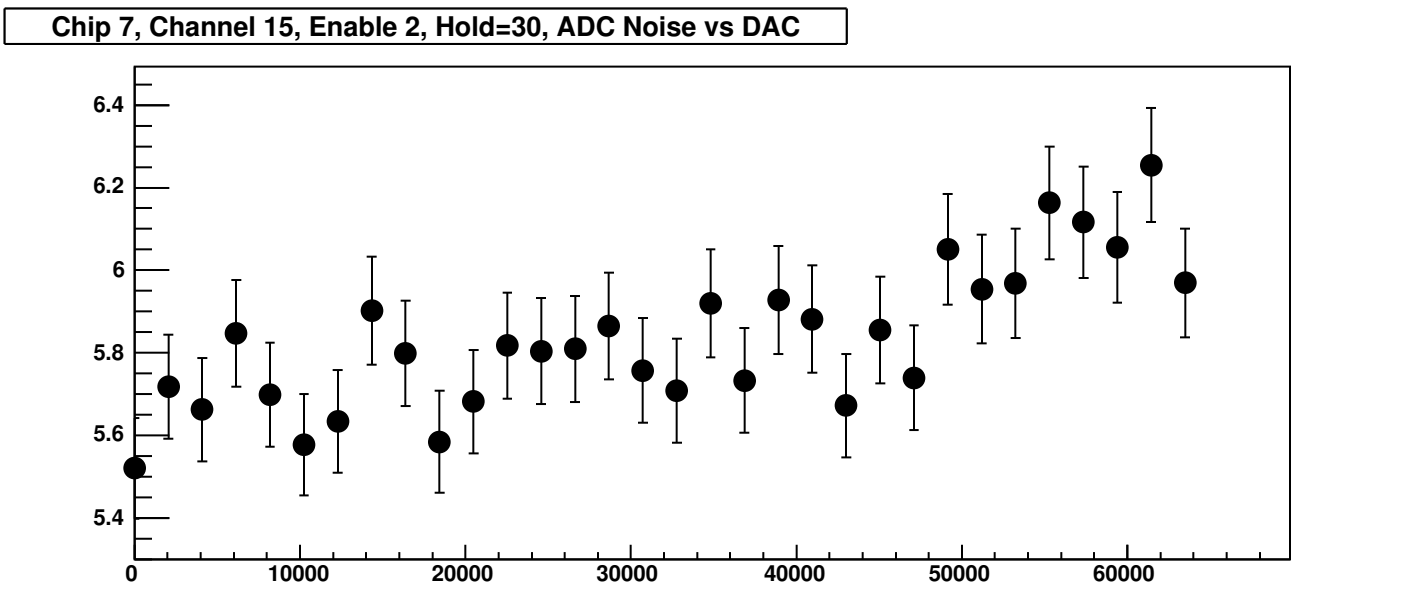

Chip 7, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

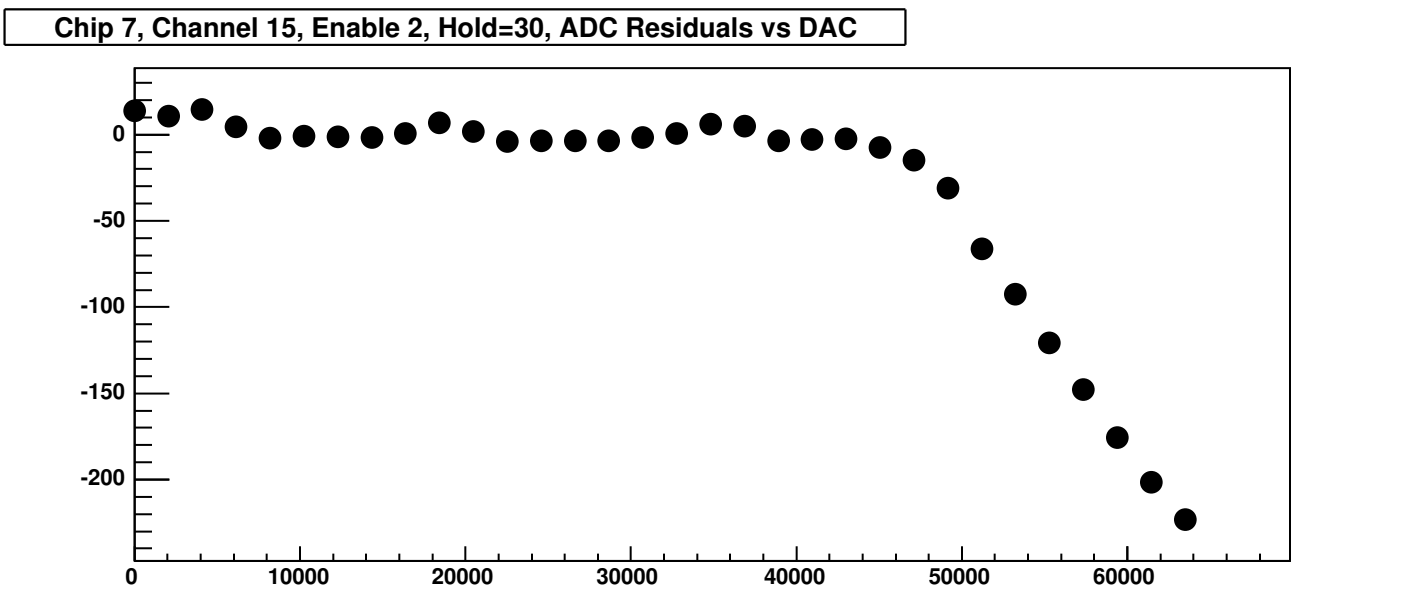

Chip 7, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6659 / 15
p0 918.9 ± 0.1037
p1 -0.0008743 ± 4.216e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

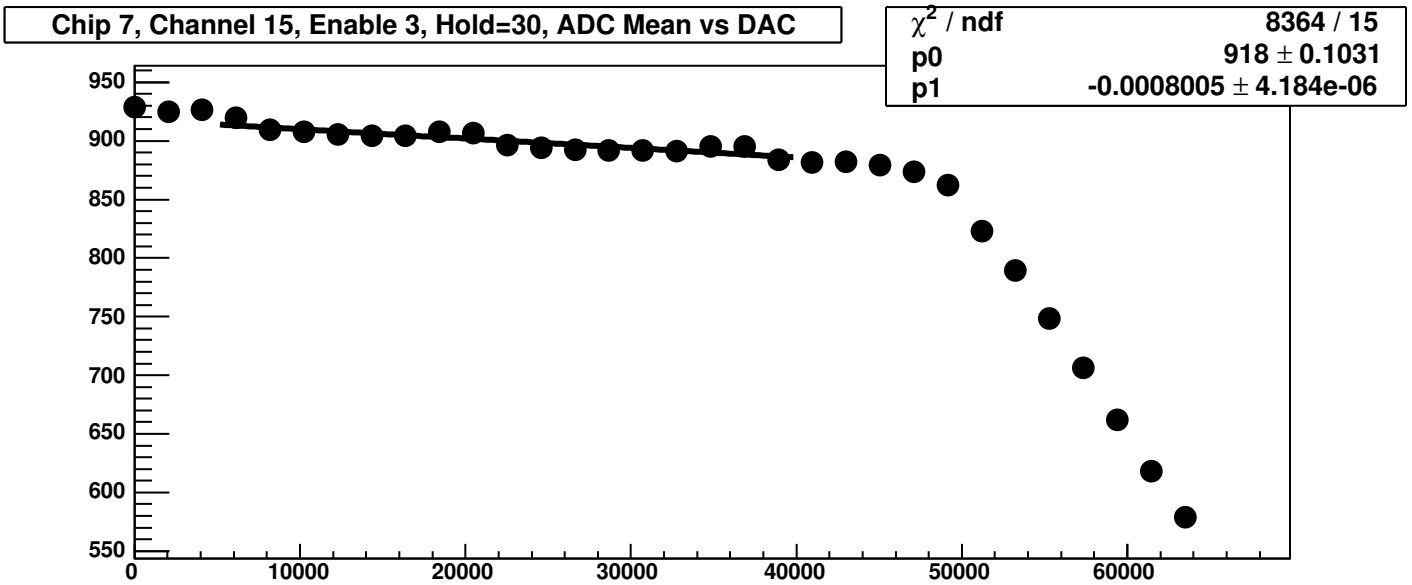

Chip 7, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

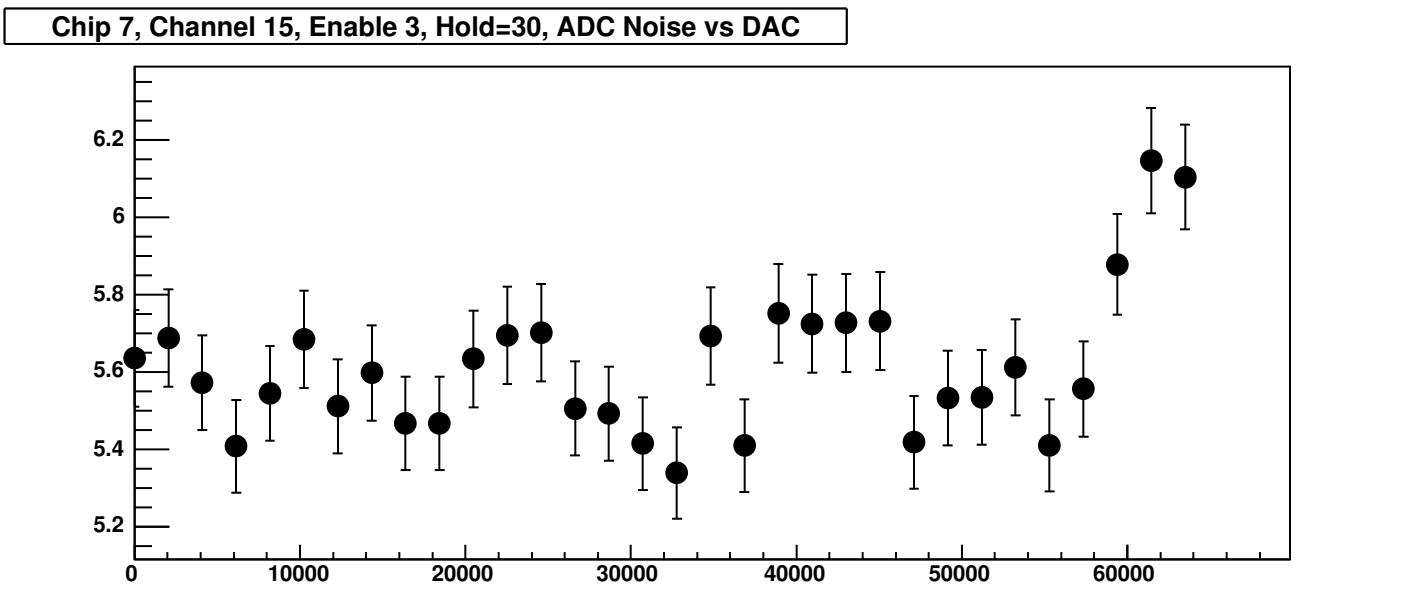

Chip 7, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

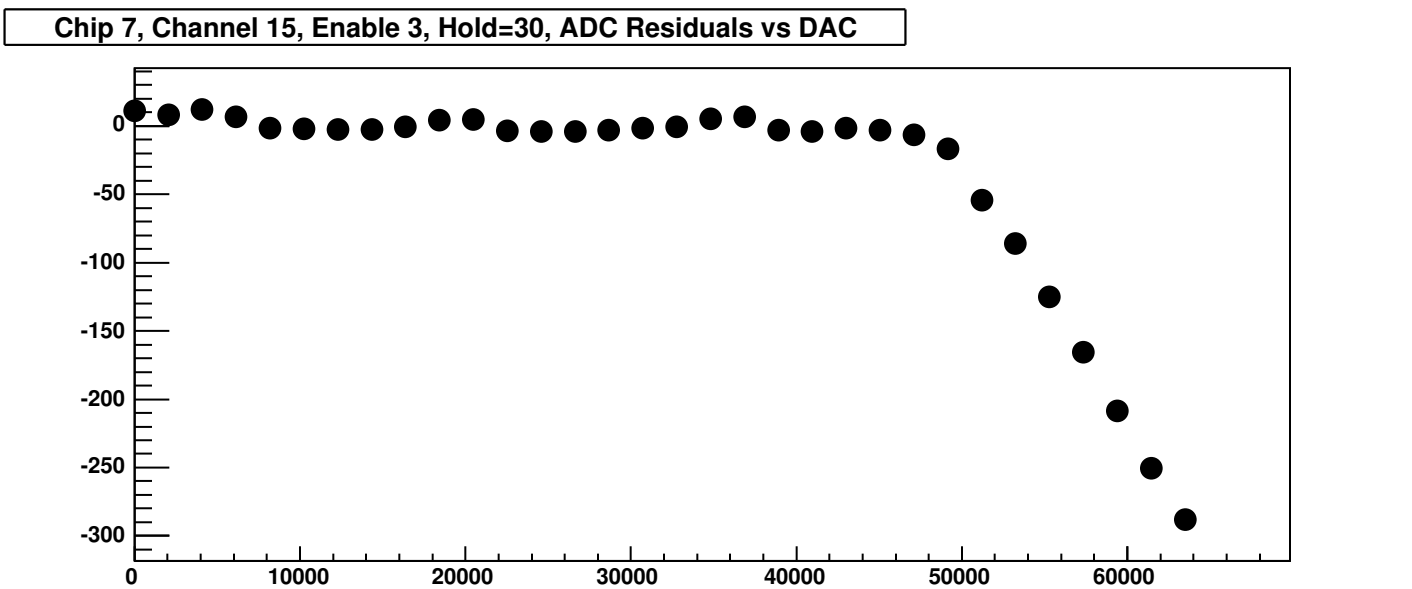

Chip 7, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6511 / 15
p0 915.3 ± 0.1071
p1 -0.0001534 ± 4.367e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

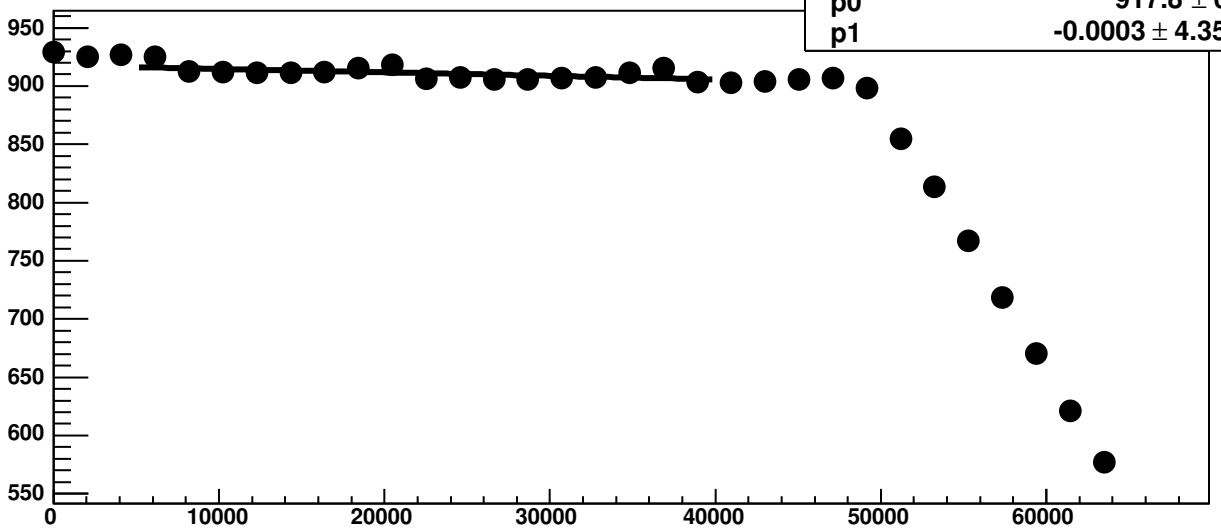

Chip 7, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8364 / 15
p0 918 ± 0.1031
p1 -0.0008005 ± 4.184e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

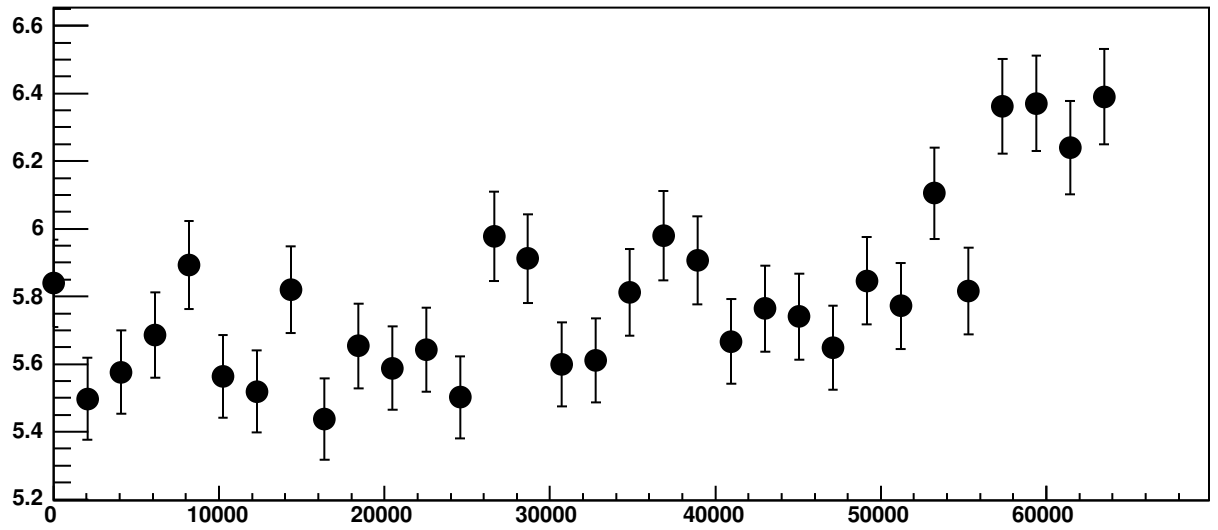

Chip 7, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

Chip 7, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

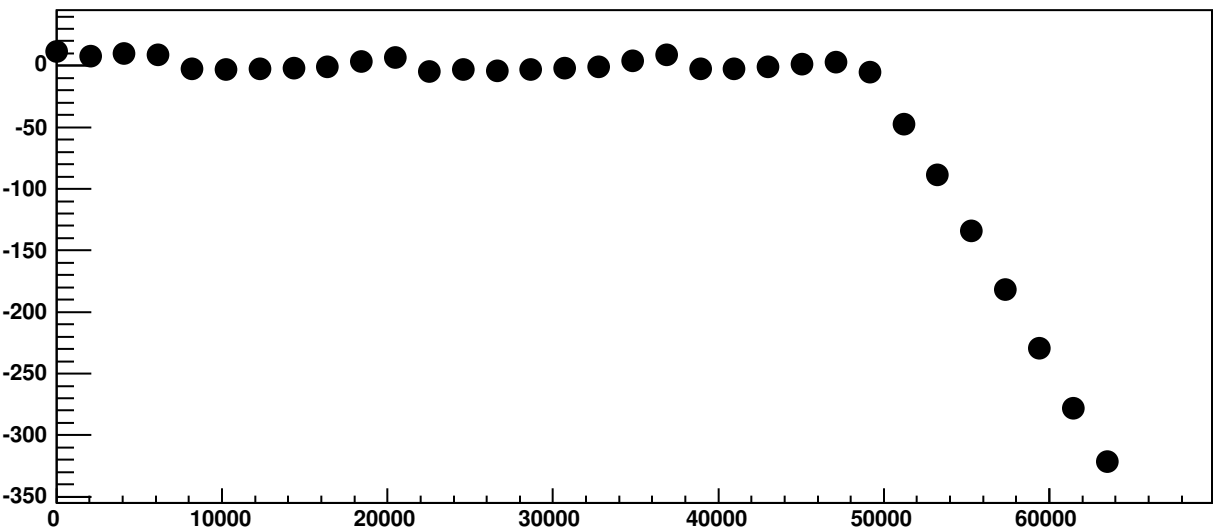
χ^2 / ndf	1.027e+04 / 15
p0	917.8 ± 0.1063
p1	-0.0003 ± 4.355e-06

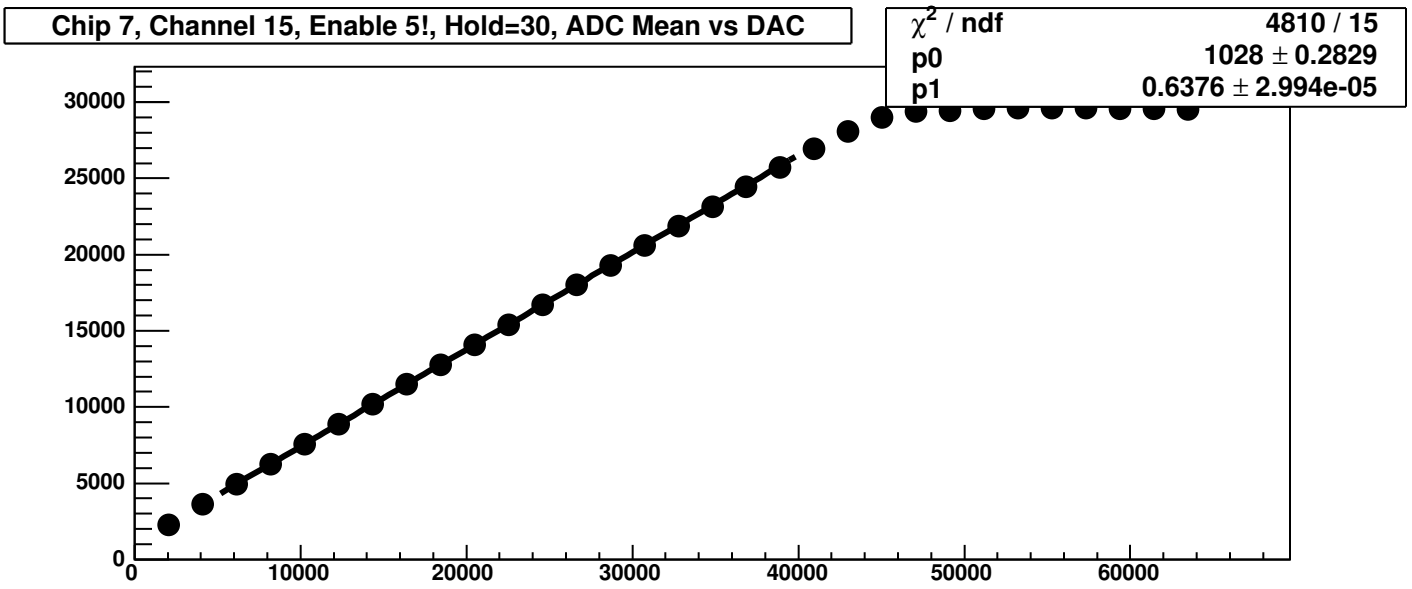


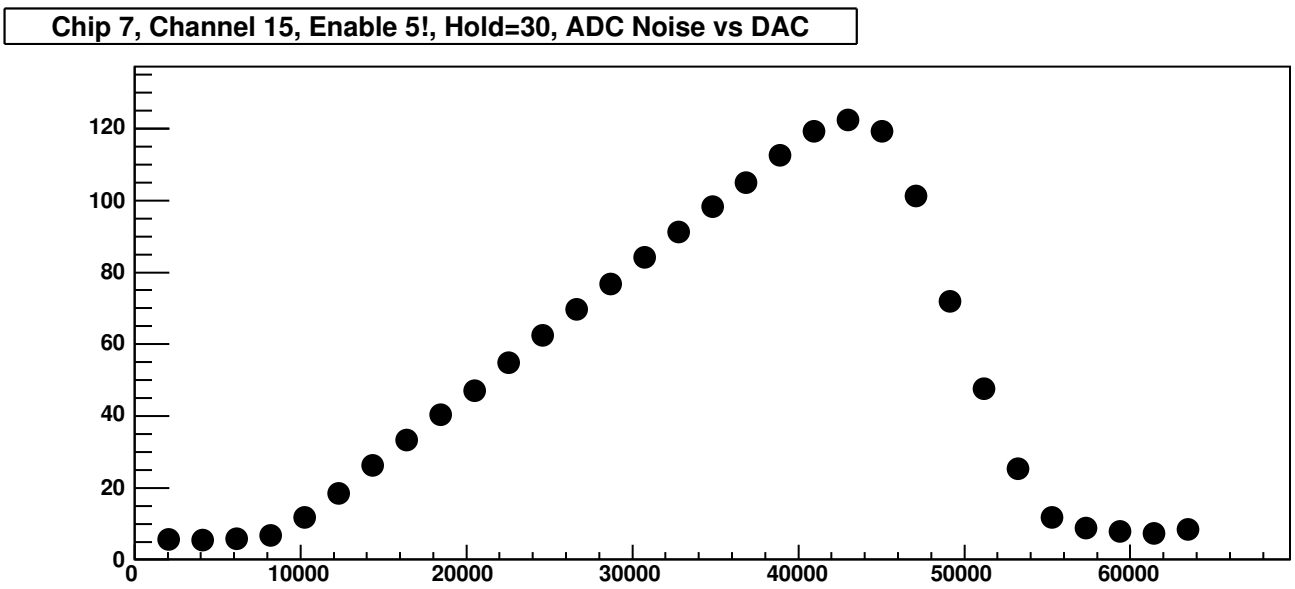
Chip 7, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC

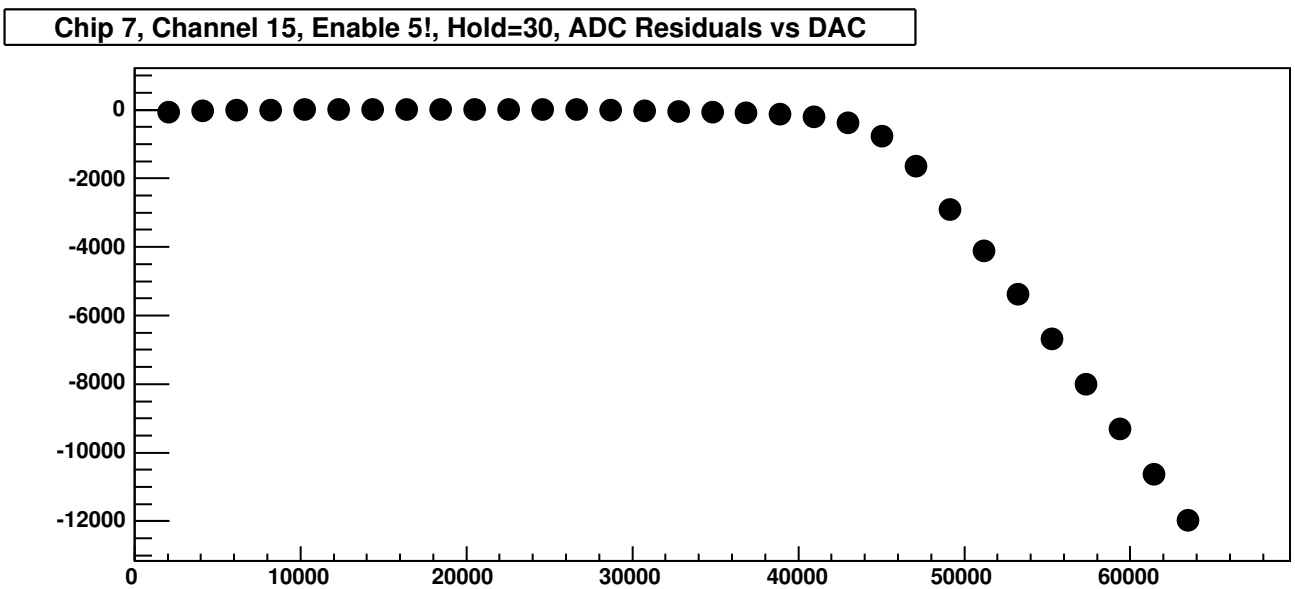


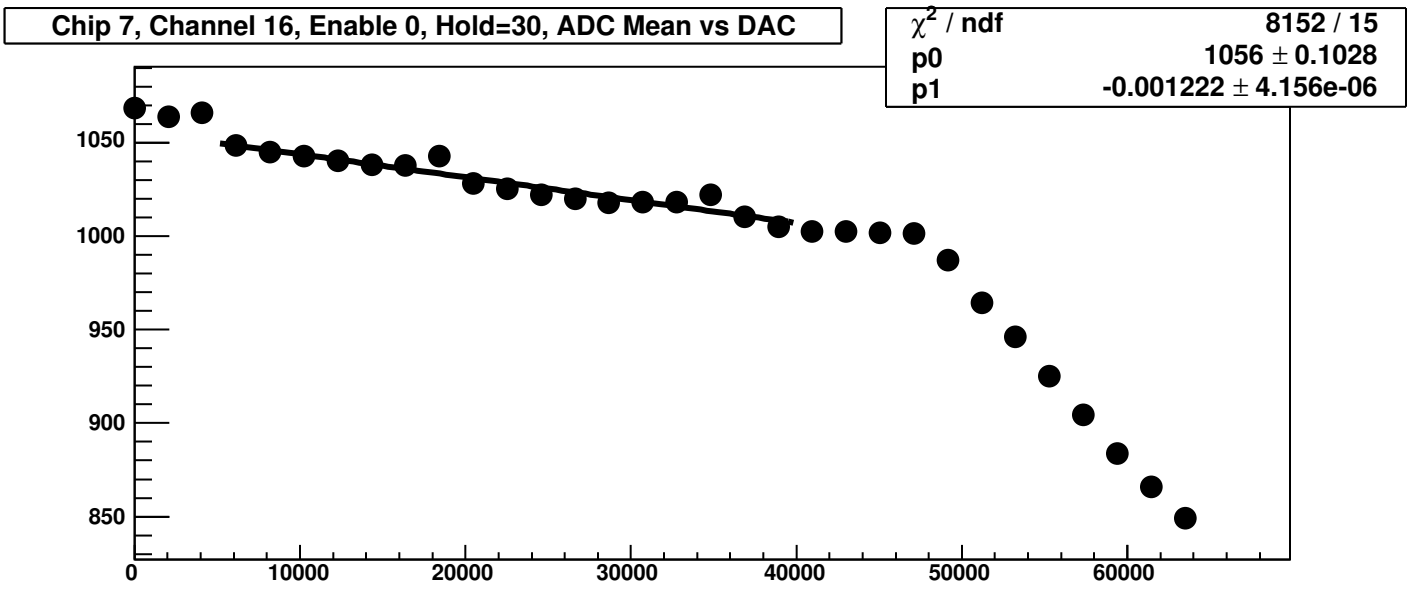
Chip 7, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

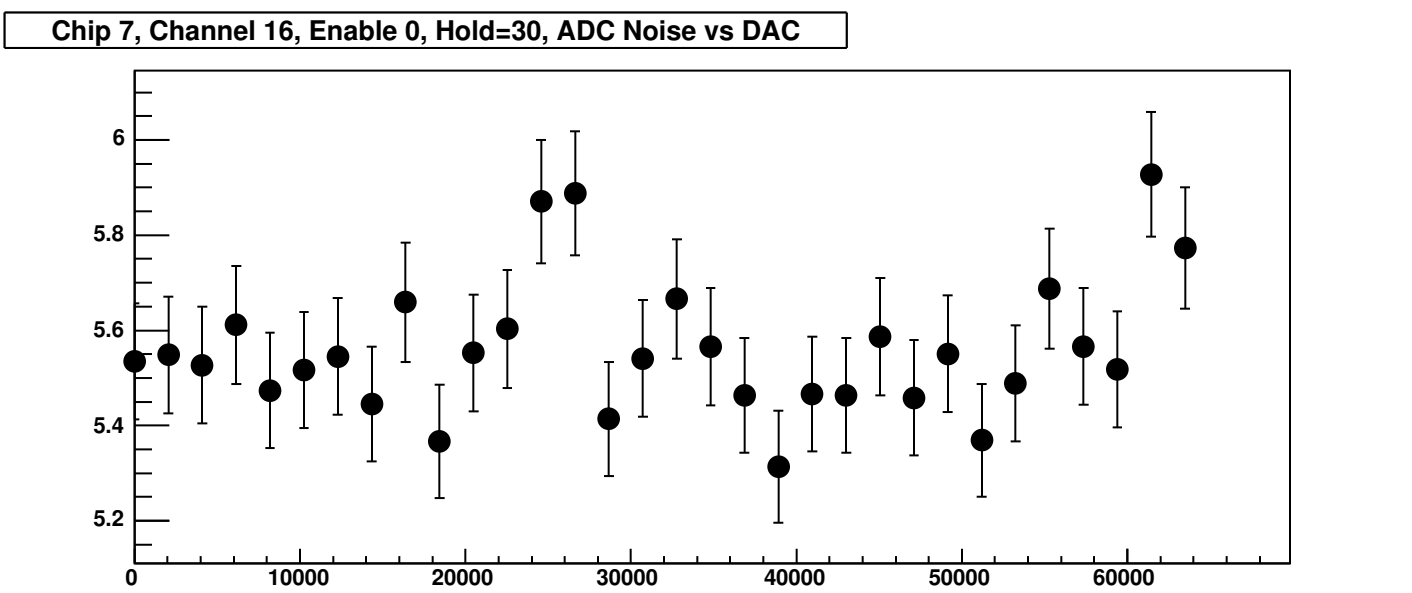


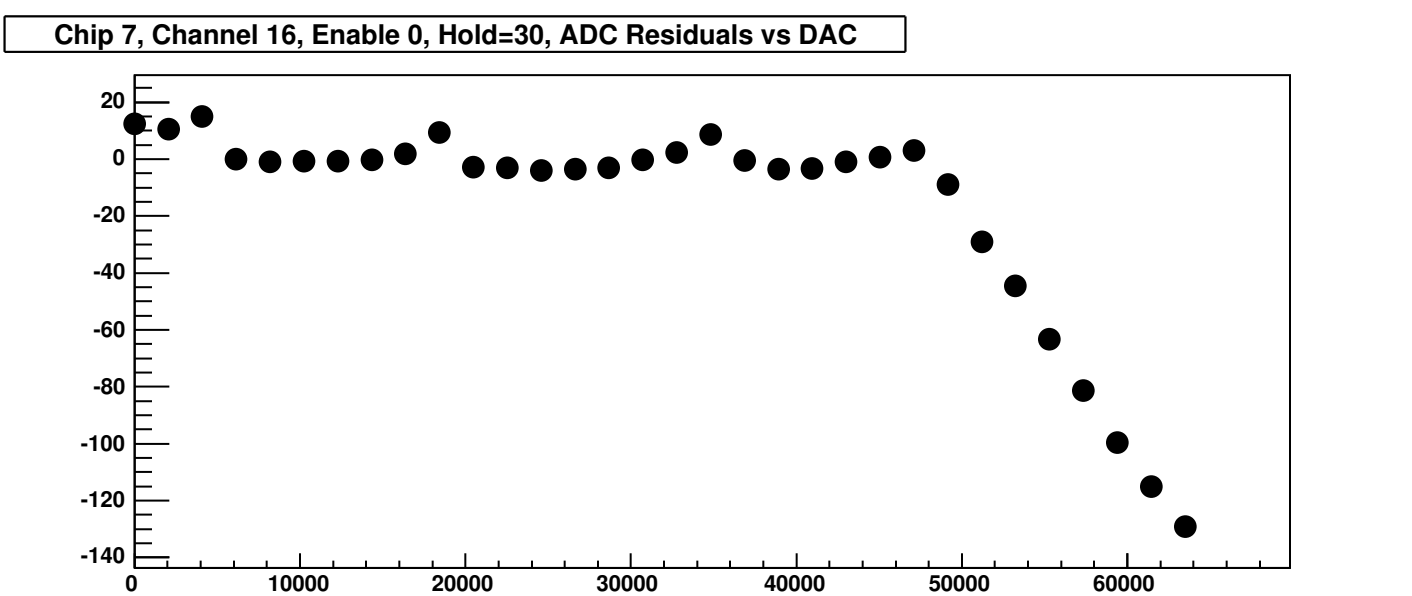

Chip 7, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 4810 / 15
p0 1028 ± 0.2829
p1 0.6376 ± 2.994e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

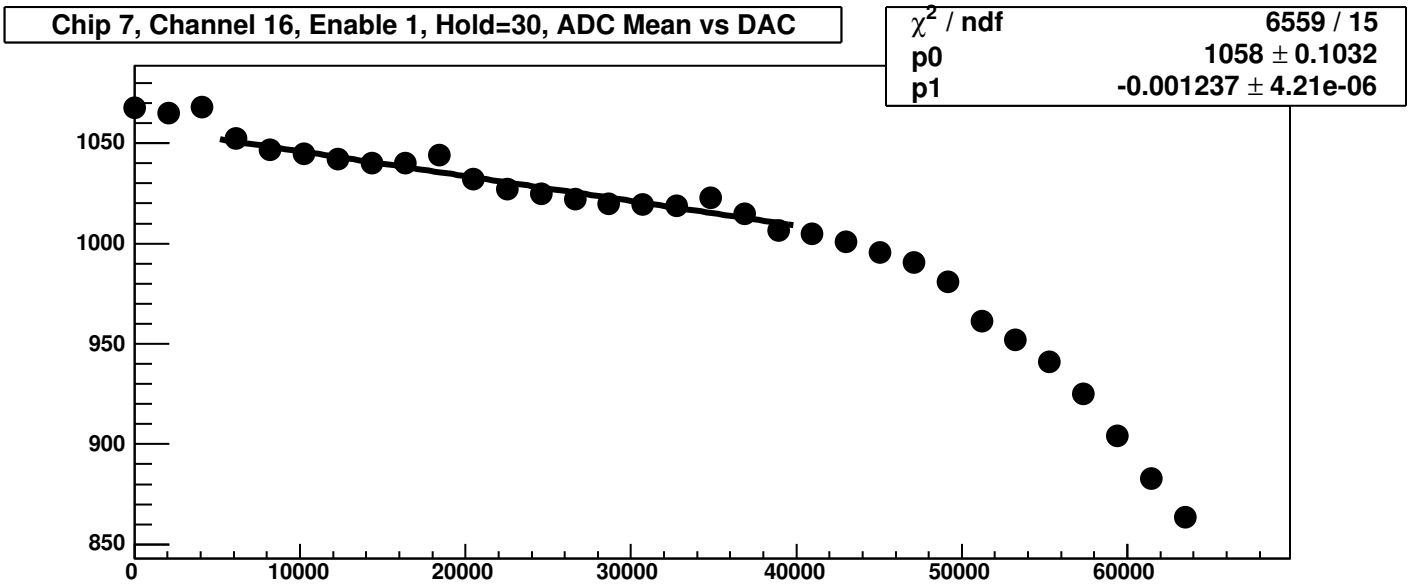

Chip 7, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

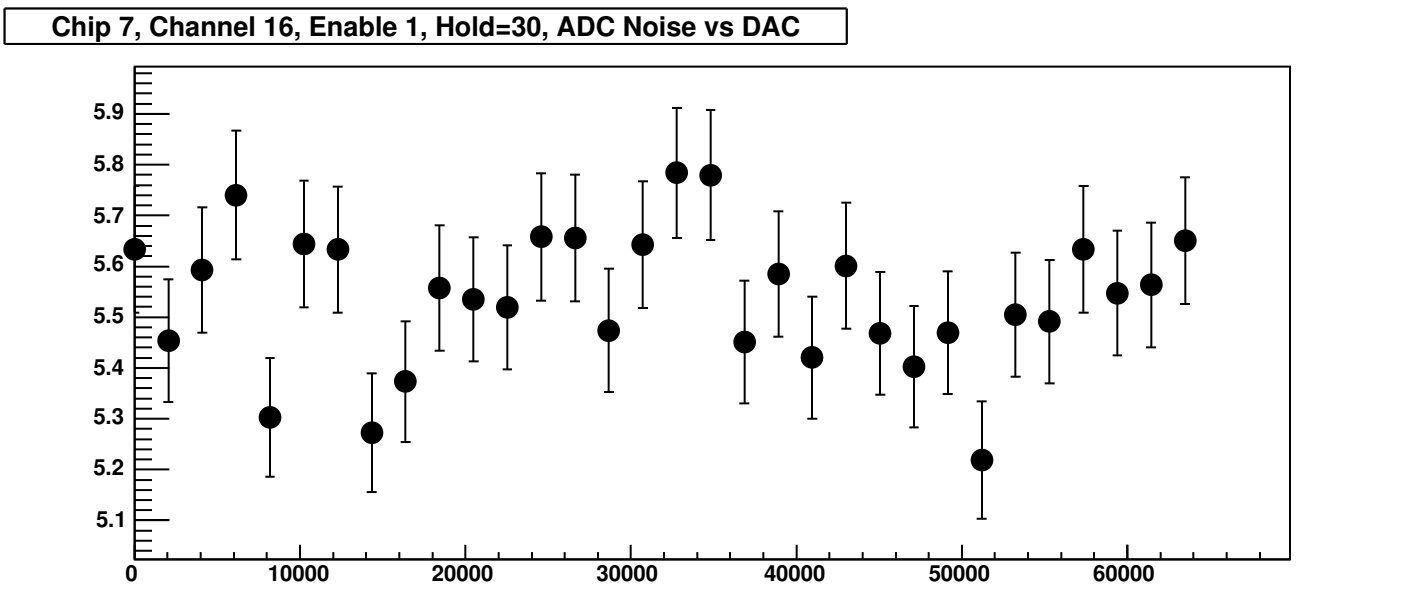

Chip 7, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

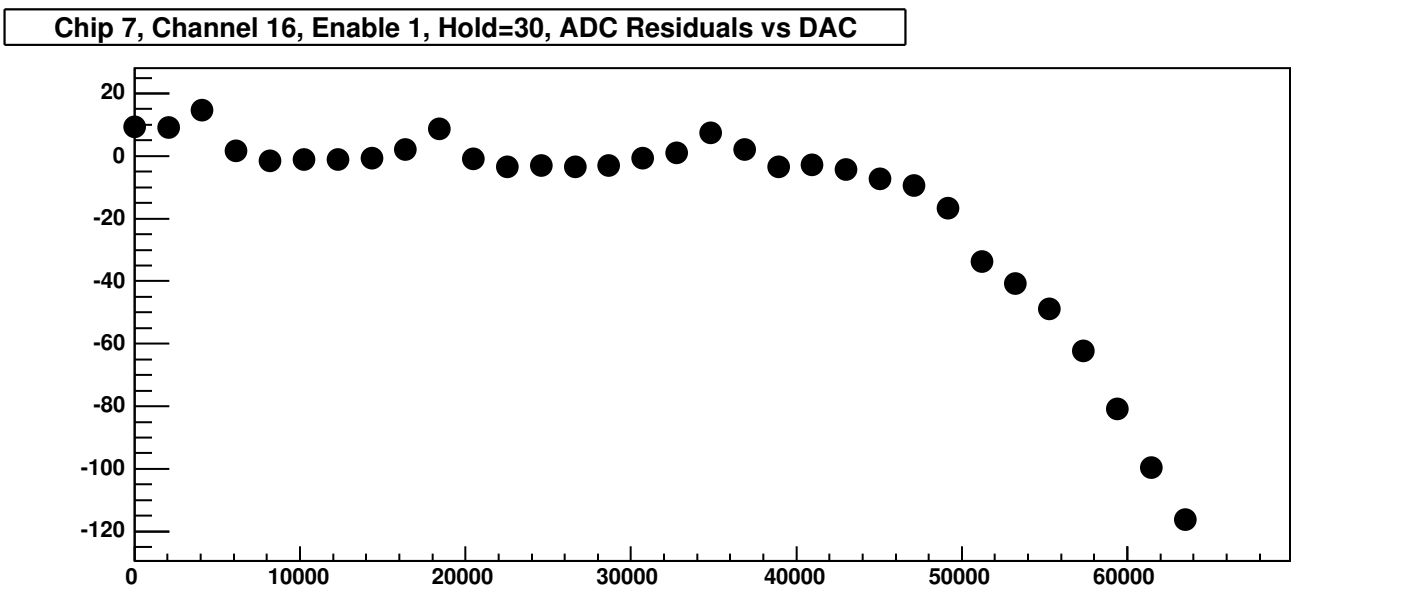

Chip 7, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8152 / 15
p0 1056 ± 0.1028
p1 -0.001222 ± 4.156e-06
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

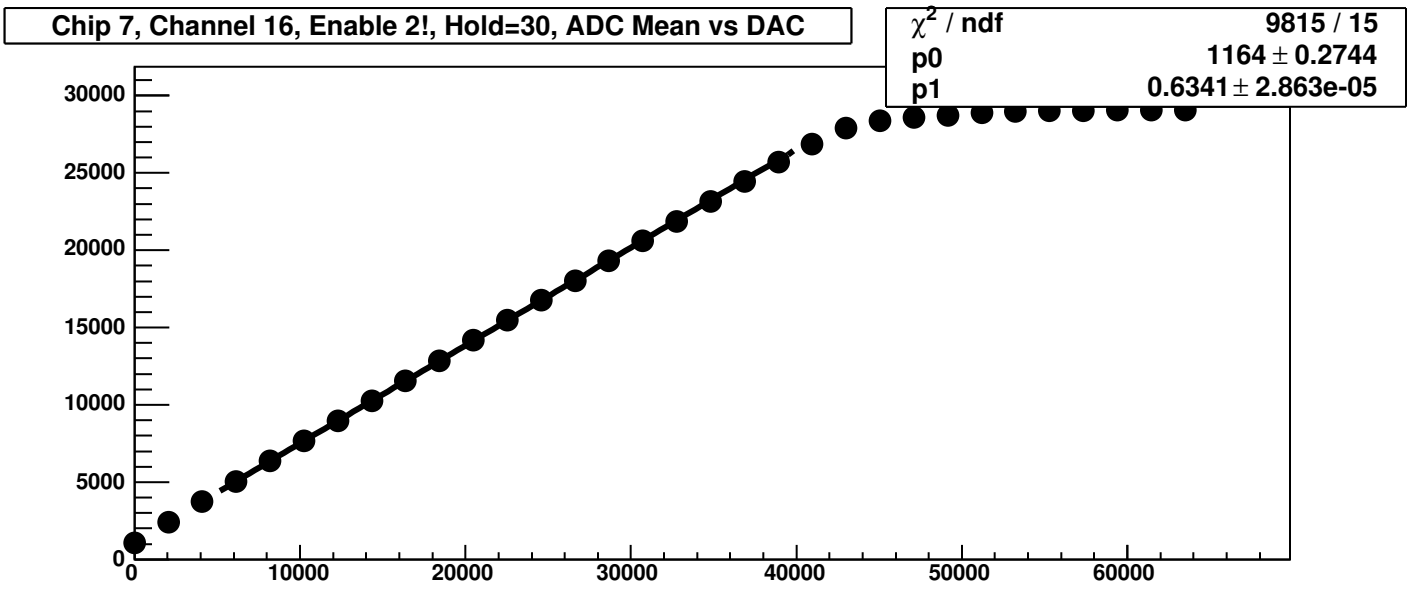

Chip 7, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

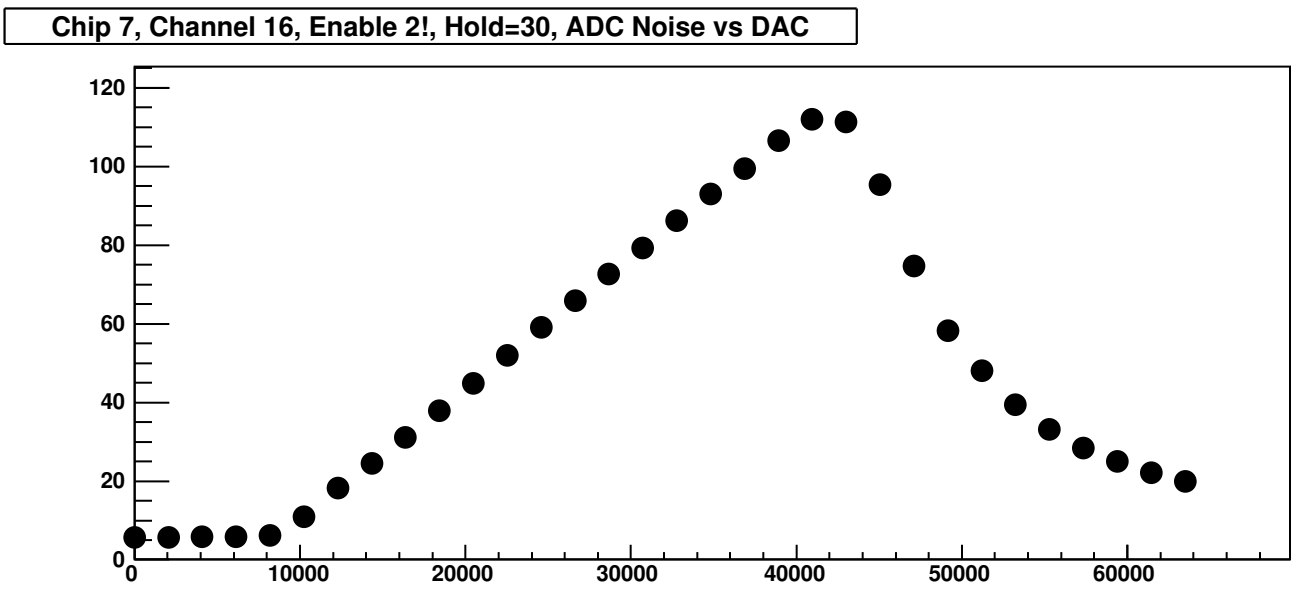

Chip 7, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

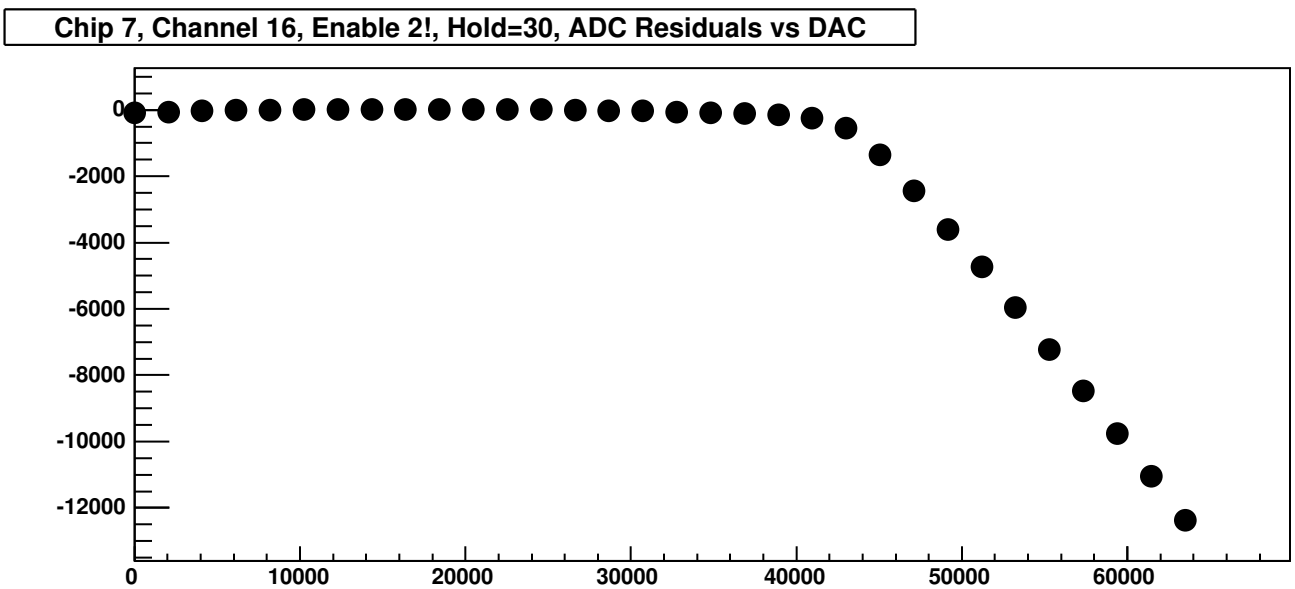

Chip 7, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6559 / 15
p0 1058 ± 0.1032
p1 -0.001237 ± 4.21e-06
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

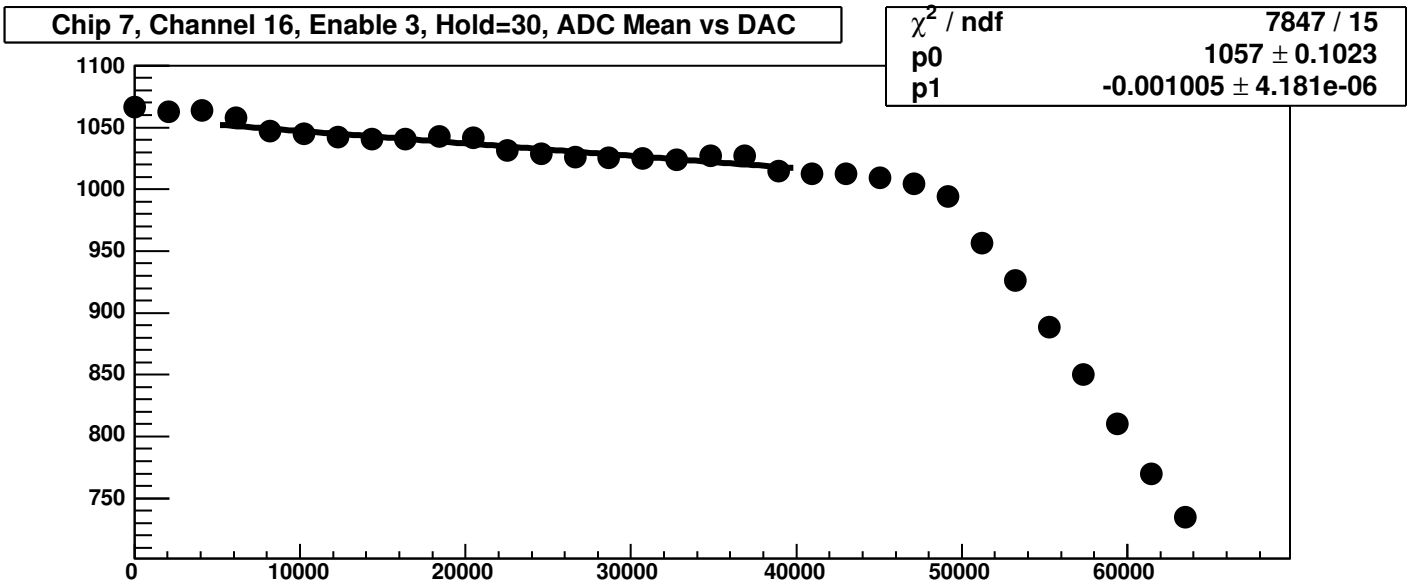

Chip 7, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
0
10000
20000
30000
40000
50000
60000

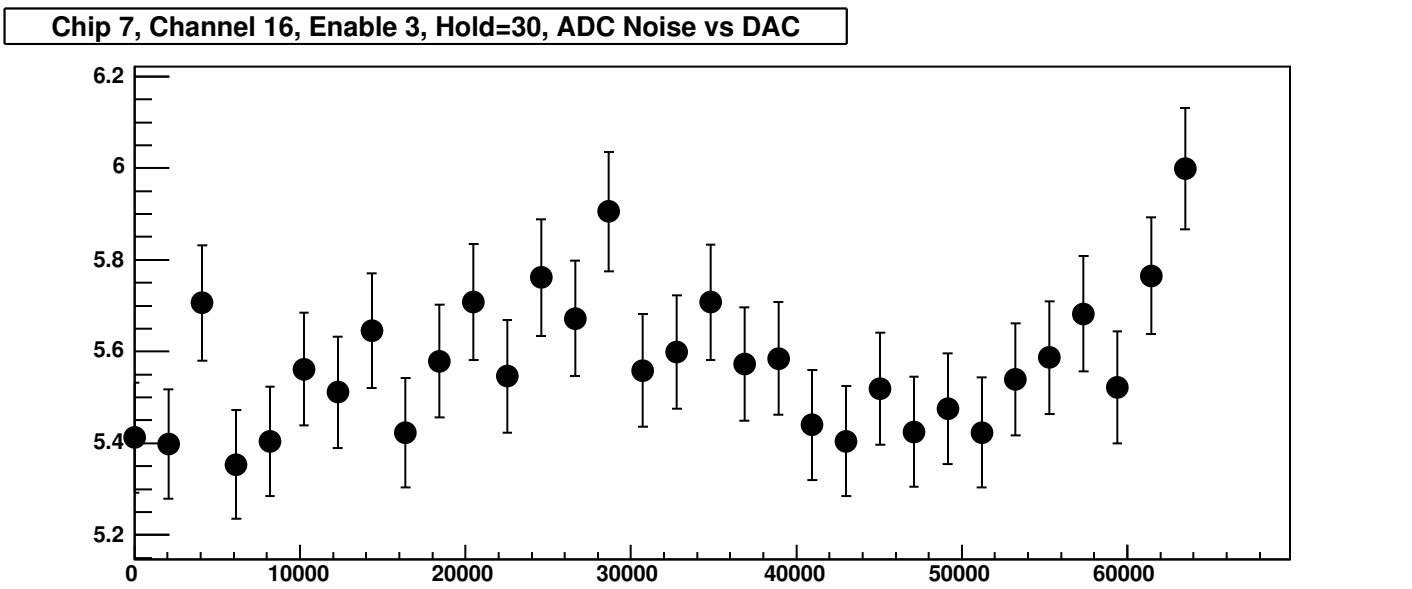

Chip 7, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

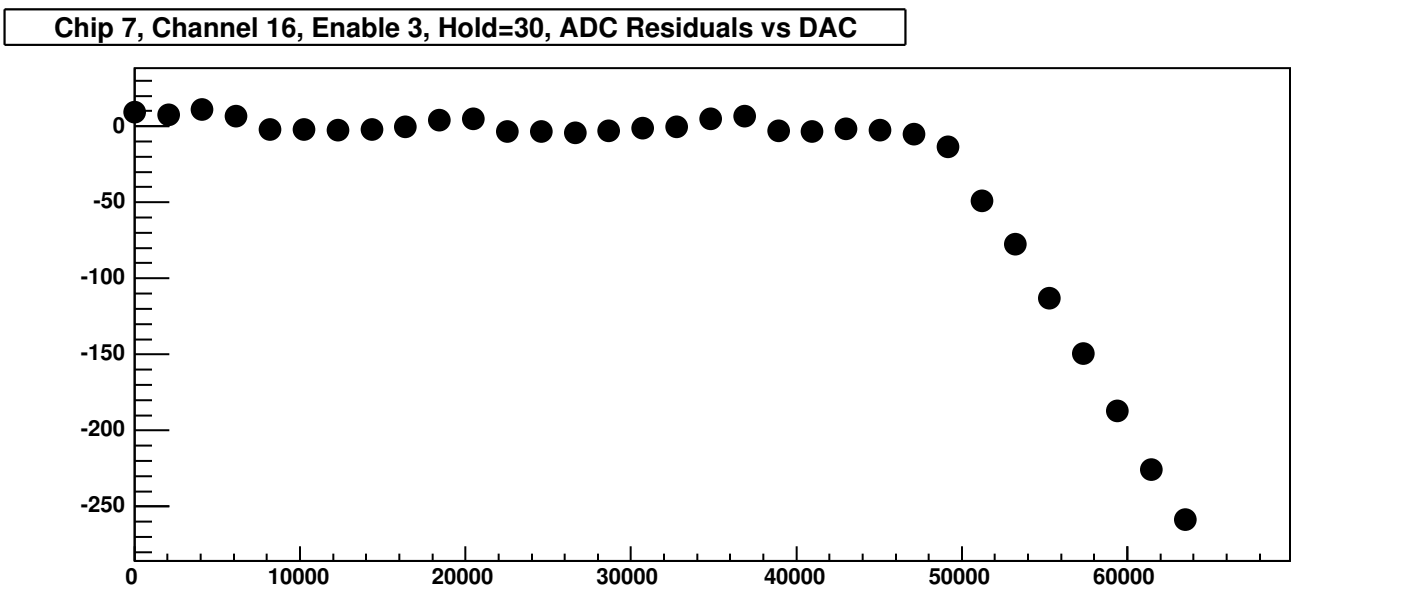

Chip 7, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 9815 / 15
p0 1164 ± 0.2744
p1 0.6341 ± 2.863e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

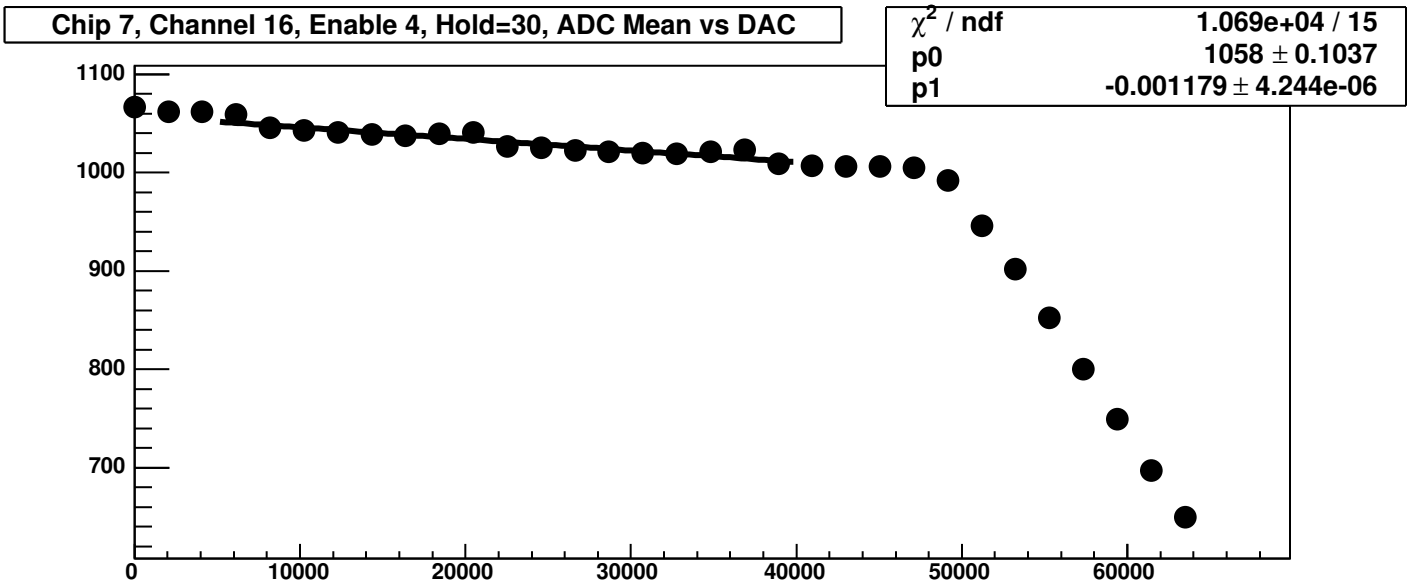

Chip 7, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

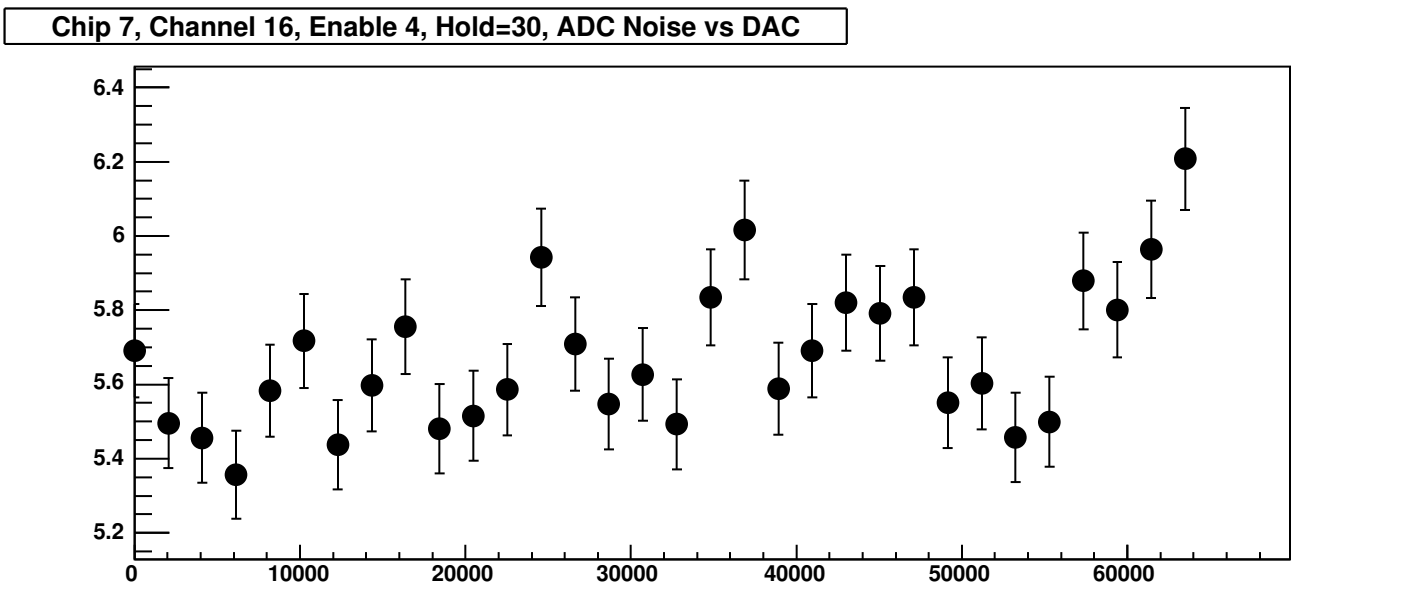

Chip 7, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

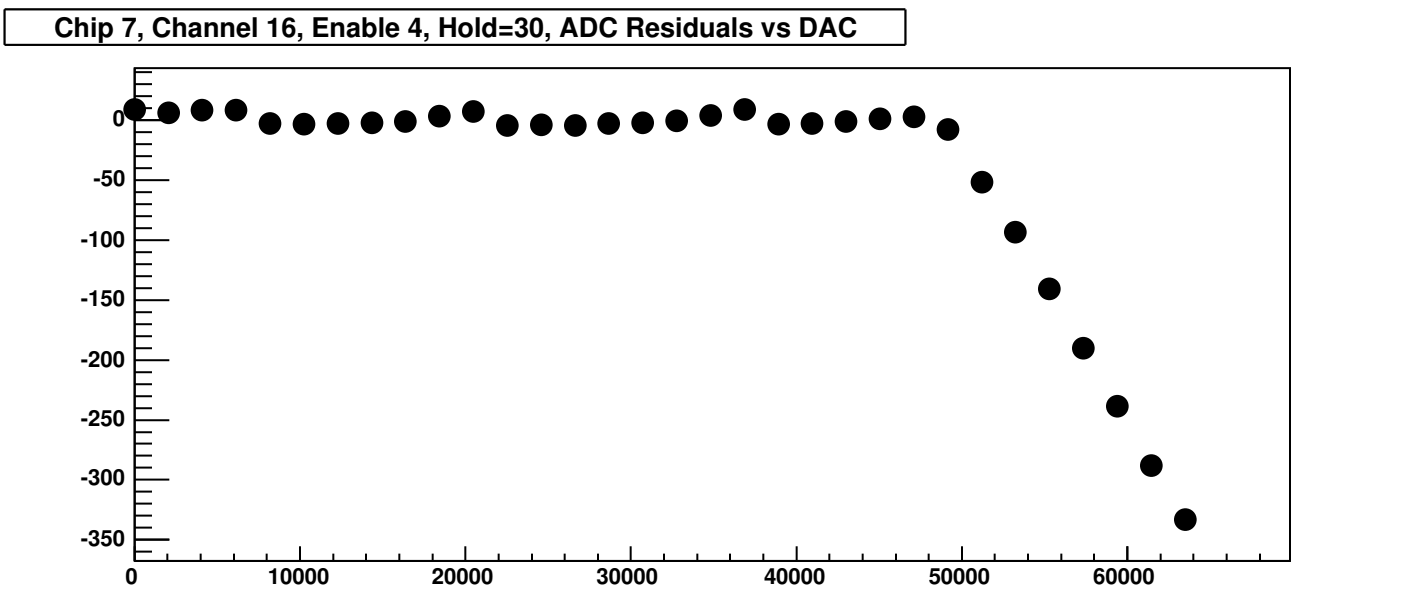

Chip 7, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7847 / 15
p0 1057 ± 0.1023
p1 -0.001005 ± 4.181e-06
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

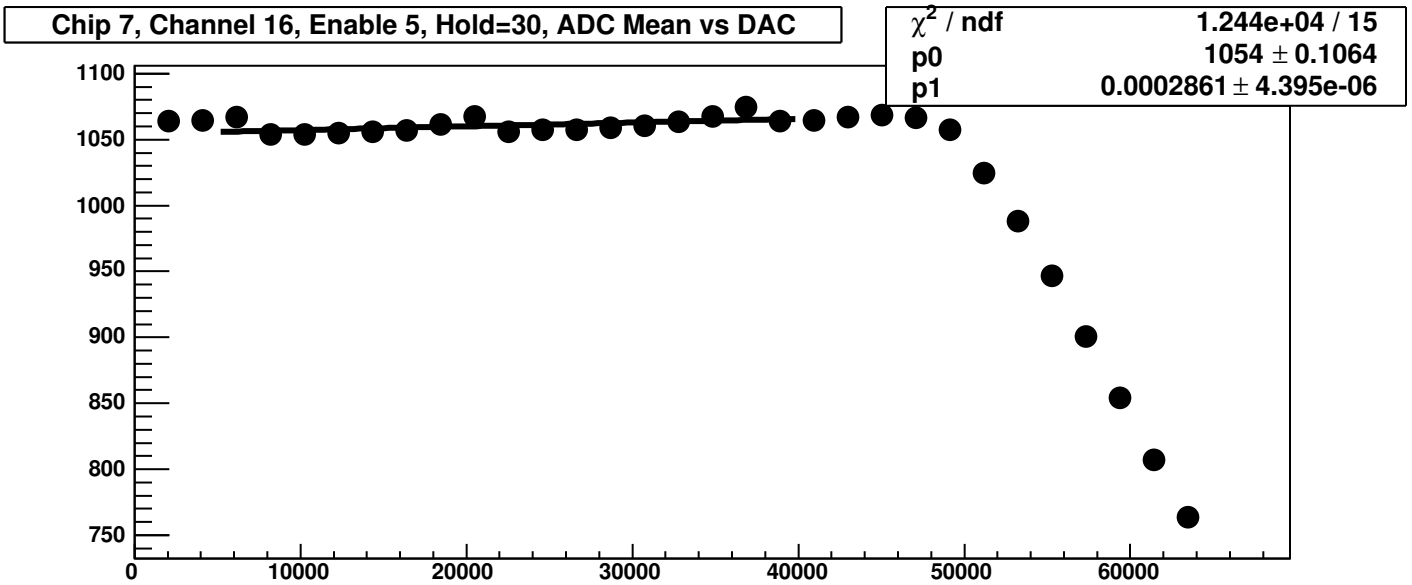

Chip 7, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

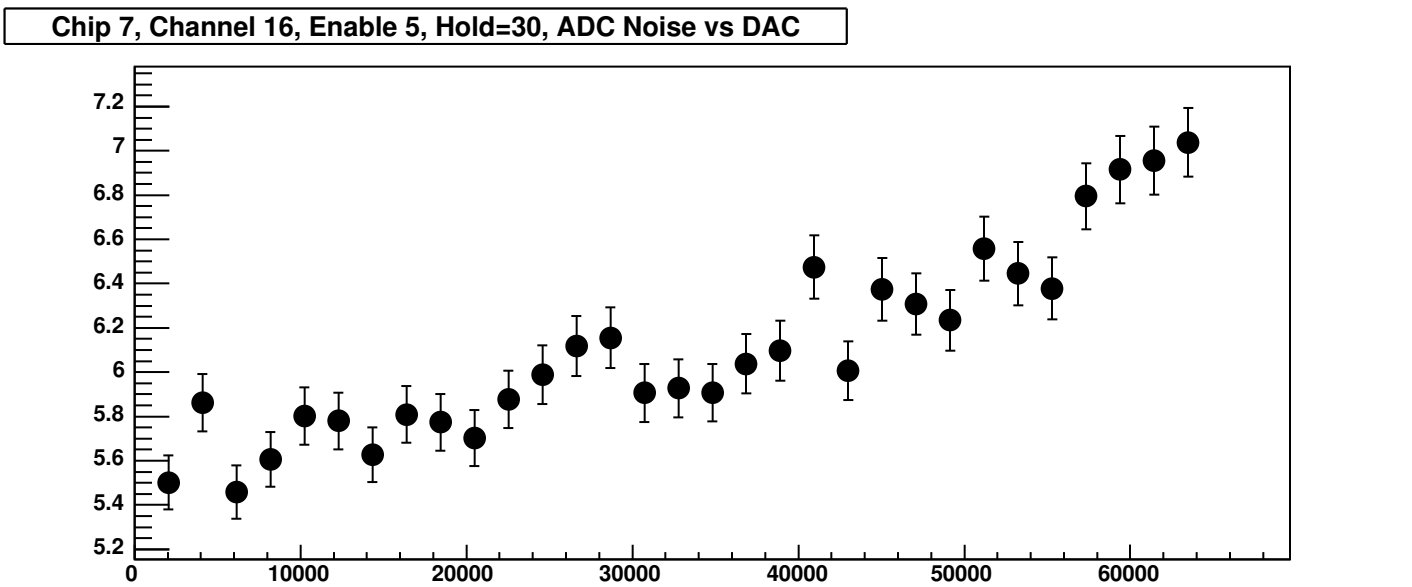

Chip 7, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

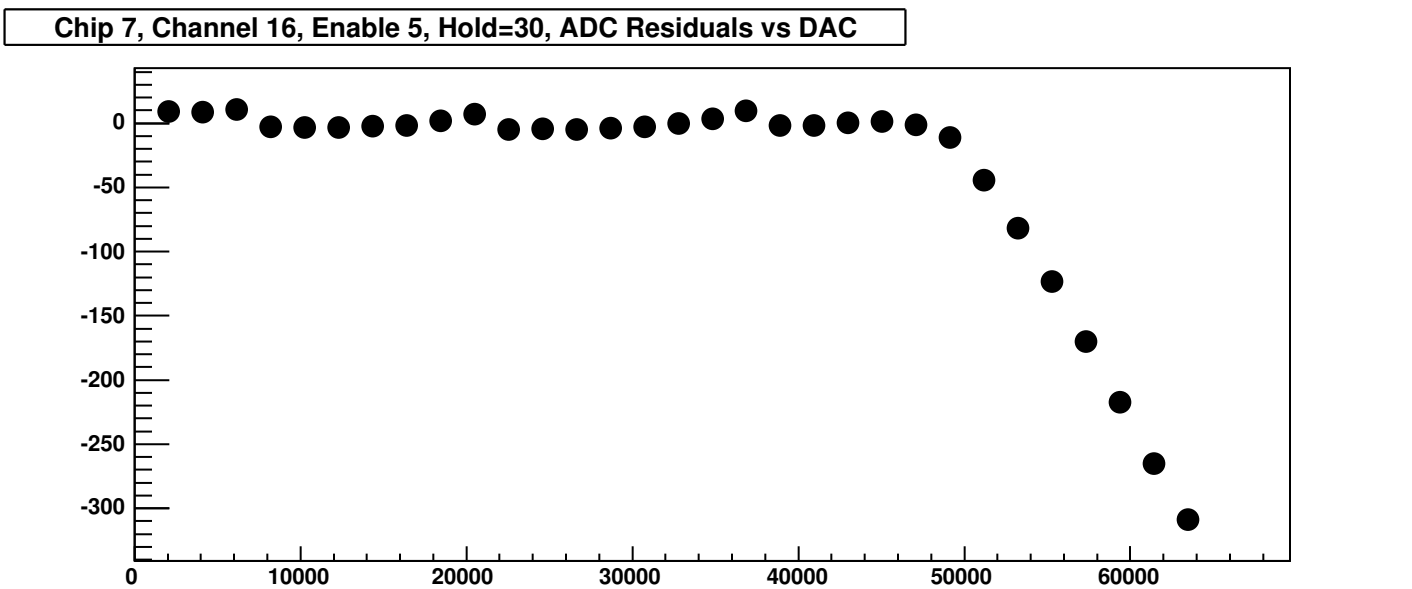

Chip 7, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.069e+04 / 15
p0 1058 ± 0.1037
p1 -0.001179 ± 4.244e-06
1100
1000
900
800
700
0
10000
20000
30000
40000
50000
60000

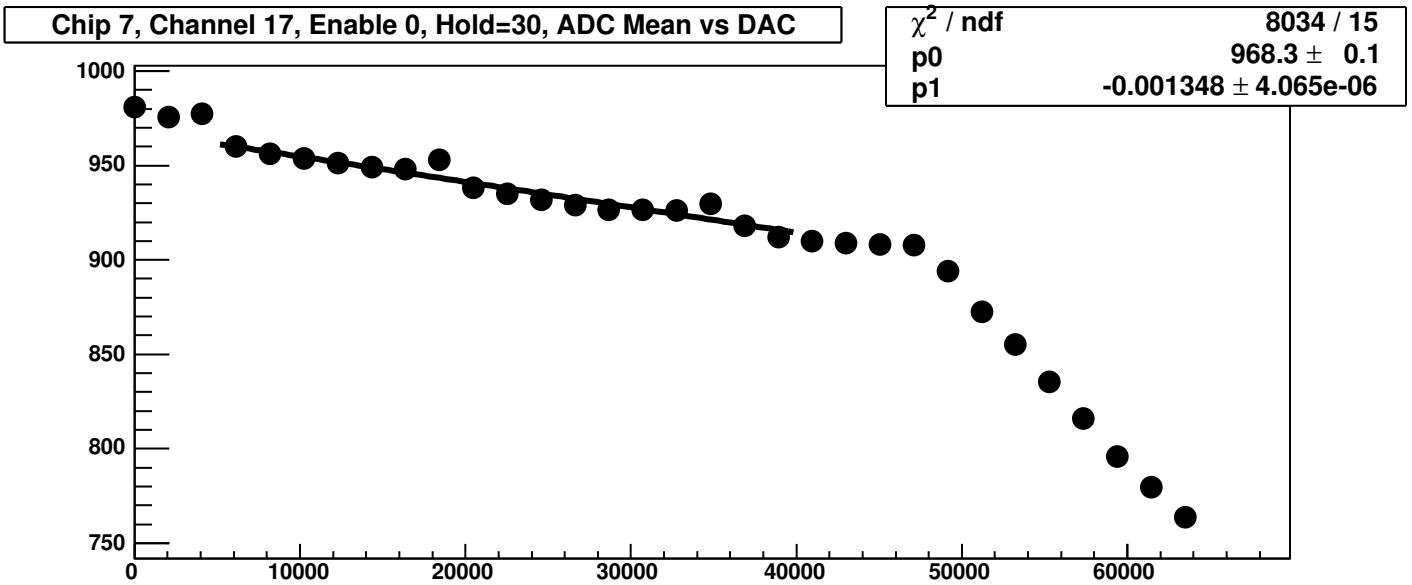

Chip 7, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

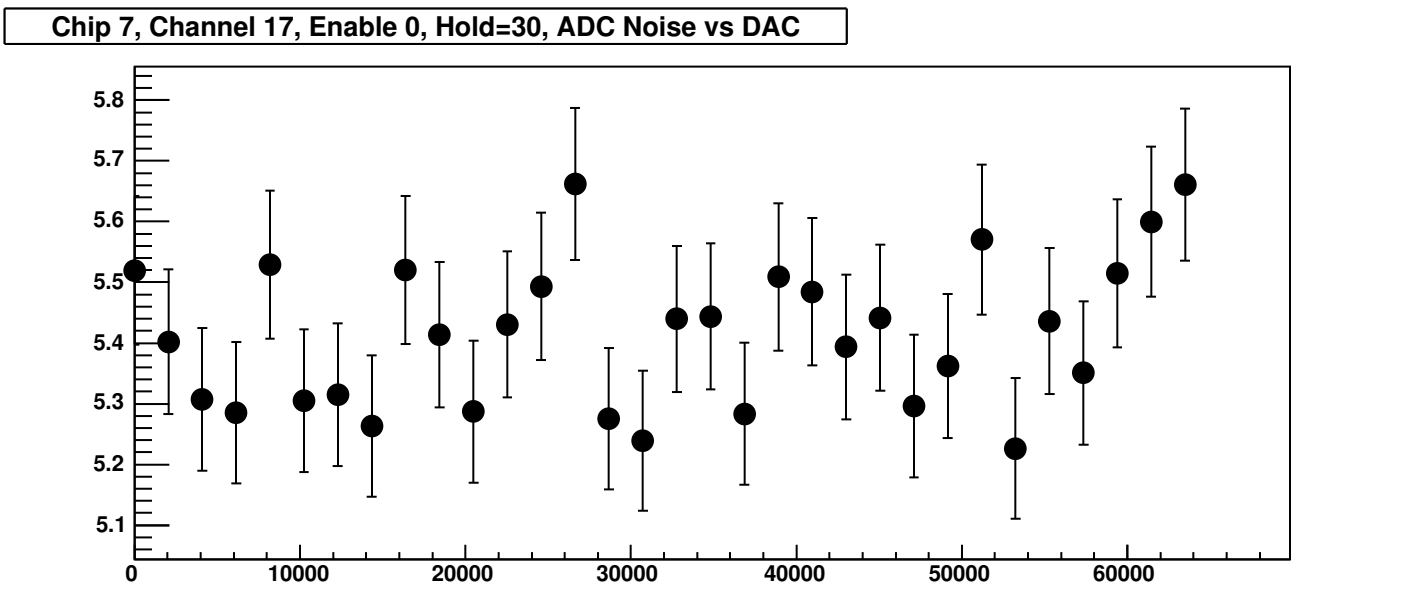

Chip 7, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

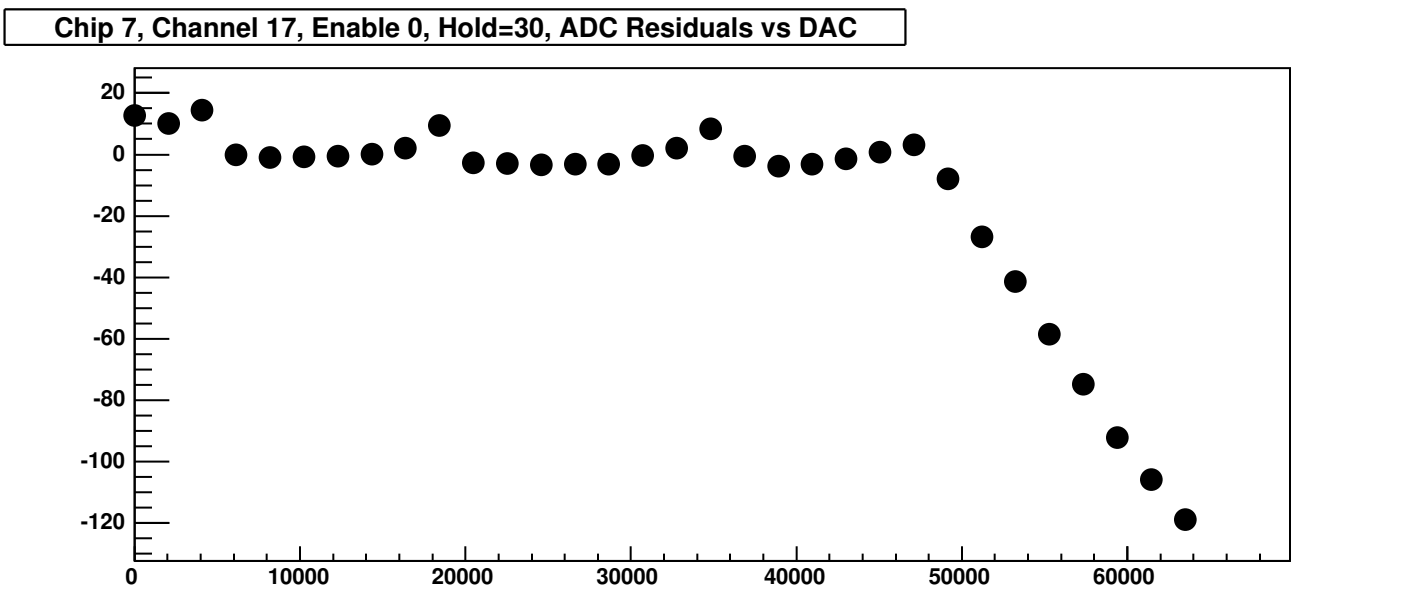

Chip 7, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 1.244e+04 / 15
p0 1054 ± 0.1064
p1 0.0002861 ± 4.395e-06
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

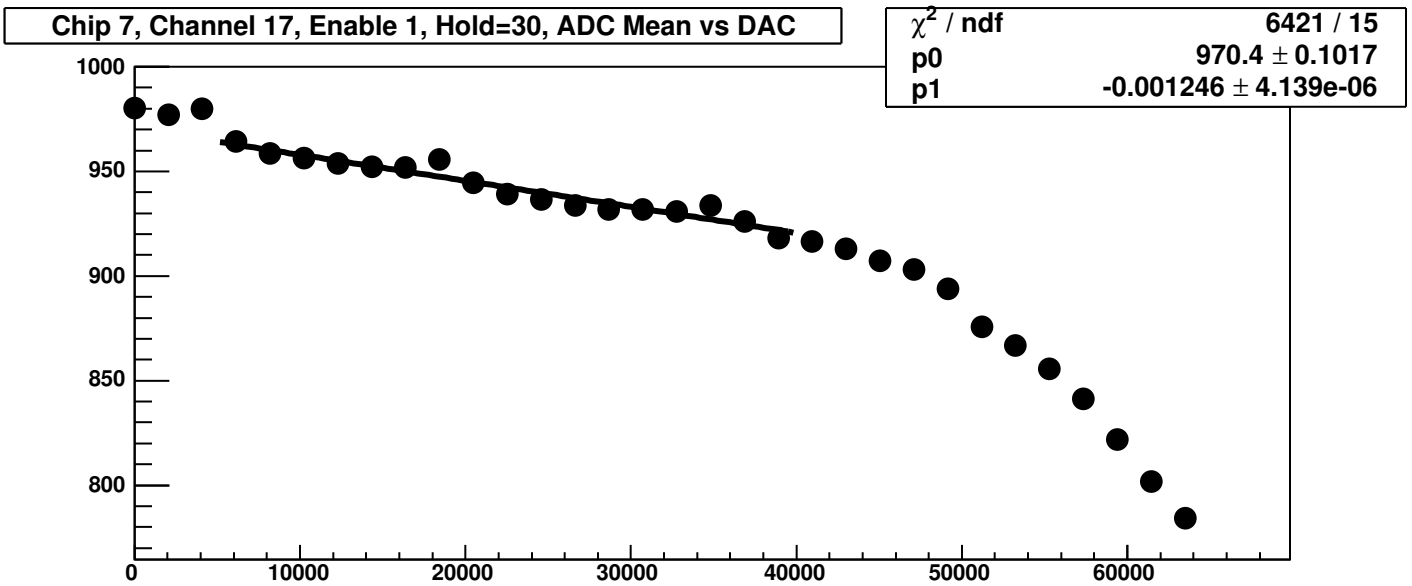

Chip 7, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

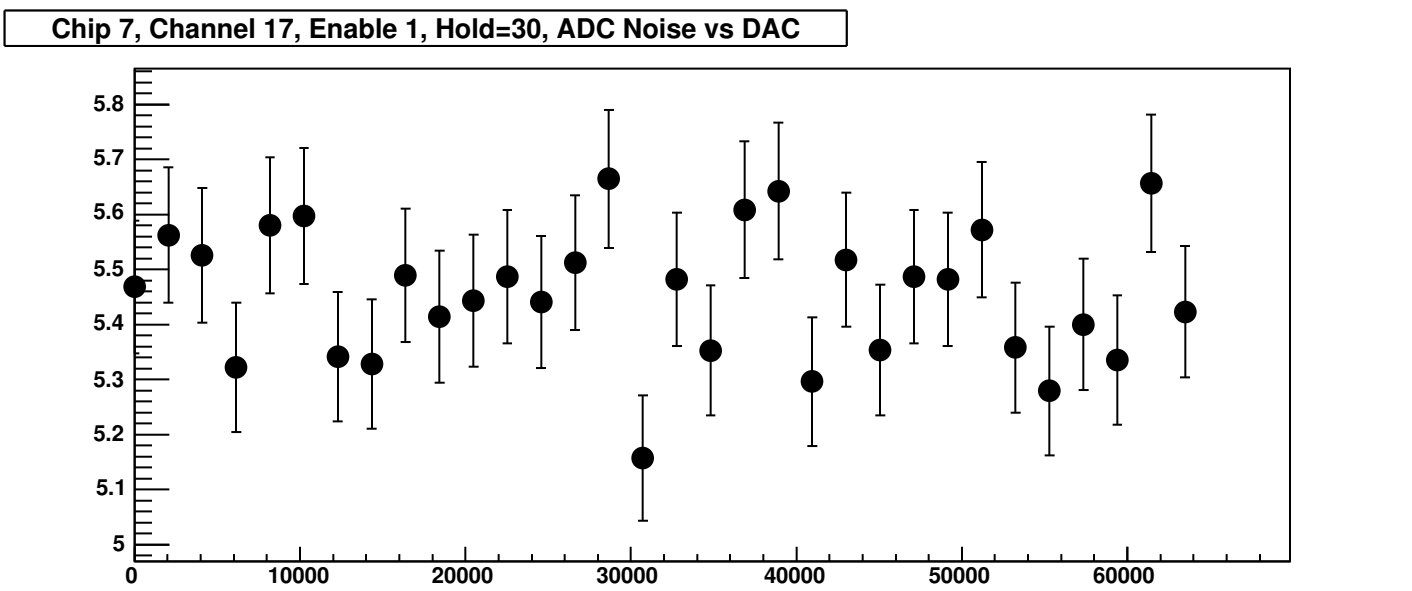

Chip 7, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

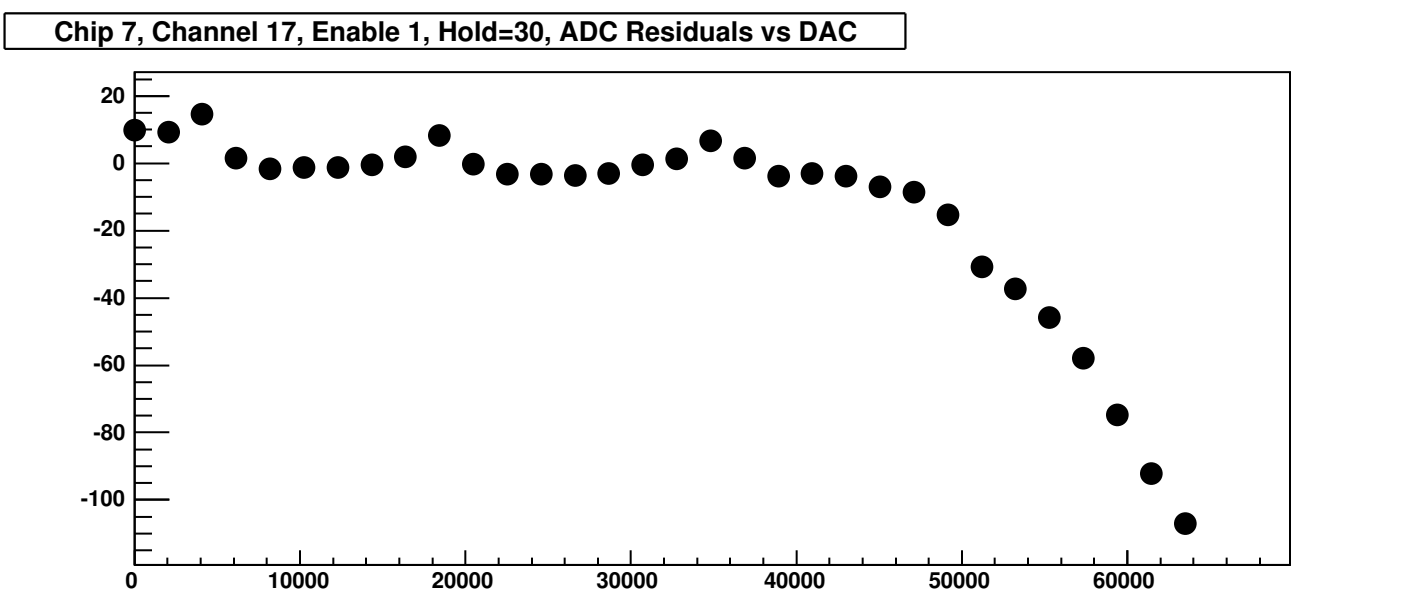

Chip 7, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8034 / 15
p0 968.3 ± 0.1
p1 -0.001348 ± 4.065e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
0
10000
20000
30000
40000
50000
60000

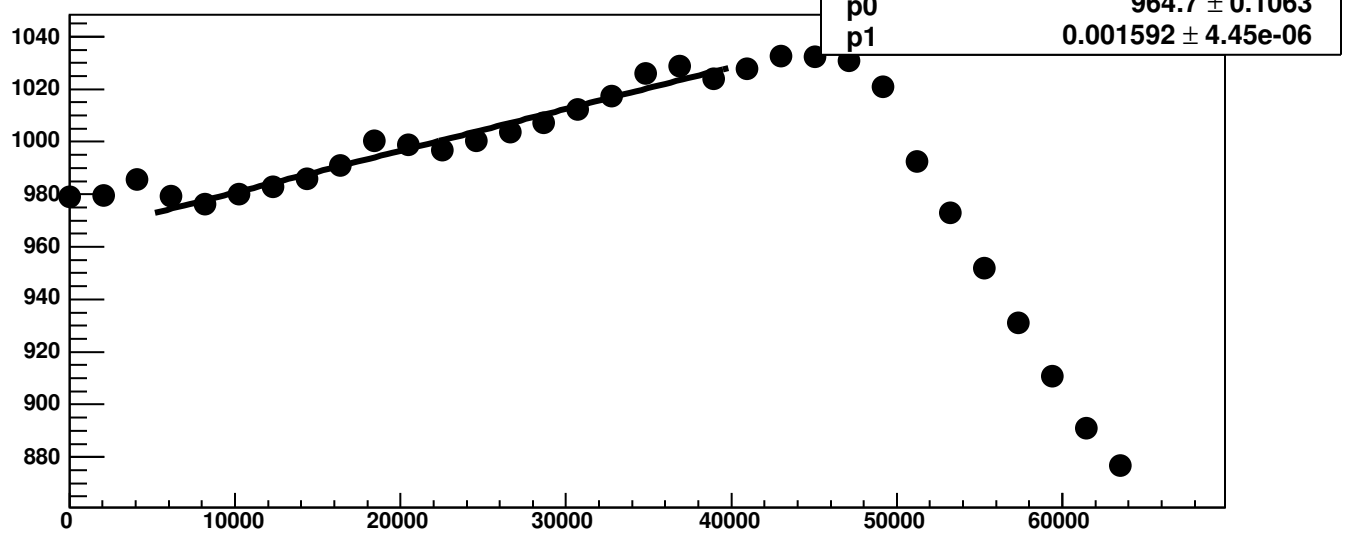

Chip 7, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6421 / 15
p0 970.4 ± 0.1017
p1 -0.001246 ± 4.139e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

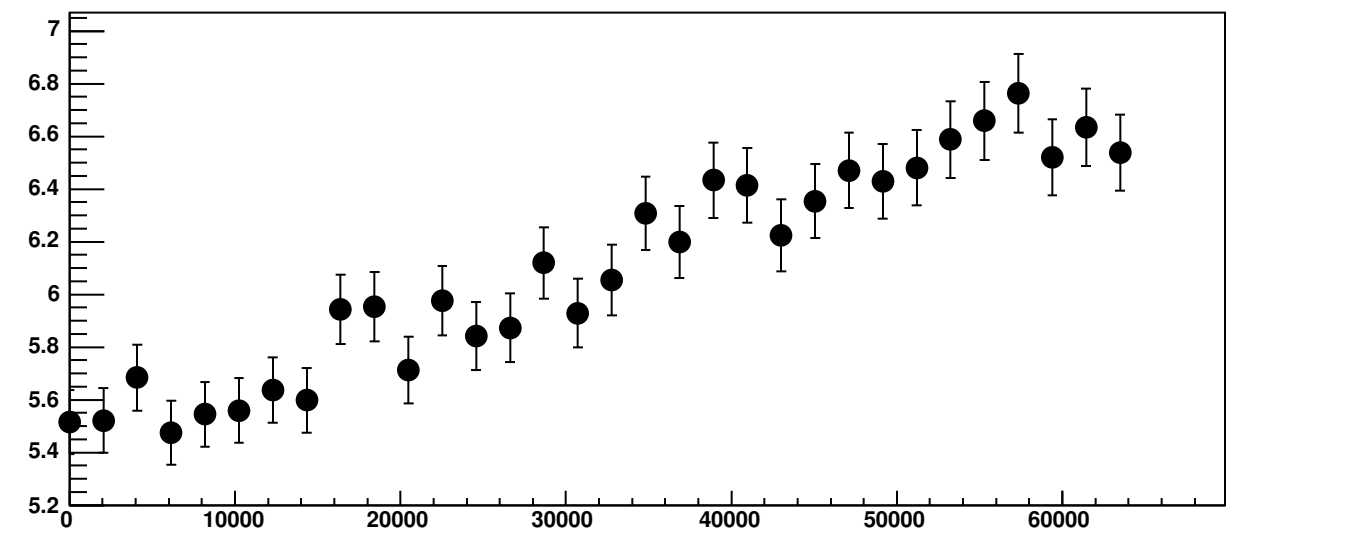

Chip 7, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
5.8
5.7
5.6
5.5
5.4
5.3
5.2
5.1
5
0
10000
20000
30000
40000
50000
60000


Chip 7, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

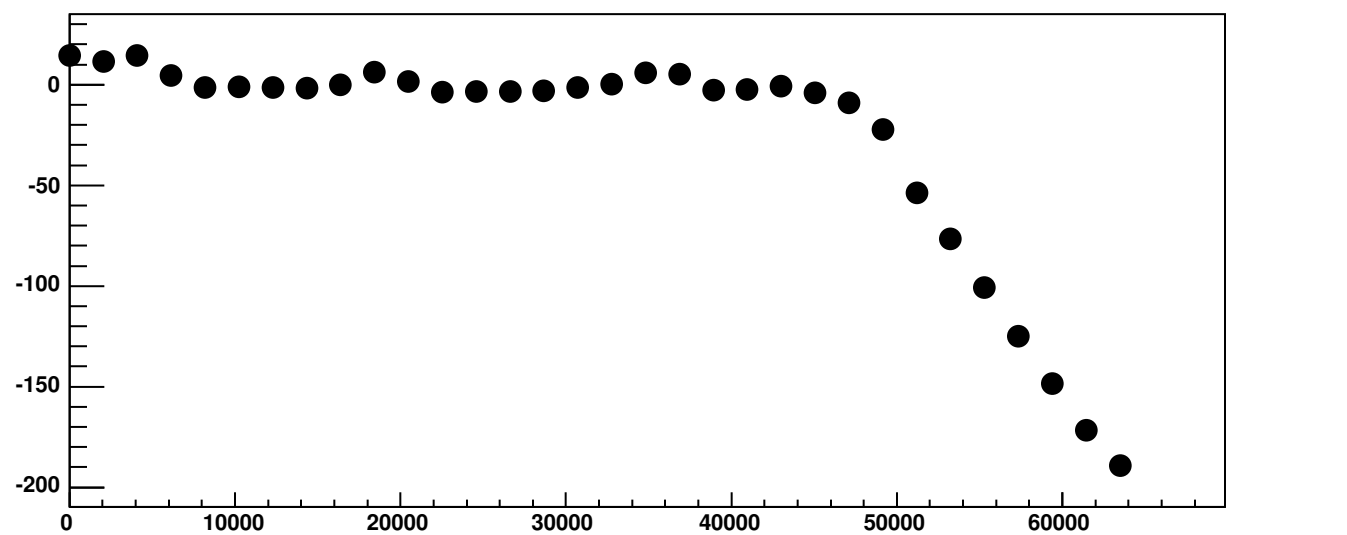
Chip 7, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC

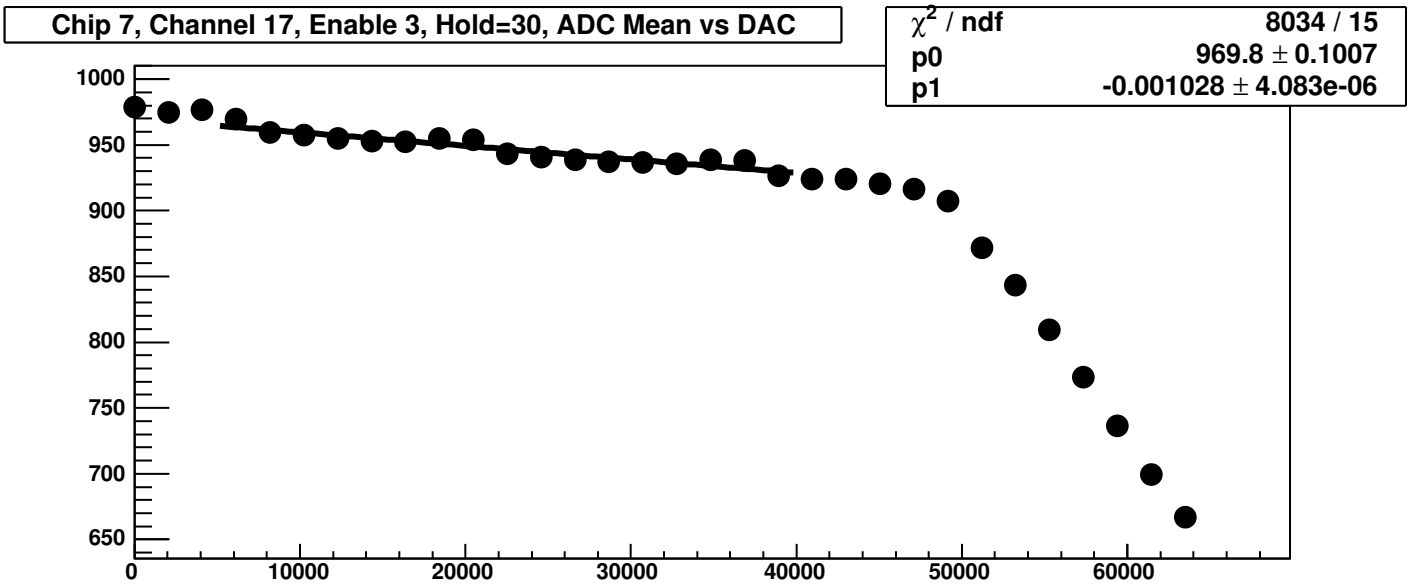


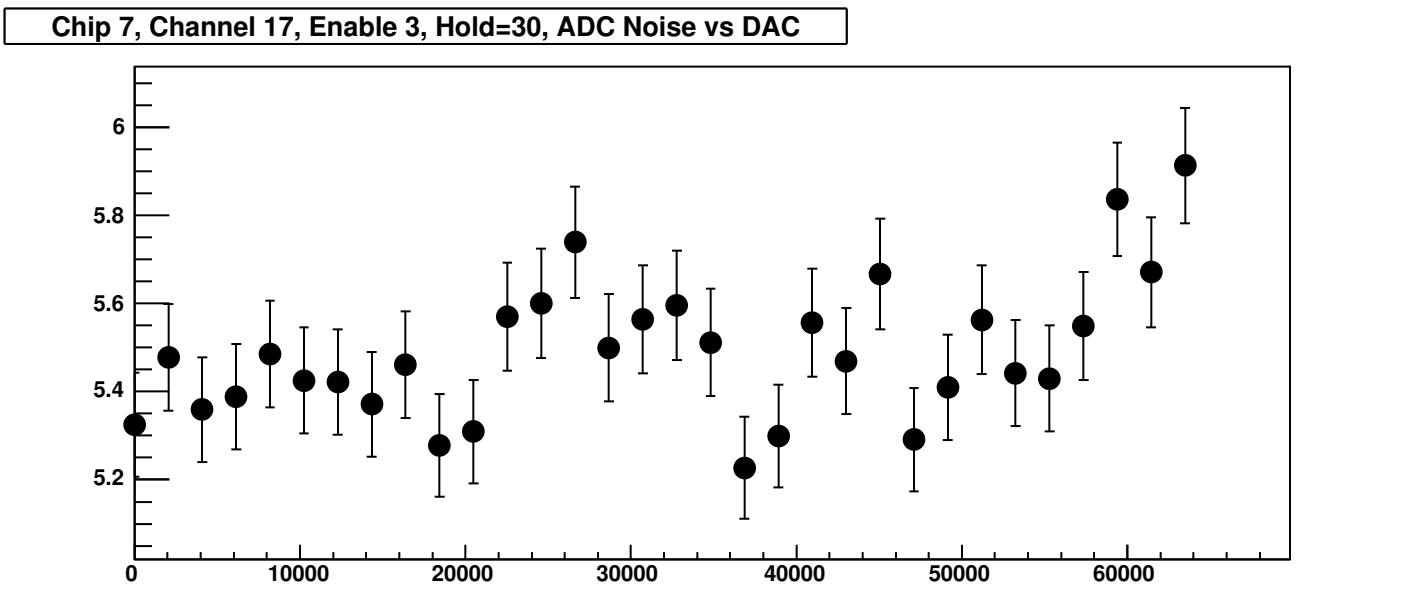
Chip 7, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC

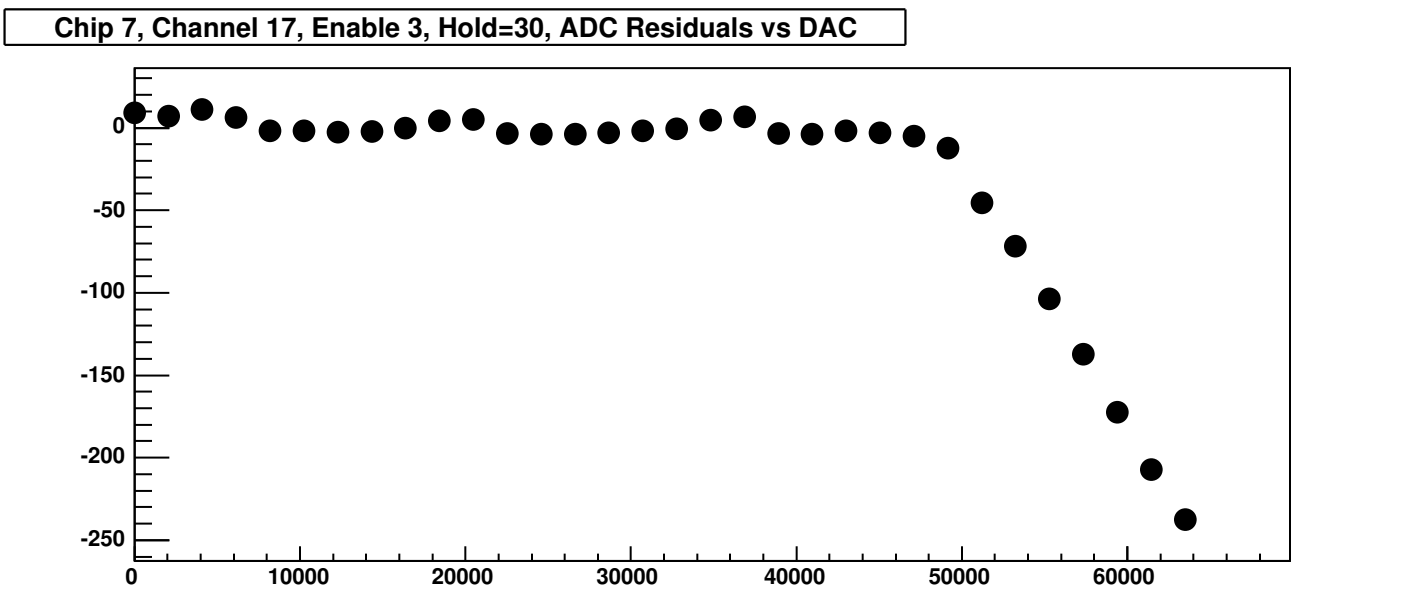


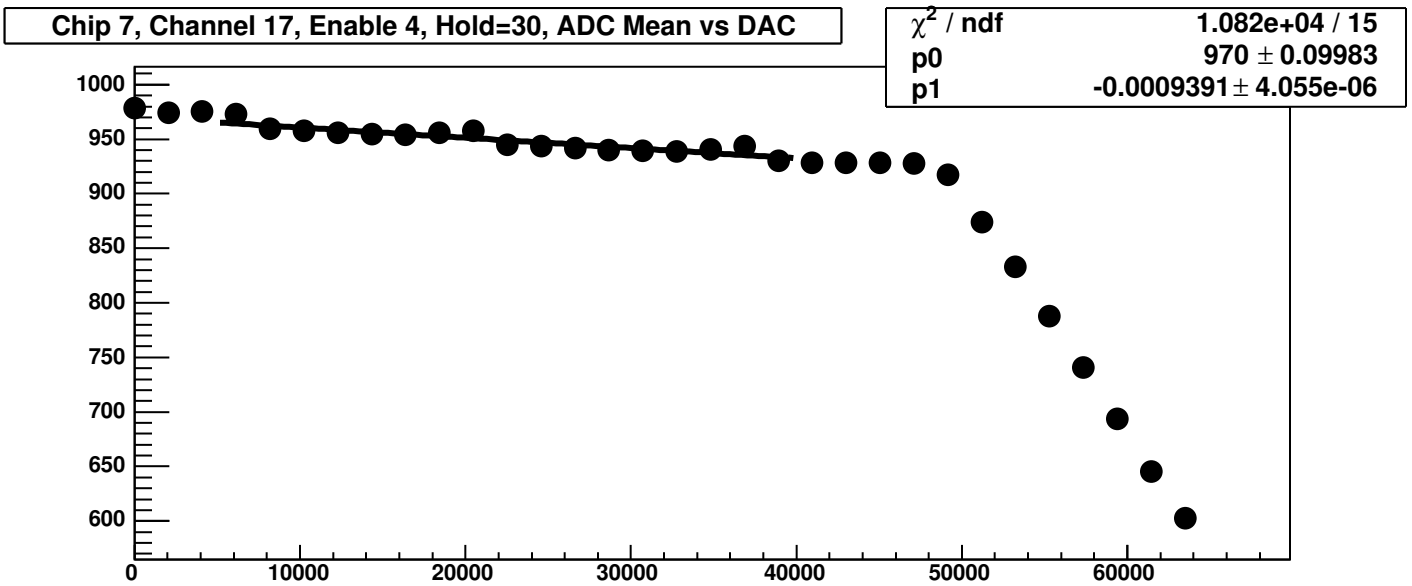
Chip 7, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC

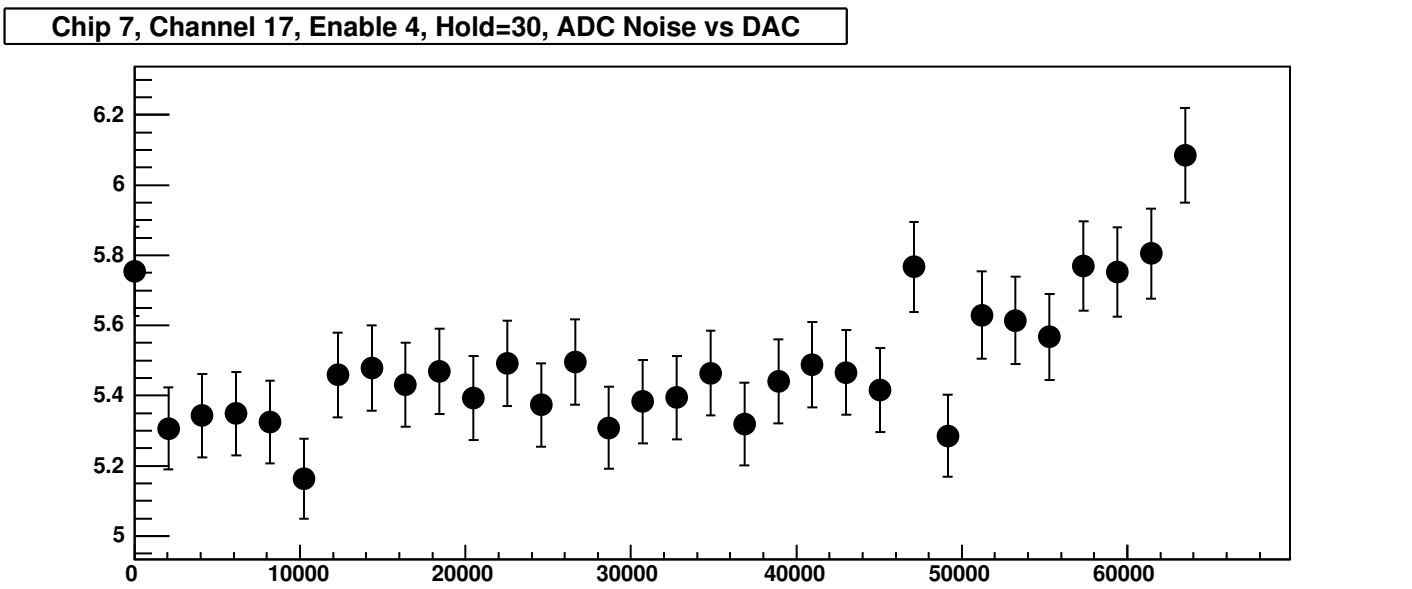


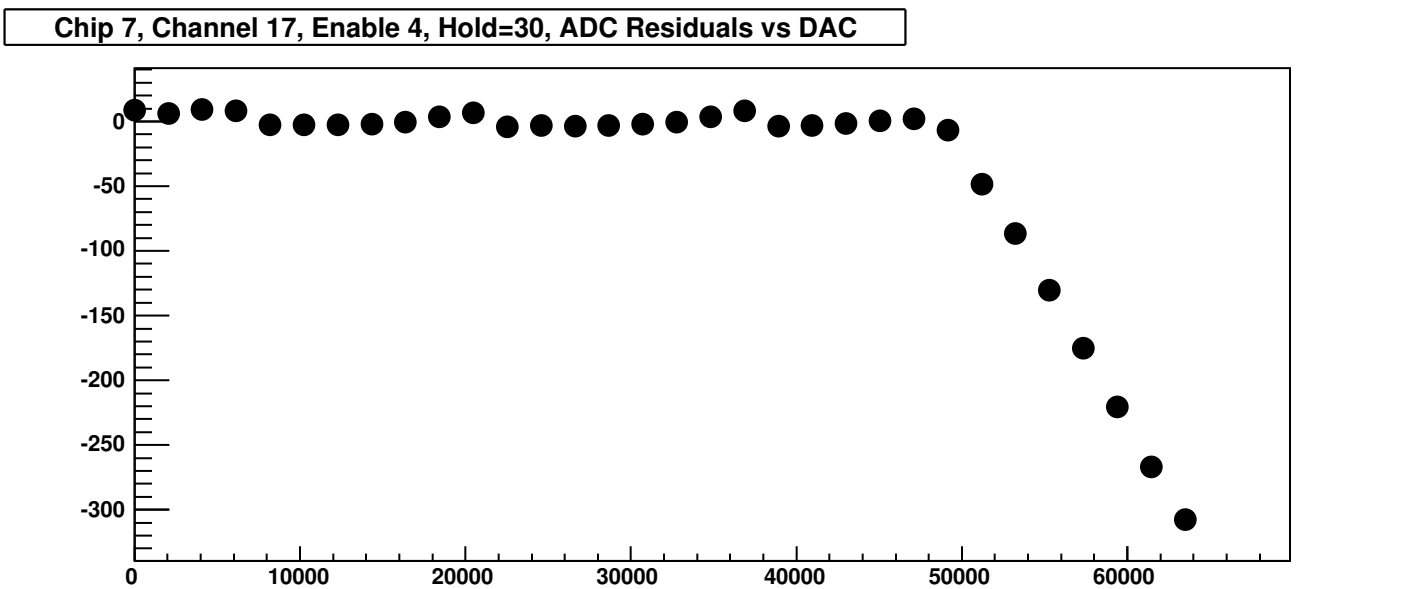

Chip 7, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8034 / 15
p0 969.8 ± 0.1007
p1 -0.001028 ± 4.083e-06
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

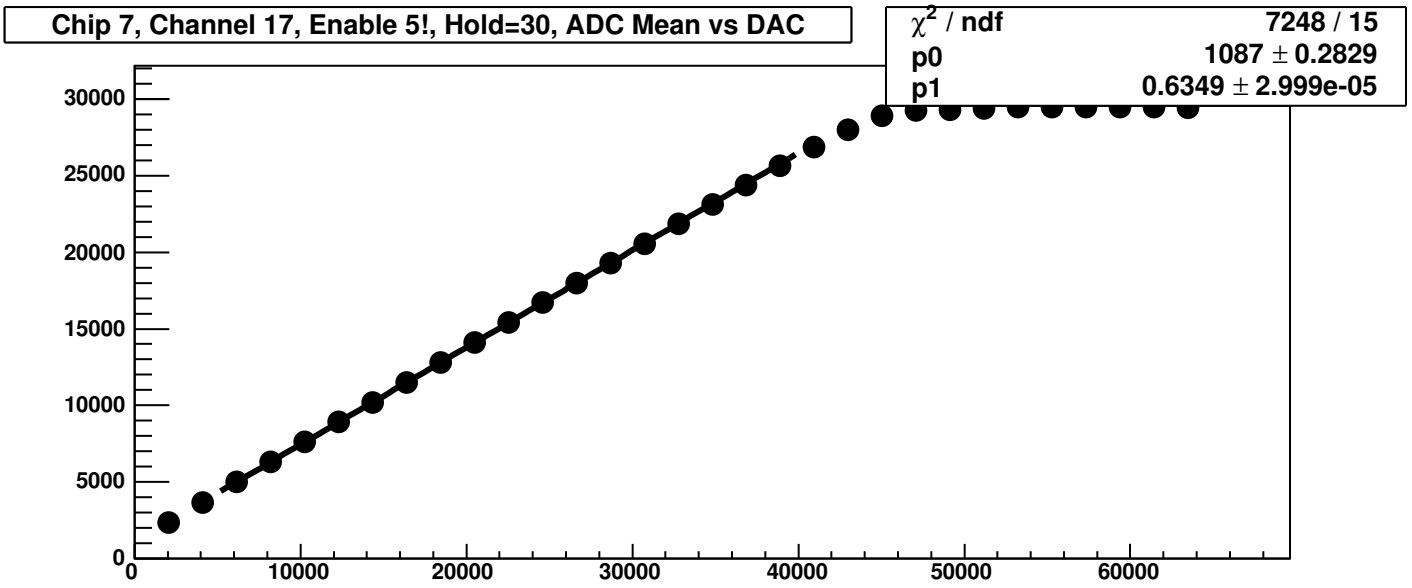

Chip 7, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

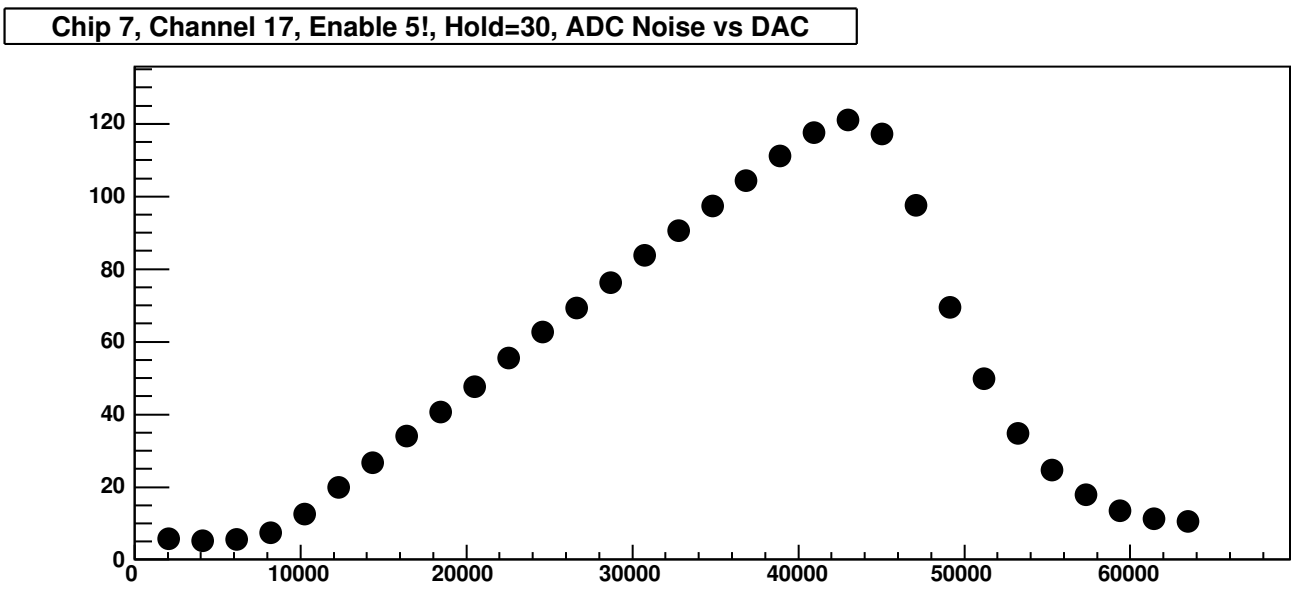

Chip 7, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

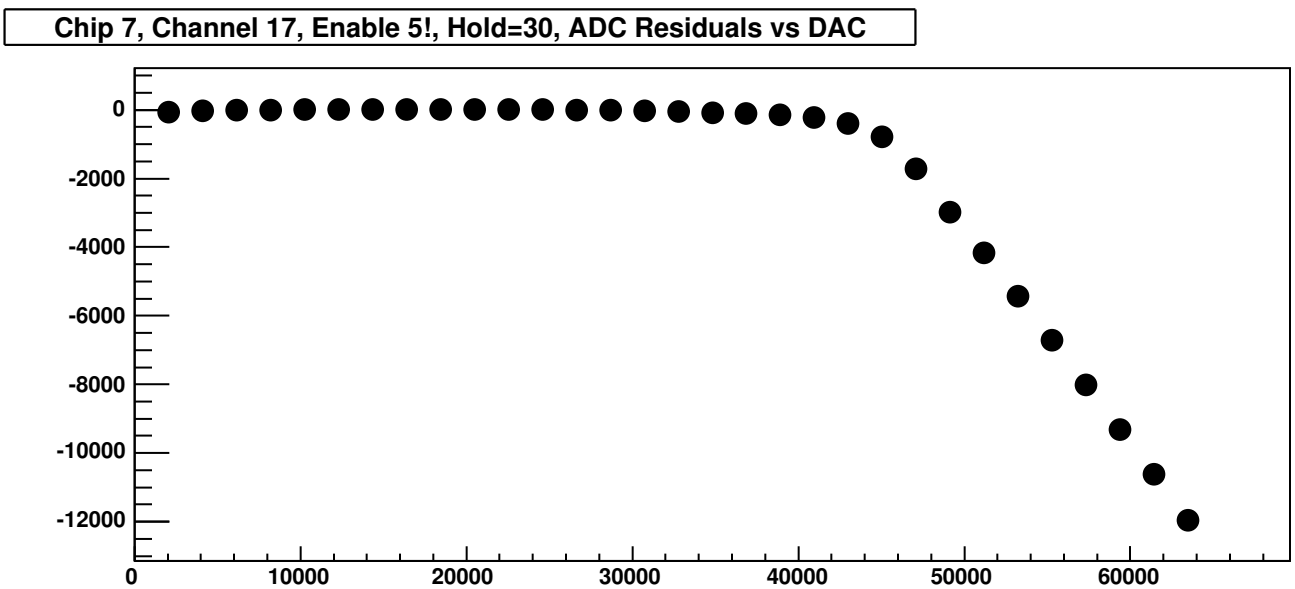

Chip 7, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 1.082e+04 / 15
p0 970 ± 0.09983
p1 -0.0009391 ± 4.055e-06
1000
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

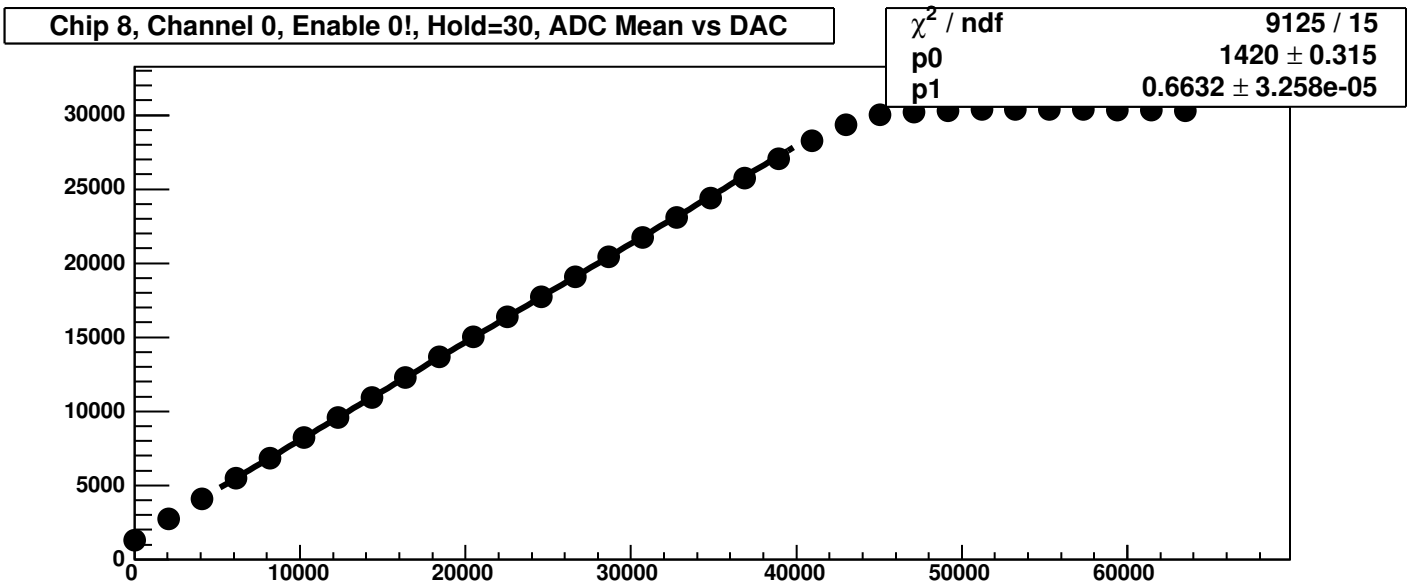

Chip 7, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

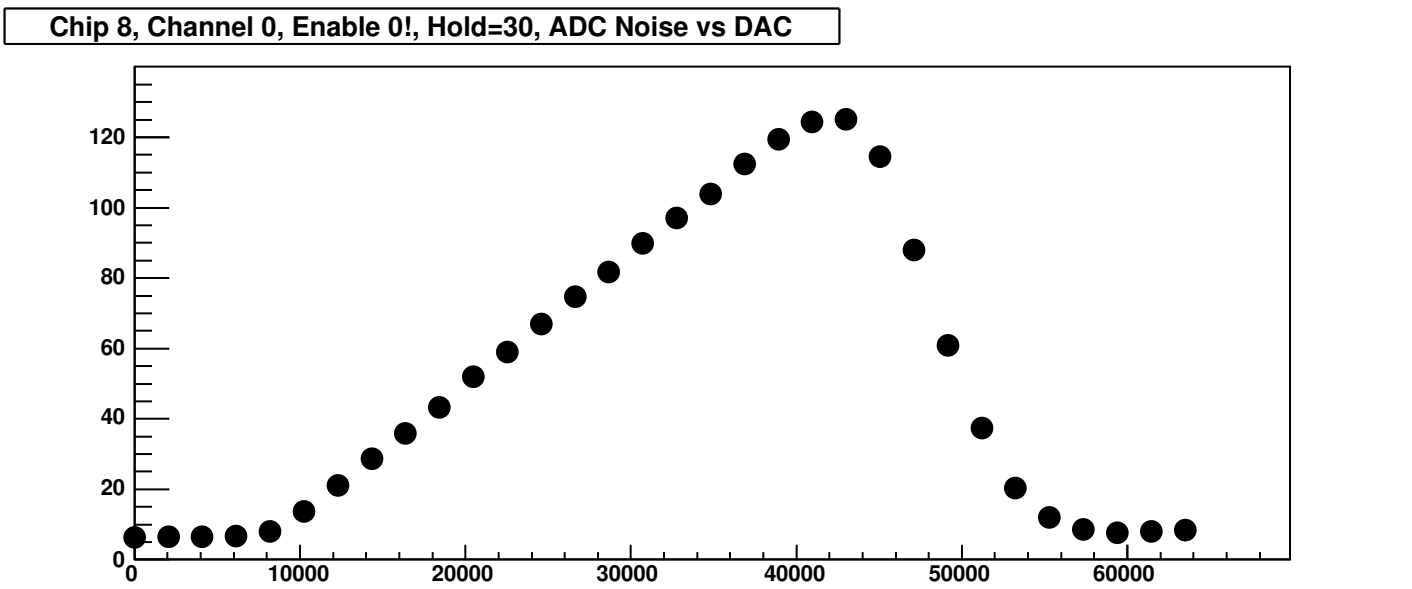

Chip 7, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

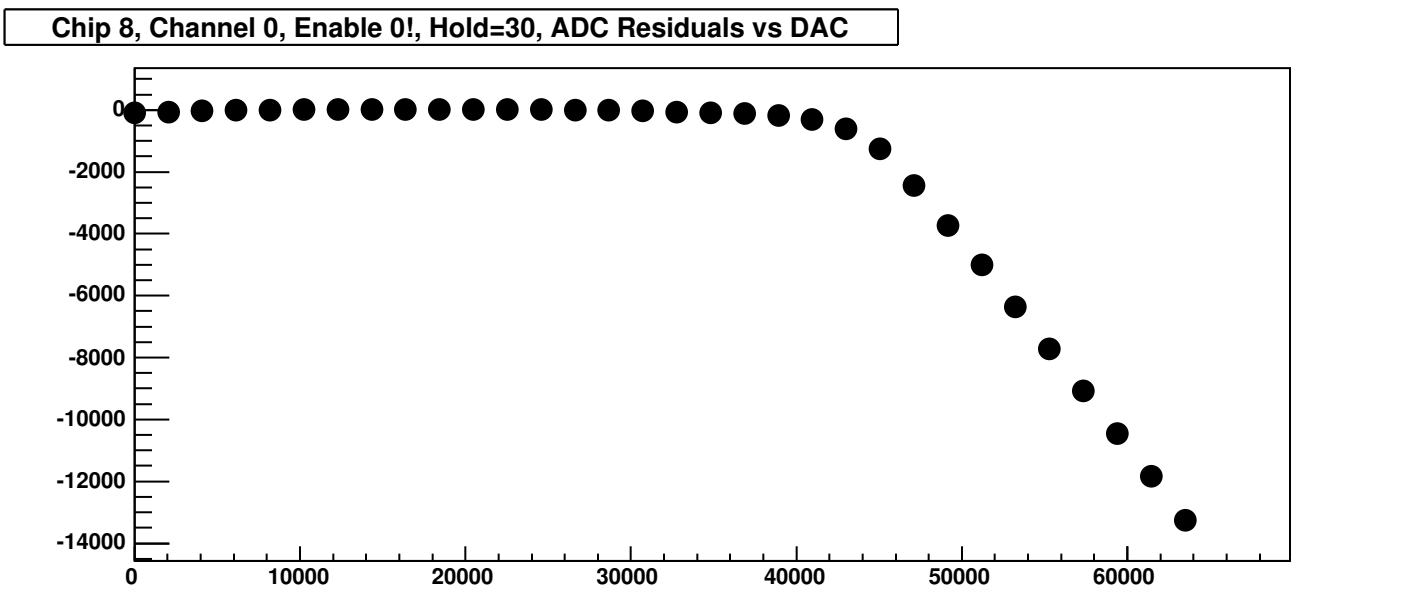

Chip 7, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7248 / 15
p0 1087 ± 0.2829
p1 0.6349 ± 2.999e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

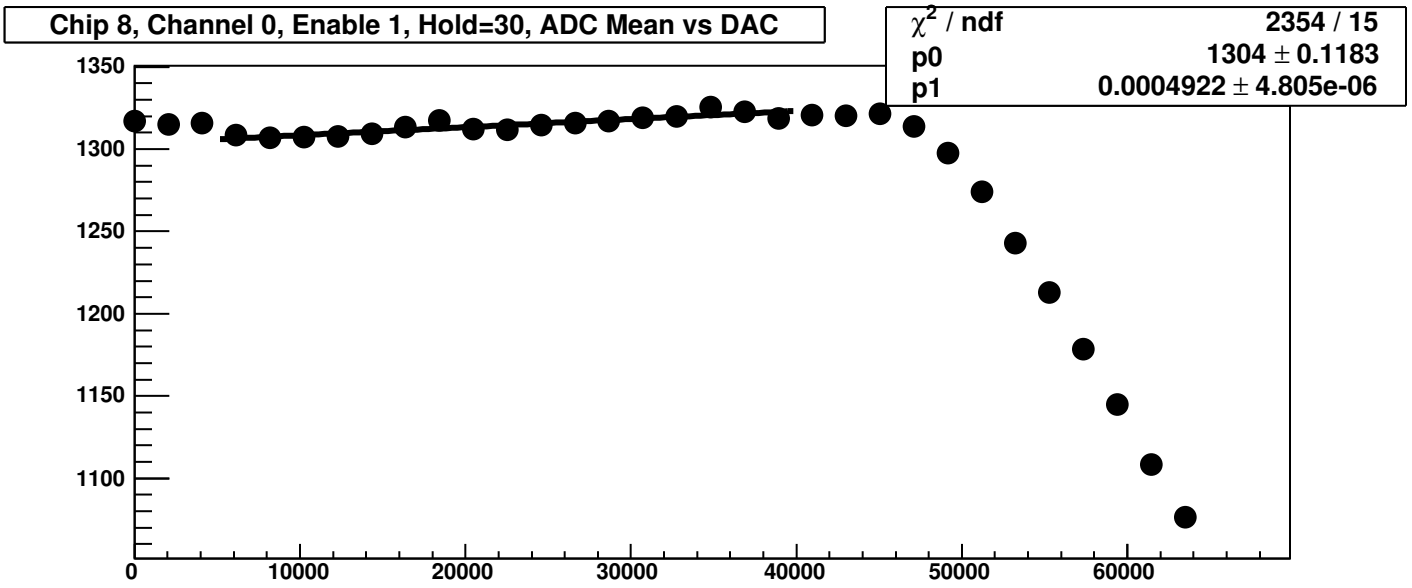

Chip 7, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

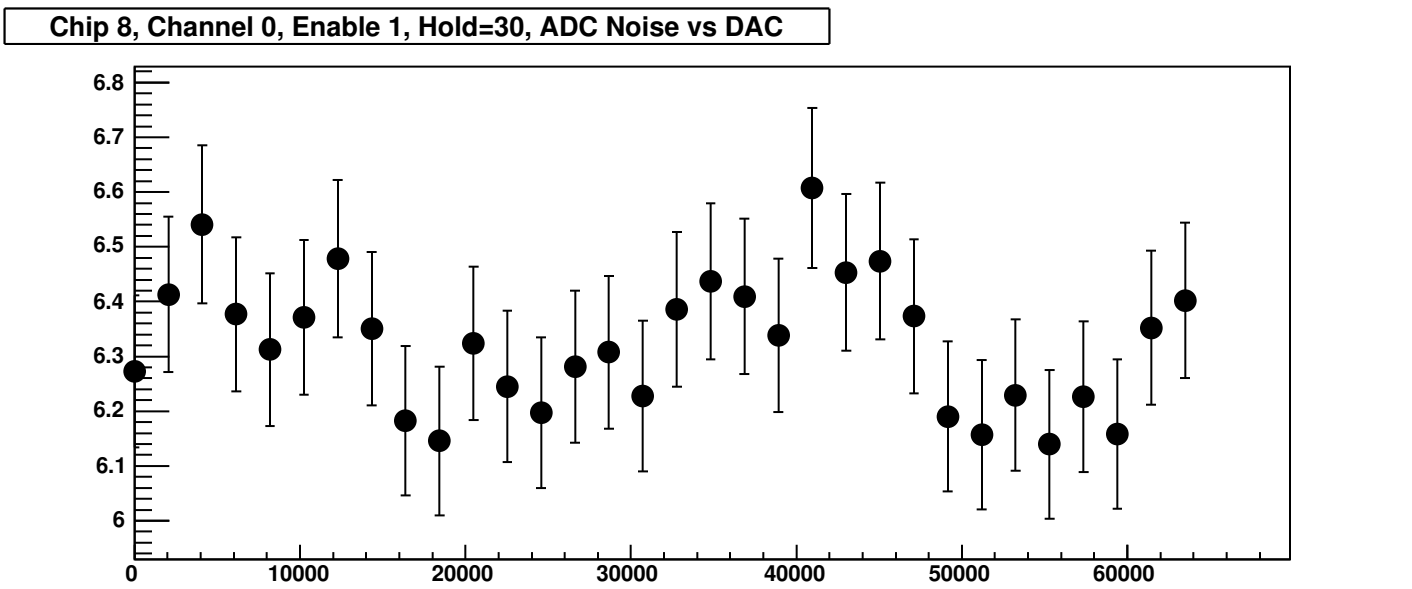

Chip 7, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

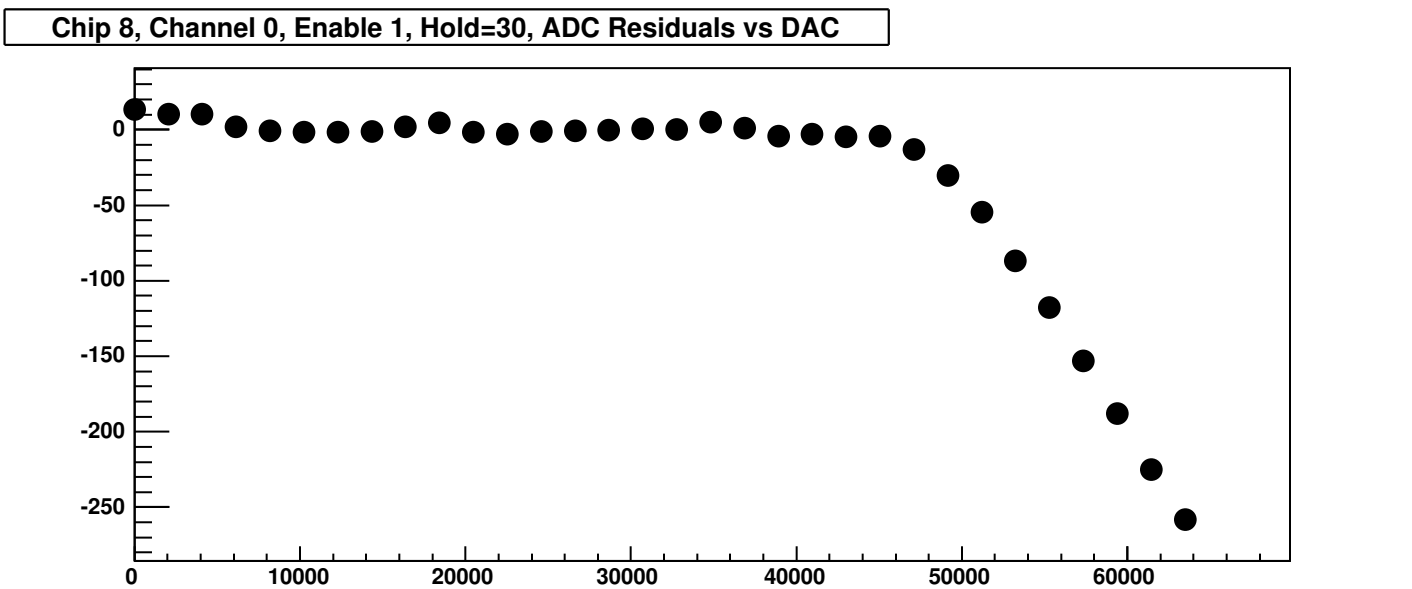

Chip 8, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 9125 / 15
p0 1420 ± 0.315
p1 0.6632 ± 3.258e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

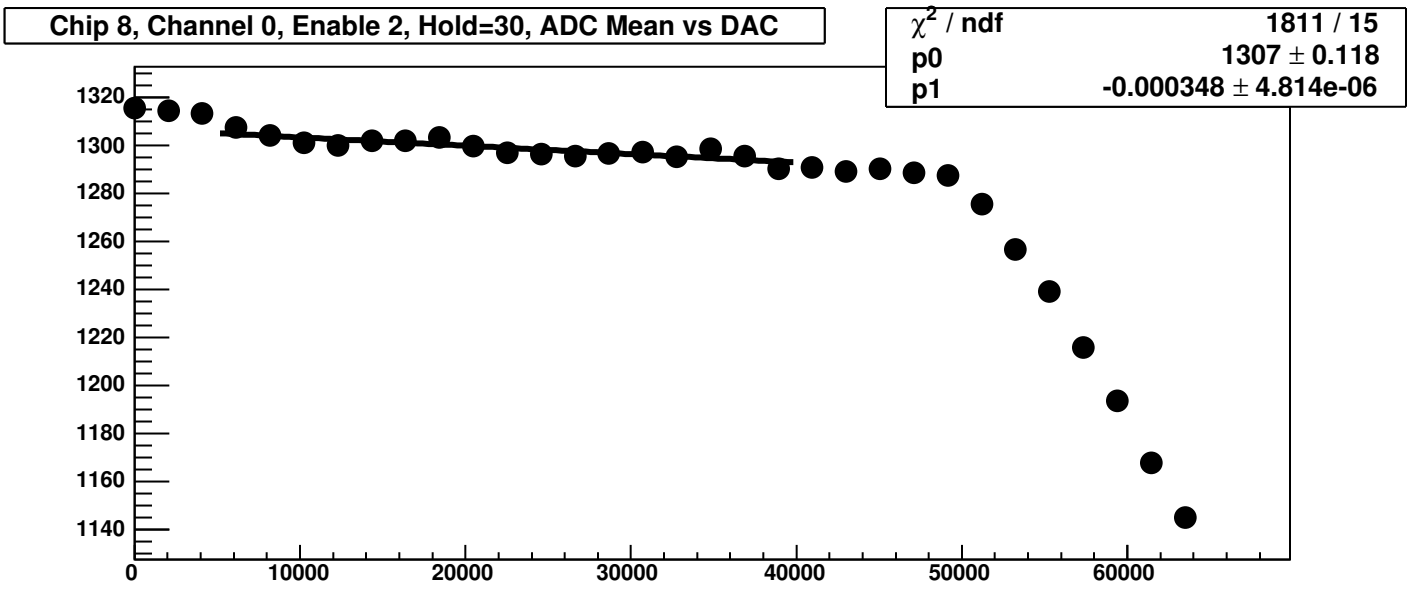

Chip 8, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

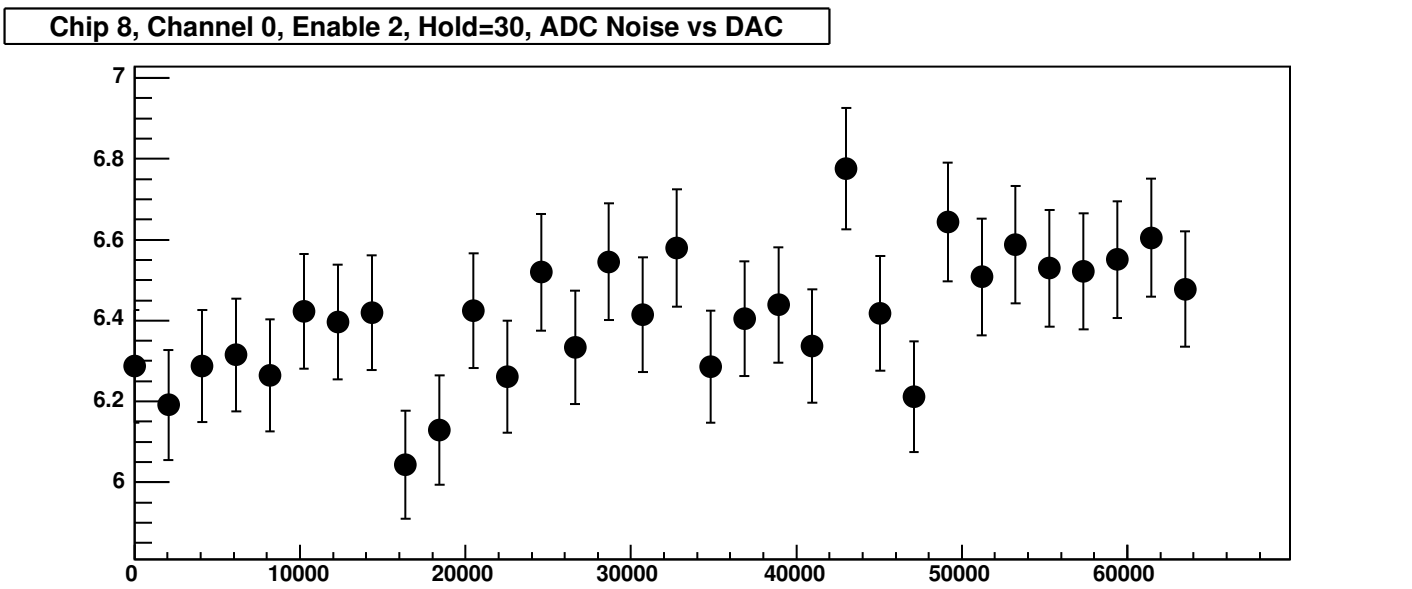

Chip 8, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

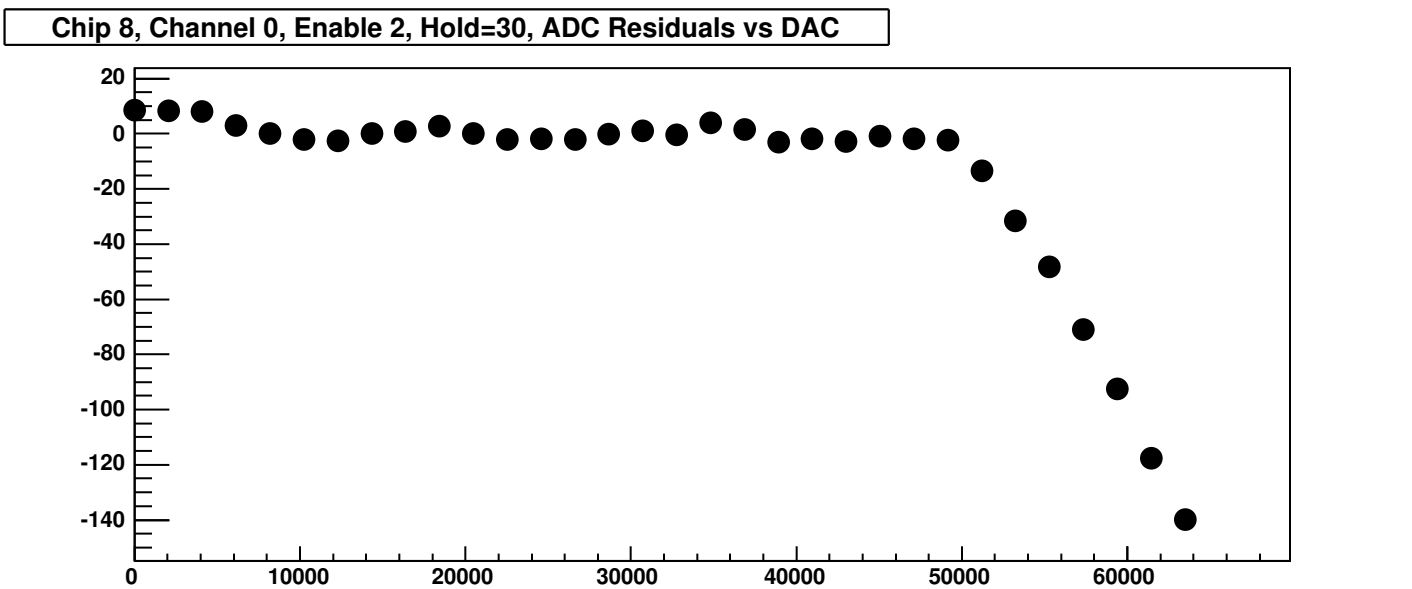

Chip 8, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2354 / 15
p0 1304 ± 0.1183
p1 0.0004922 ± 4.805e-06
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

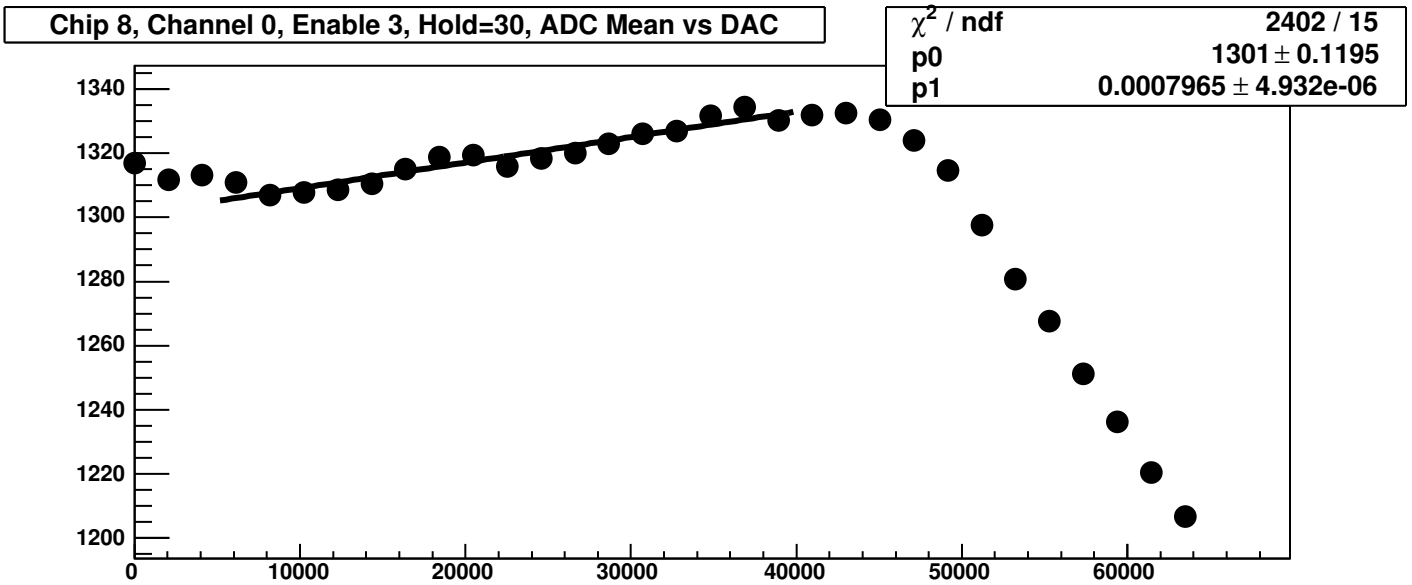

Chip 8, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.7
6.6
6.5
6.4
6.3
6.2
6.1
6
0
10000
20000
30000
40000
50000
60000

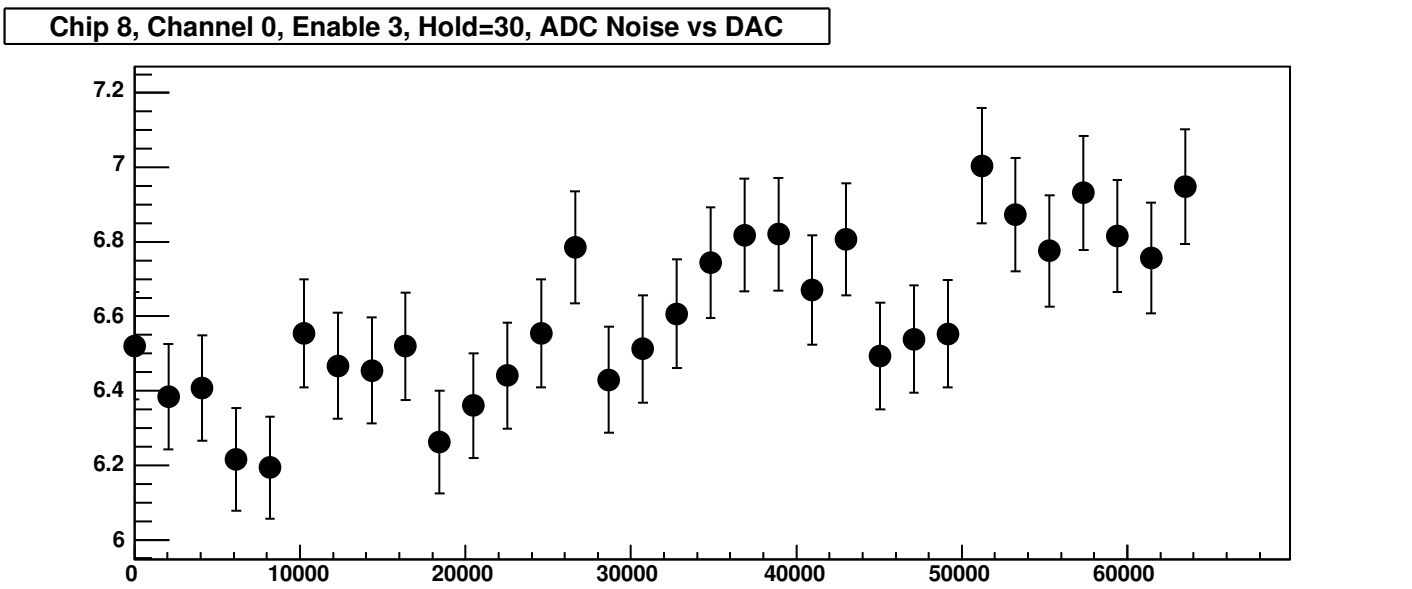

Chip 8, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

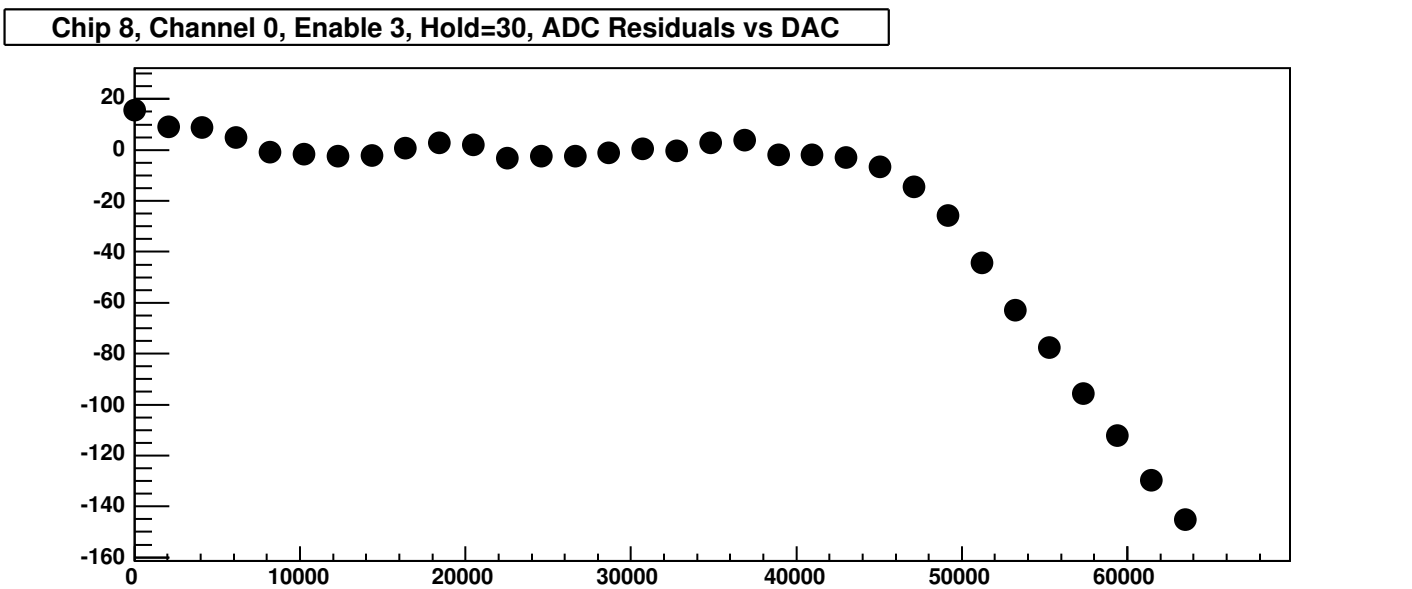

Chip 8, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1811 / 15
p0 1307 ± 0.118
p1 -0.000348 ± 4.814e-06
1320
1300
1280
1260
1240
1220
1200
1180
1160
1140
0
10000
20000
30000
40000
50000
60000

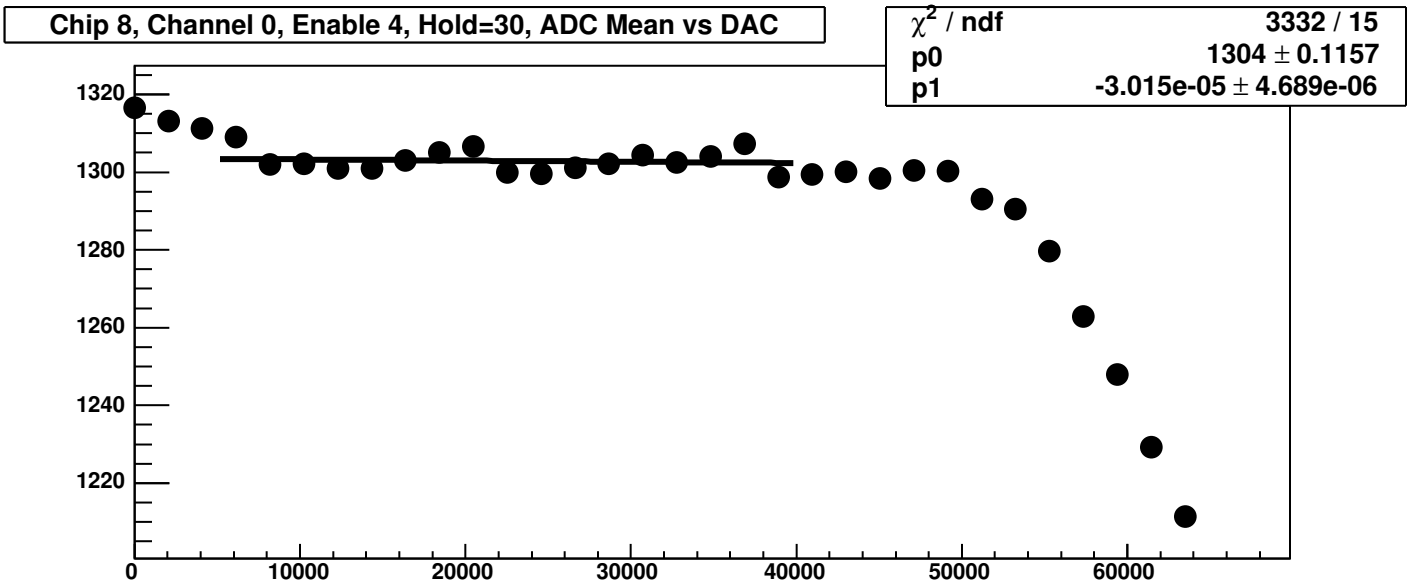

Chip 8, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

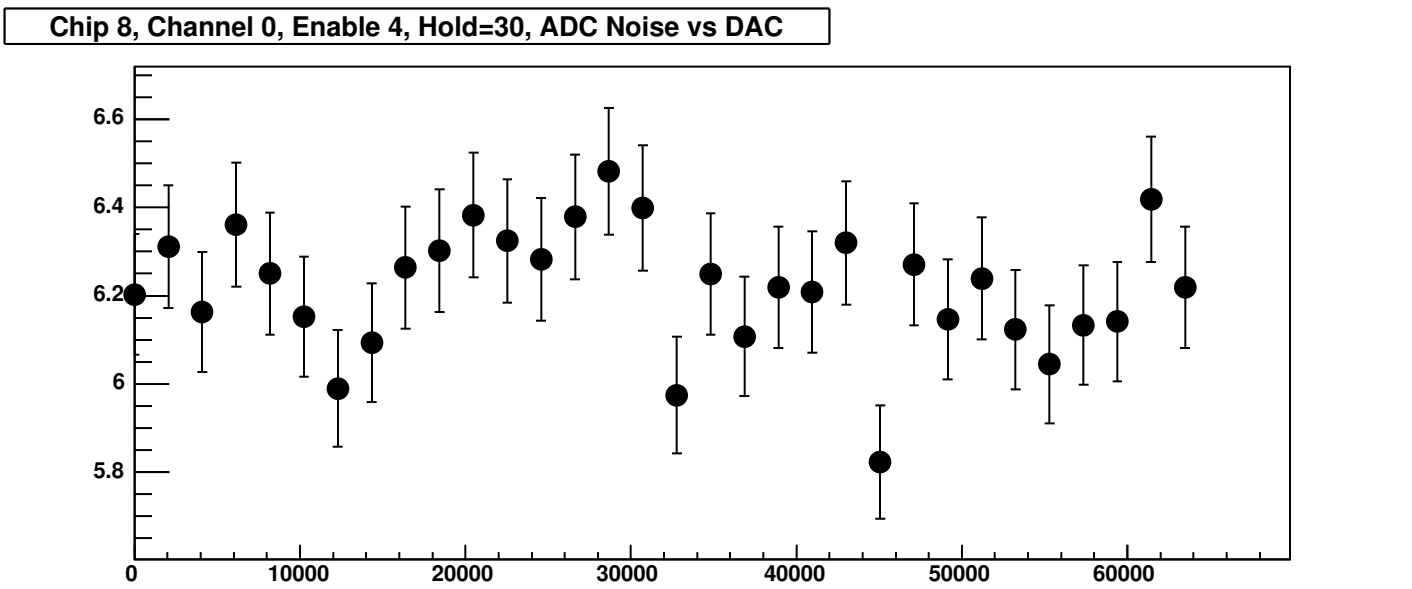

Chip 8, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

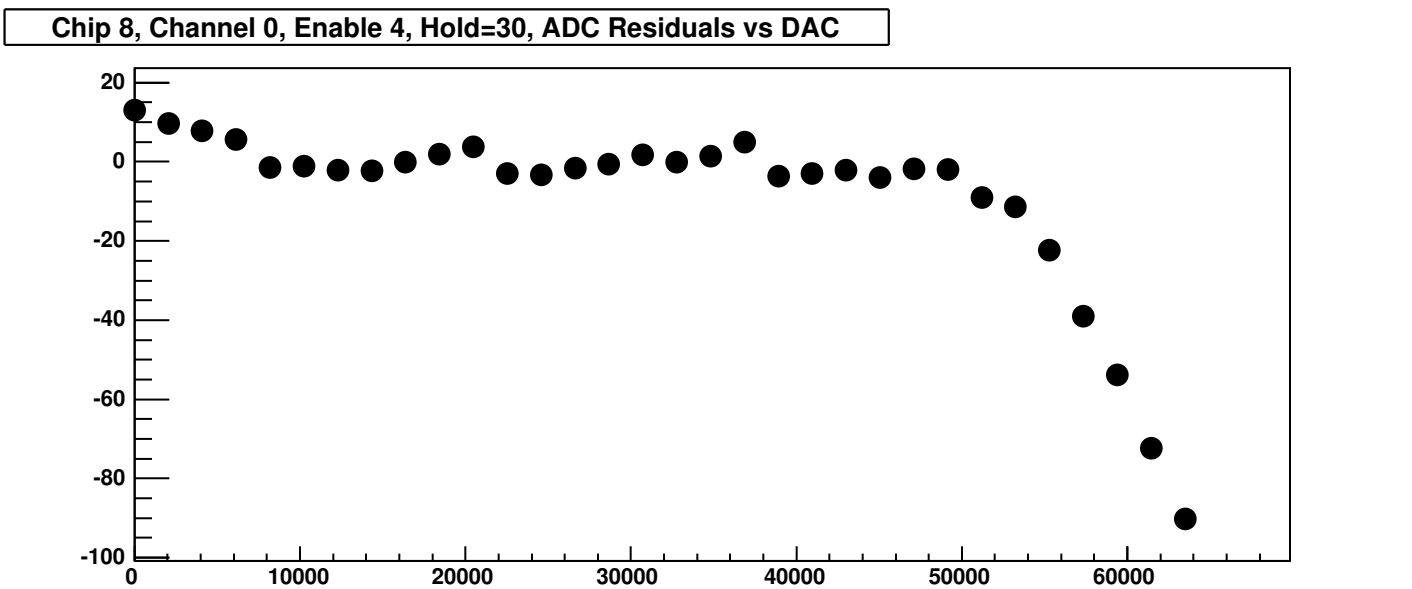

Chip 8, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2402 / 15
p0 1301 ± 0.1195
p1 0.0007965 ± 4.932e-06
1340
1320
1300
1280
1260
1240
1220
1200
0
10000
20000
30000
40000
50000
60000

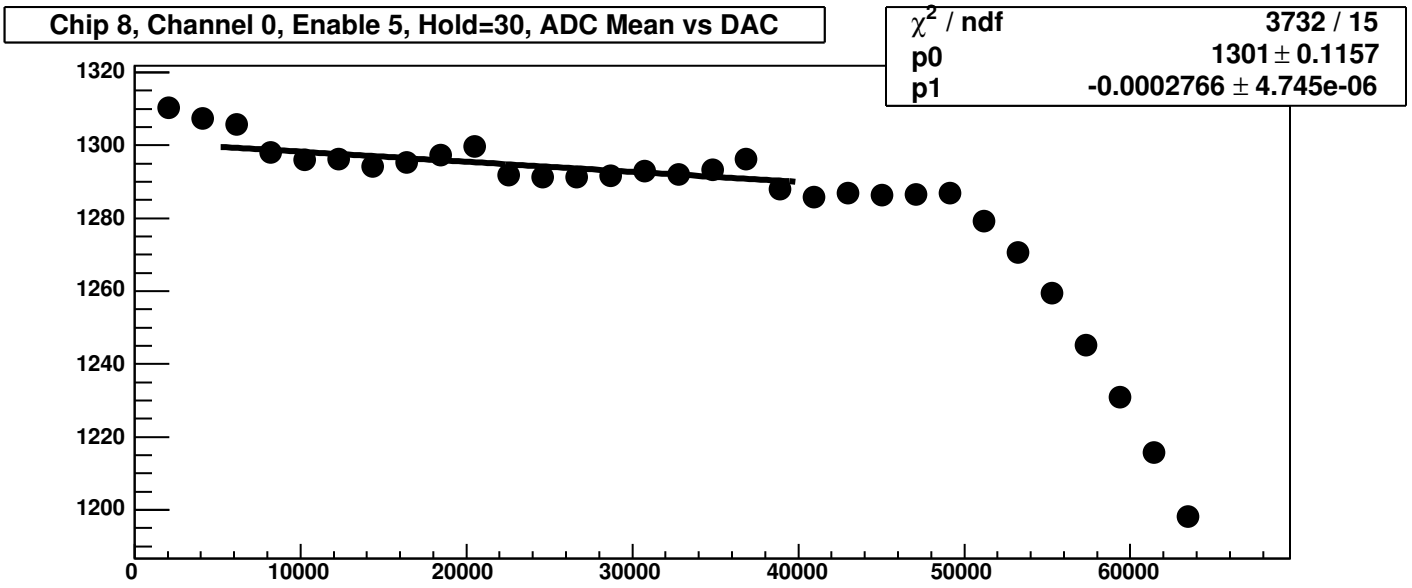

Chip 8, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

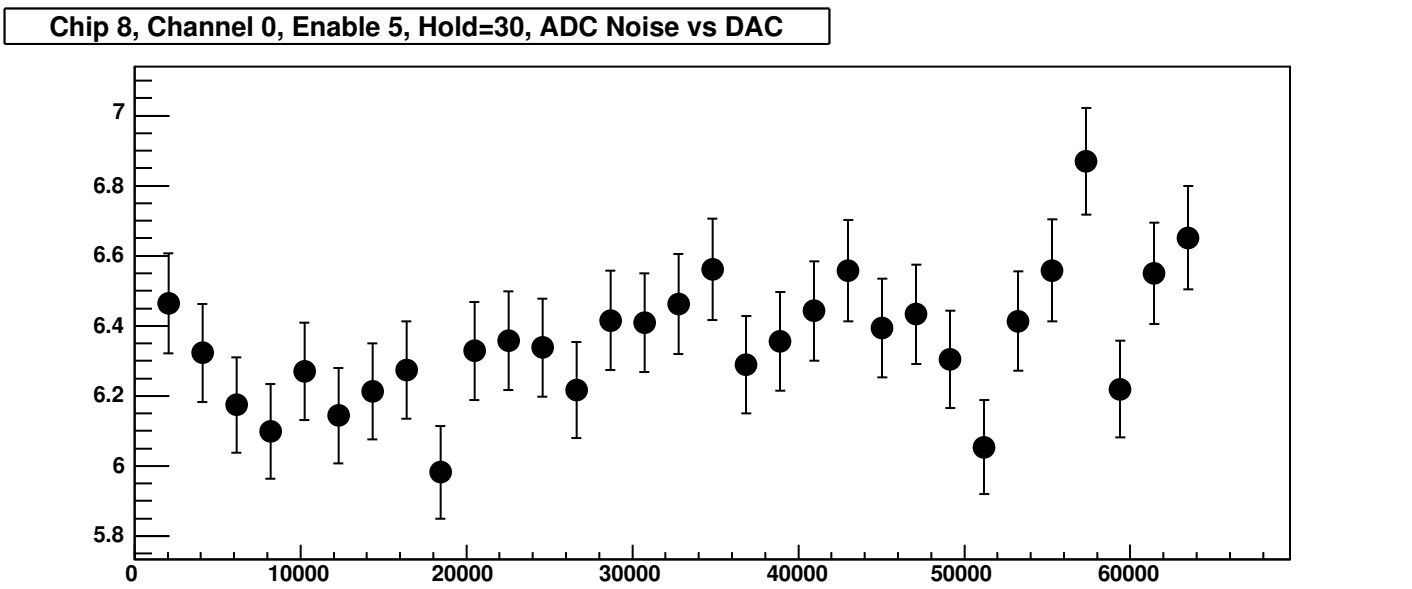

Chip 8, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

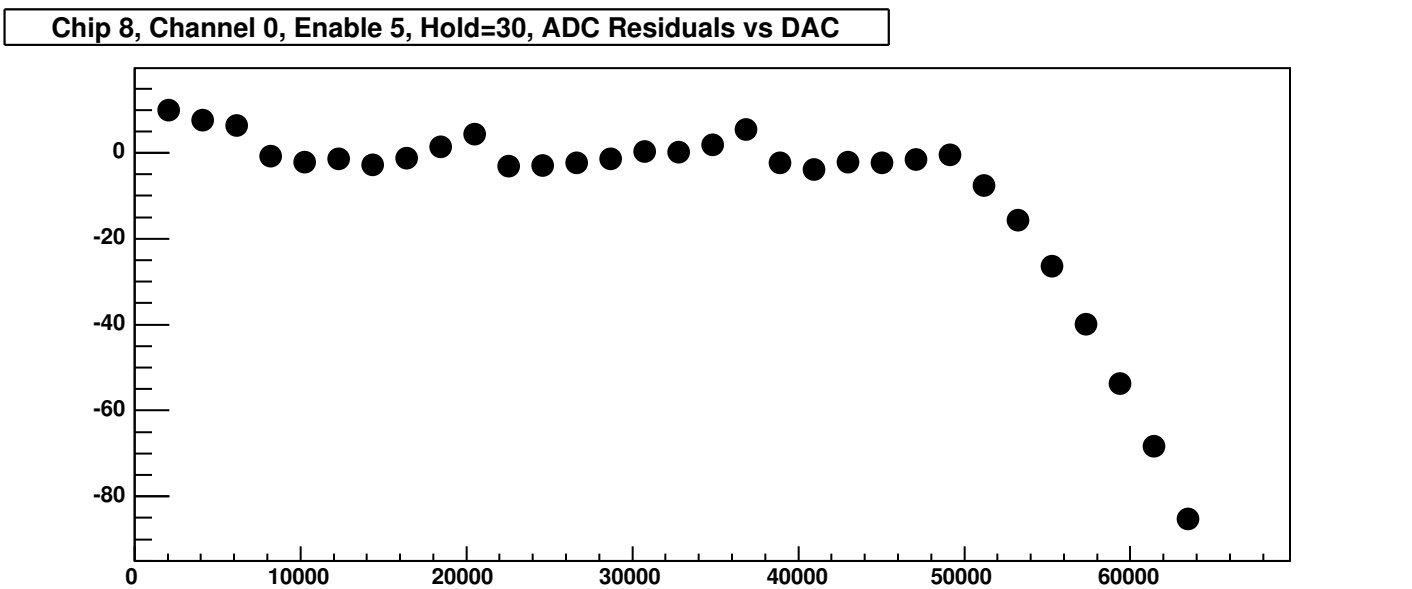

Chip 8, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 3332 / 15
p0 1304 ± 0.1157
p1 -3.015e-05 ± 4.689e-06
1320
1300
1280
1260
1240
1220
0
10000
20000
30000
40000
50000
60000

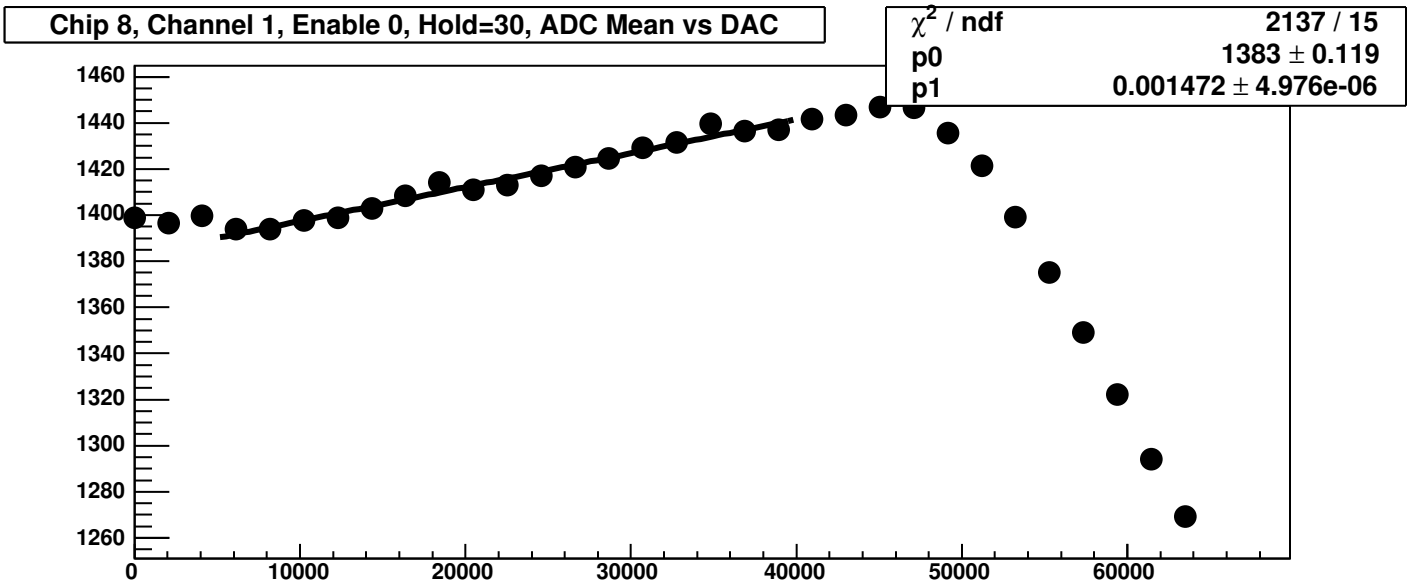

Chip 8, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

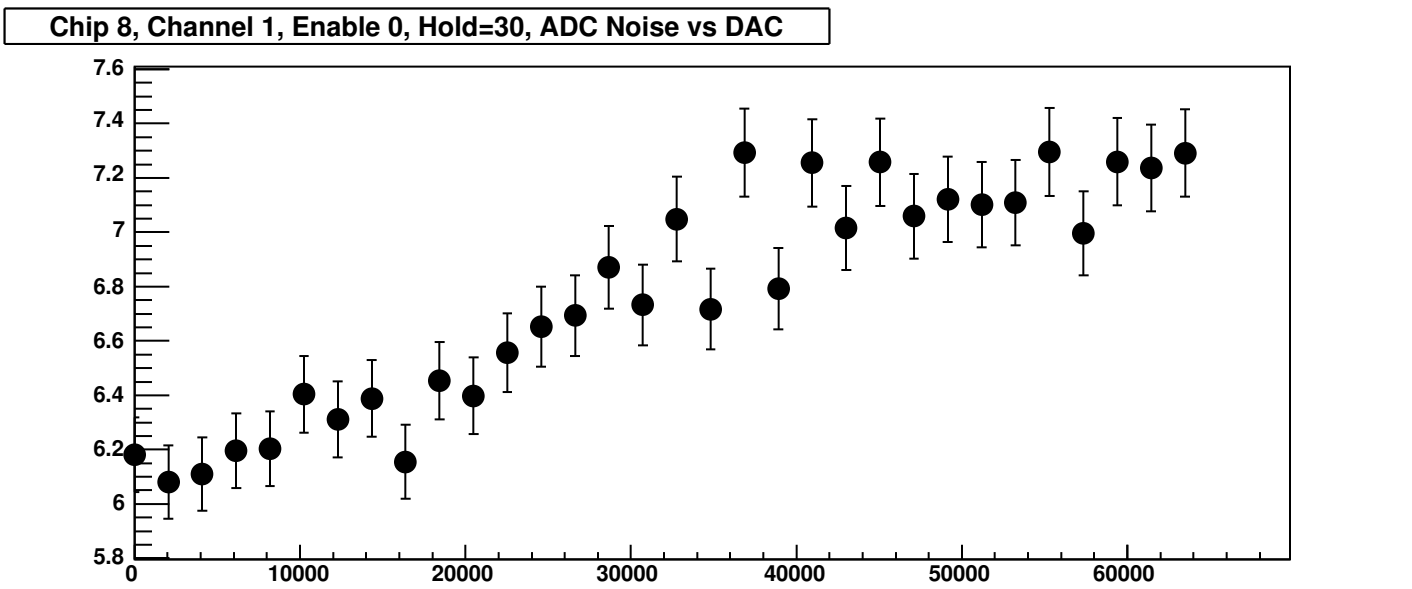

Chip 8, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

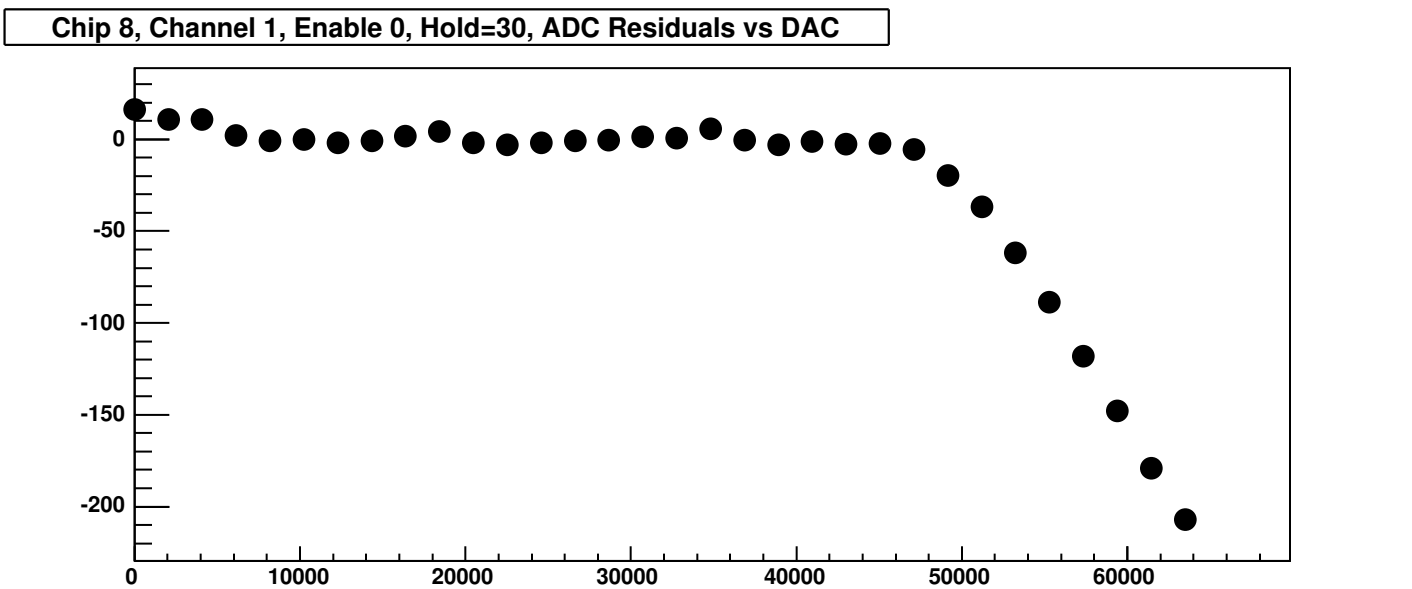

Chip 8, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3732 / 15
p0 1301 ± 0.1157
p1 -0.0002766 ± 4.745e-06
1320
1300
1280
1260
1240
1220
1200
0
10000
20000
30000
40000
50000
60000

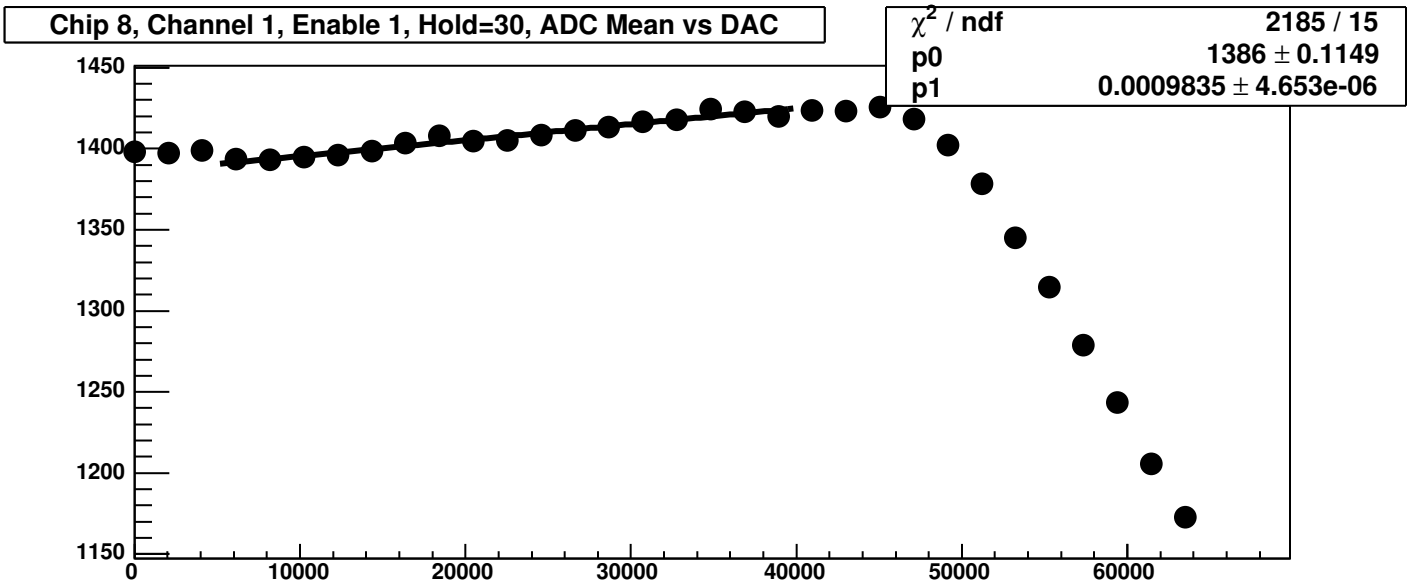

Chip 8, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

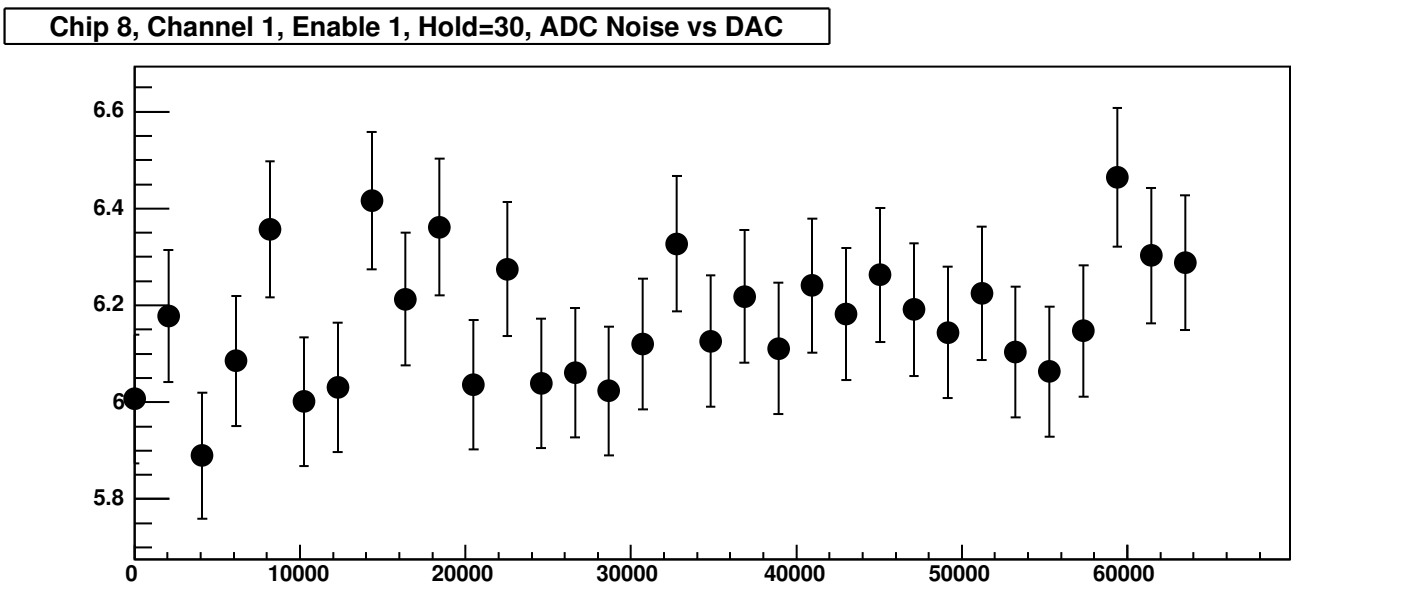

Chip 8, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

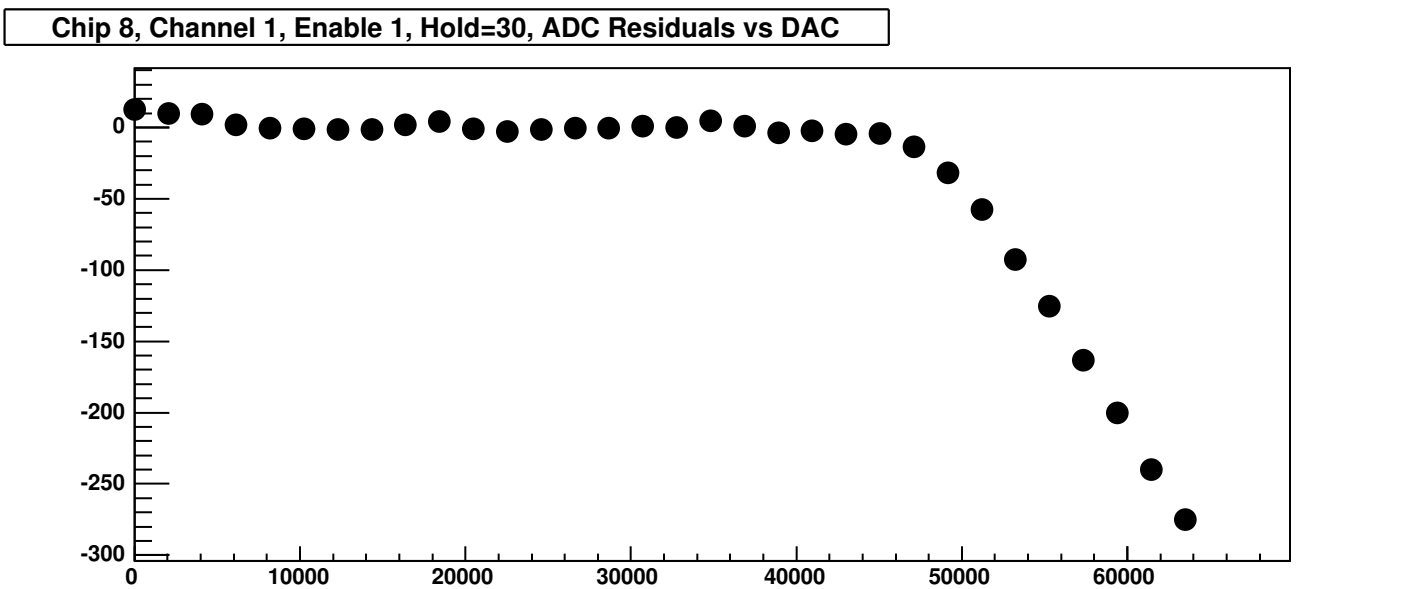

Chip 8, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2137 / 15
p0 1383 ± 0.119
p1 0.001472 ± 4.976e-06
1460
1440
1420
1400
1380
1360
1340
1320
1300
1280
1260
0
10000
20000
30000
40000
50000
60000

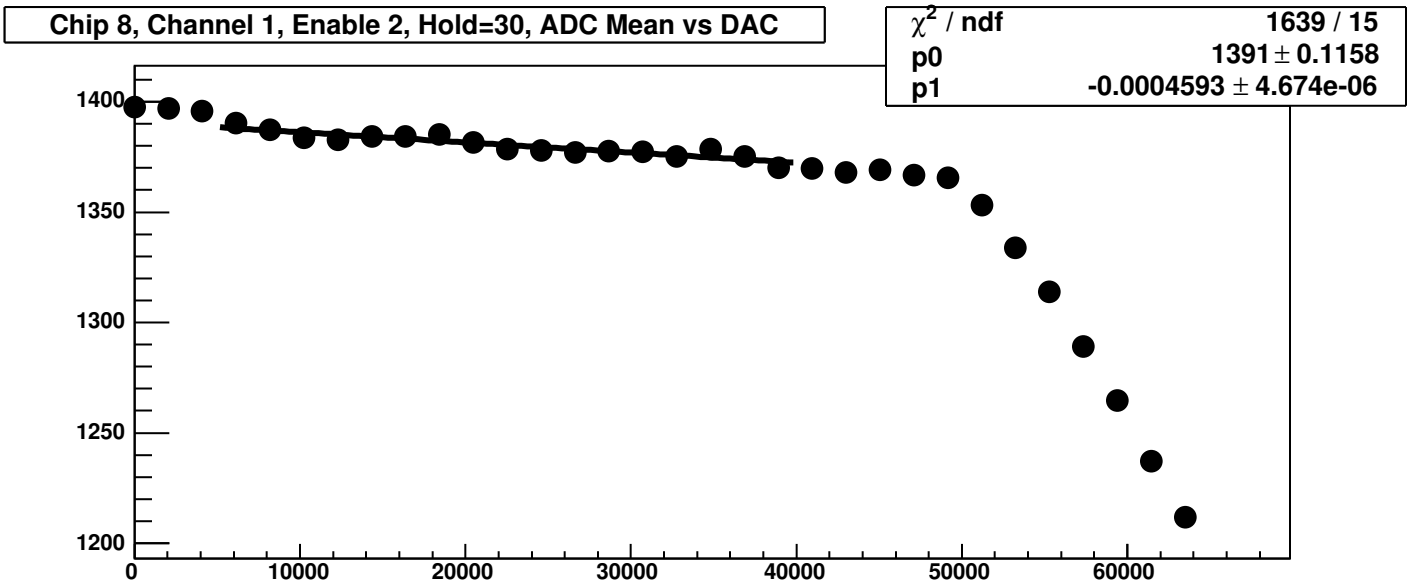

Chip 8, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

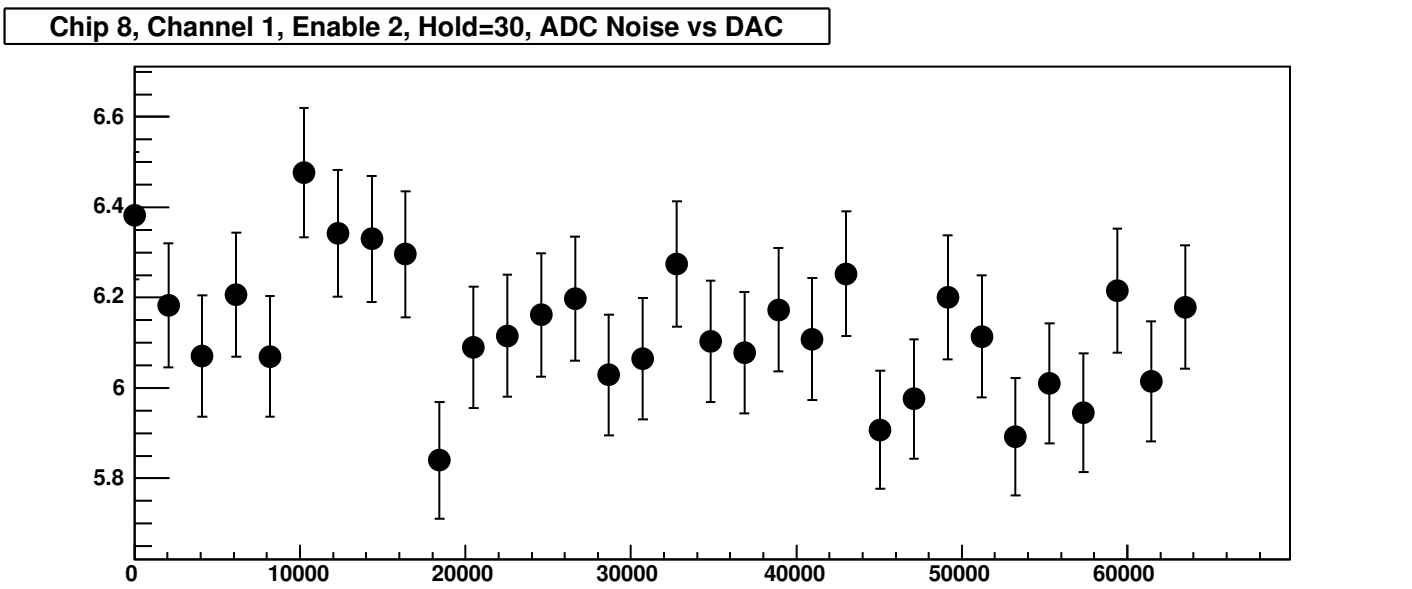

Chip 8, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

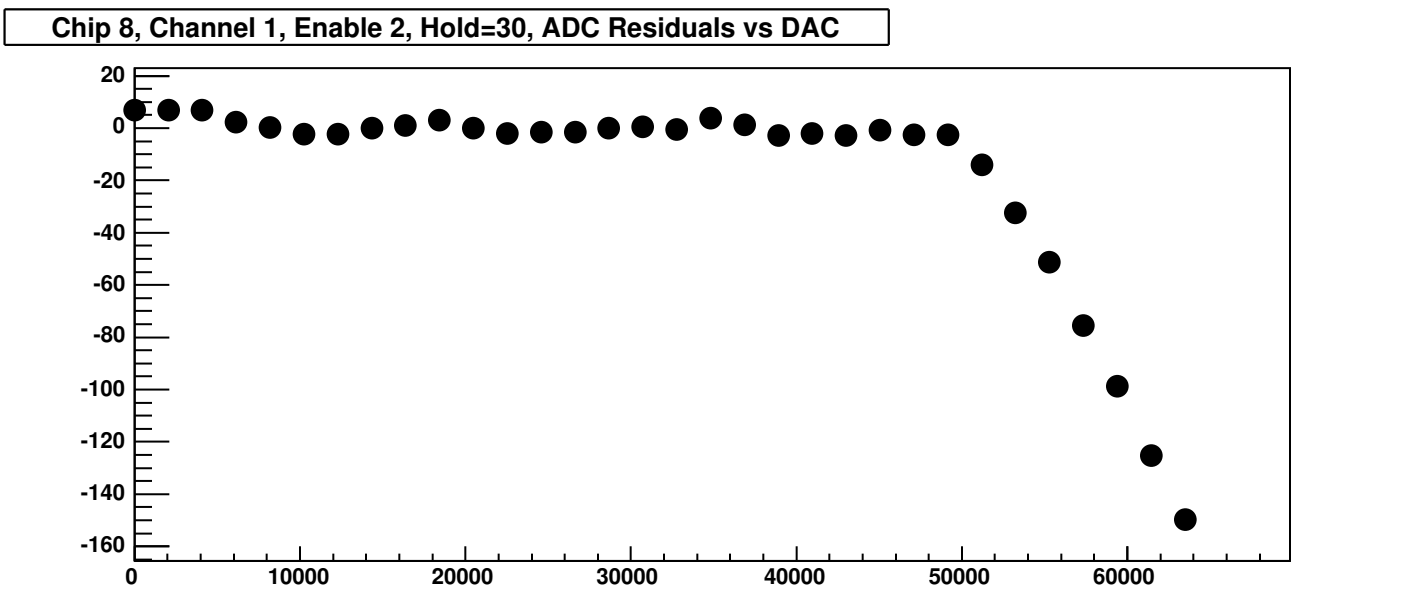

Chip 8, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2185 / 15
p0 1386 ± 0.1149
p1 0.0009835 ± 4.653e-06
1450
1400
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

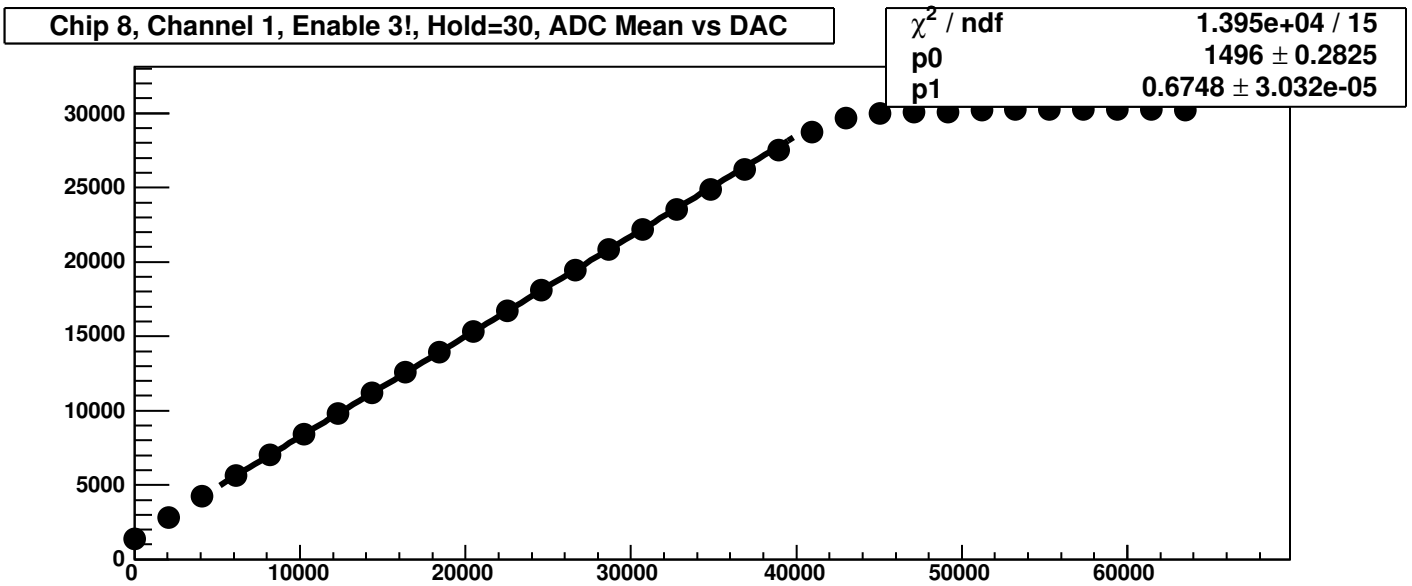

Chip 8, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

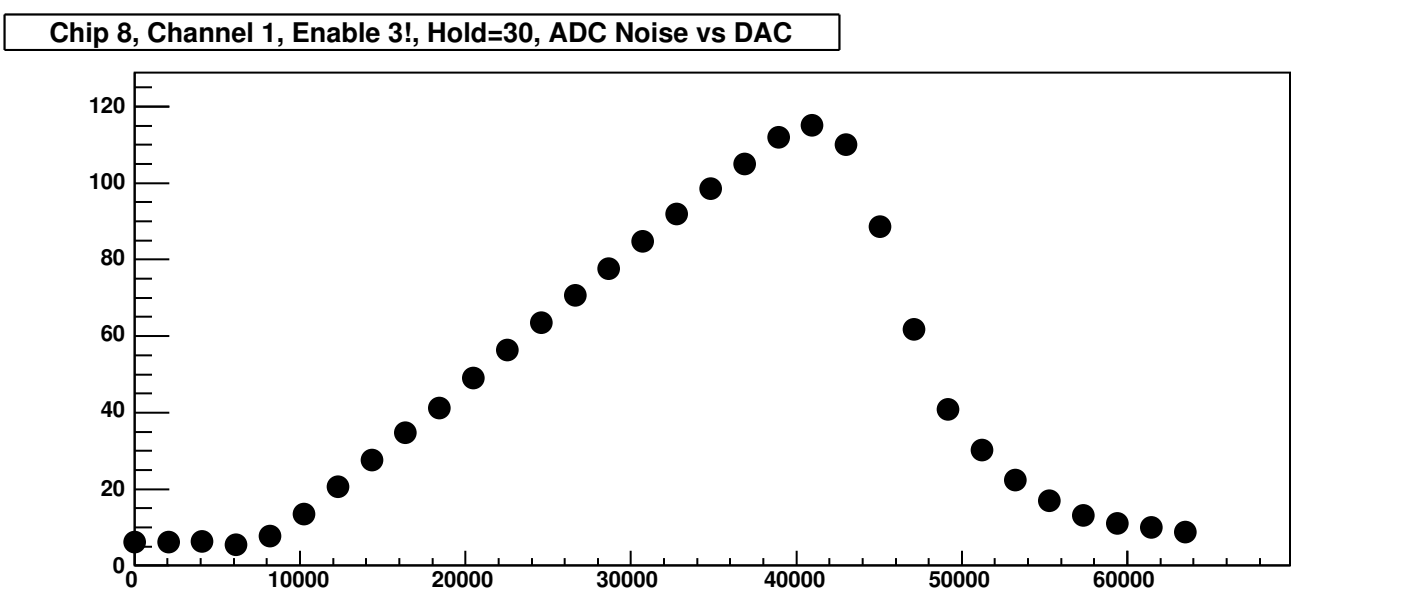

Chip 8, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

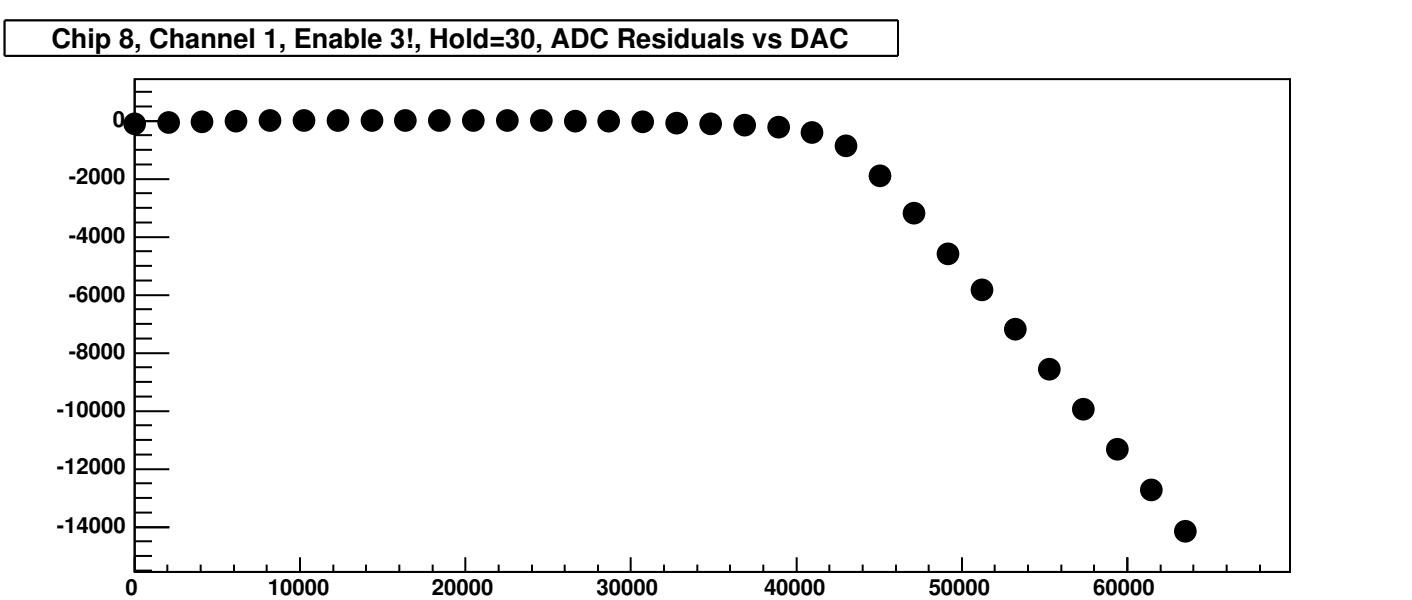

Chip 8, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1639 / 15
p0 1391 ± 0.1158
p1 -0.0004593 ± 4.674e-06
1400
1350
1300
1250
1200
0
10000
20000
30000
40000
50000
60000

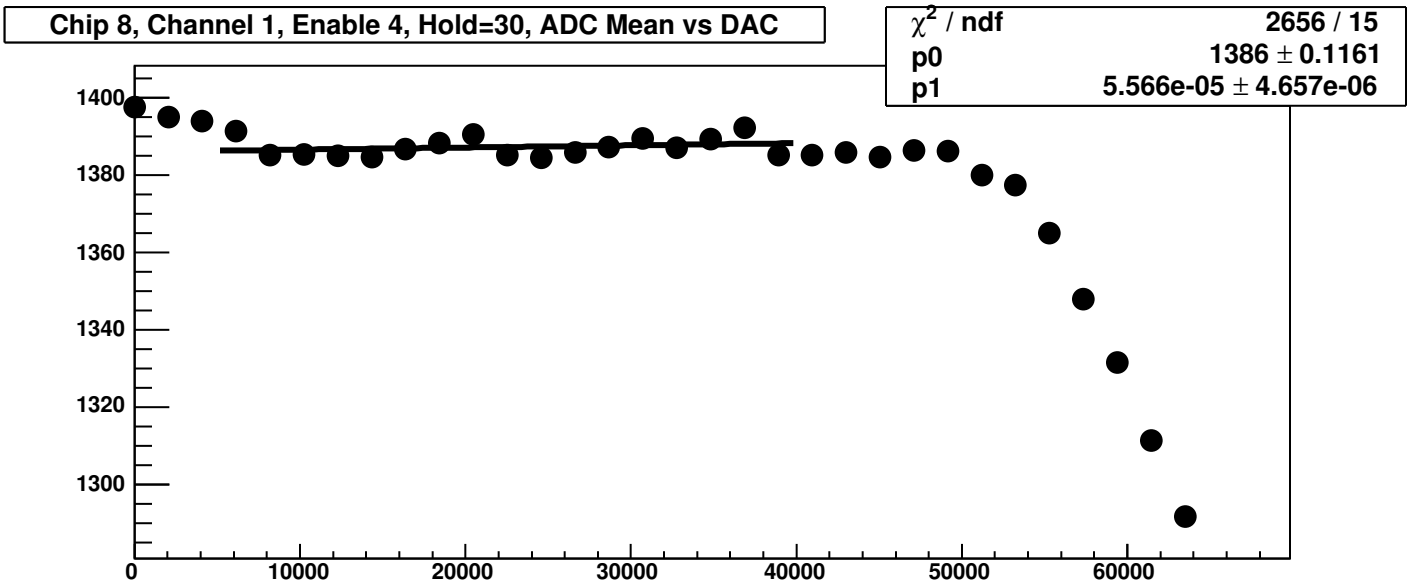

Chip 8, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

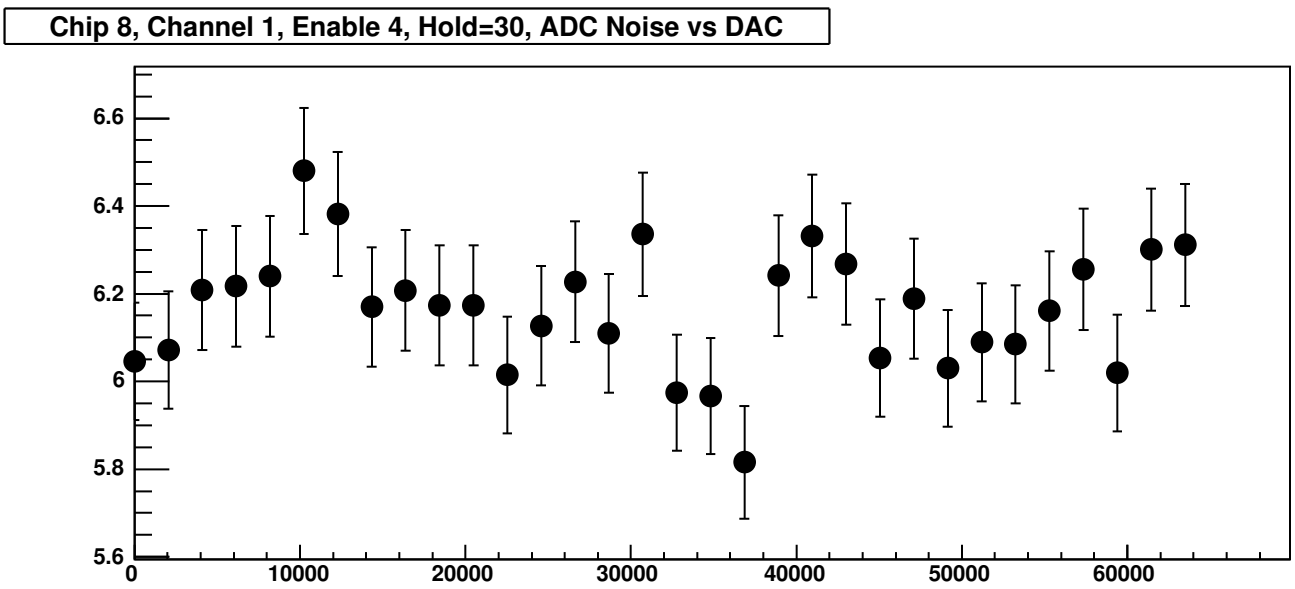

Chip 8, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

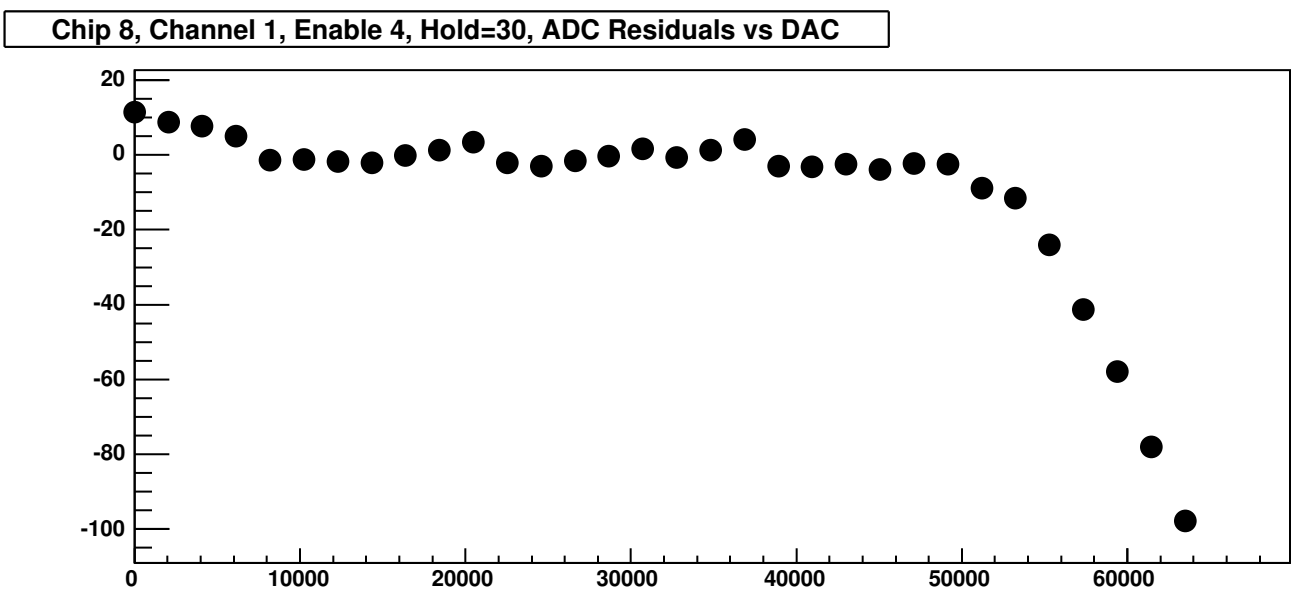

Chip 8, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.395e+04 / 15
p0 1496 ± 0.2825
p1 0.6748 ± 3.032e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

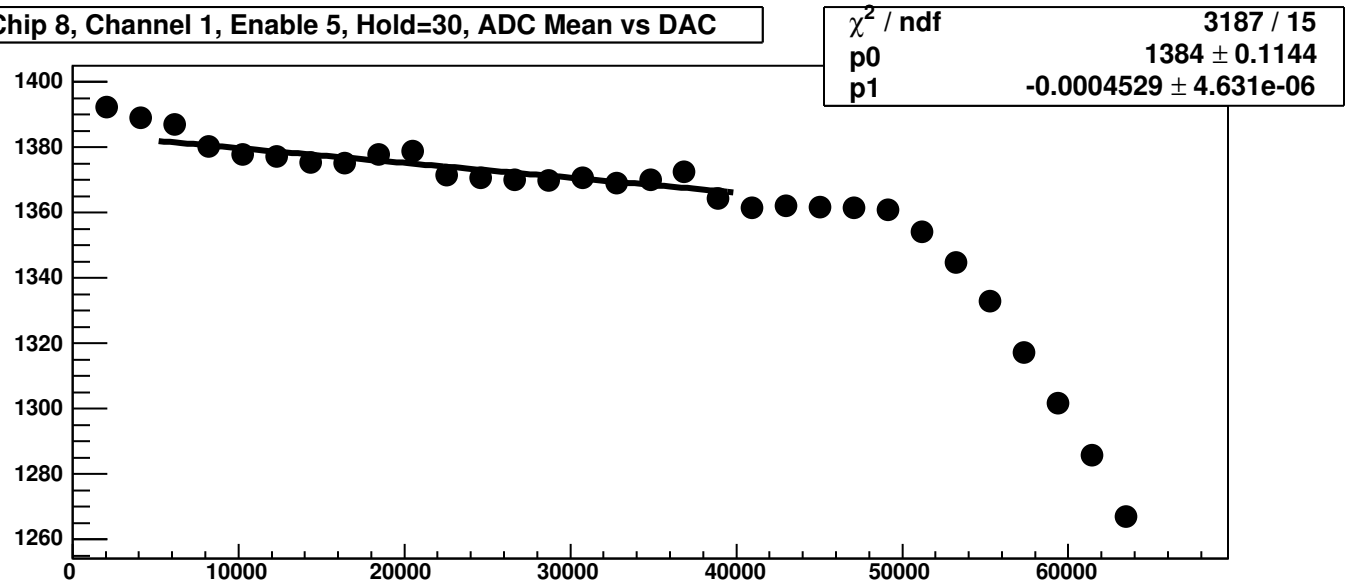

Chip 8, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 2656 / 15
p0 1386 ± 0.1161
p1 5.566e-05 ± 4.657e-06
1400
1380
1360
1340
1320
1300
0
10000
20000
30000
40000
50000
60000

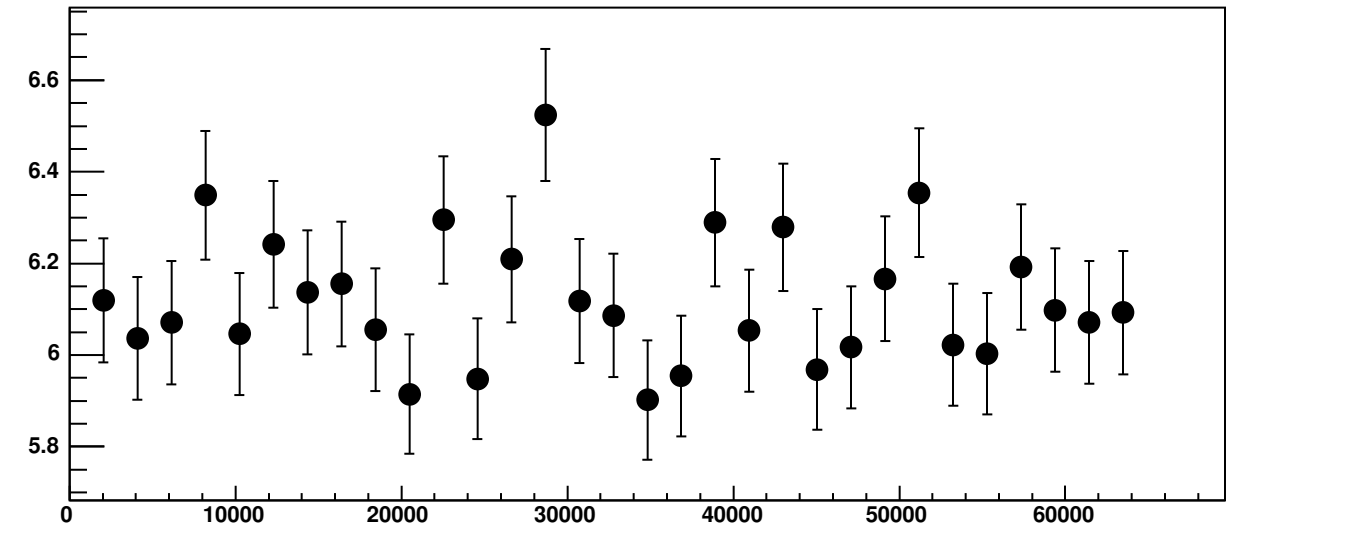

Chip 8, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

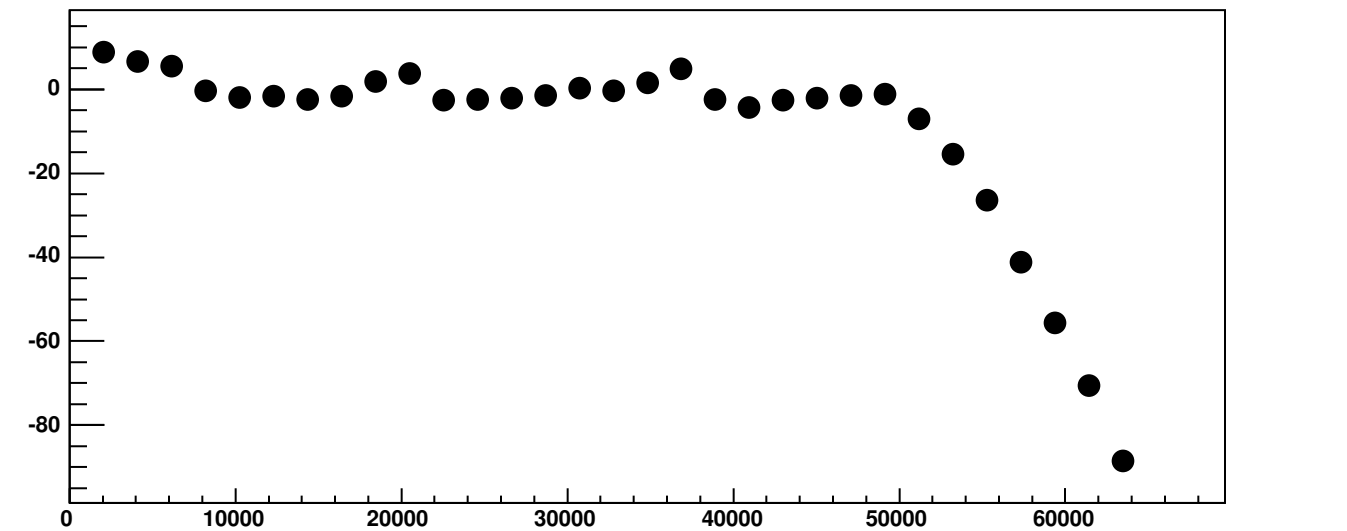
Chip 8, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC

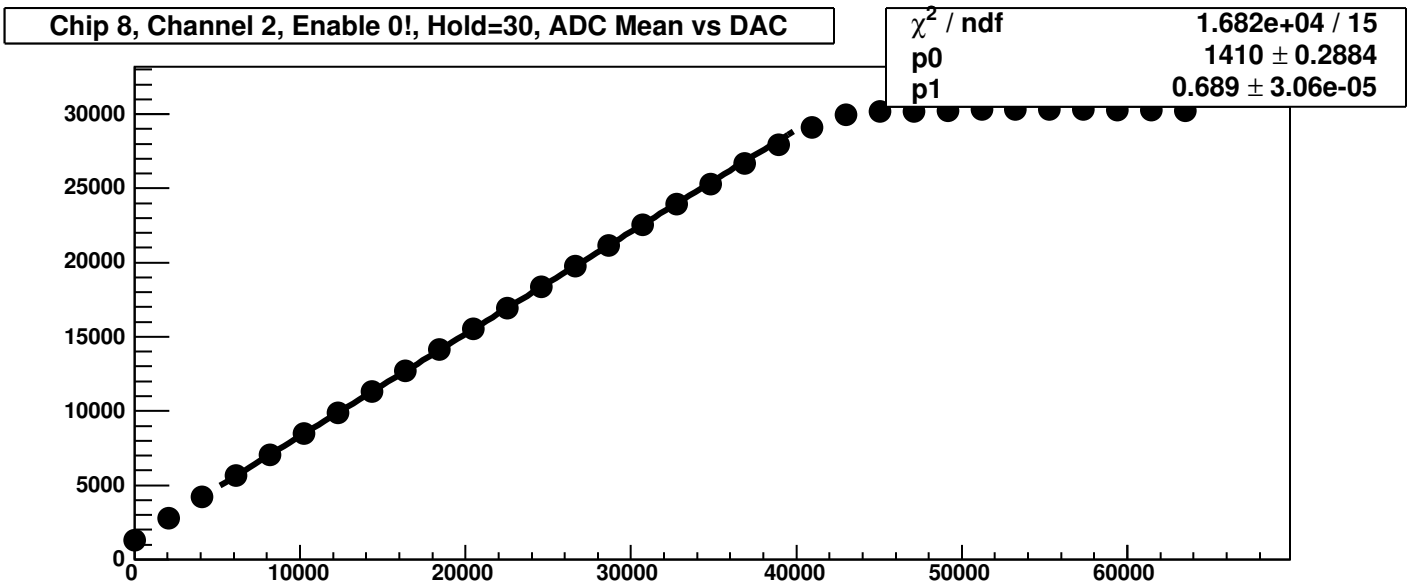


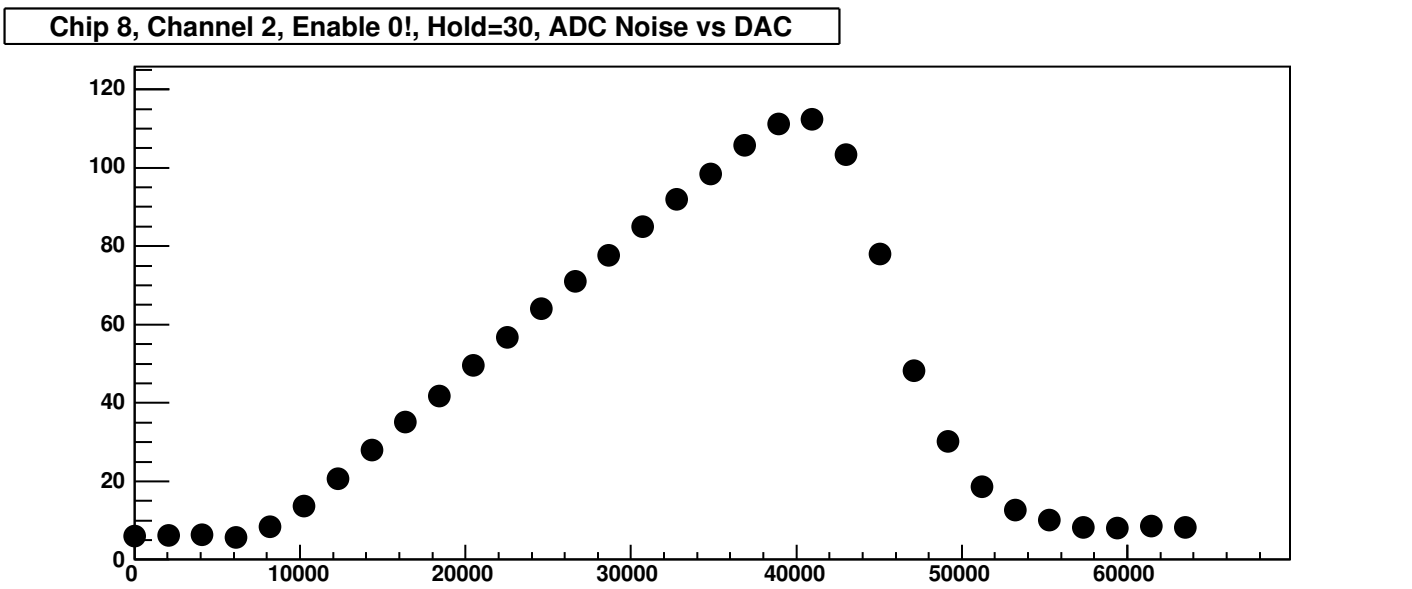
Chip 8, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC

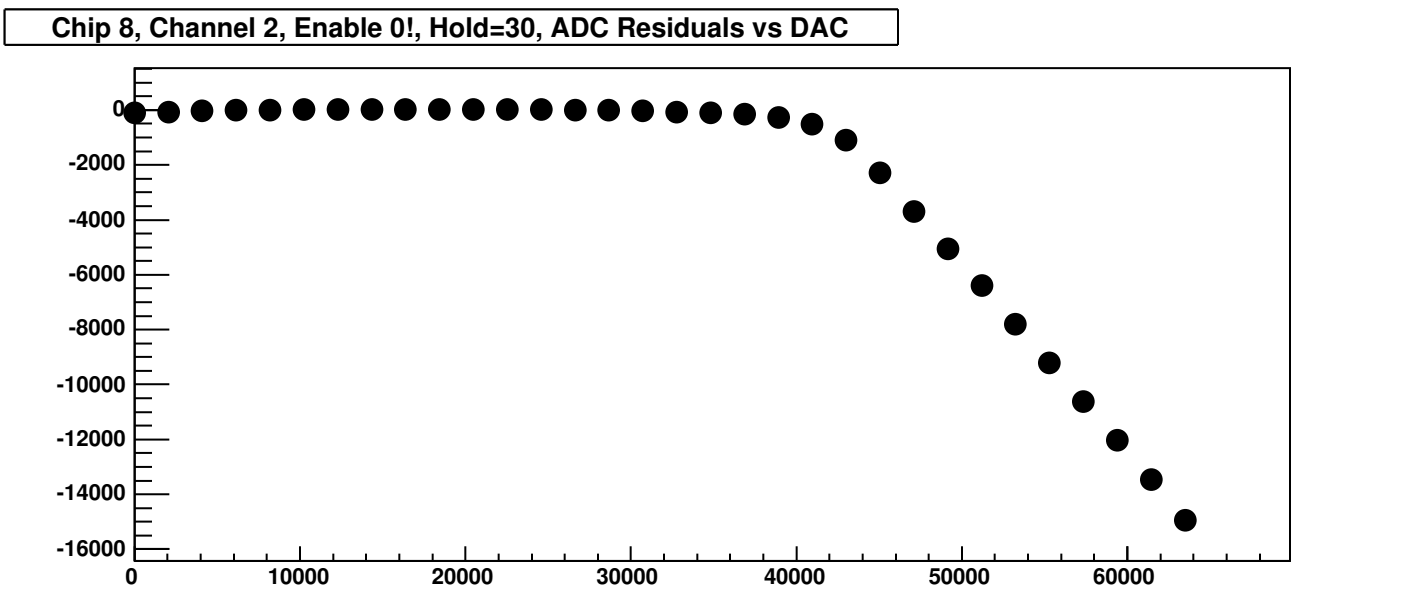


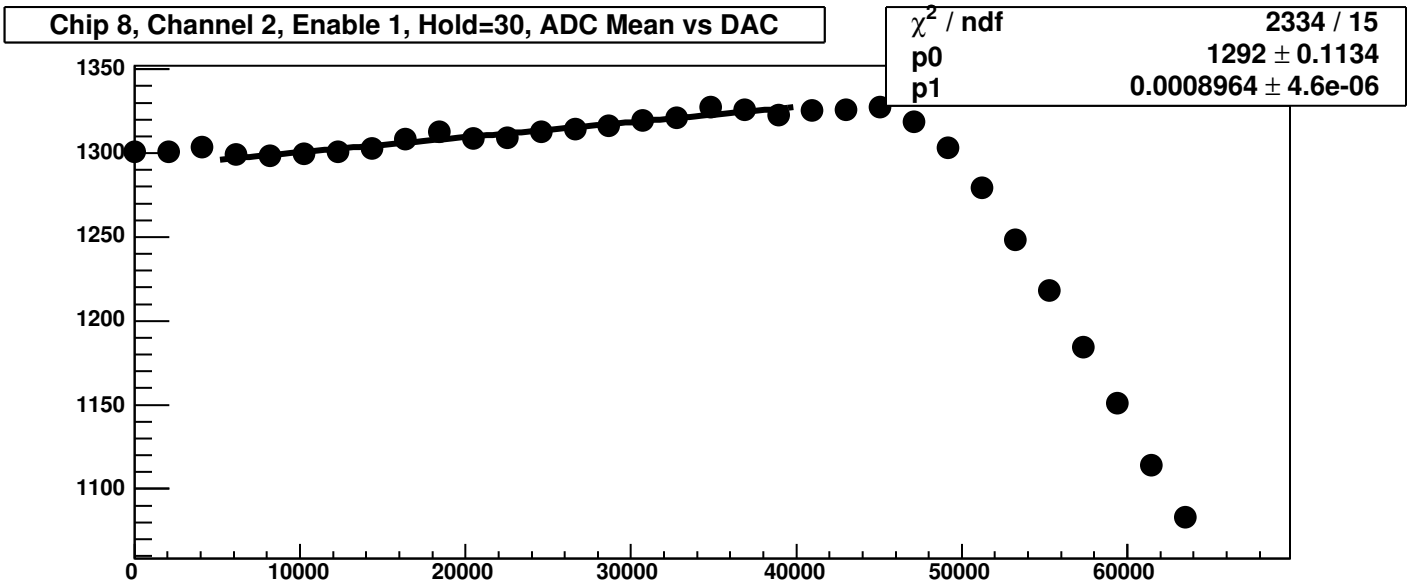
Chip 8, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC

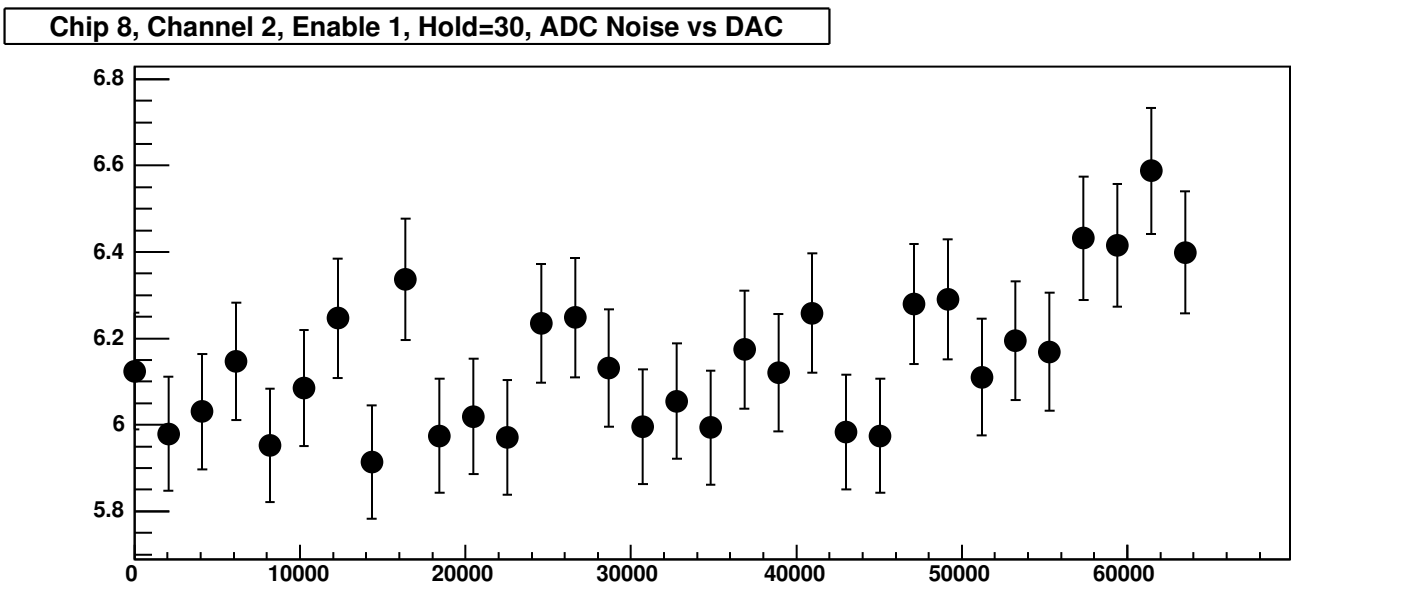


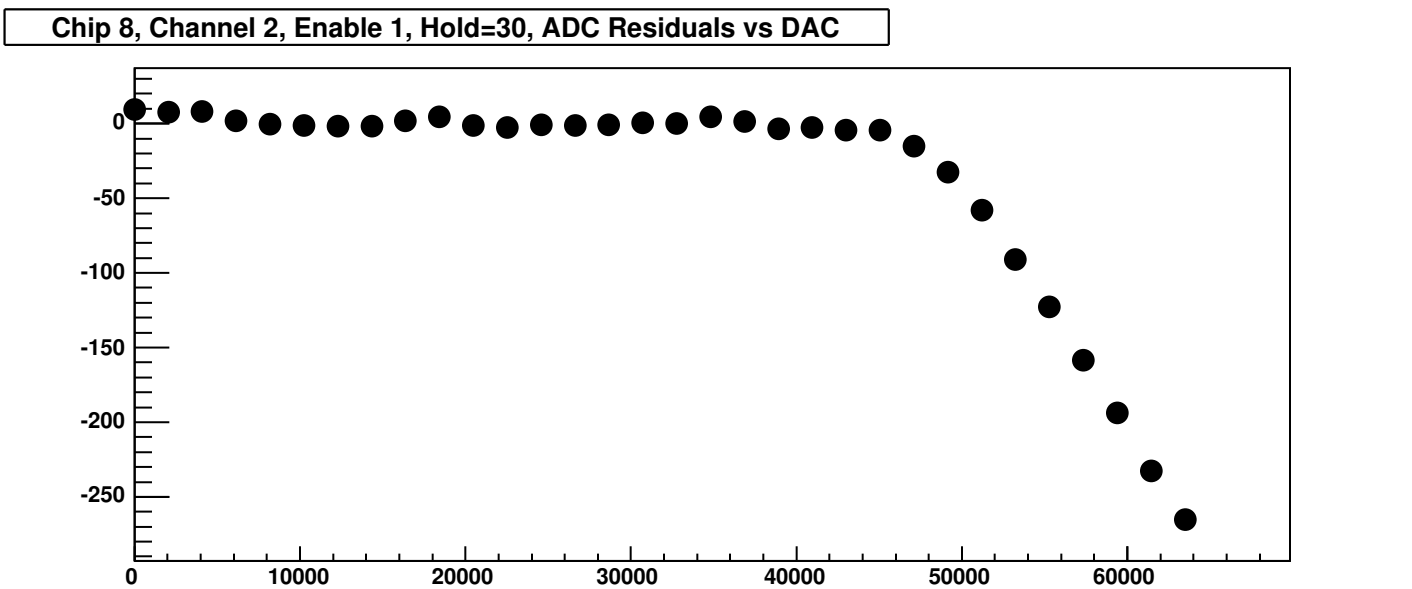

Chip 8, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.682e+04 / 15
p0 1410 ± 0.2884
p1 0.689 ± 3.06e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

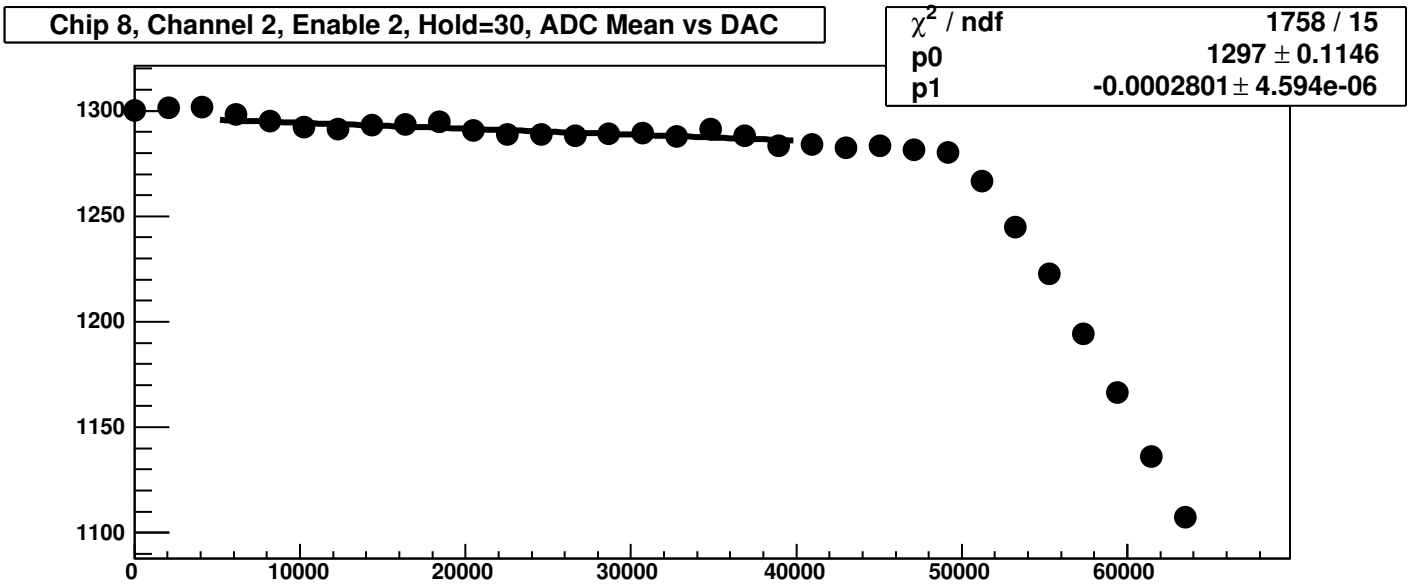

Chip 8, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

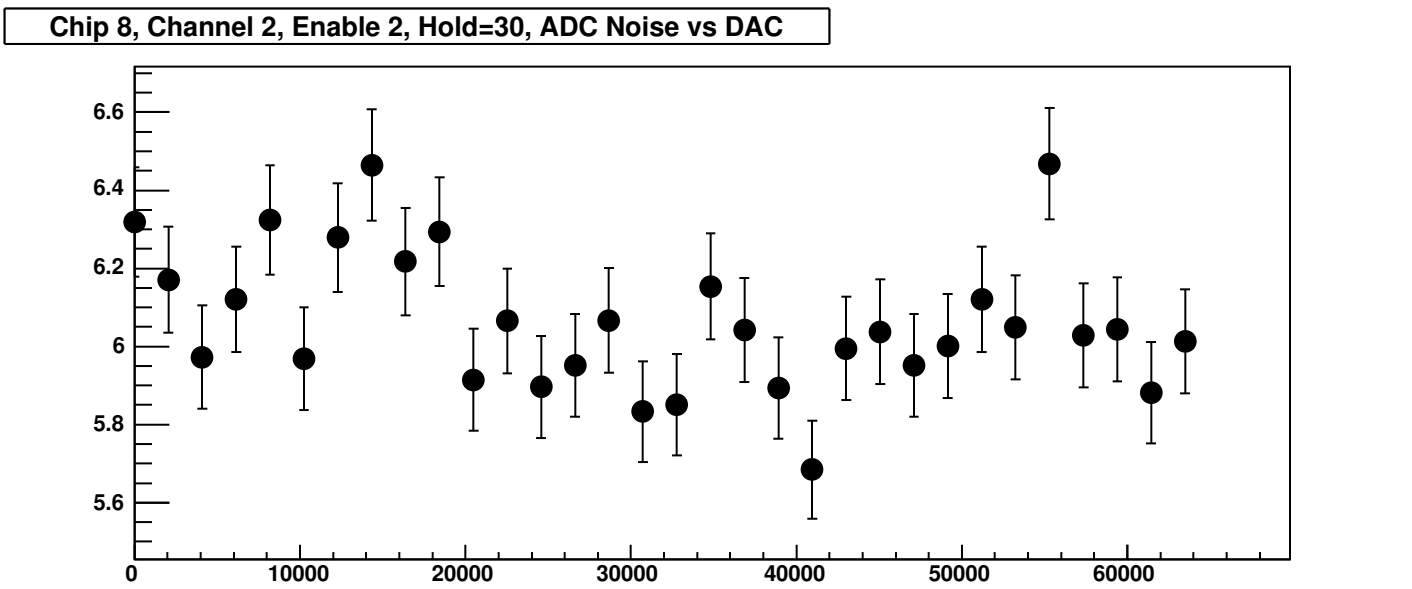

Chip 8, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
10000
20000
30000
40000
50000
60000

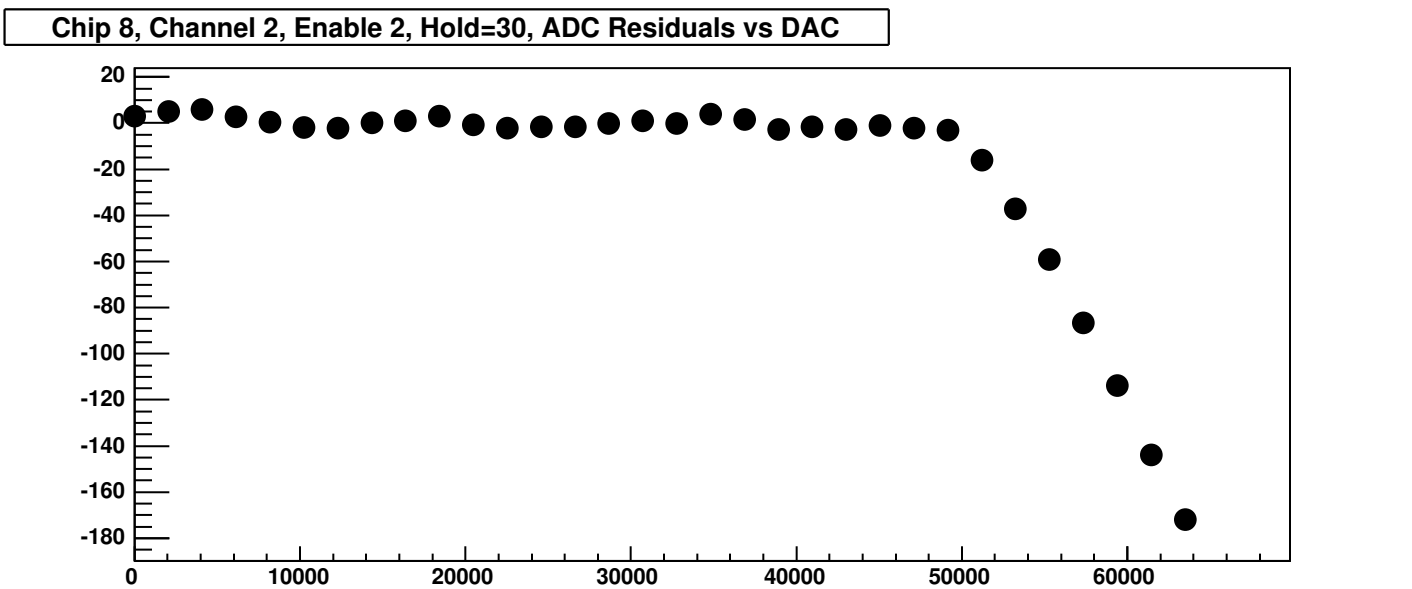

Chip 8, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2334 / 15
p0 1292 ± 0.1134
p1 0.0008964 ± 4.6e-06
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

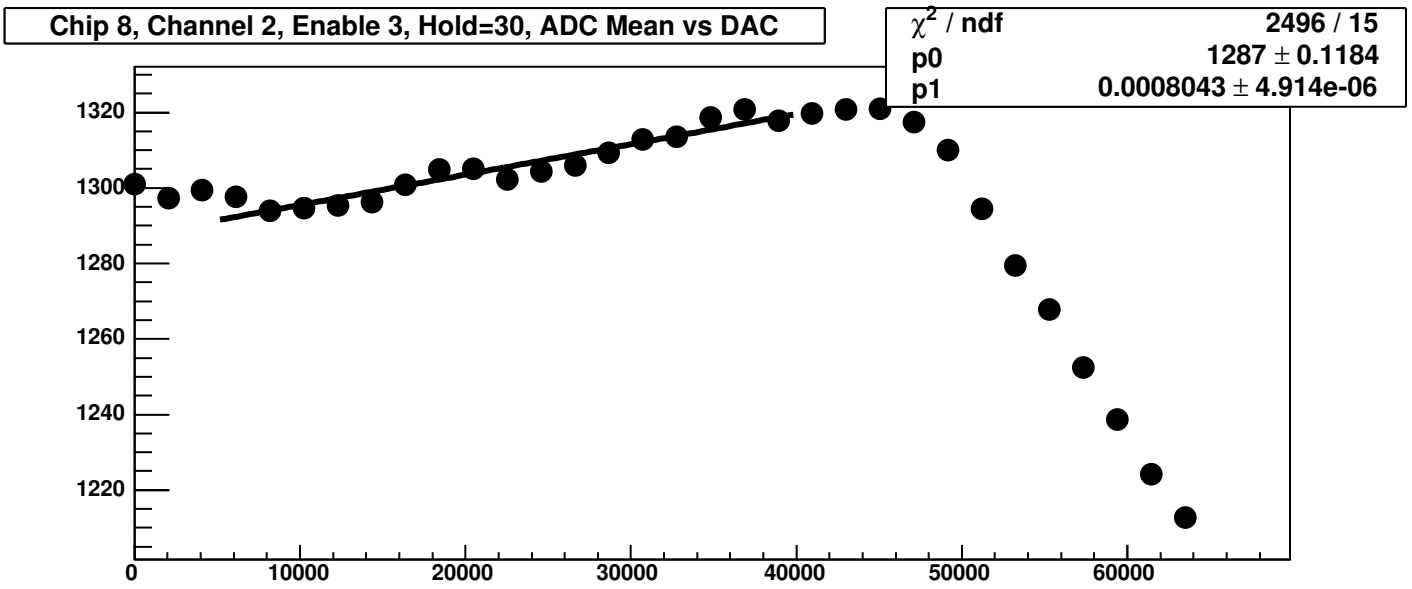

Chip 8, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

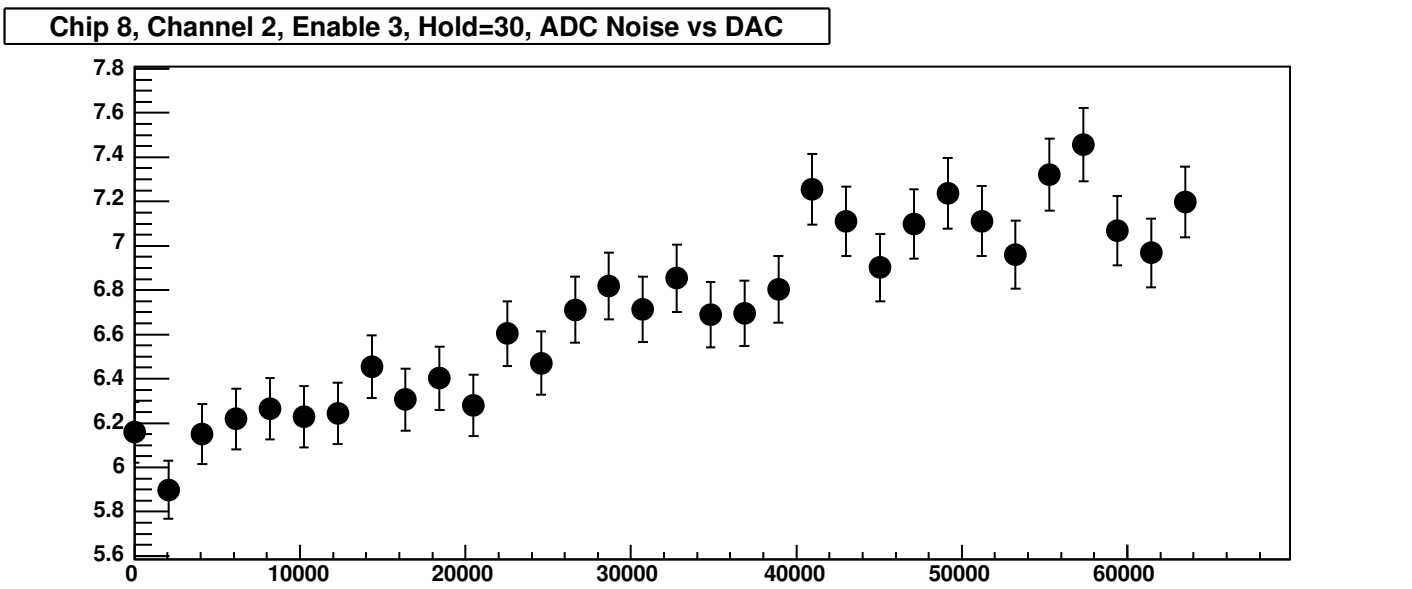

Chip 8, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

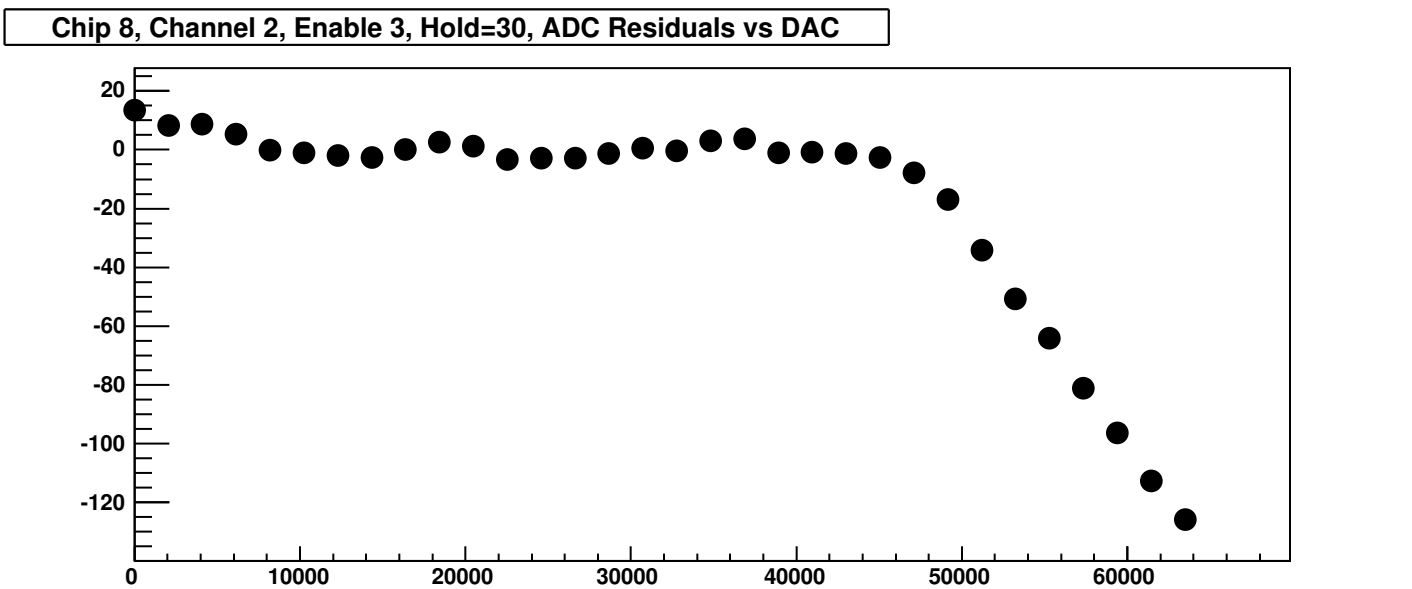

Chip 8, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1758 / 15
p0 1297 ± 0.1146
p1 -0.0002801 ± 4.594e-06
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

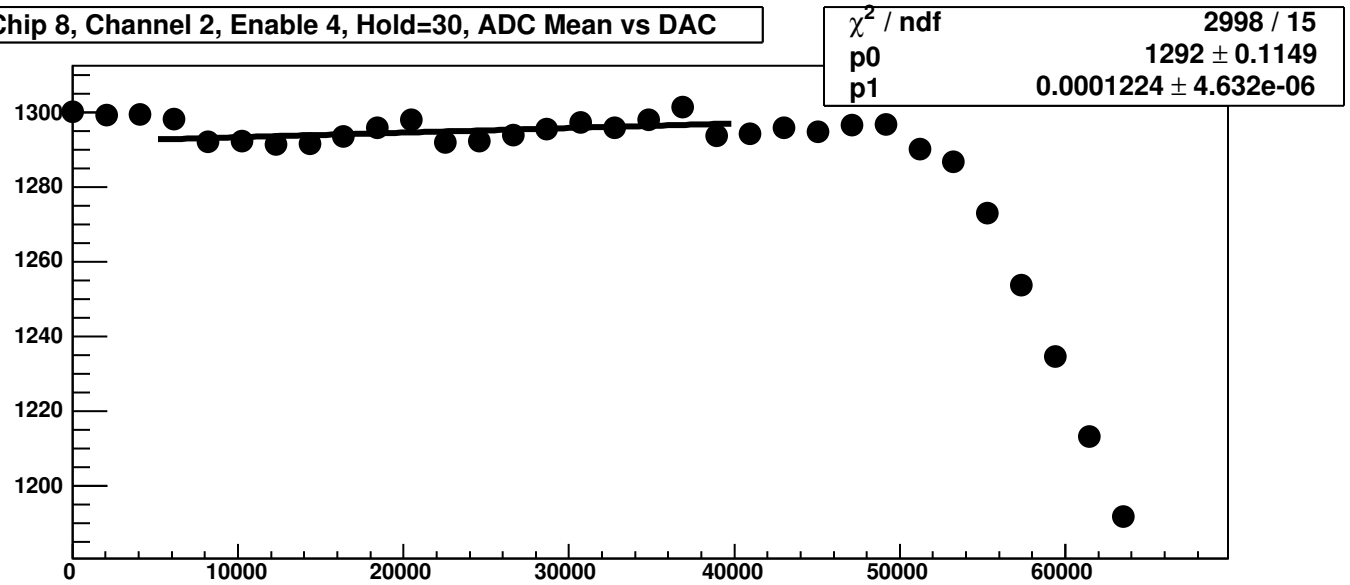

Chip 8, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2496 / 15
p0 1287 ± 0.1184
p1 0.0008043 ± 4.914e-06
1320
1300
1280
1260
1240
1220
0
10000
20000
30000
40000
50000
60000

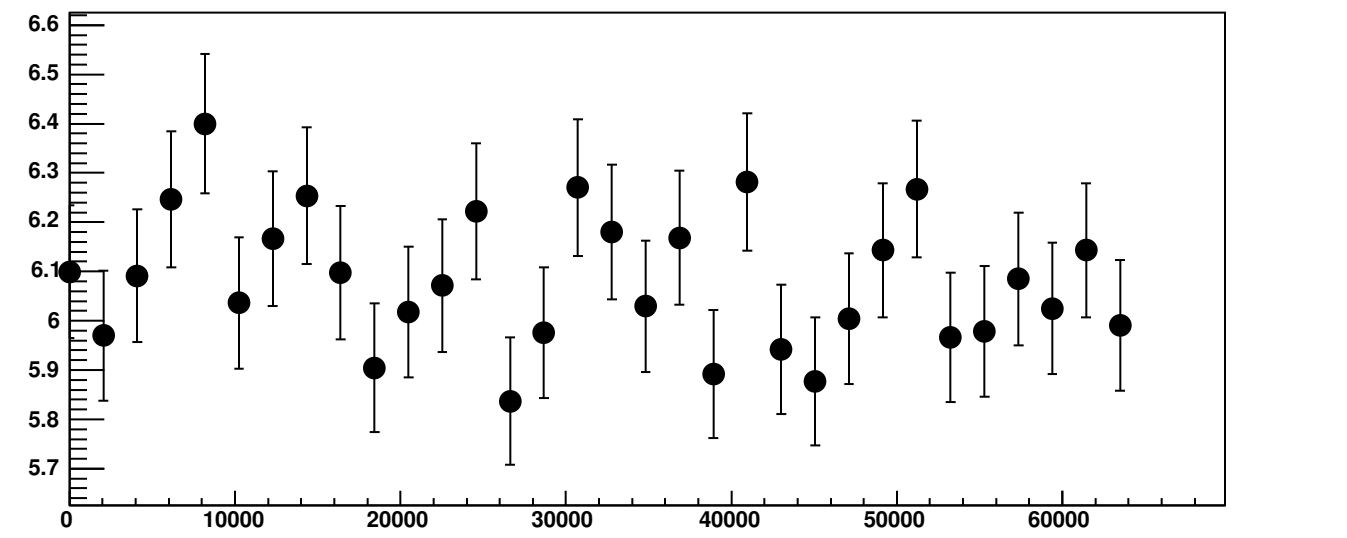

Chip 8, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

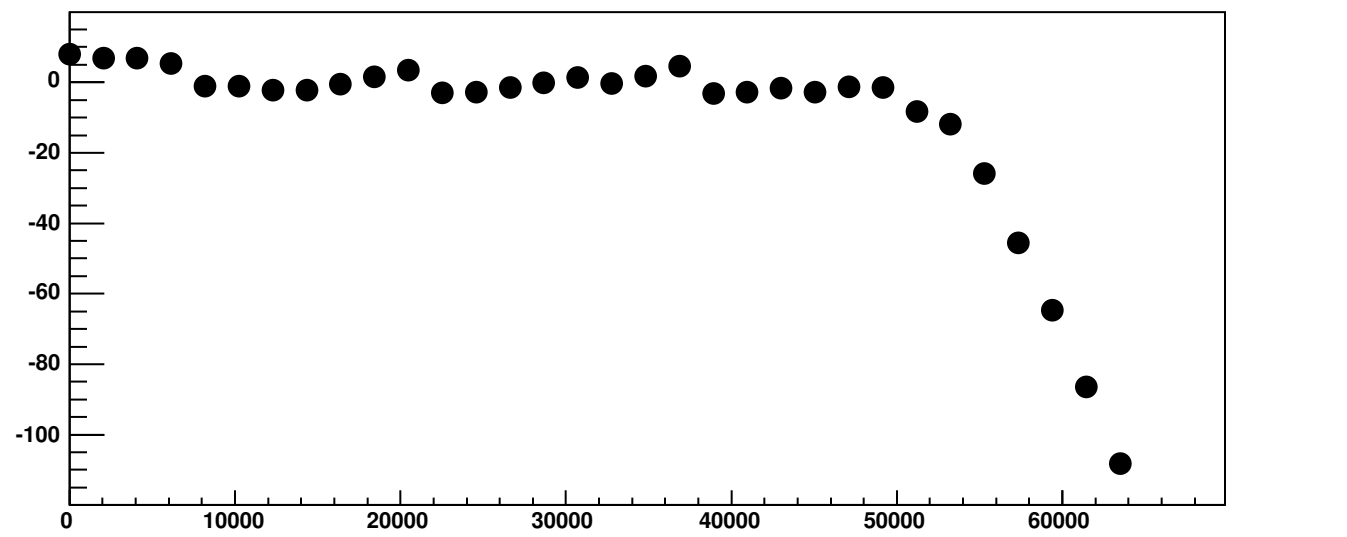
Chip 8, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC



Chip 8, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC

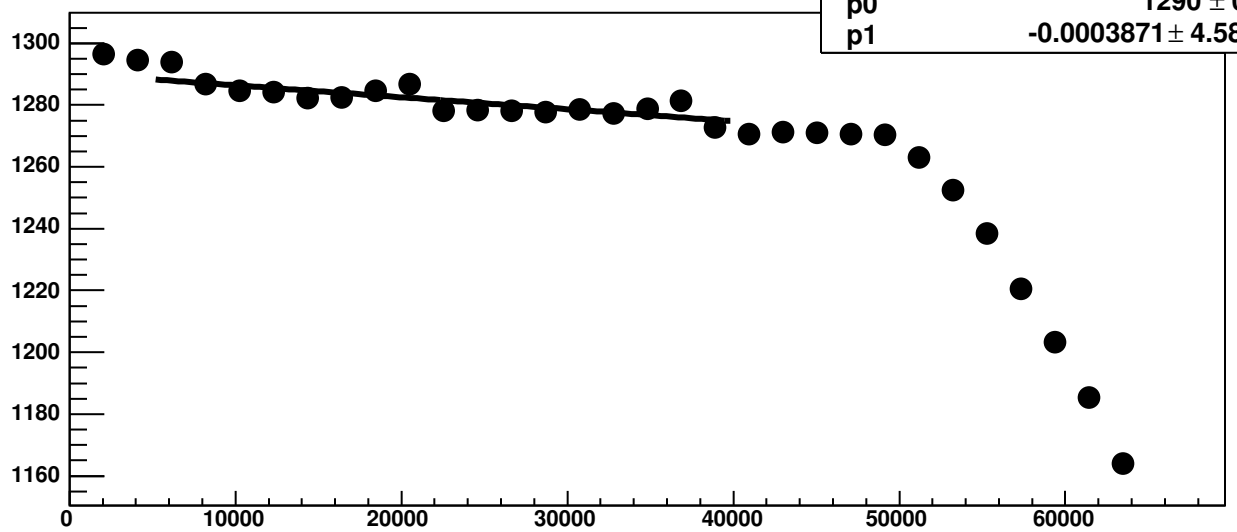


Chip 8, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

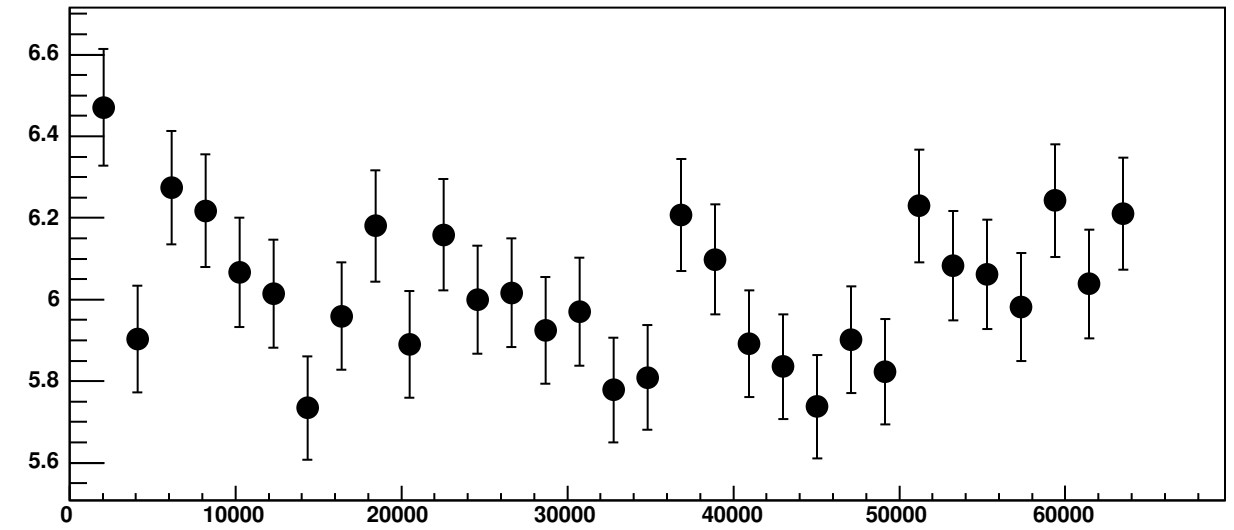


Chip 8, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC

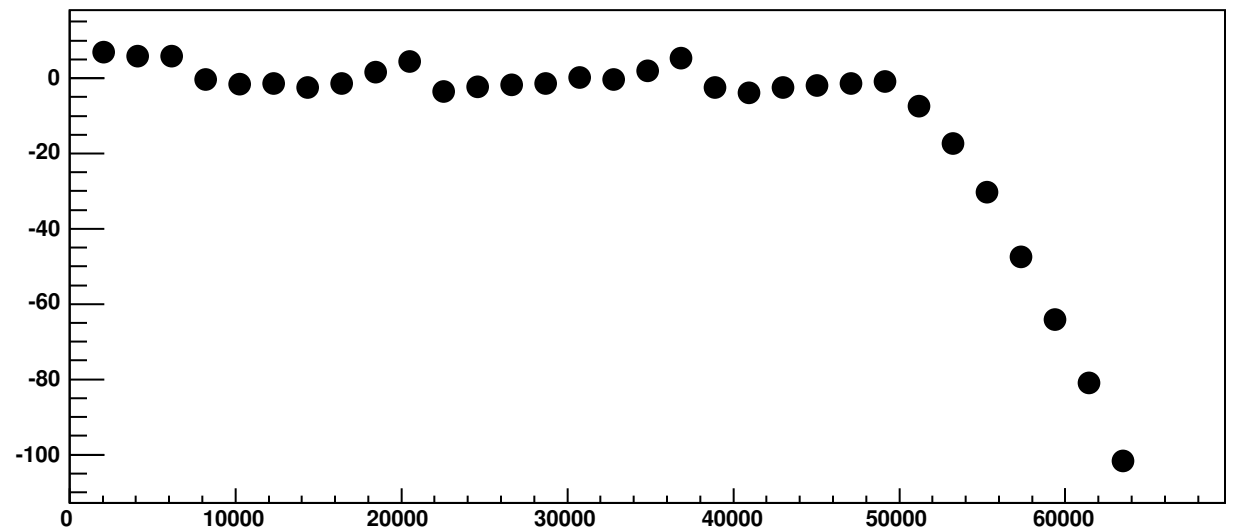
χ^2 / ndf	3604 / 15
p0	1290 ± 0.1134
p1	-0.0003871 ± 4.583e-06

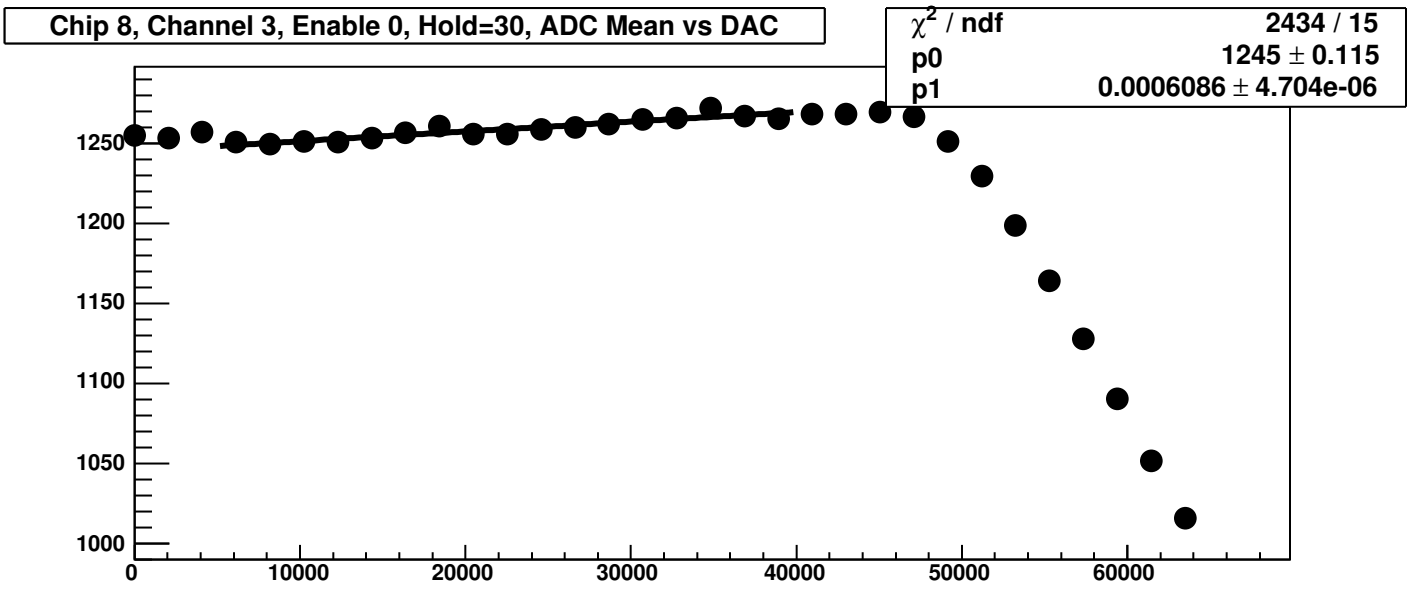


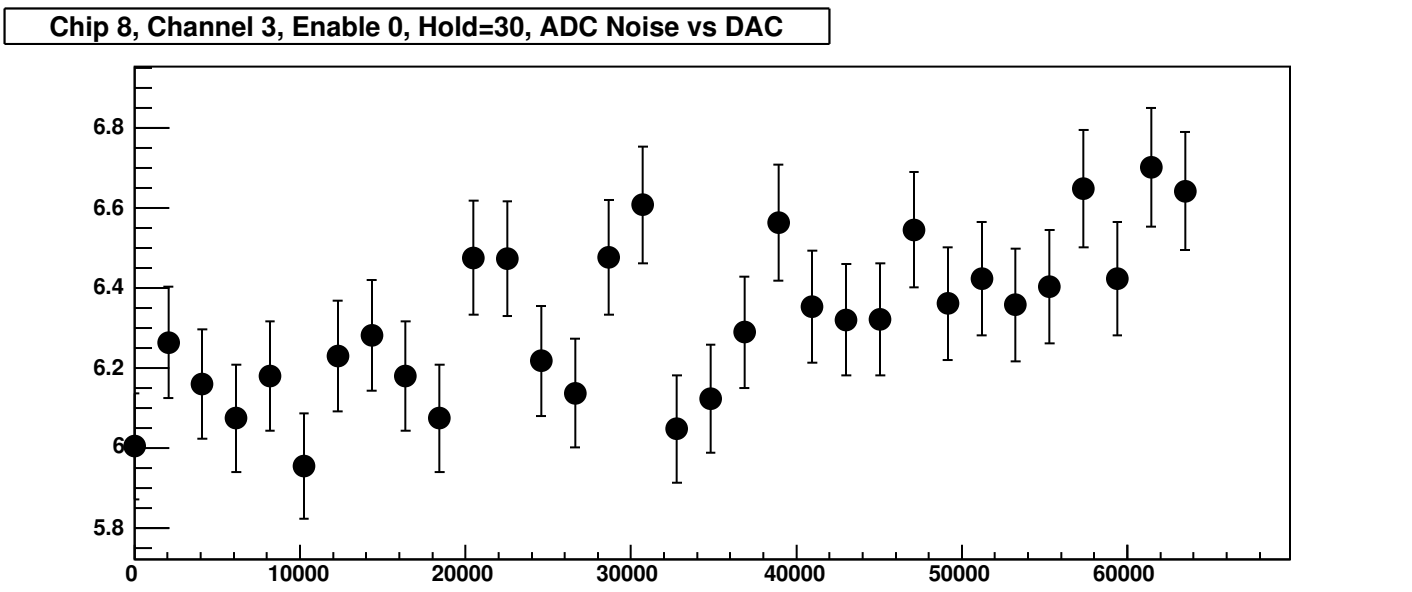
Chip 8, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

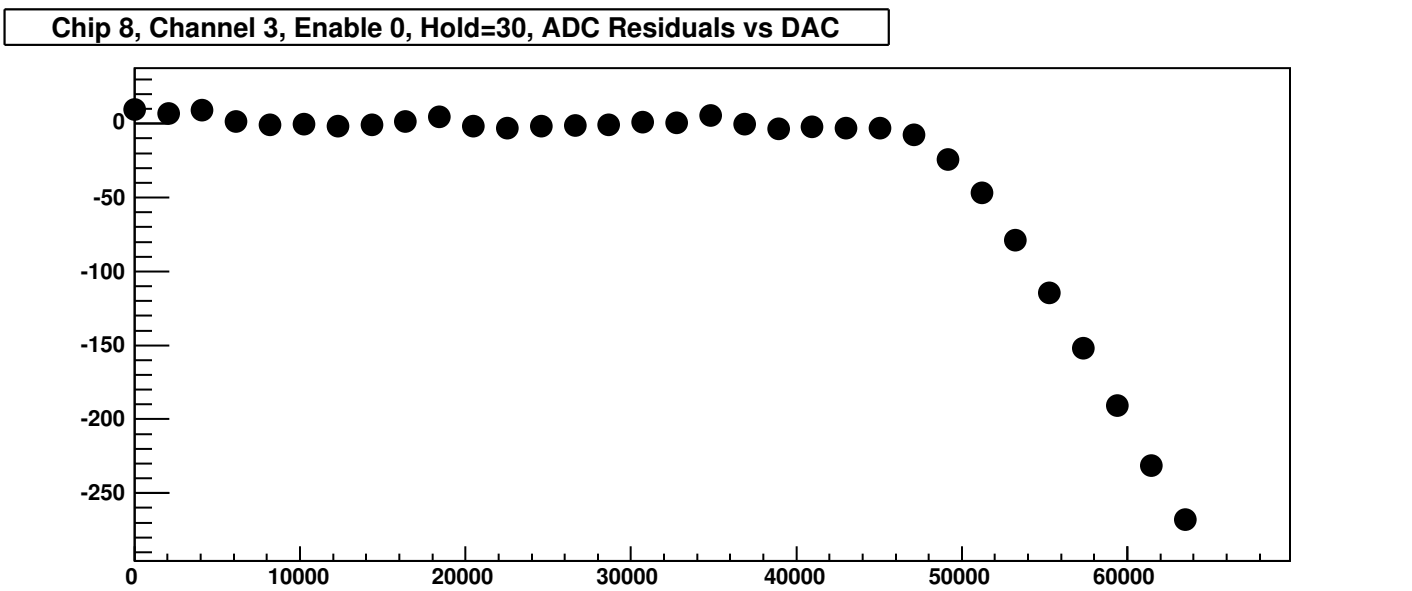


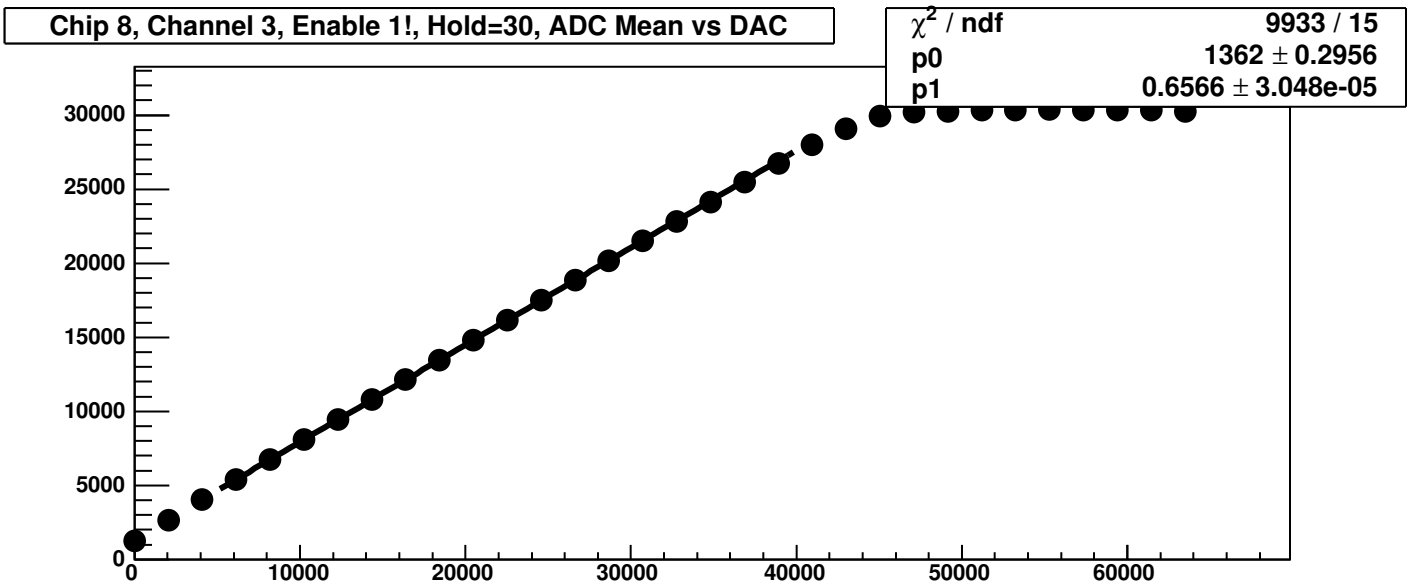
Chip 8, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC

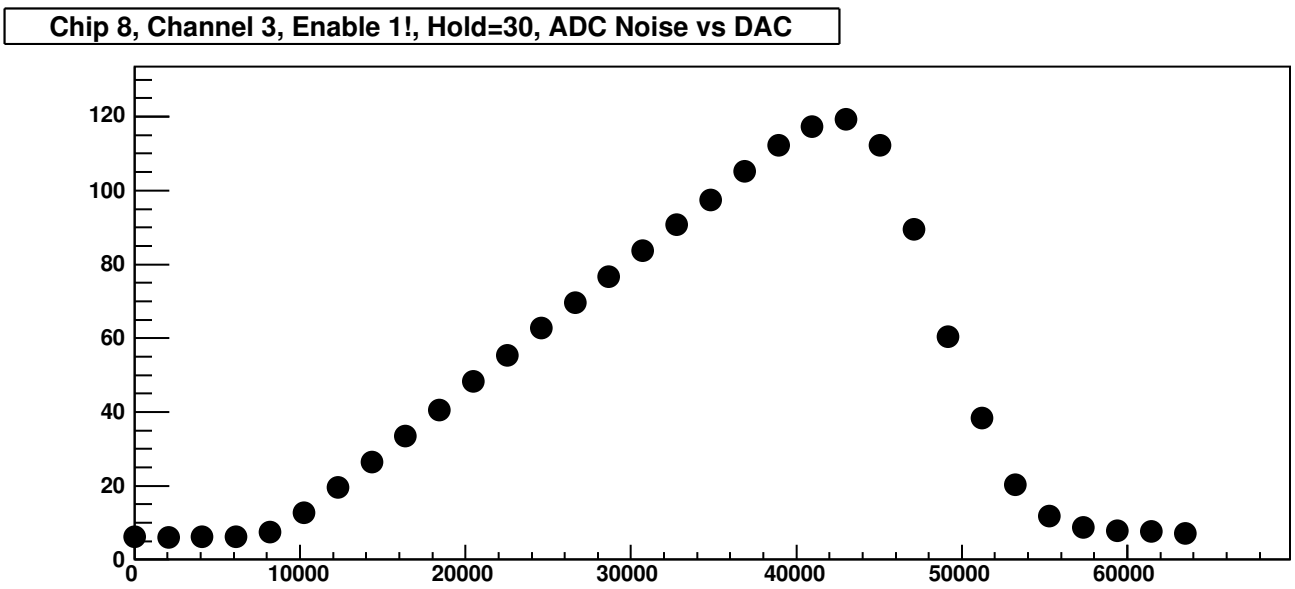


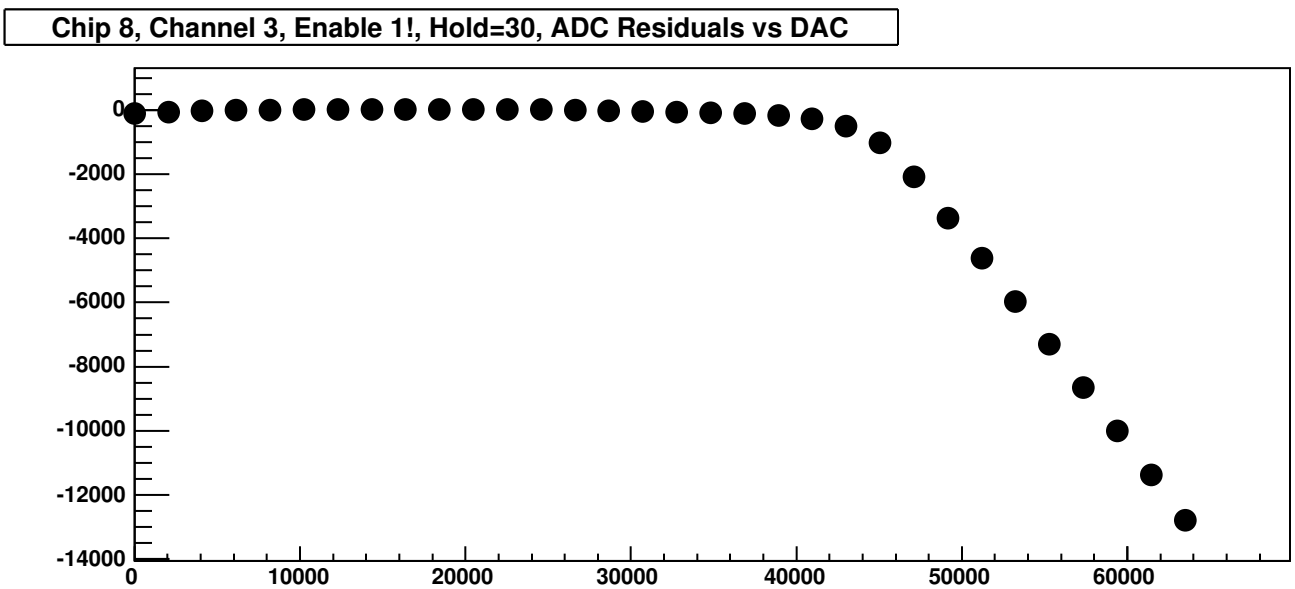

Chip 8, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2434 / 15
p0 1245 ± 0.115
p1 0.0006086 ± 4.704e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

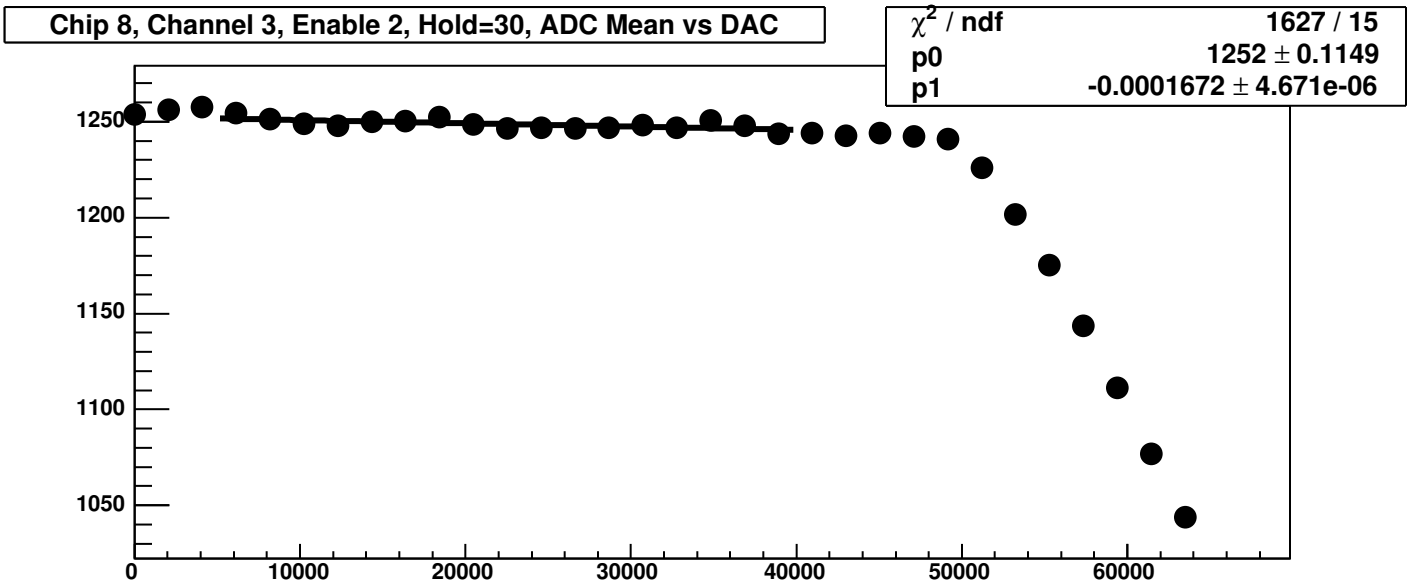

Chip 8, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

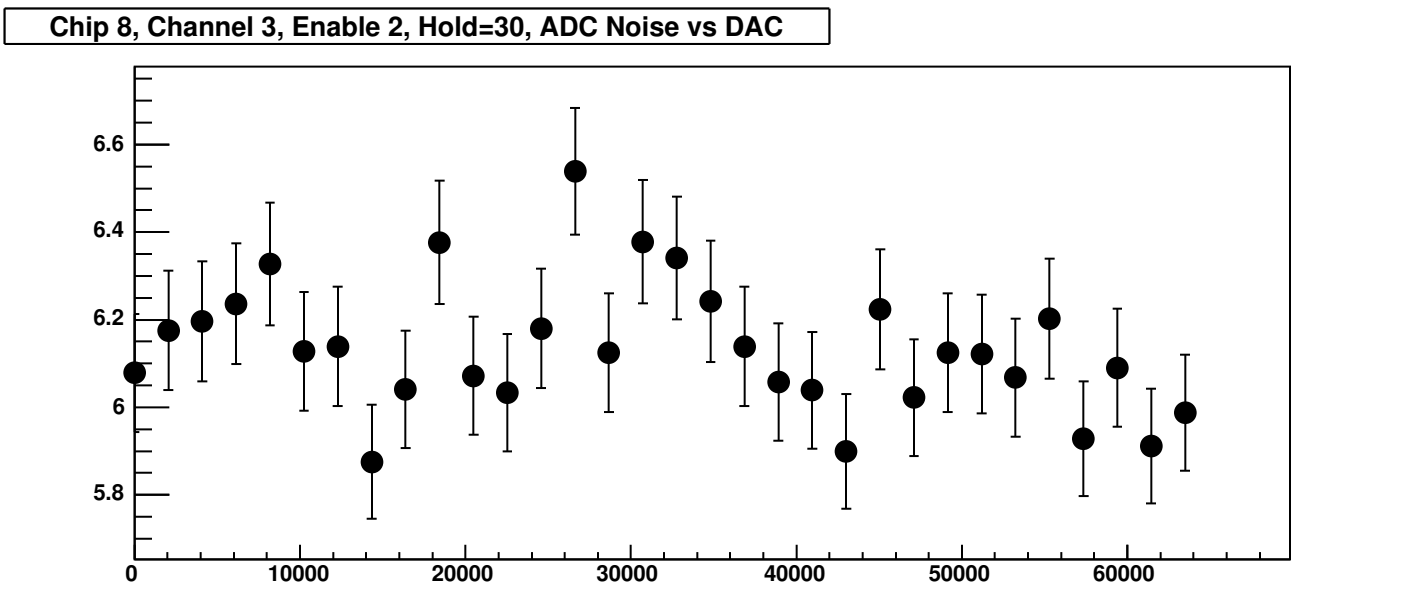

Chip 8, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

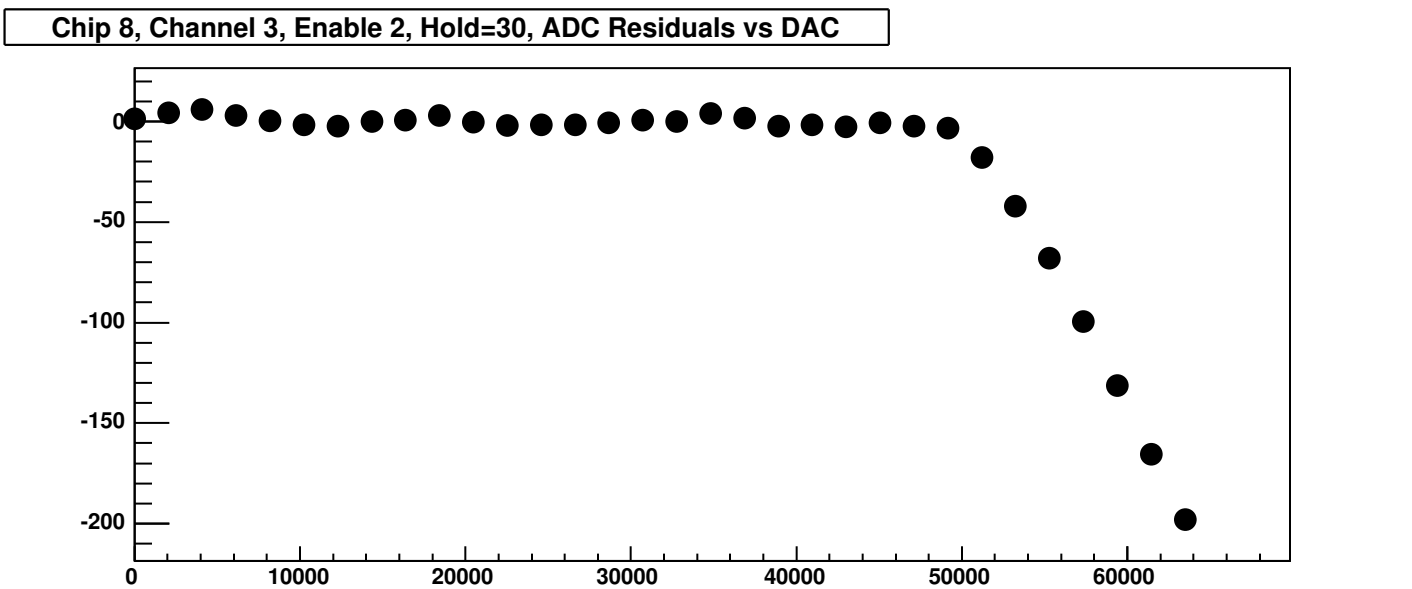

Chip 8, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 9933 / 15
p0 1362 ± 0.2956
p1 0.6566 ± 3.048e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

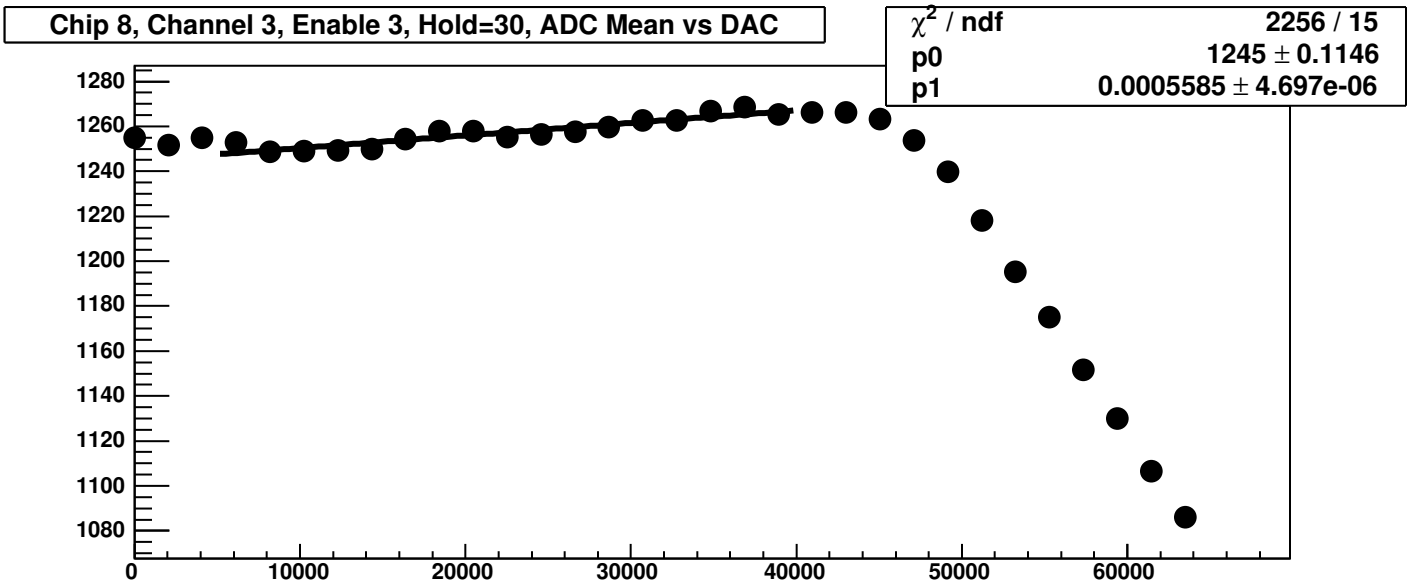

Chip 8, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

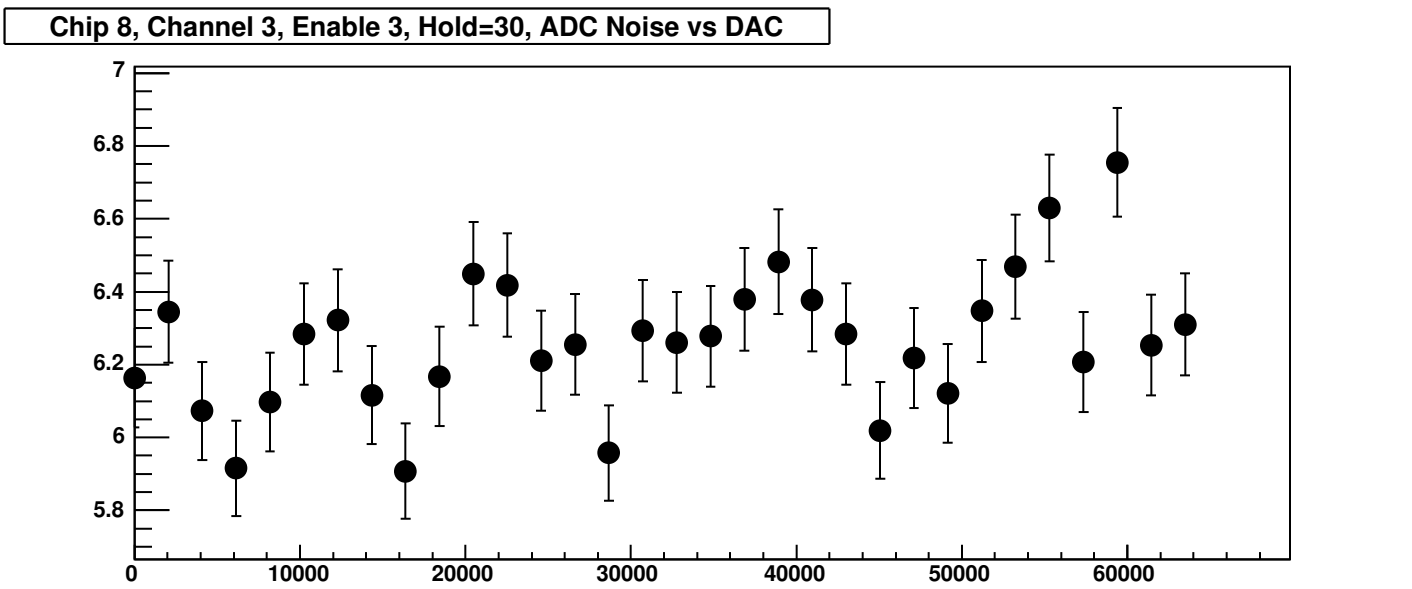

Chip 8, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

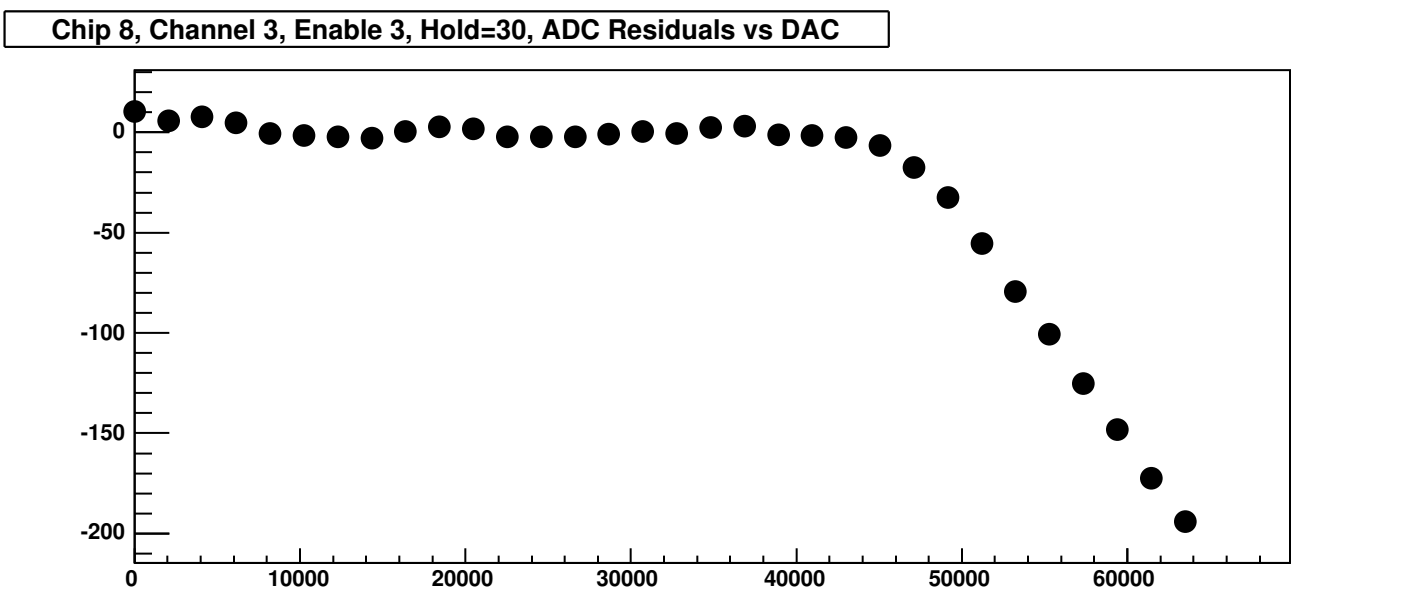

Chip 8, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1627 / 15
p0 1252 ± 0.1149
p1 -0.0001672 ± 4.671e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

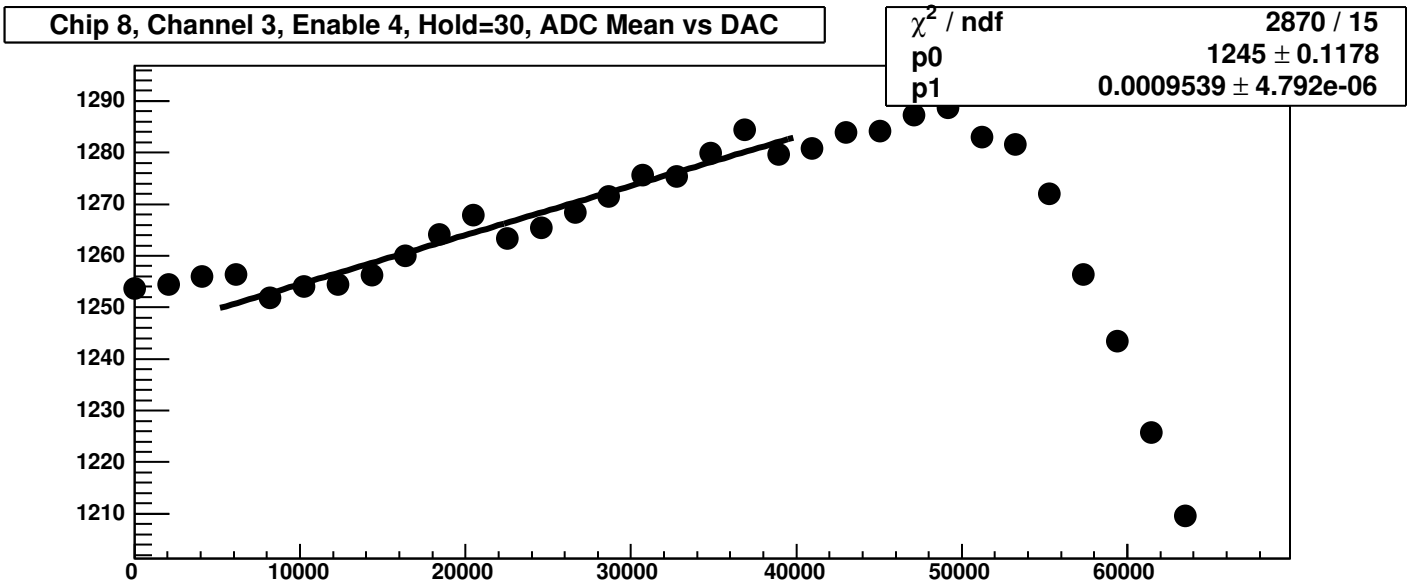

Chip 8, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

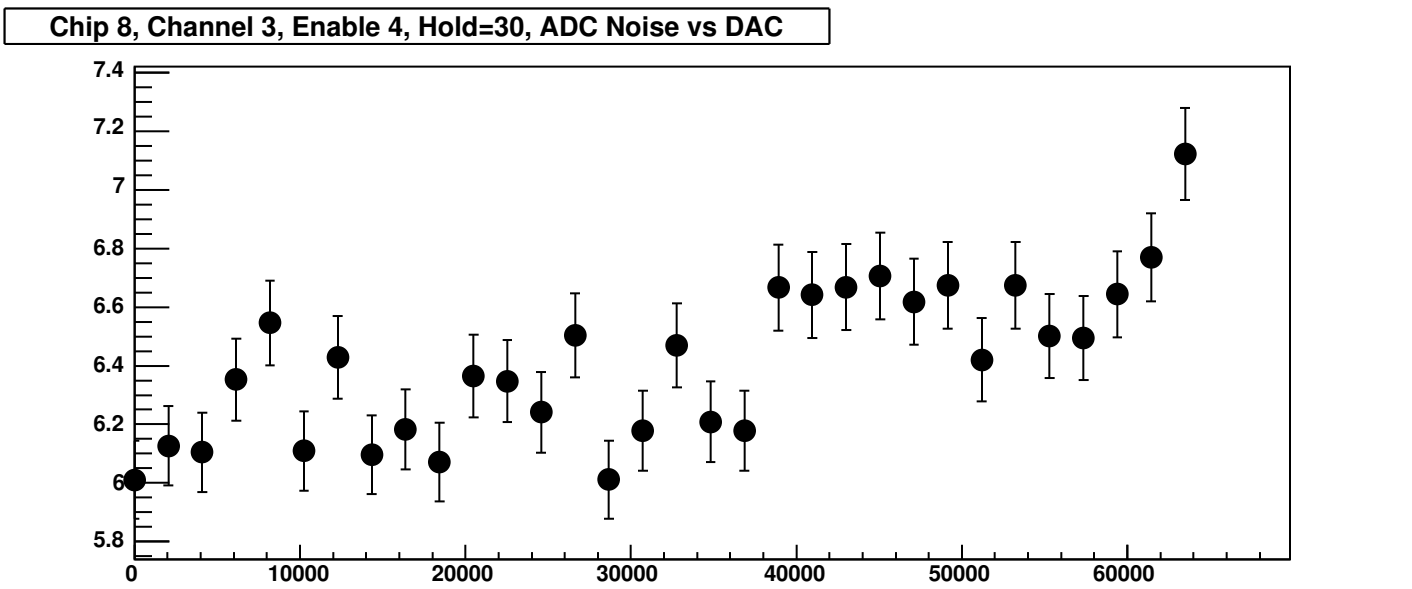

Chip 8, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

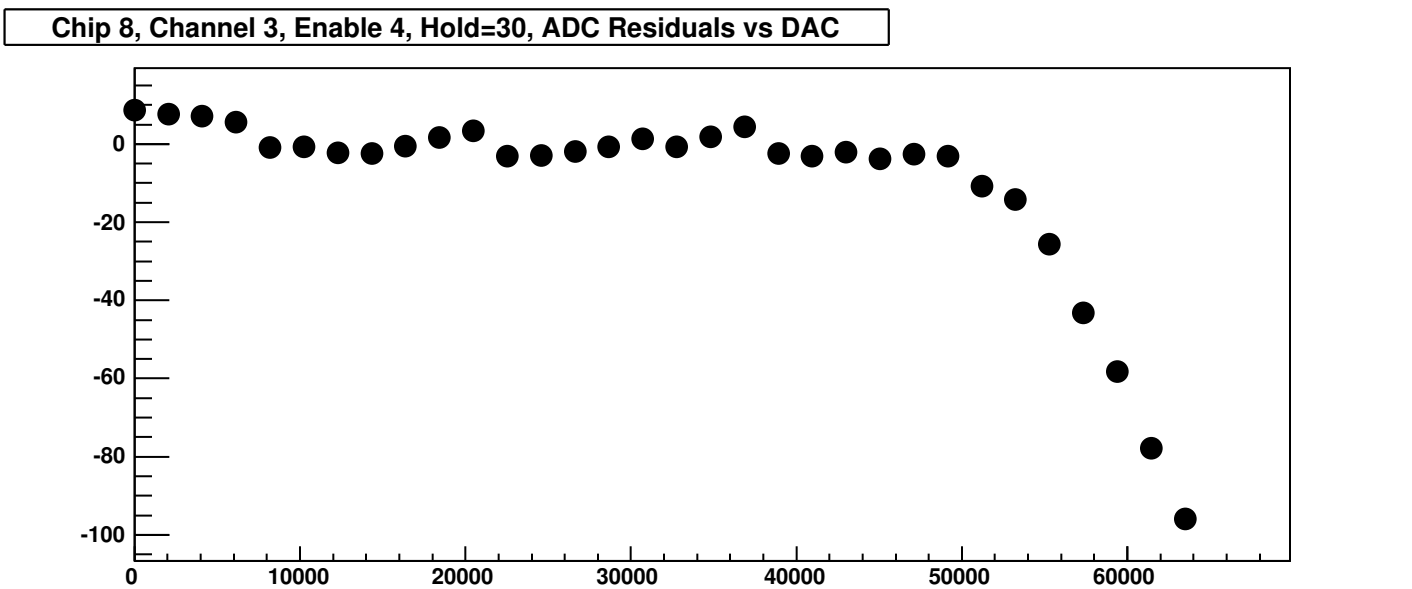

Chip 8, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2256 / 15
p0 1245 ± 0.1146
p1 0.0005585 ± 4.697e-06
1280
1260
1240
1220
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

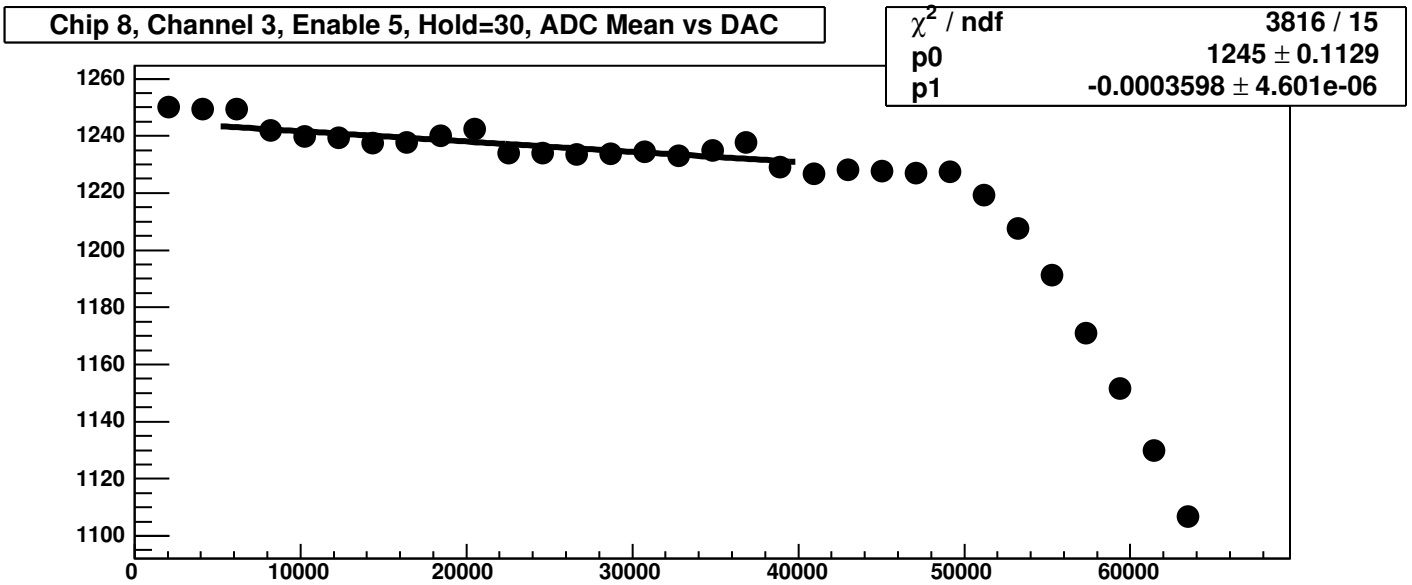

Chip 8, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

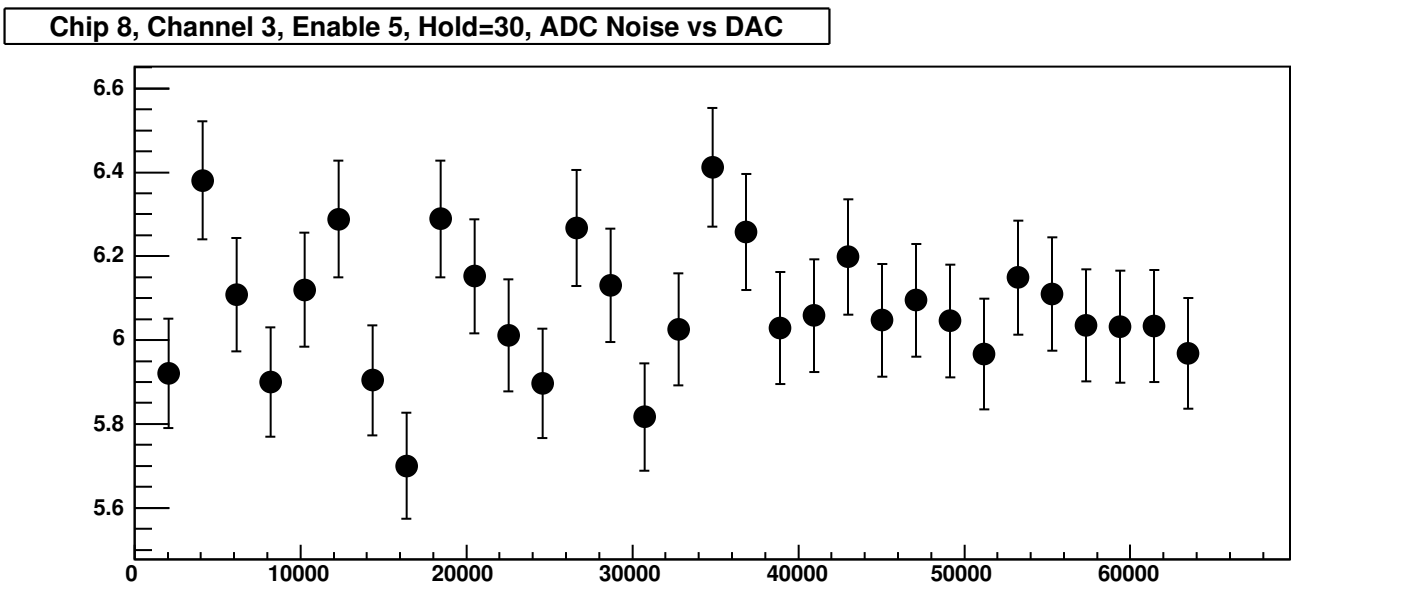

Chip 8, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

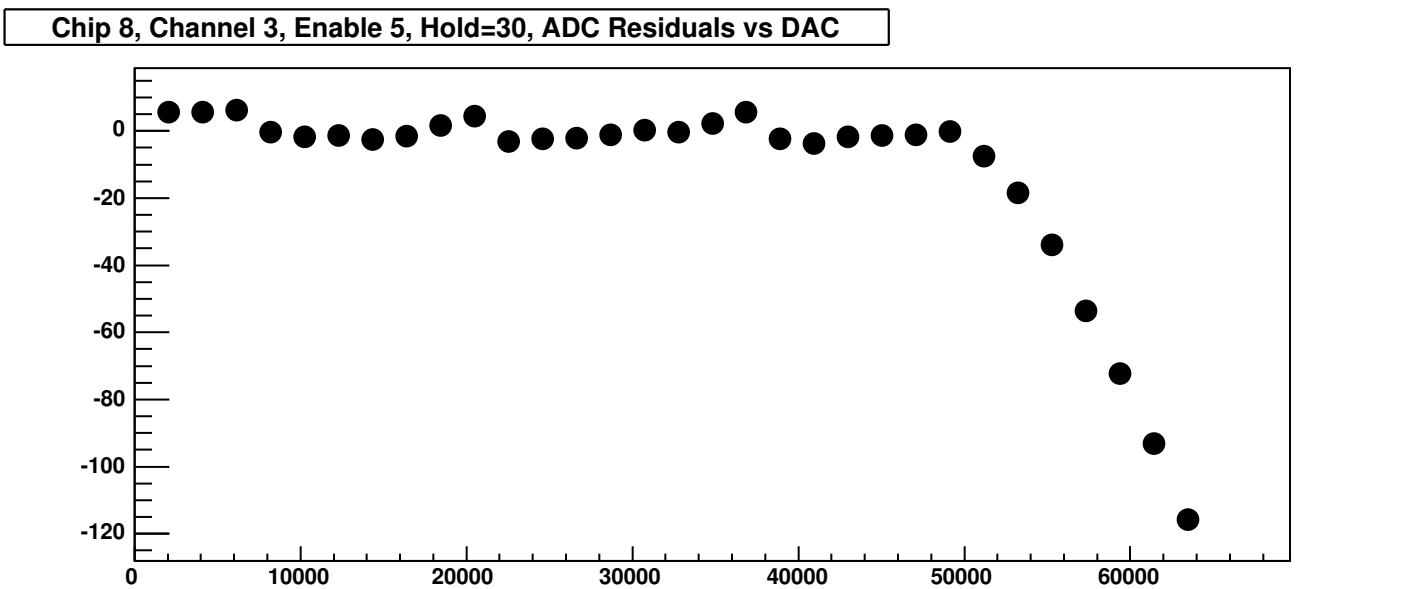

Chip 8, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 2870 / 15
p0 1245 ± 0.1178
p1 0.0009539 ± 4.792e-06
1290
1280
1270
1260
1250
1240
1230
1220
1210
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

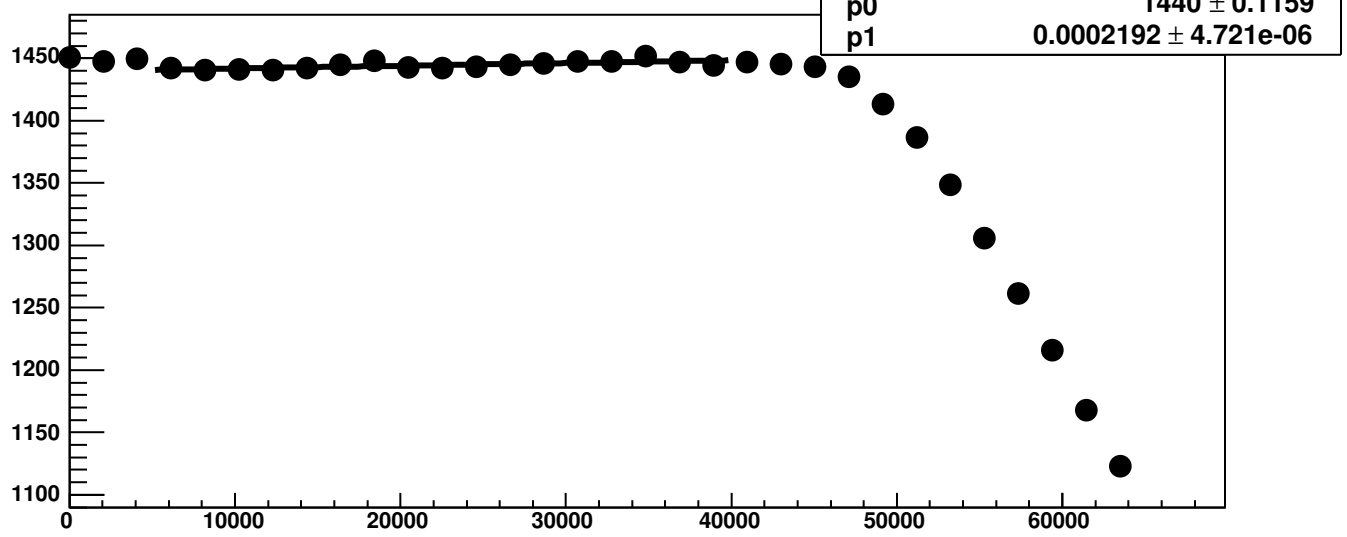

Chip 8, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3816 / 15
p0 1245 ± 0.1129
p1 -0.0003598 ± 4.601e-06
1260
1240
1220
1200
1180
1160
1140
1120
1100
0
10000
20000
30000
40000
50000
60000

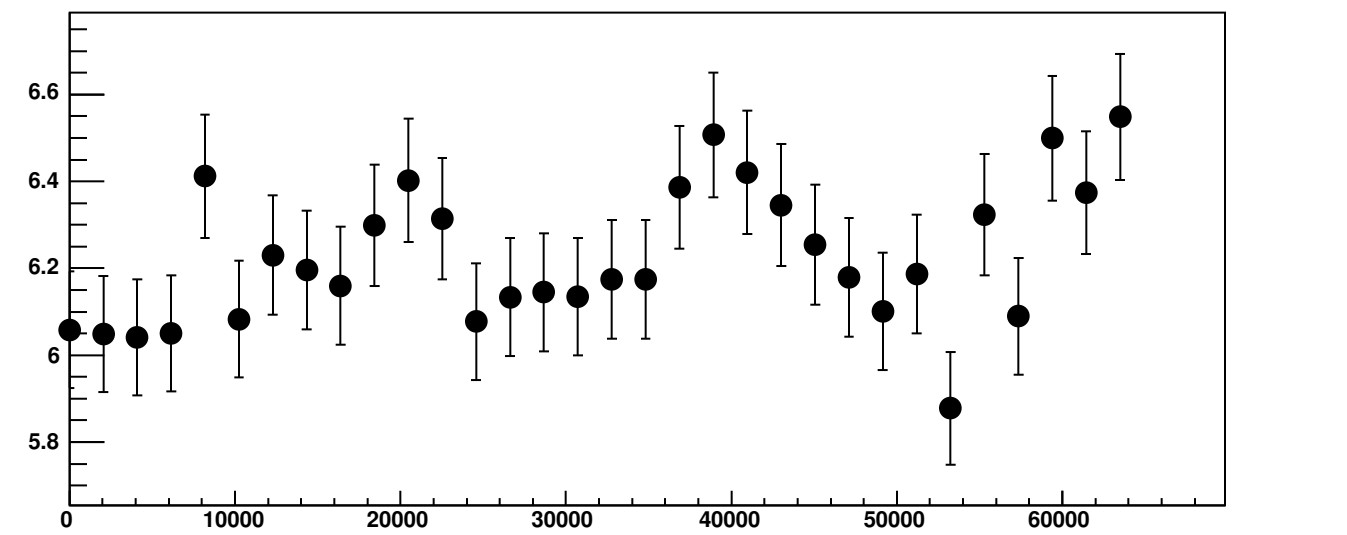

Chip 8, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

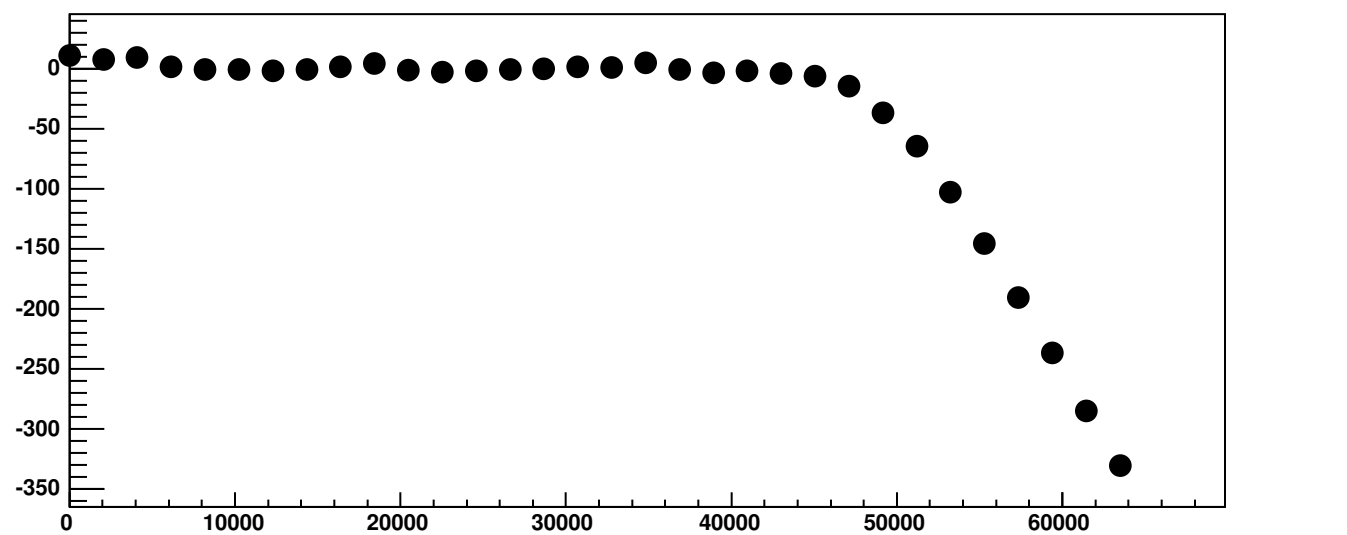
Chip 8, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

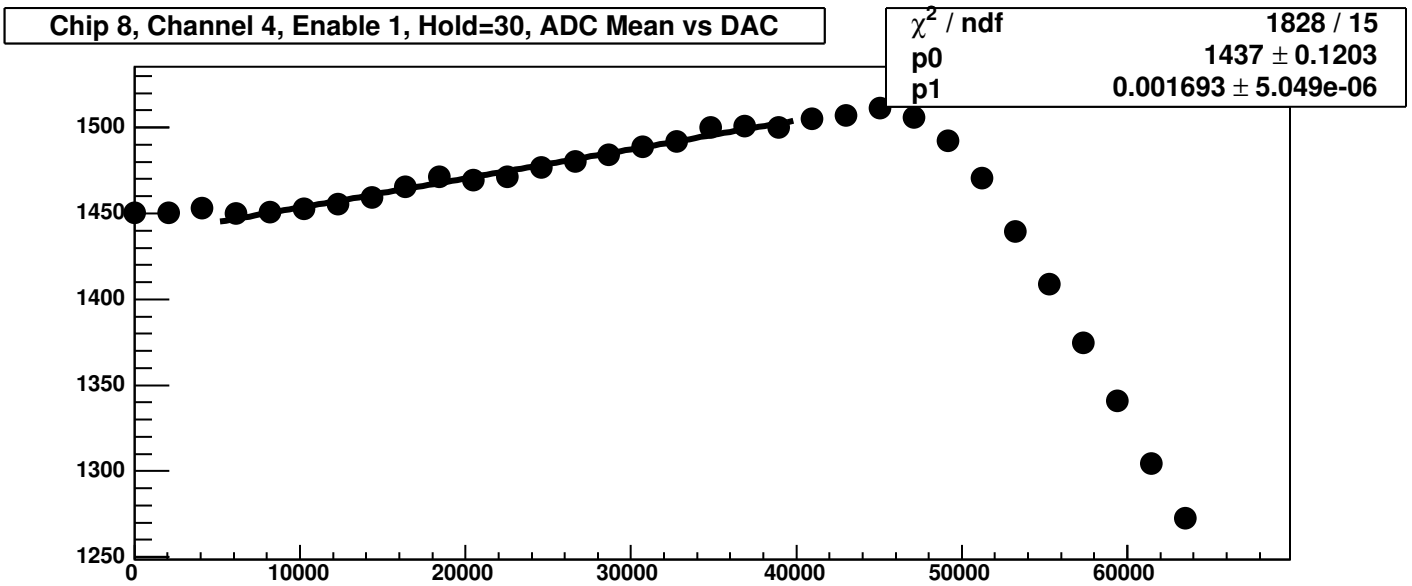


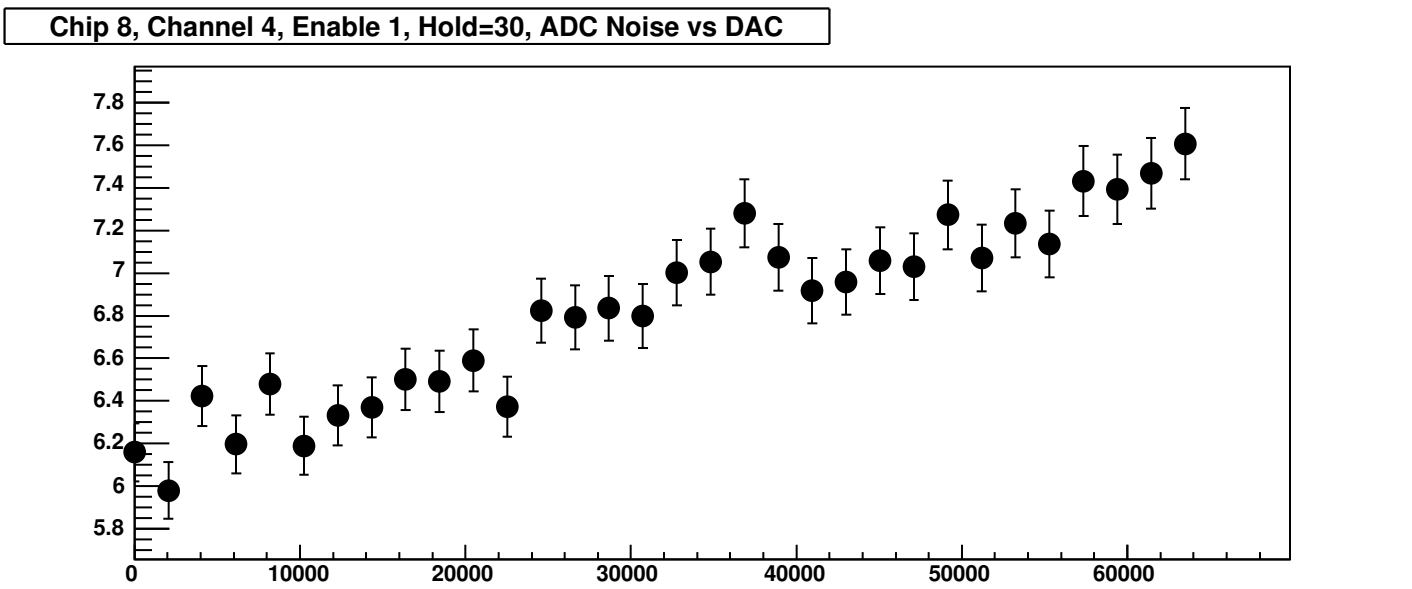
Chip 8, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC

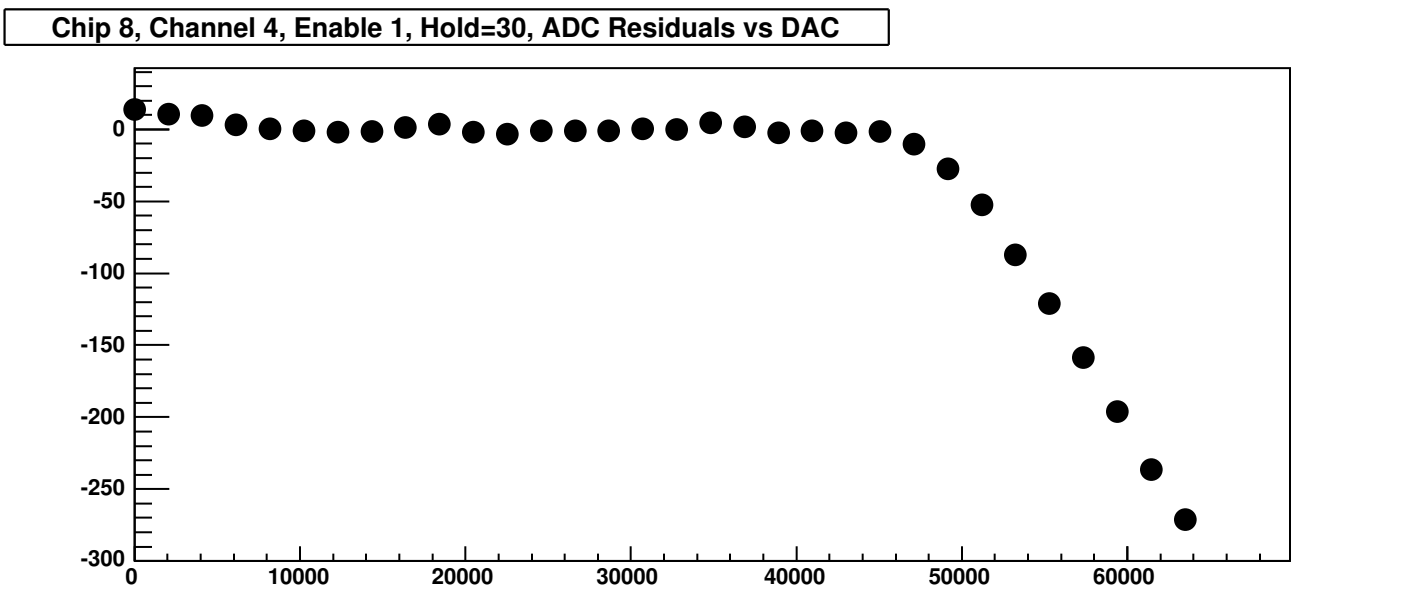


Chip 8, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

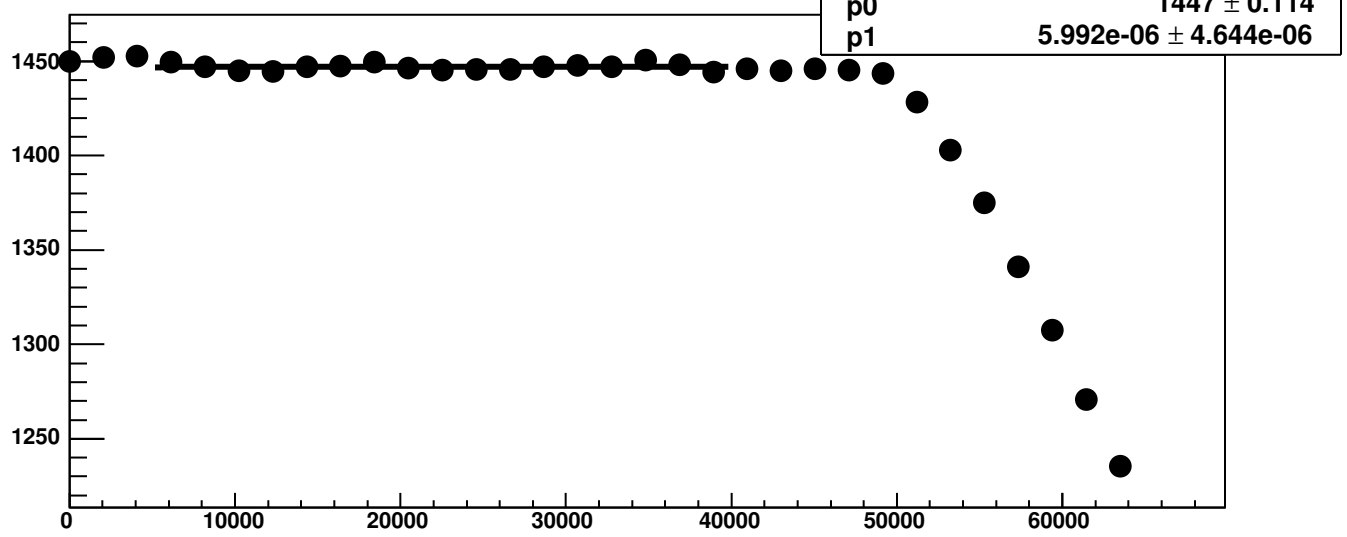



Chip 8, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1828 / 15
p0 1437 ± 0.1203
p1 0.001693 ± 5.049e-06
1500
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

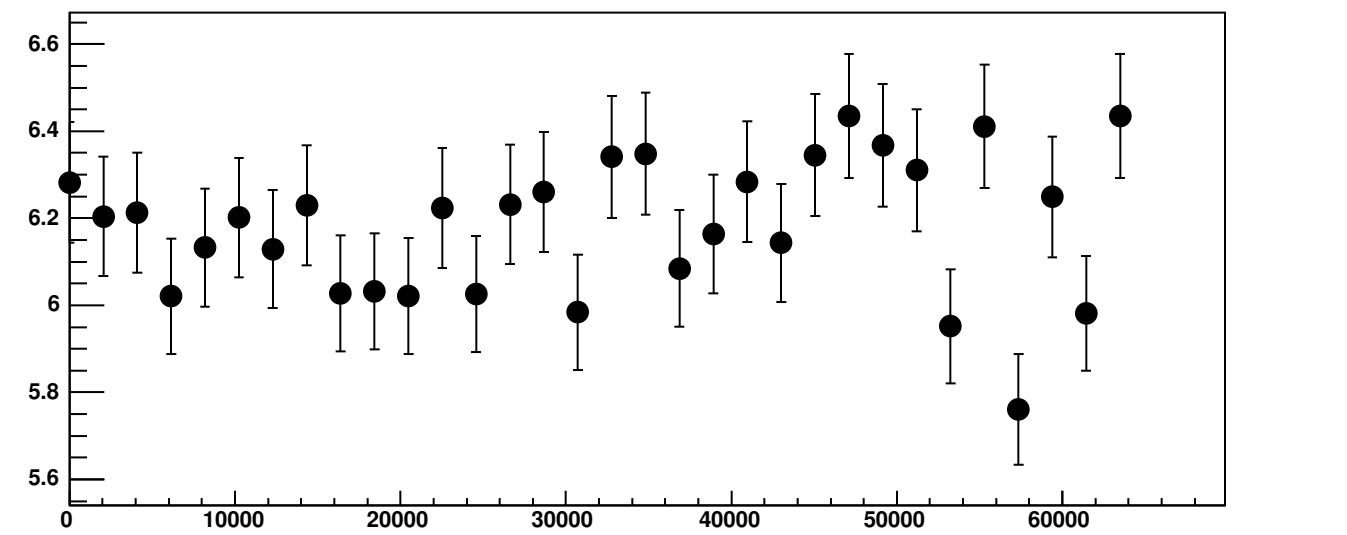

Chip 8, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

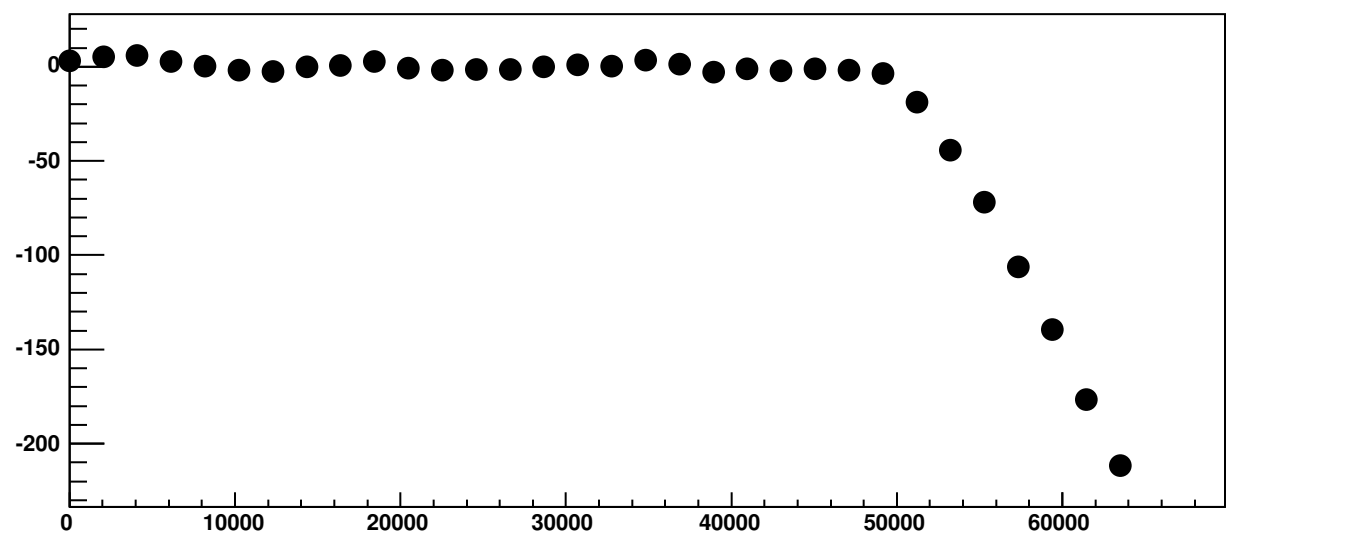
Chip 8, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

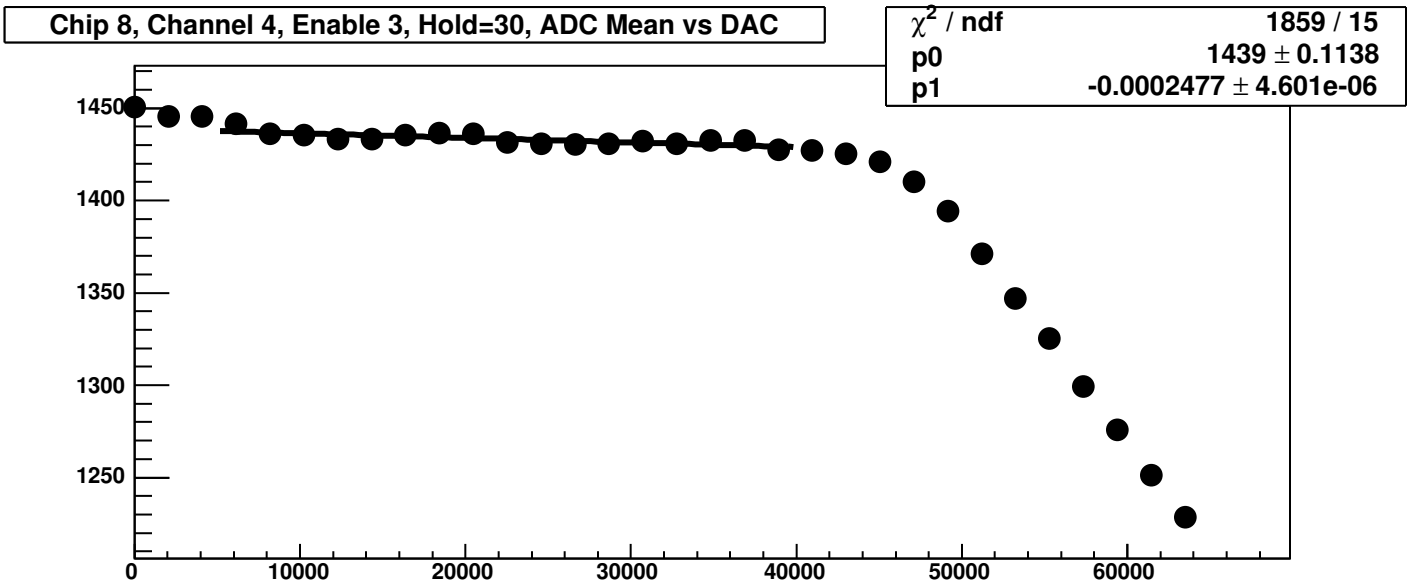


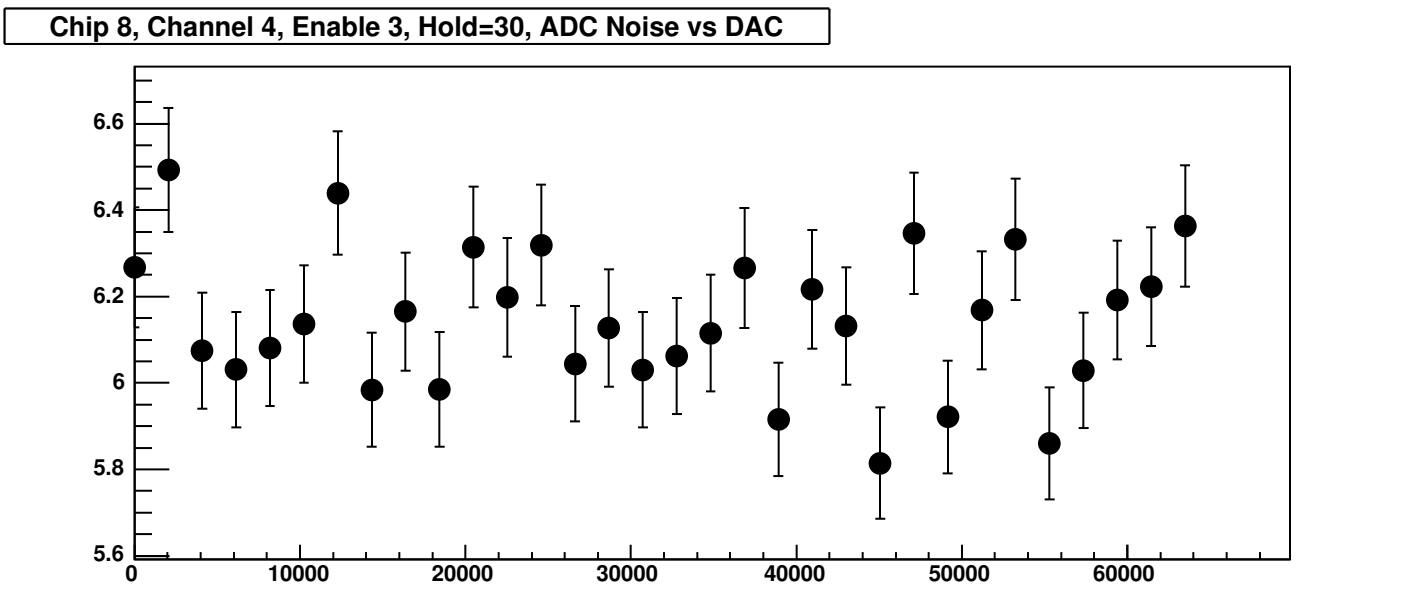
Chip 8, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC

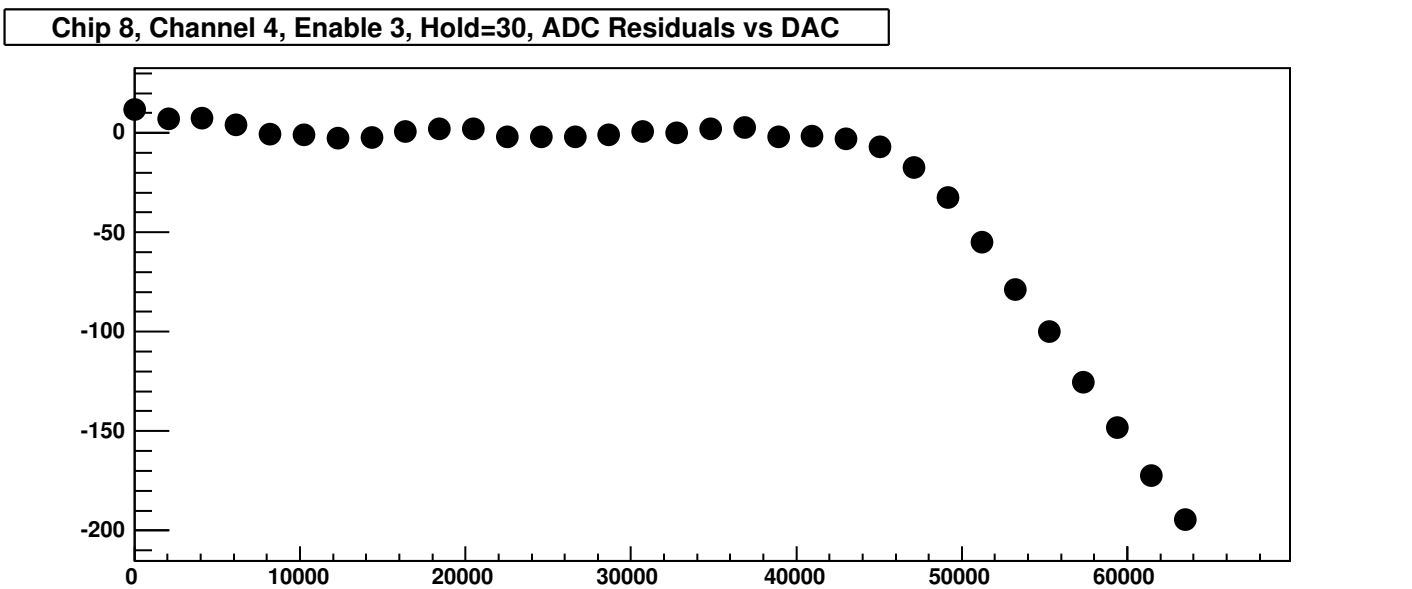


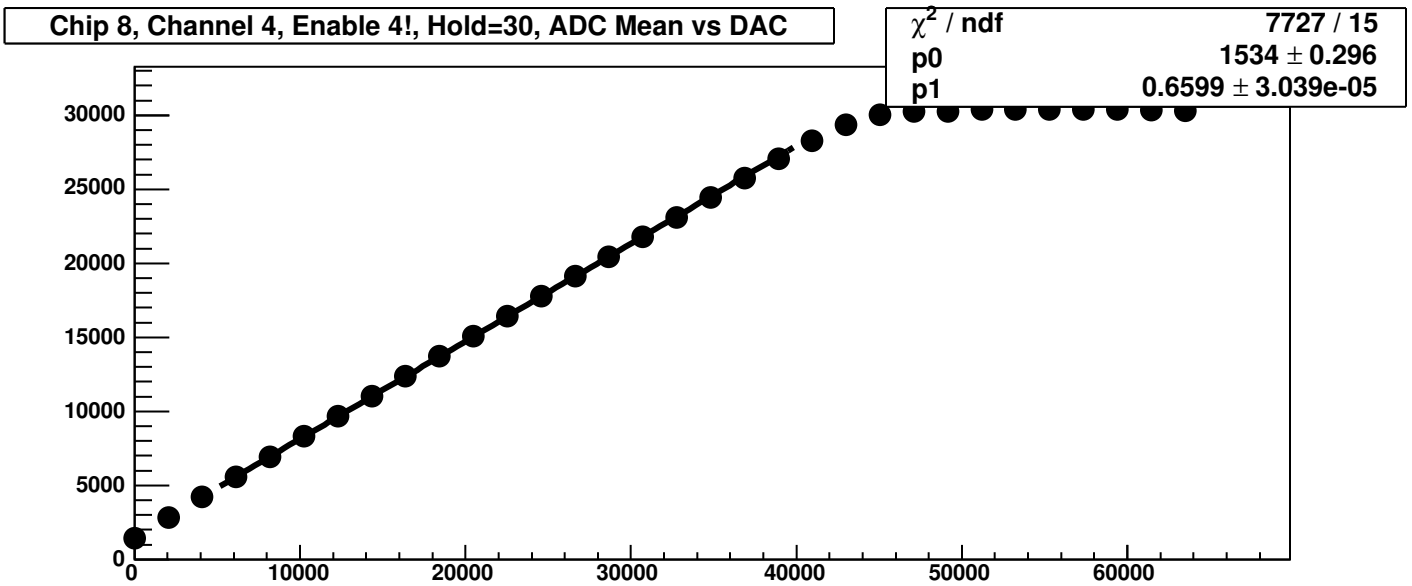
Chip 8, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

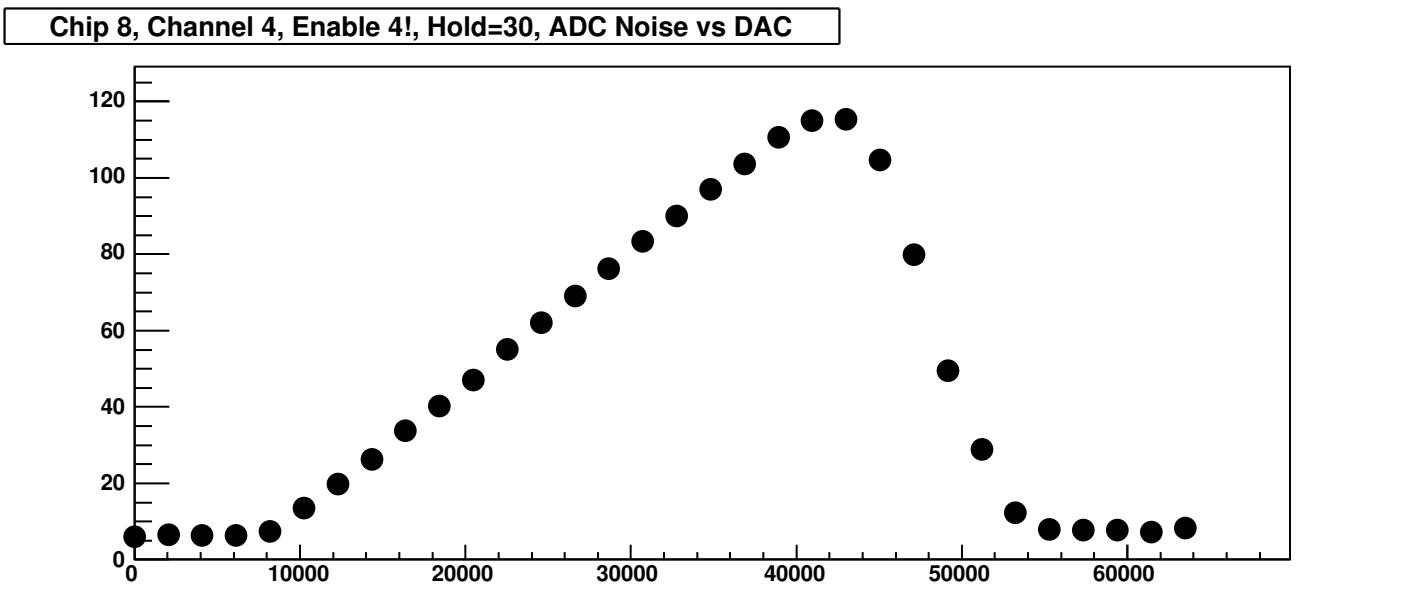


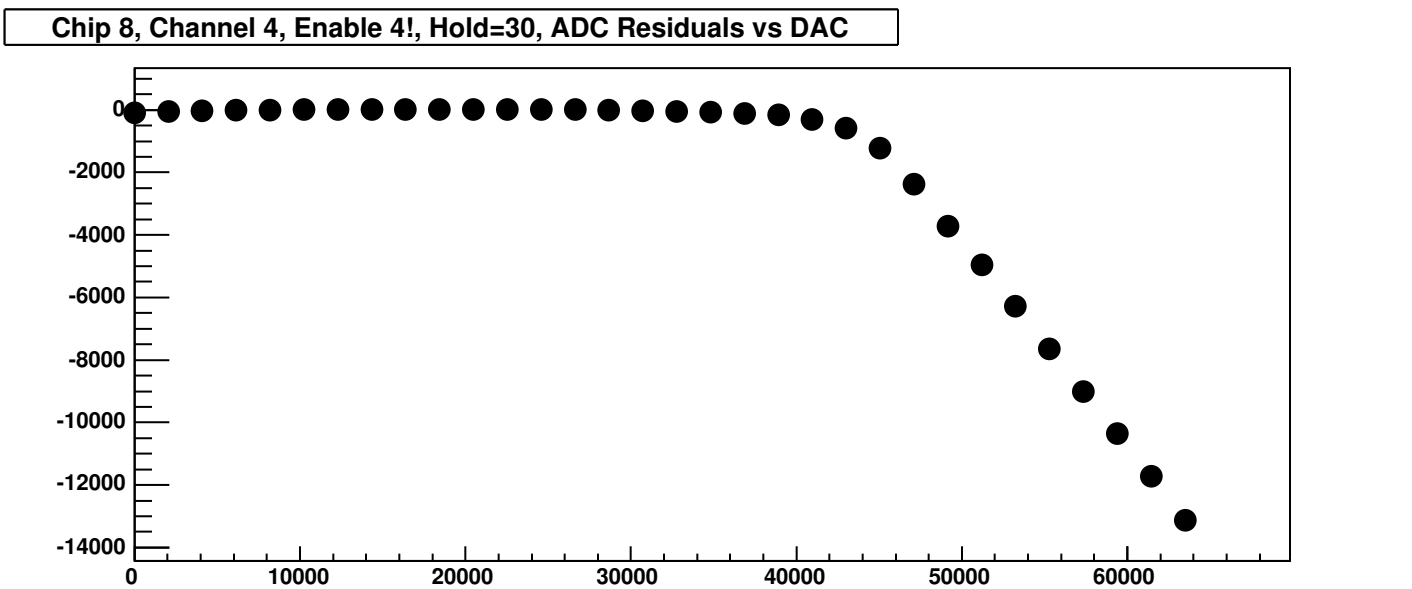

Chip 8, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 1859 / 15
p0 1439 ± 0.1138
p1 -0.0002477 ± 4.601e-06
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

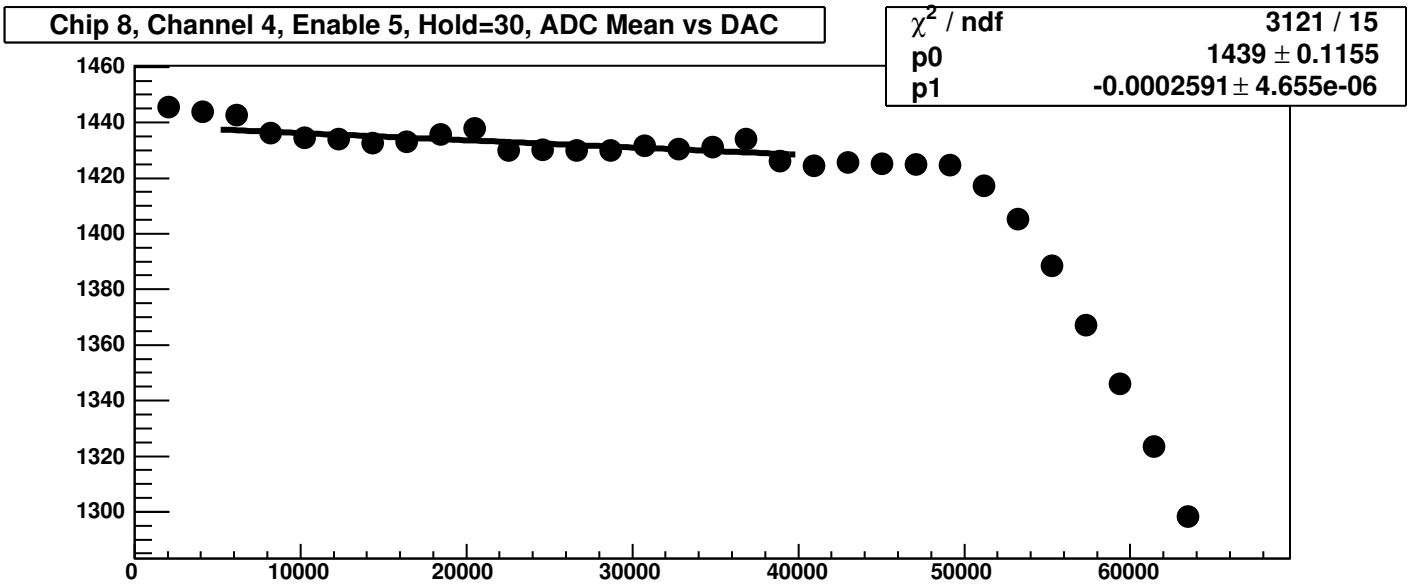

Chip 8, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

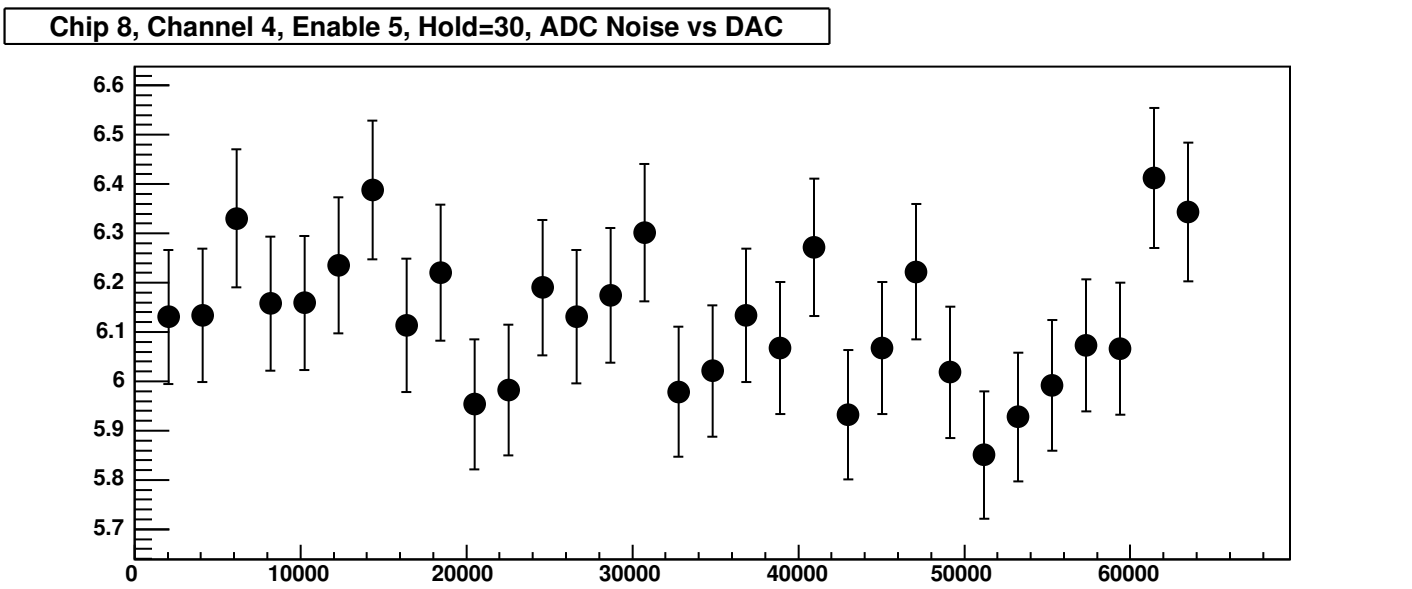

Chip 8, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

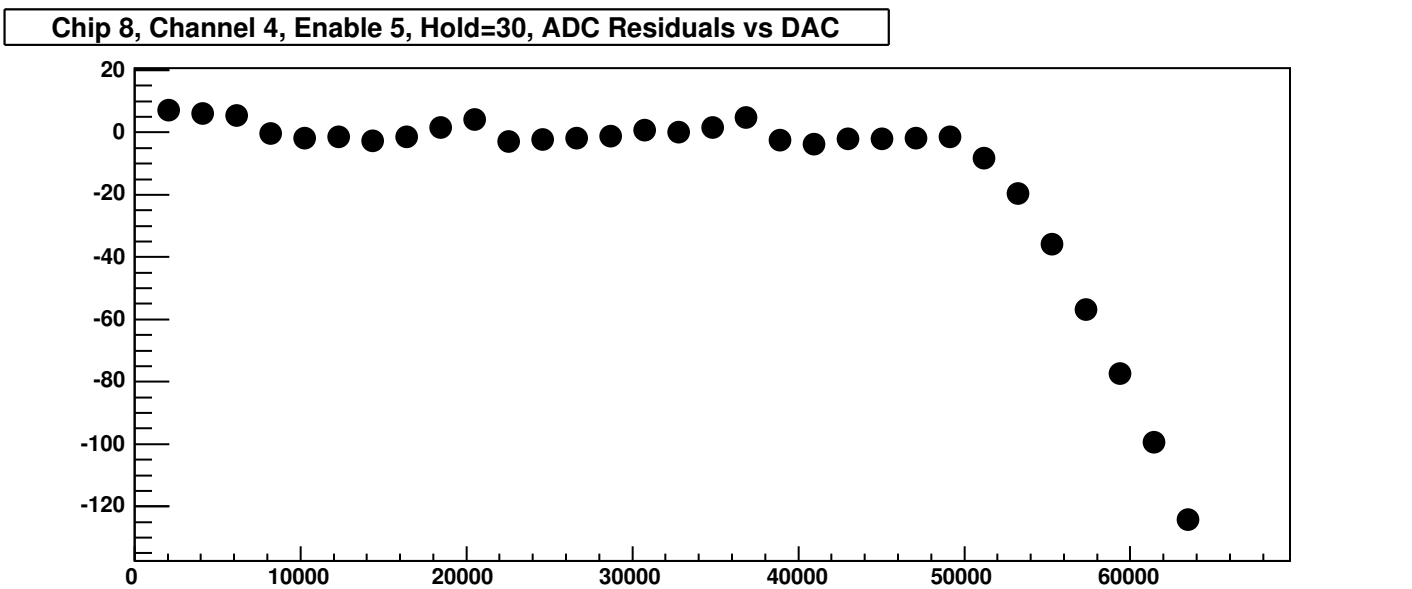

Chip 8, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 7727 / 15
p0 1534 ± 0.296
p1 0.6599 ± 3.039e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

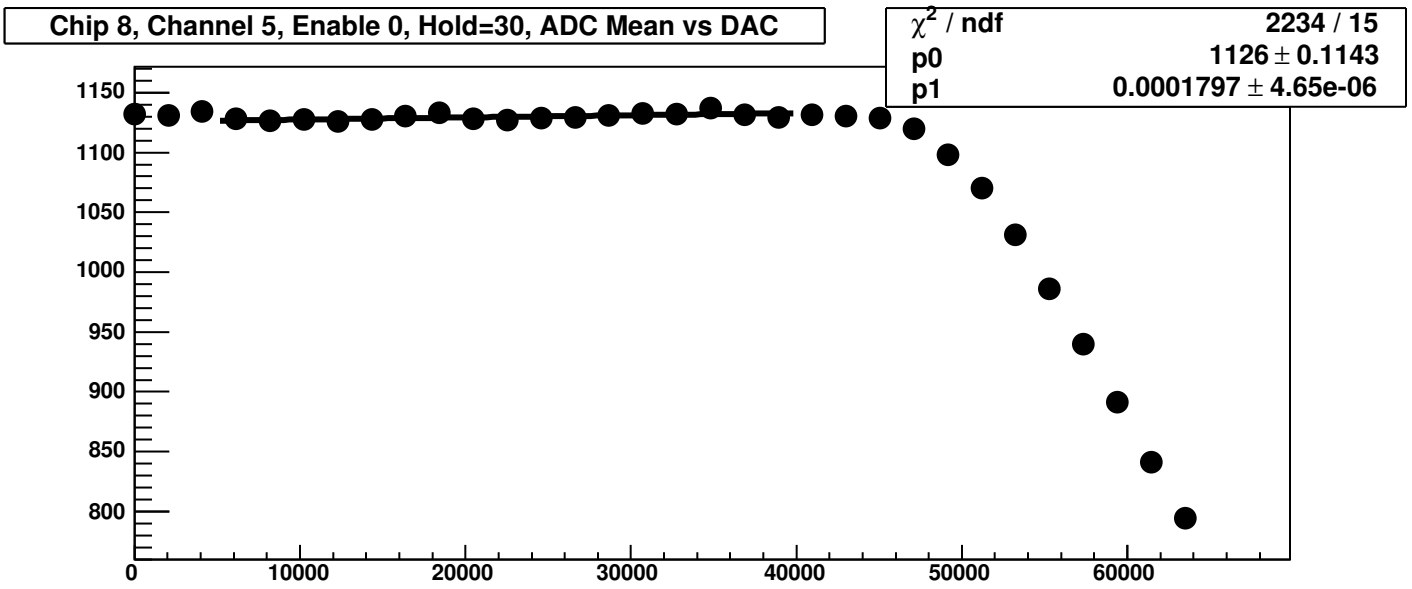

Chip 8, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

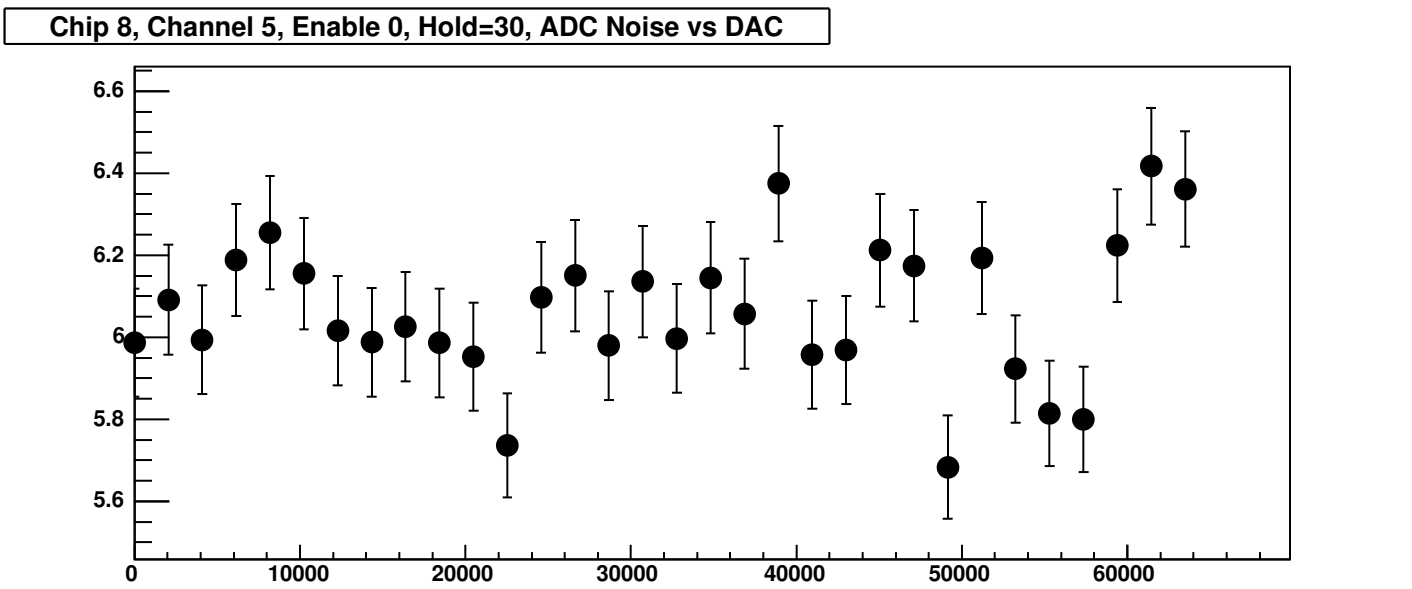

Chip 8, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

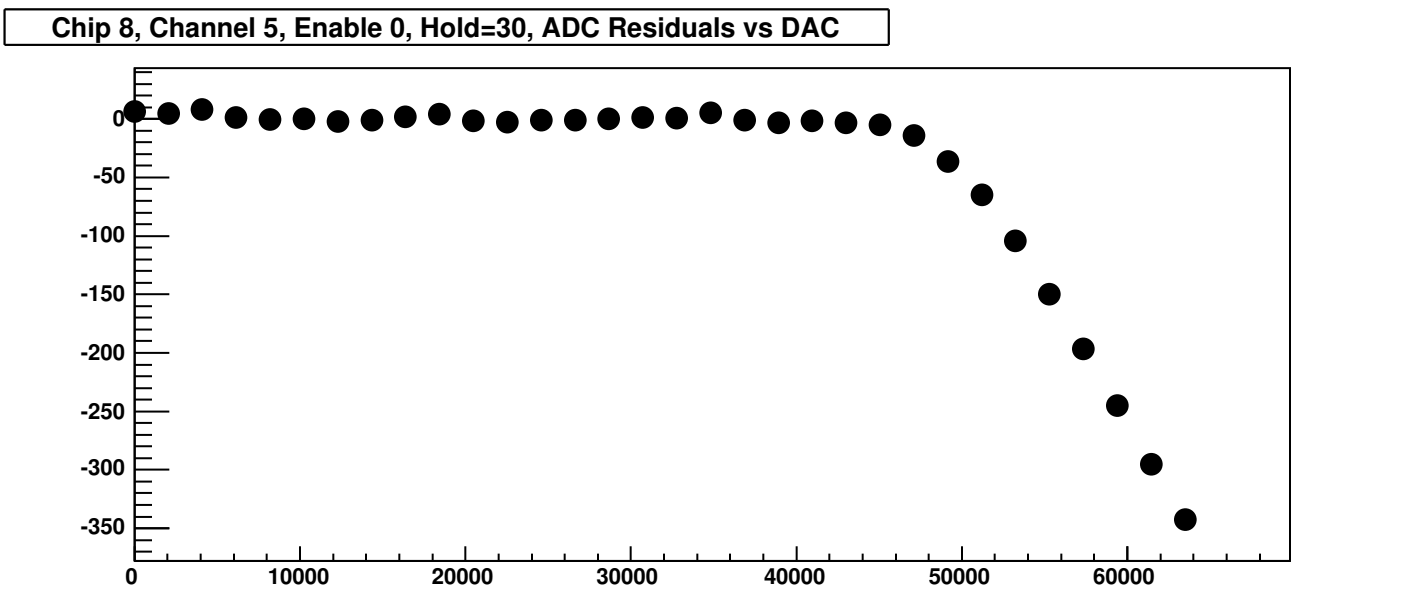

Chip 8, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3121 / 15
p0 1439 ± 0.1155
p1 -0.0002591 ± 4.655e-06
1460
1440
1420
1400
1380
1360
1340
1320
1300
0
10000
20000
30000
40000
50000
60000

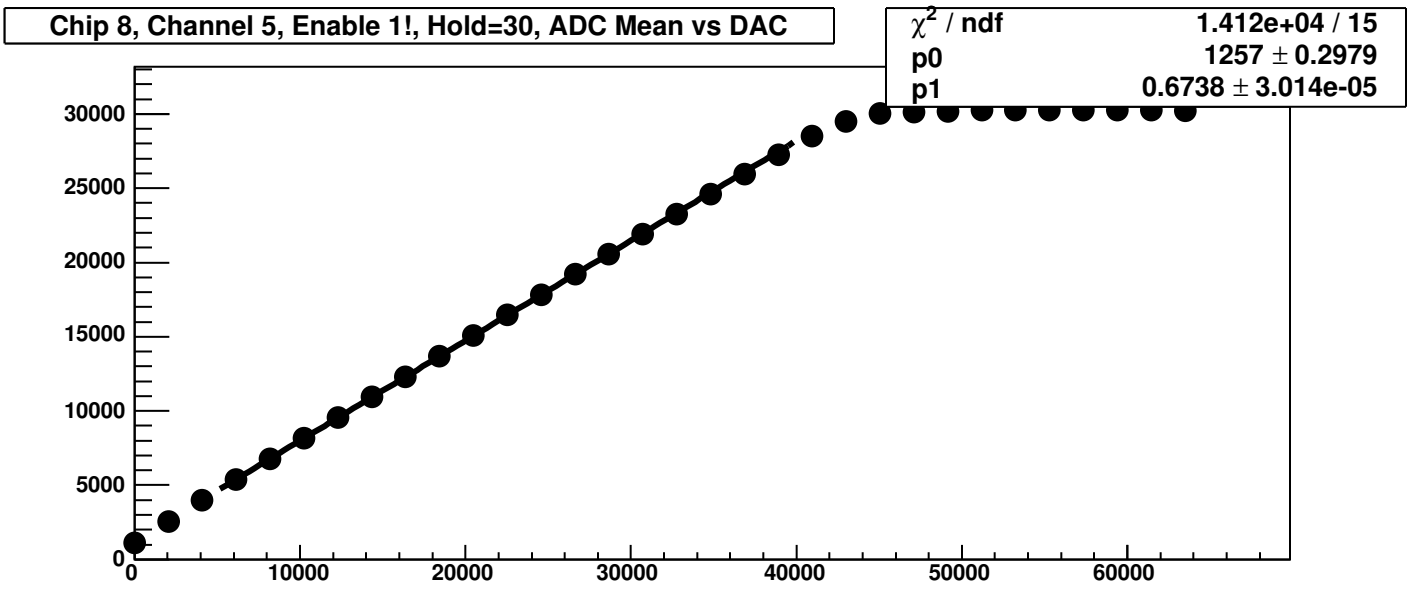

Chip 8, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
0
10000
20000
30000
40000
50000
60000

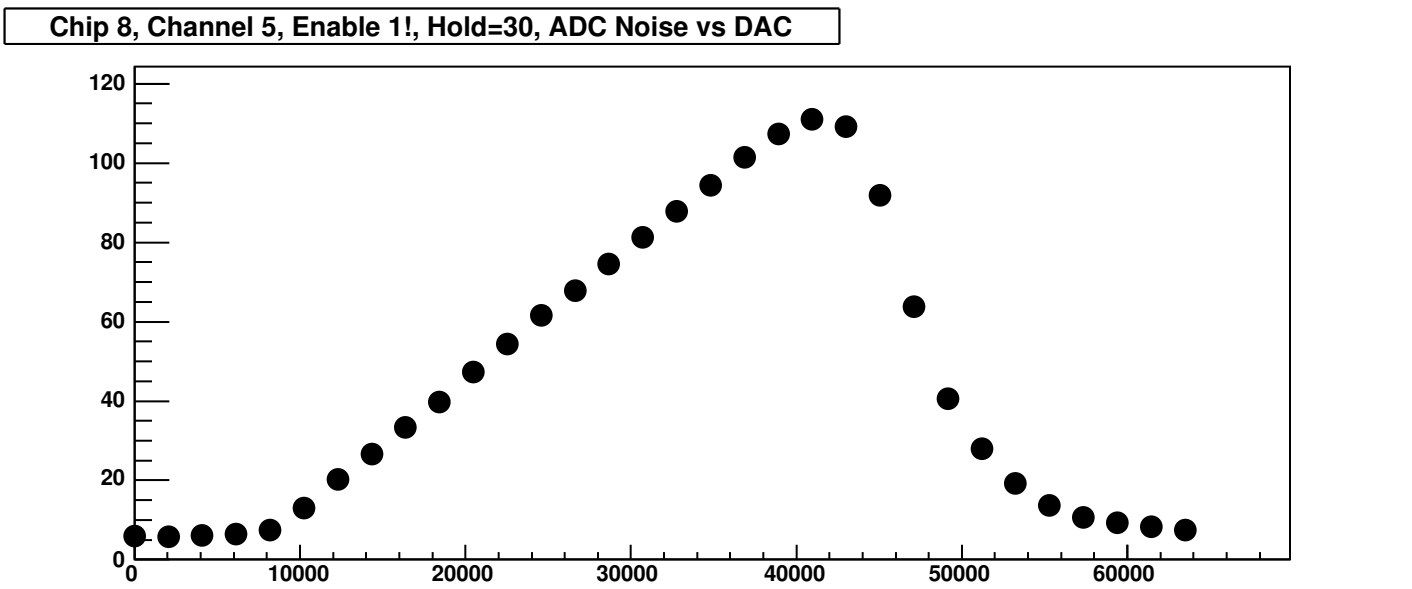

Chip 8, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

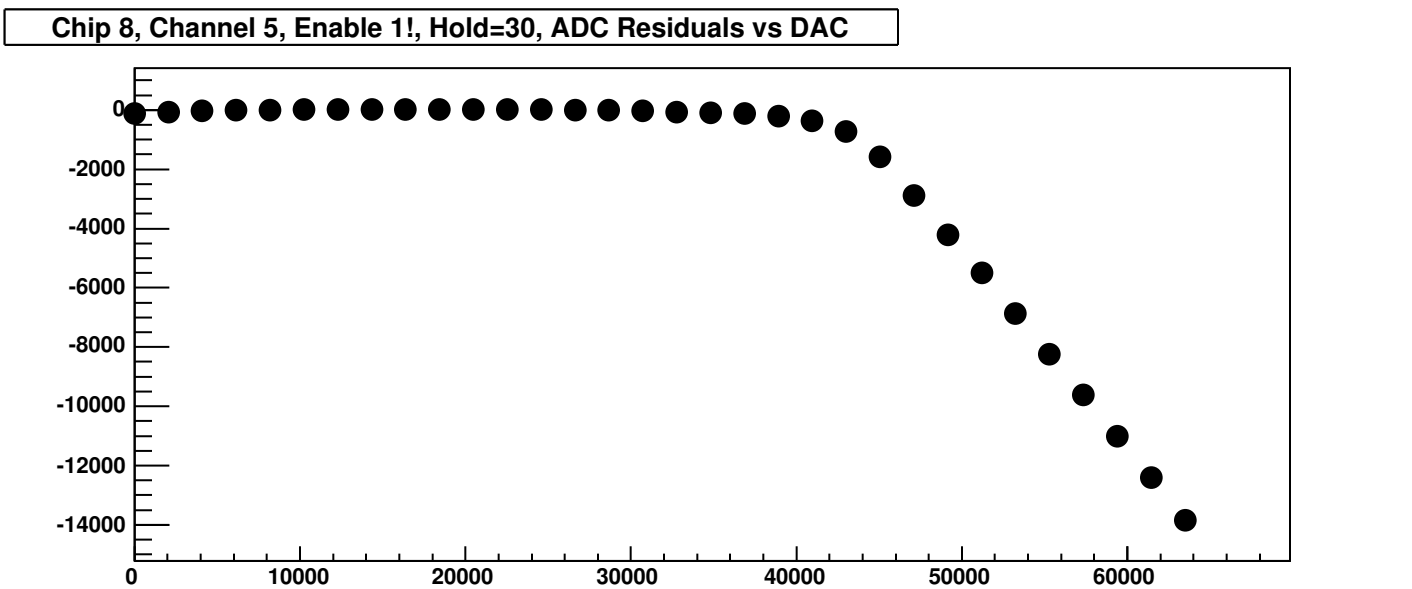

Chip 8, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2234 / 15
p0 1126 ± 0.1143
p1 0.0001797 ± 4.65e-06
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

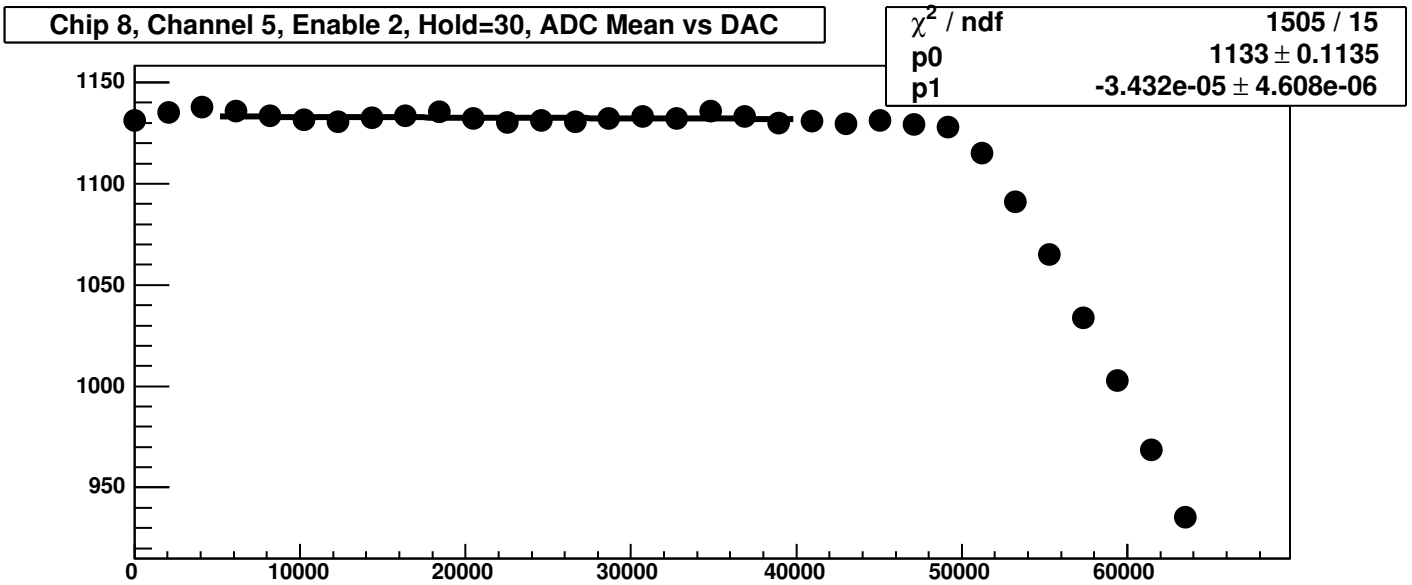

Chip 8, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

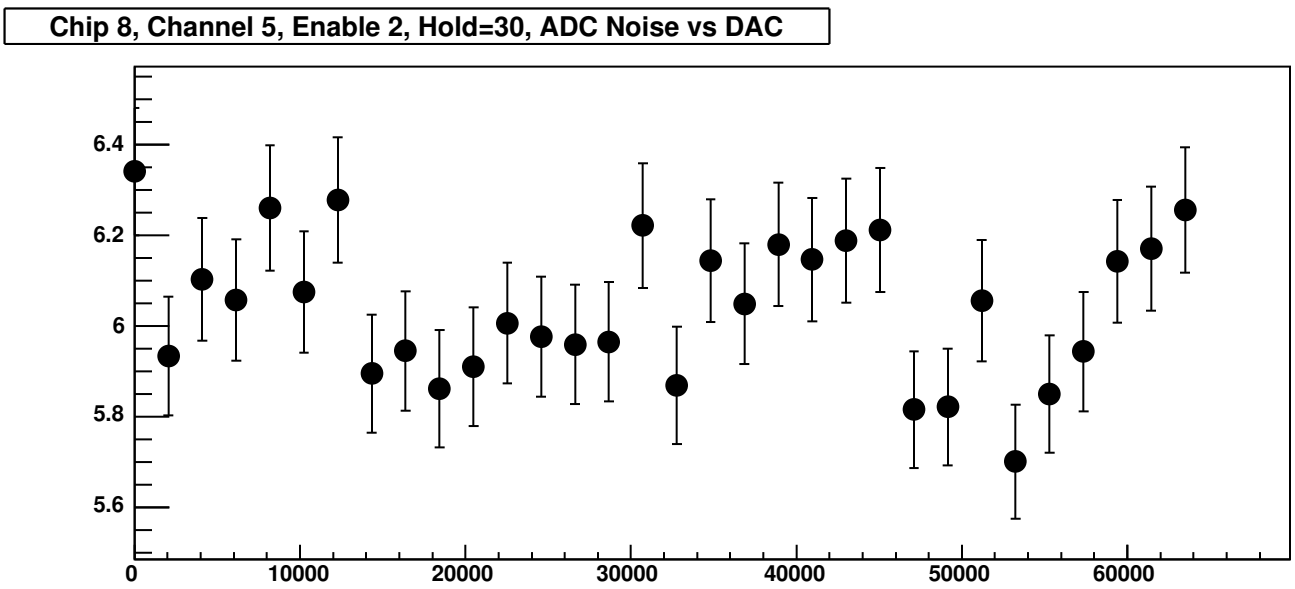

Chip 8, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

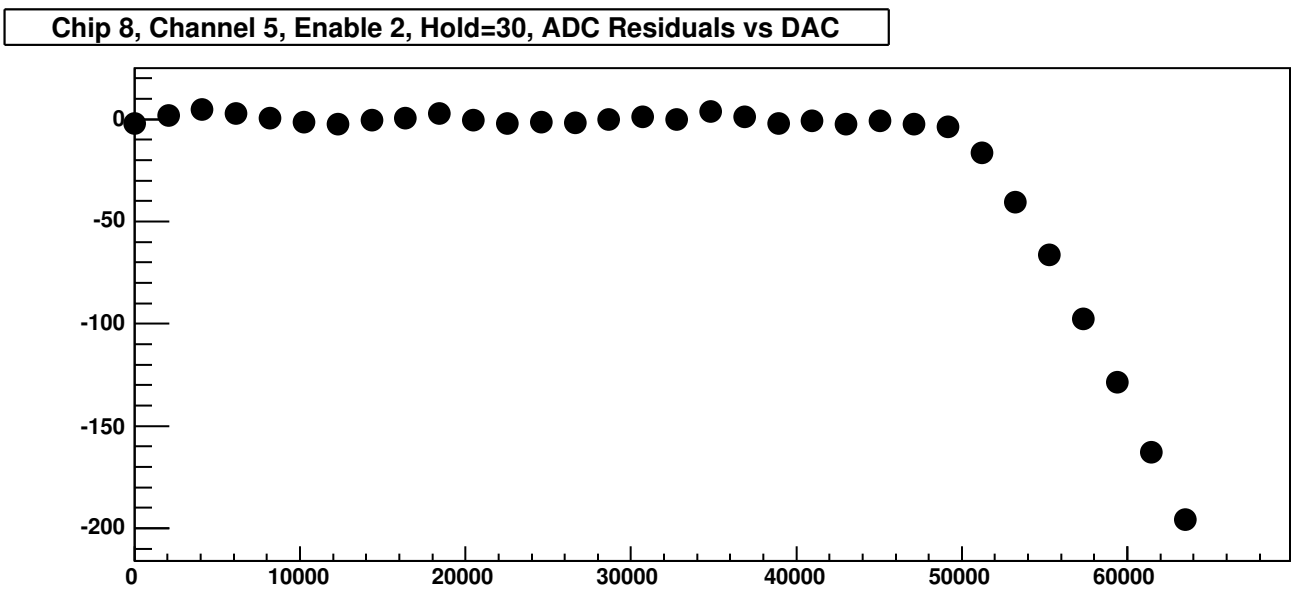

Chip 8, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.412e+04 / 15
p0 1257 ± 0.2979
p1 0.6738 ± 3.014e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

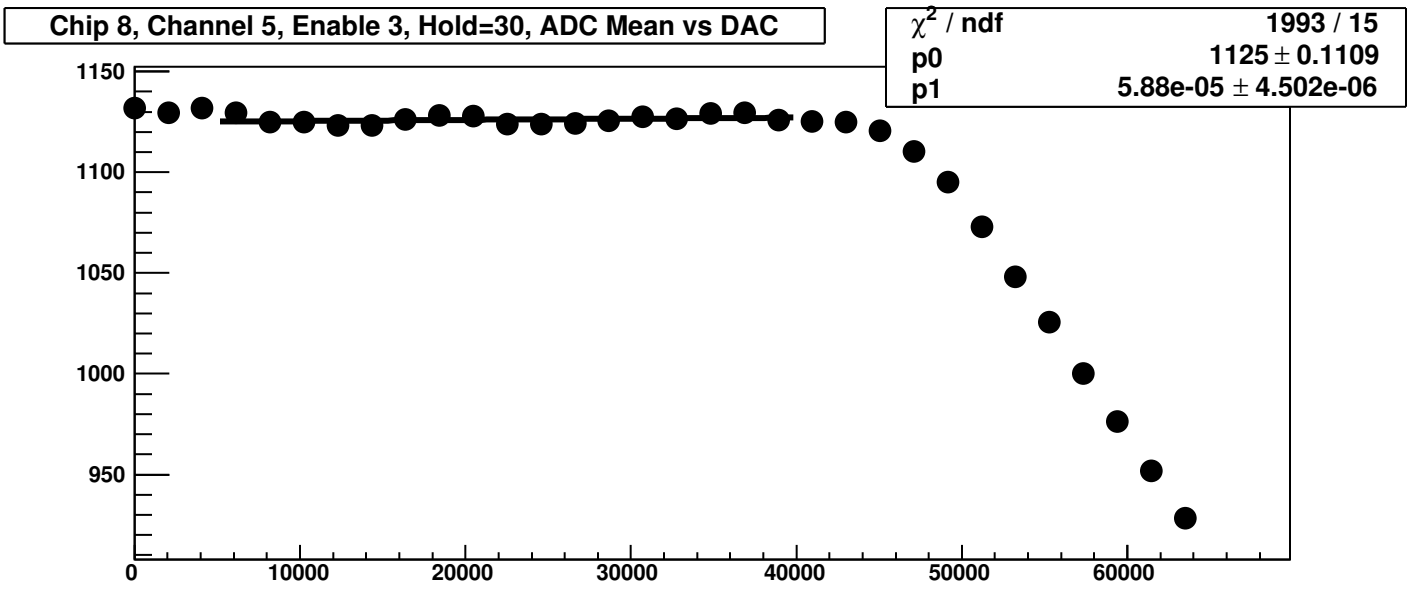

Chip 8, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

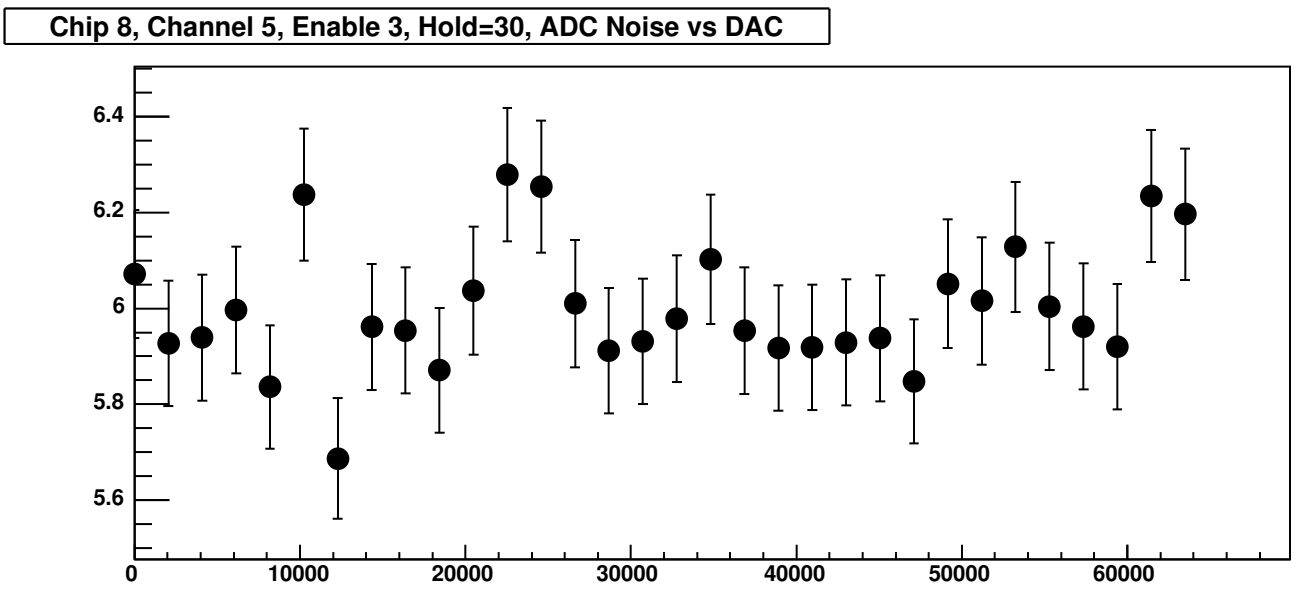

Chip 8, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

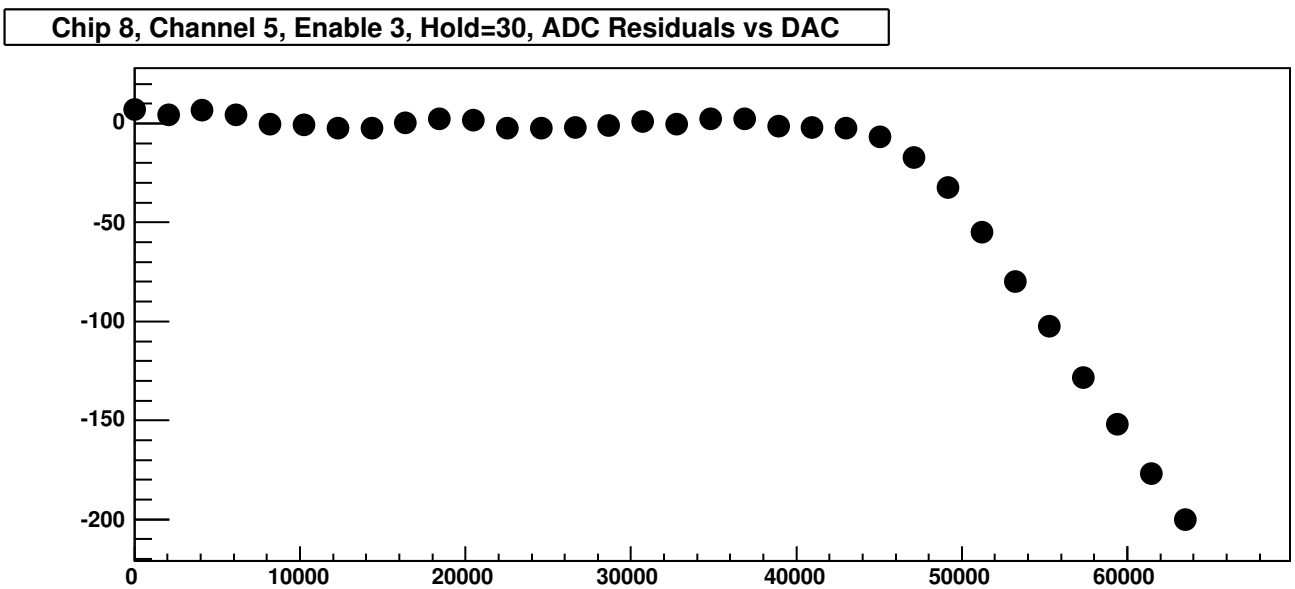

Chip 8, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1505 / 15
p0 1133 ± 0.1135
p1 -3.432e-05 ± 4.608e-06
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

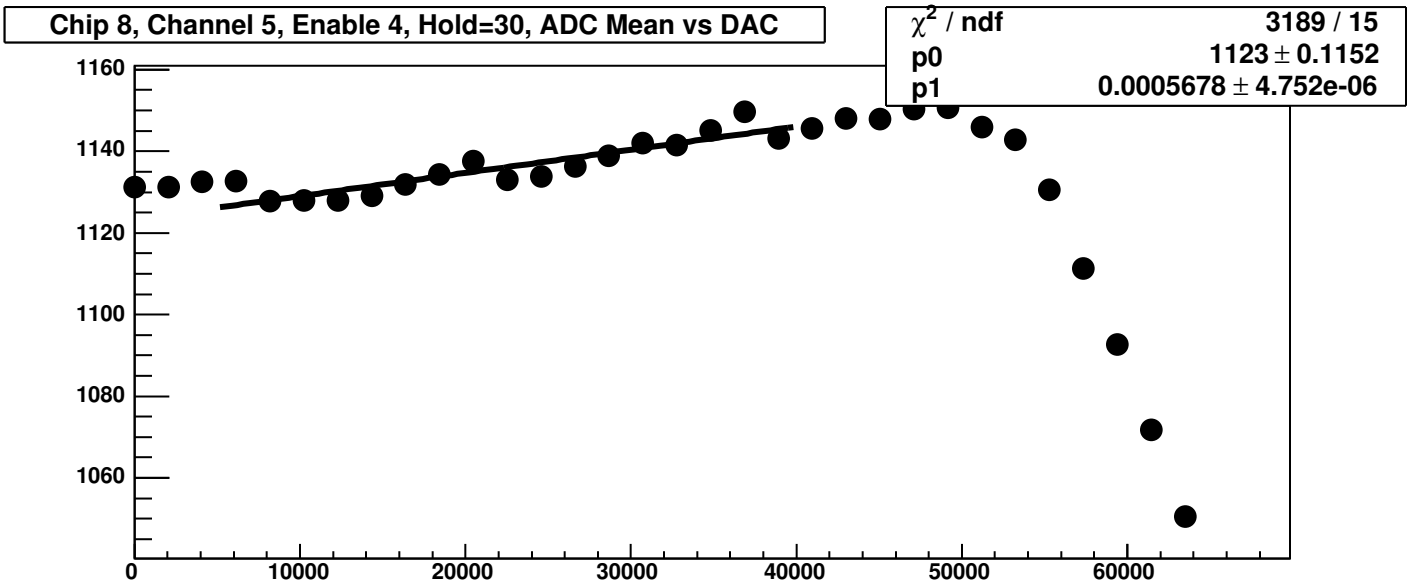

Chip 8, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

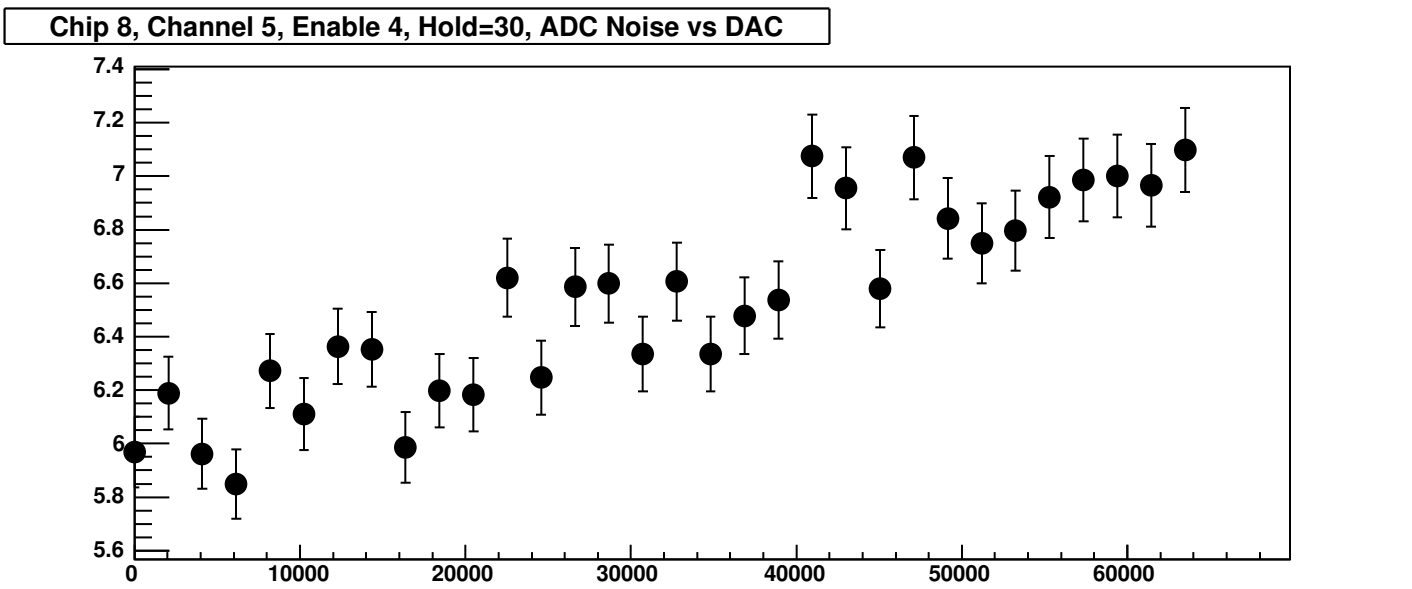

Chip 8, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

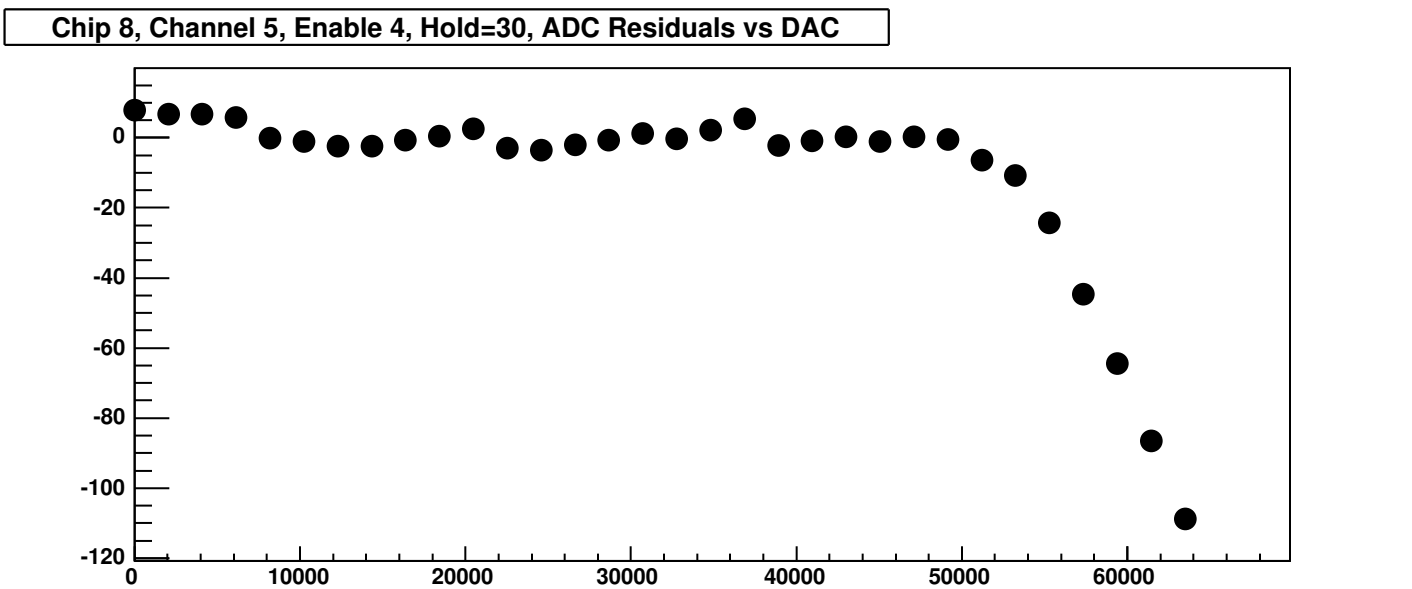

Chip 8, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 1993 / 15
p0 1125 ± 0.1109
p1 5.88e-05 ± 4.502e-06
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

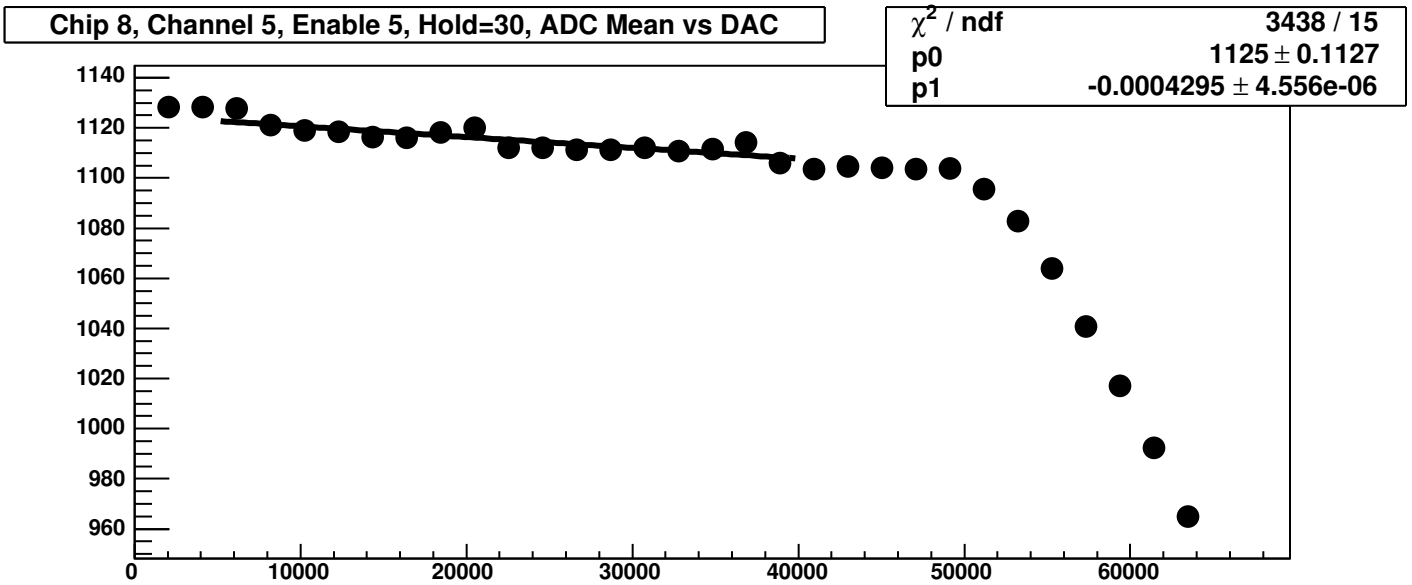

Chip 8, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

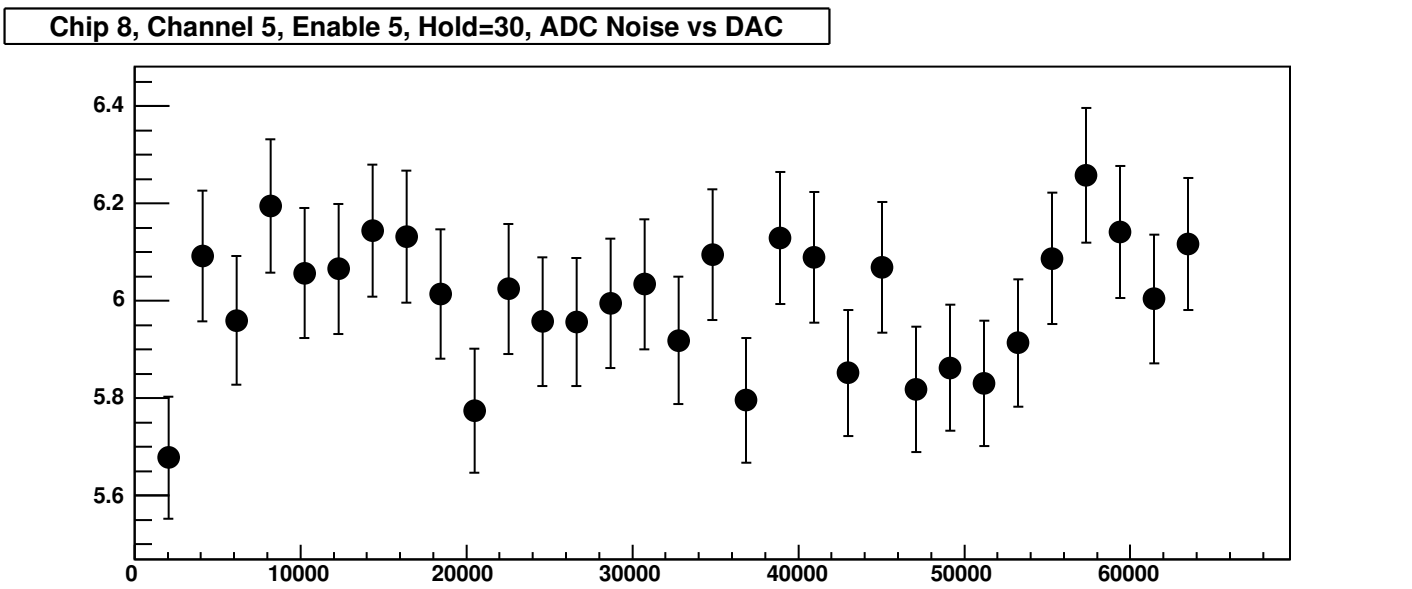

Chip 8, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

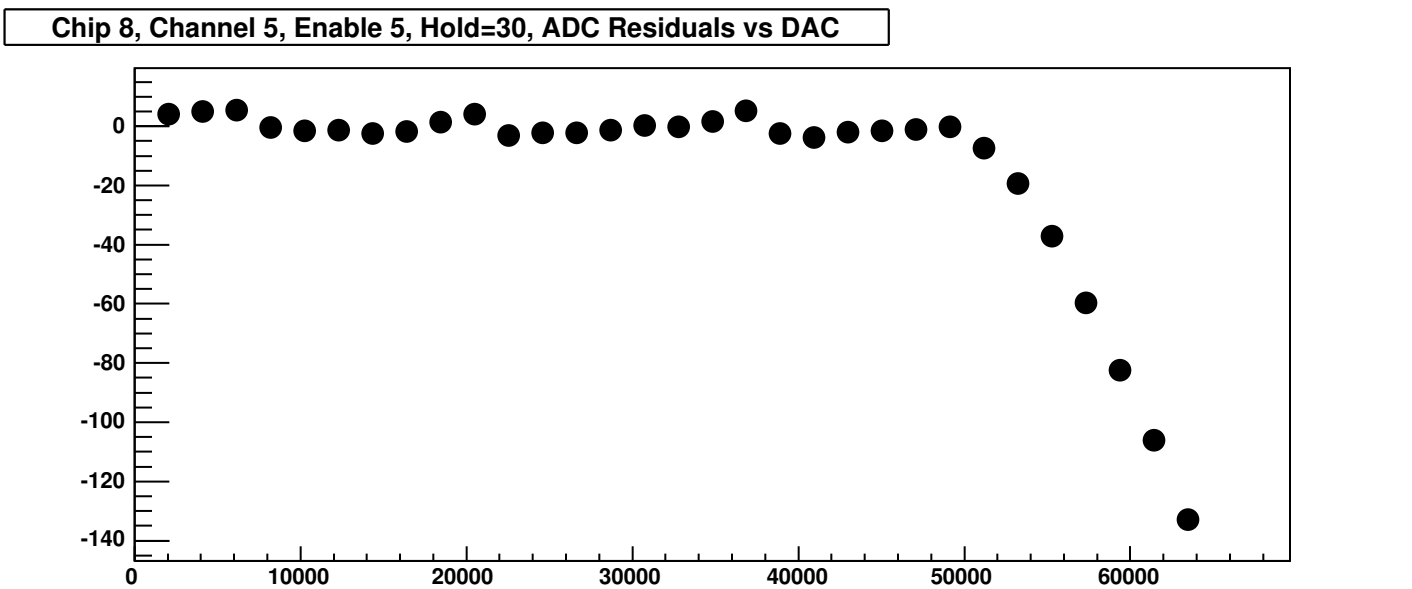

Chip 8, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 3189 / 15
p0 1123 ± 0.1152
p1 0.0005678 ± 4.752e-06
1160
1140
1120
1100
1080
1060
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

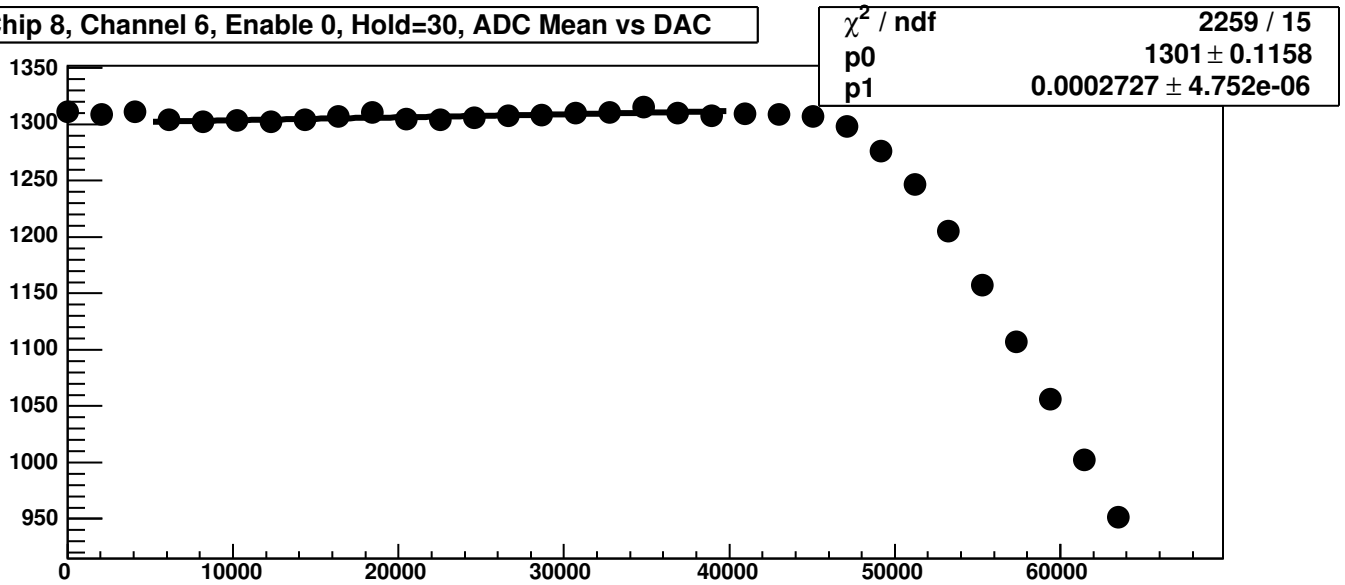

Chip 8, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3438 / 15
p0 1125 ± 0.1127
p1 -0.0004295 ± 4.556e-06
1140
1120
1100
1080
1060
1040
1020
1000
980
960
0
10000
20000
30000
40000
50000
60000

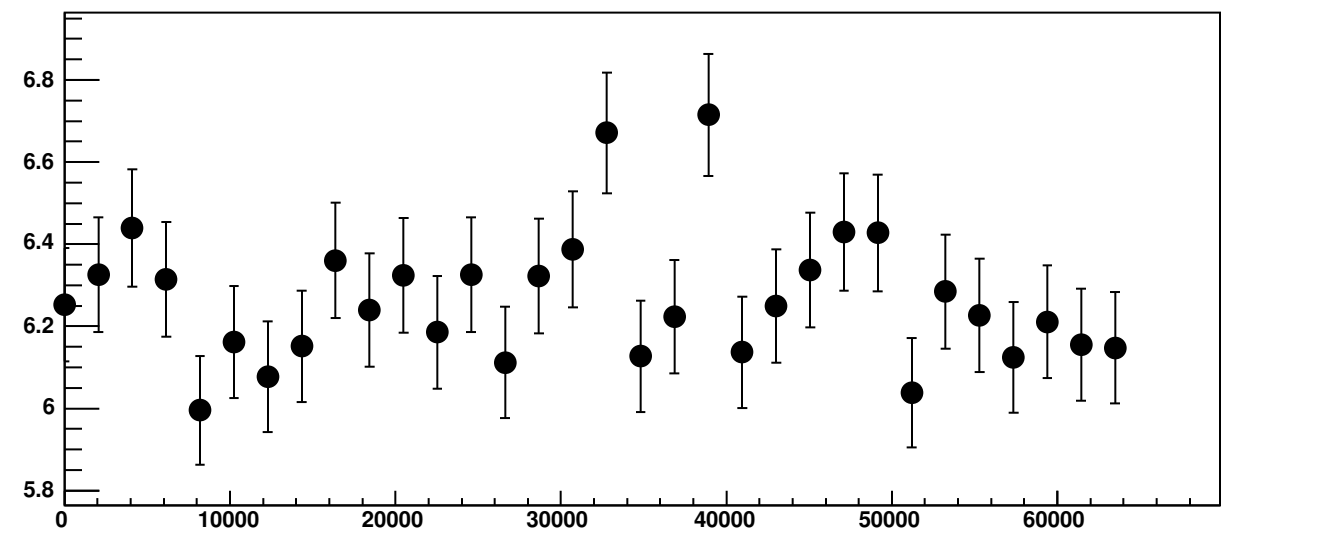

Chip 8, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

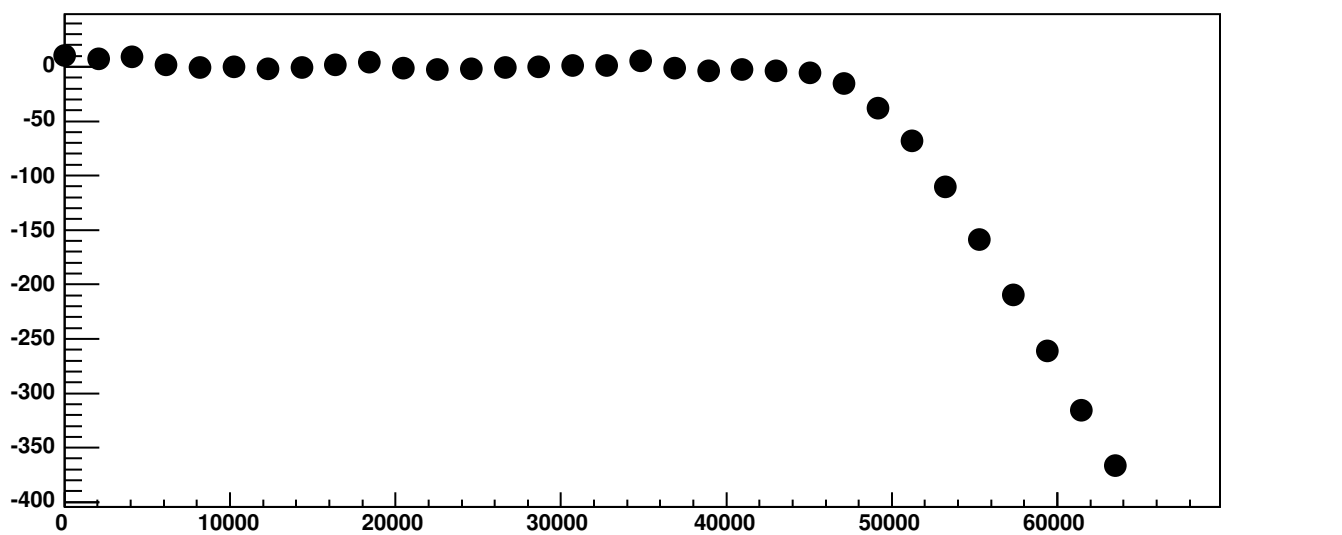
Chip 8, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC

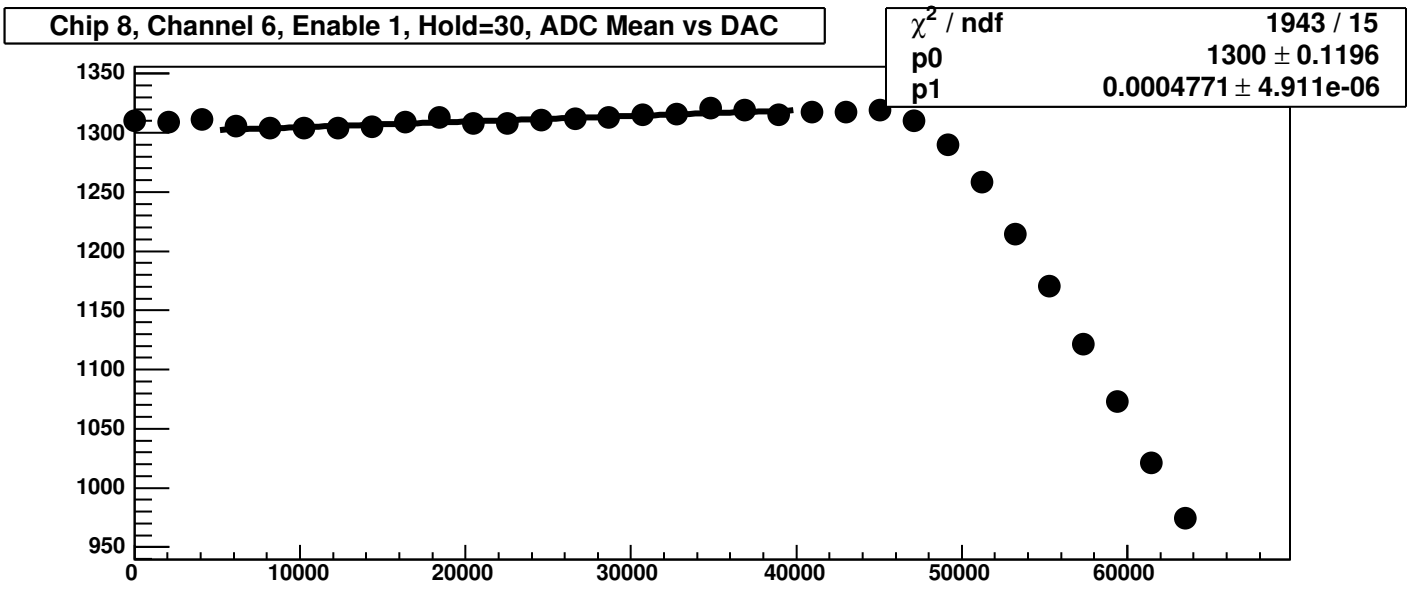


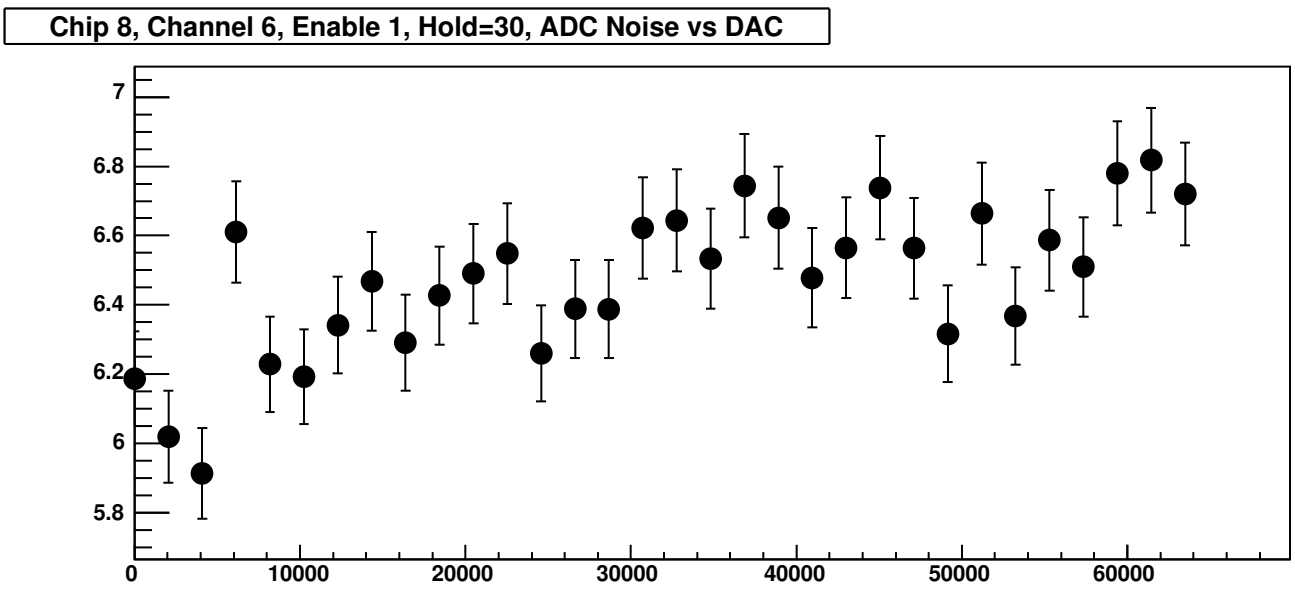
Chip 8, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC

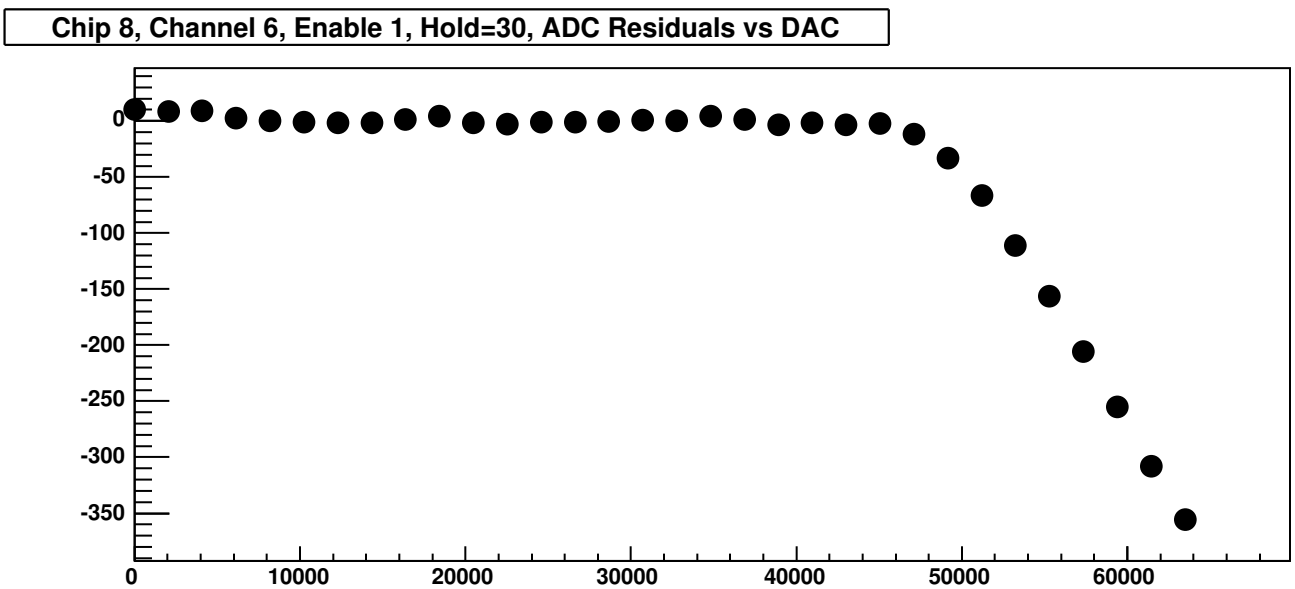


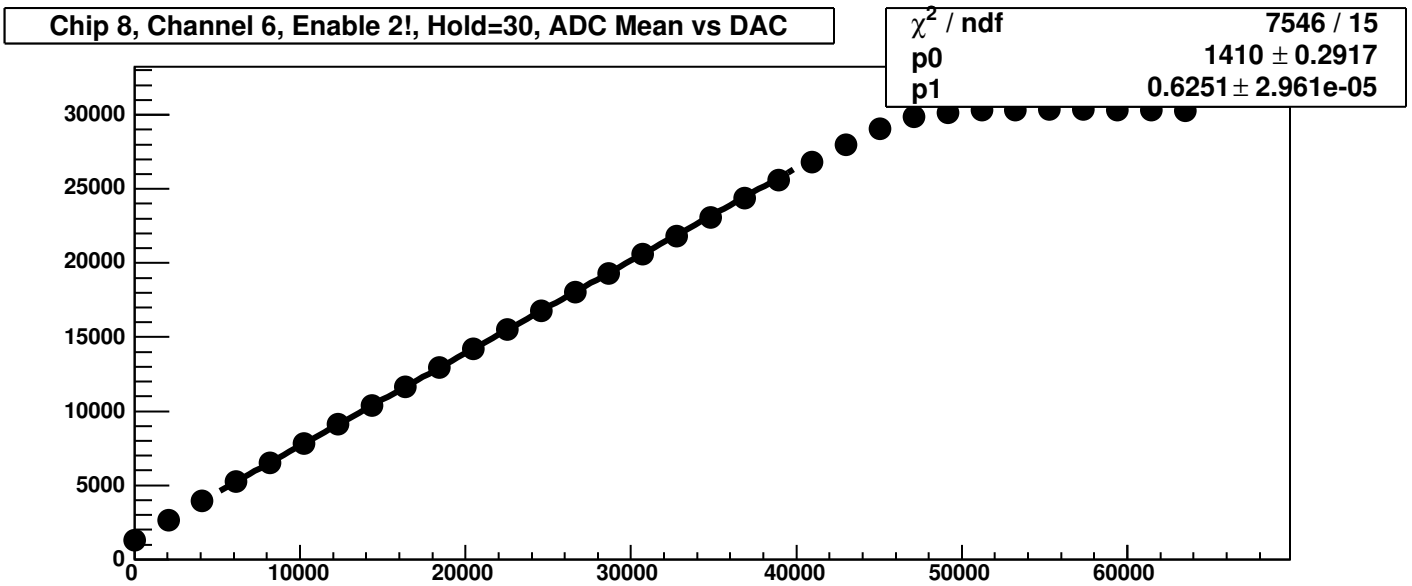
Chip 8, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

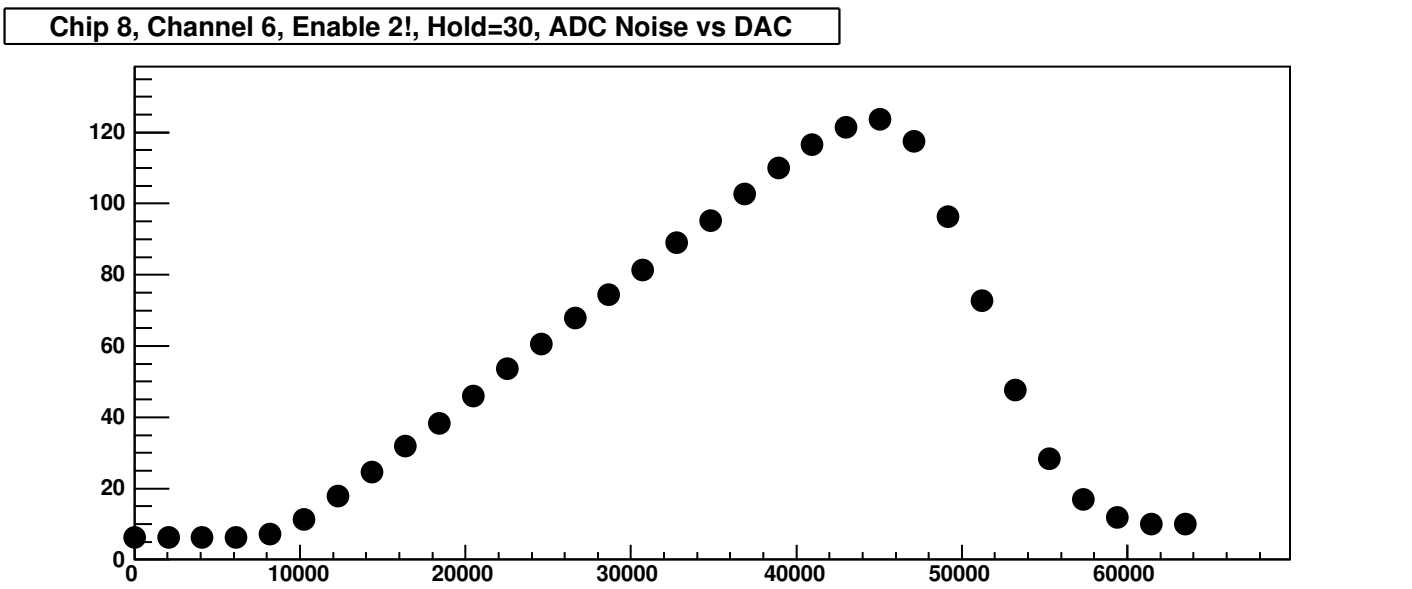


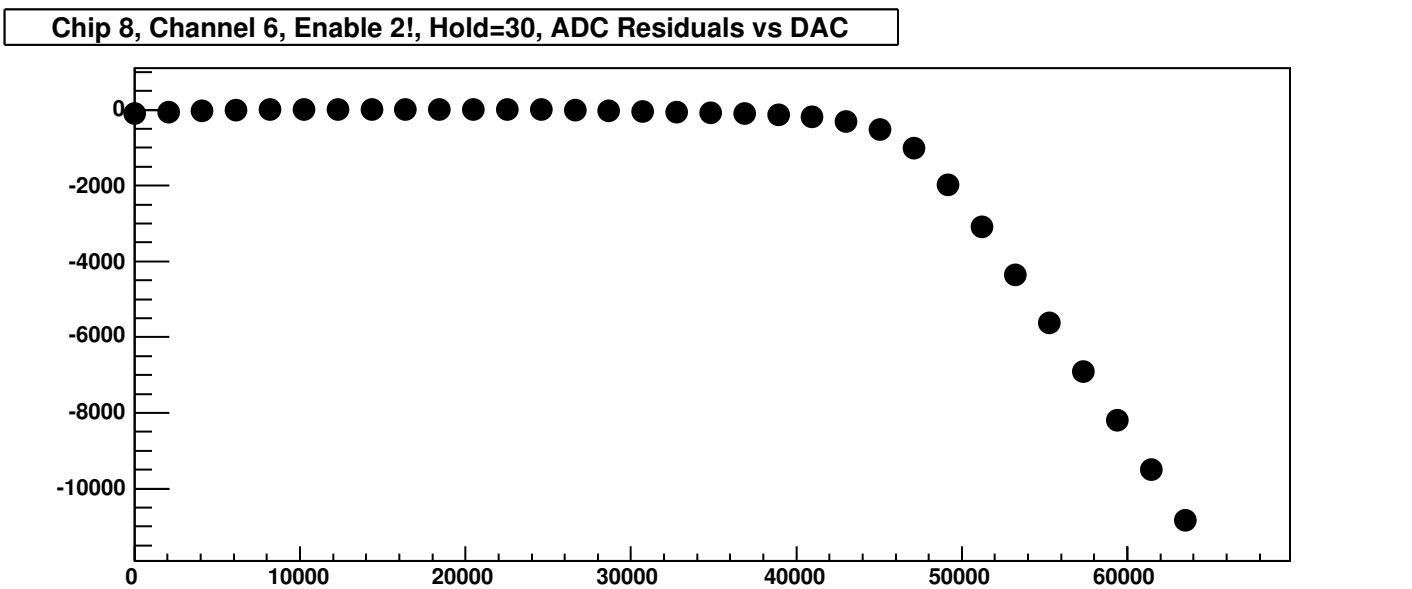

Chip 8, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1943 / 15
p0 1300 ± 0.1196
p1 0.0004771 ± 4.911e-06
1350
1300
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000

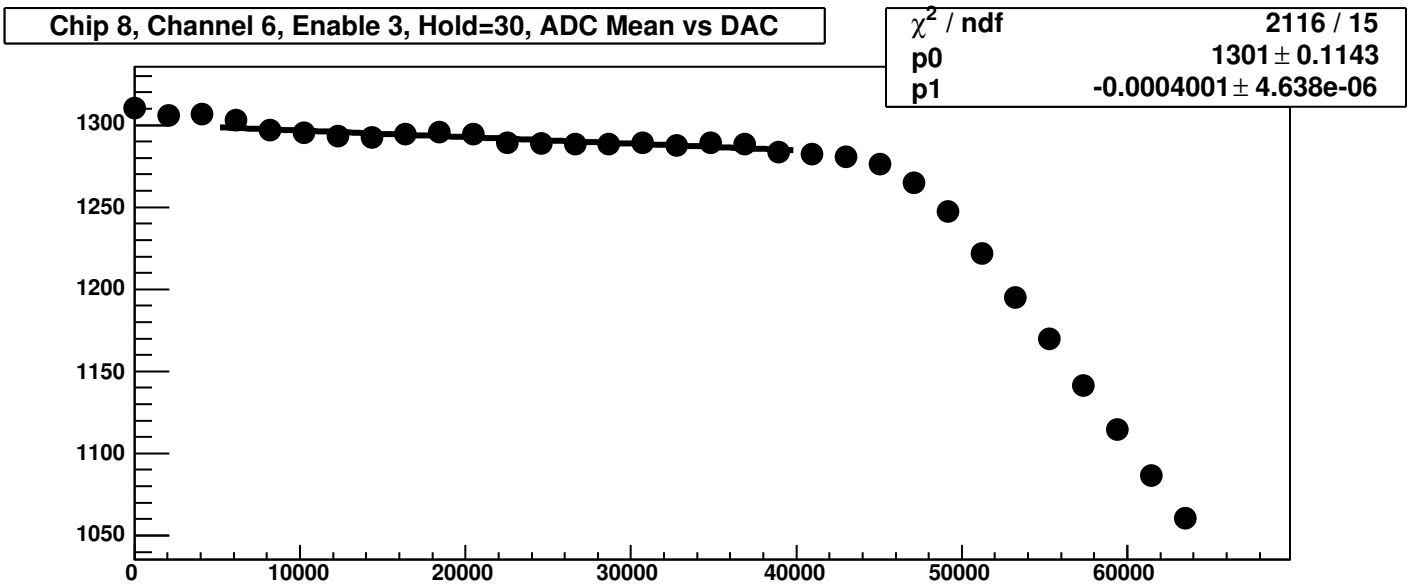

Chip 8, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

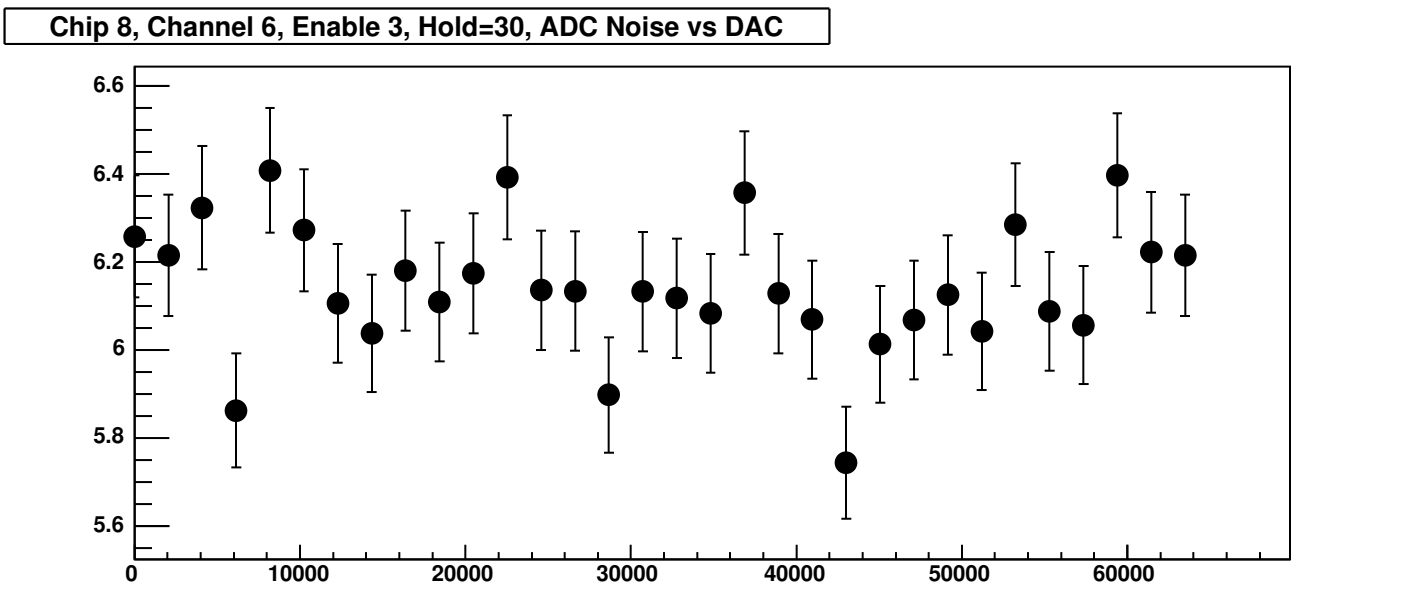

Chip 8, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

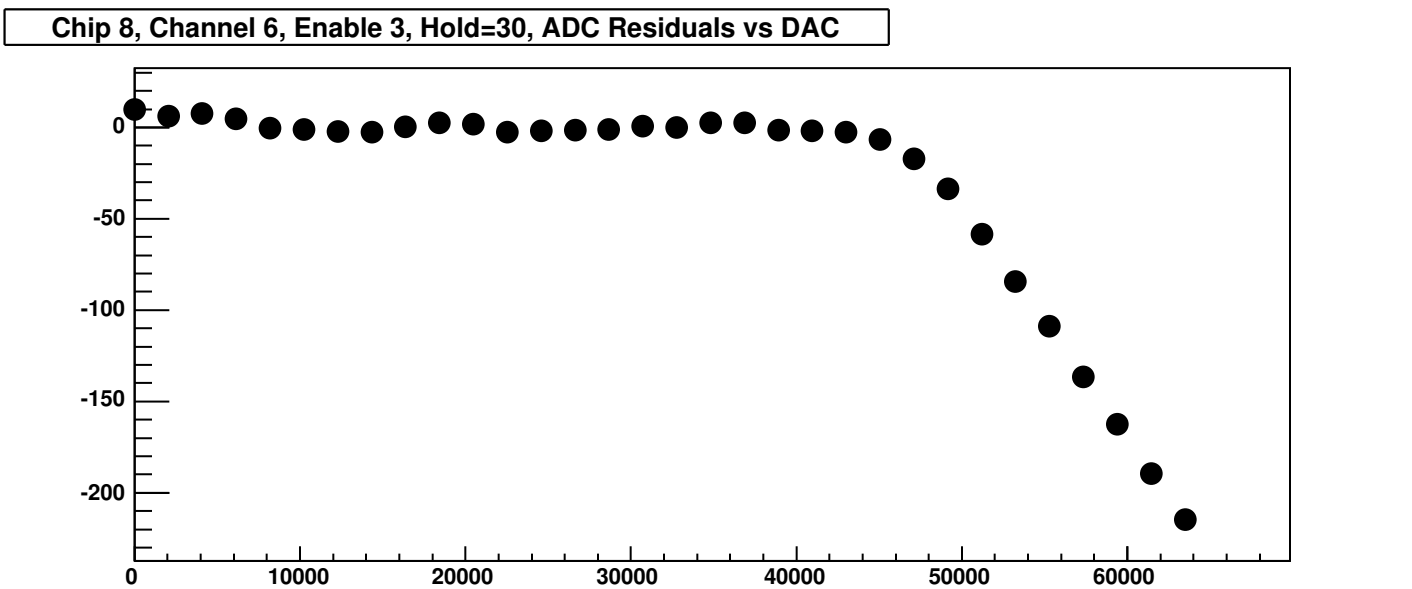

Chip 8, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 7546 / 15
p0 1410 ± 0.2917
p1 0.6251 ± 2.961e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

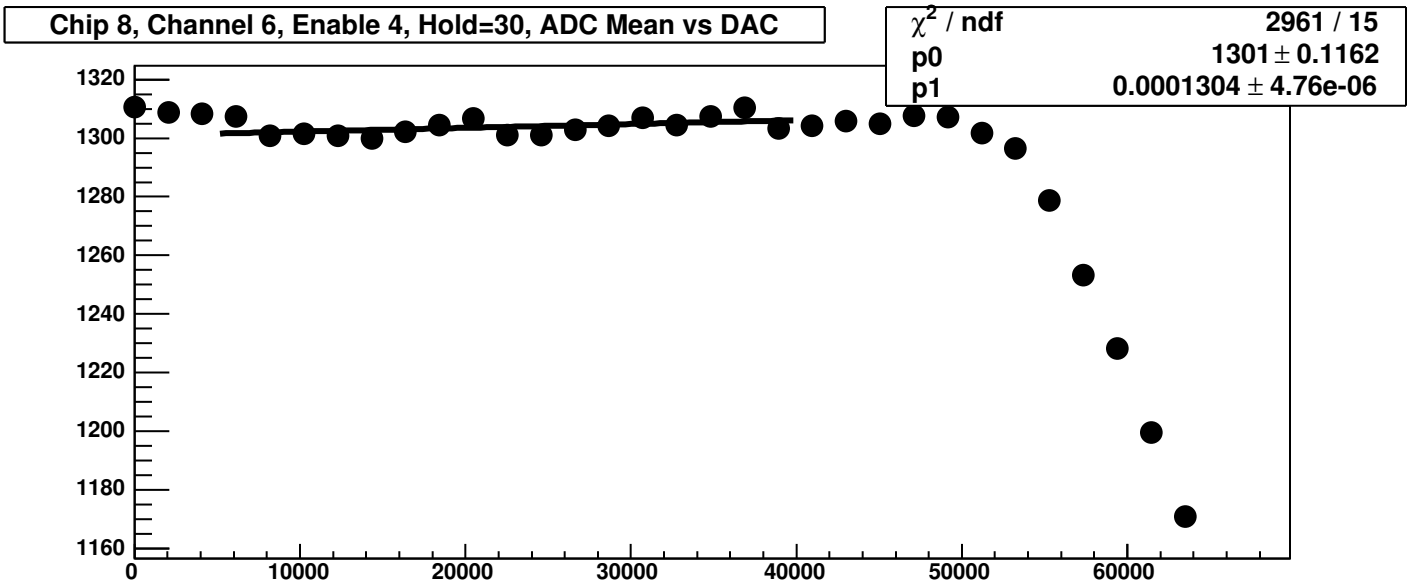

Chip 8, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

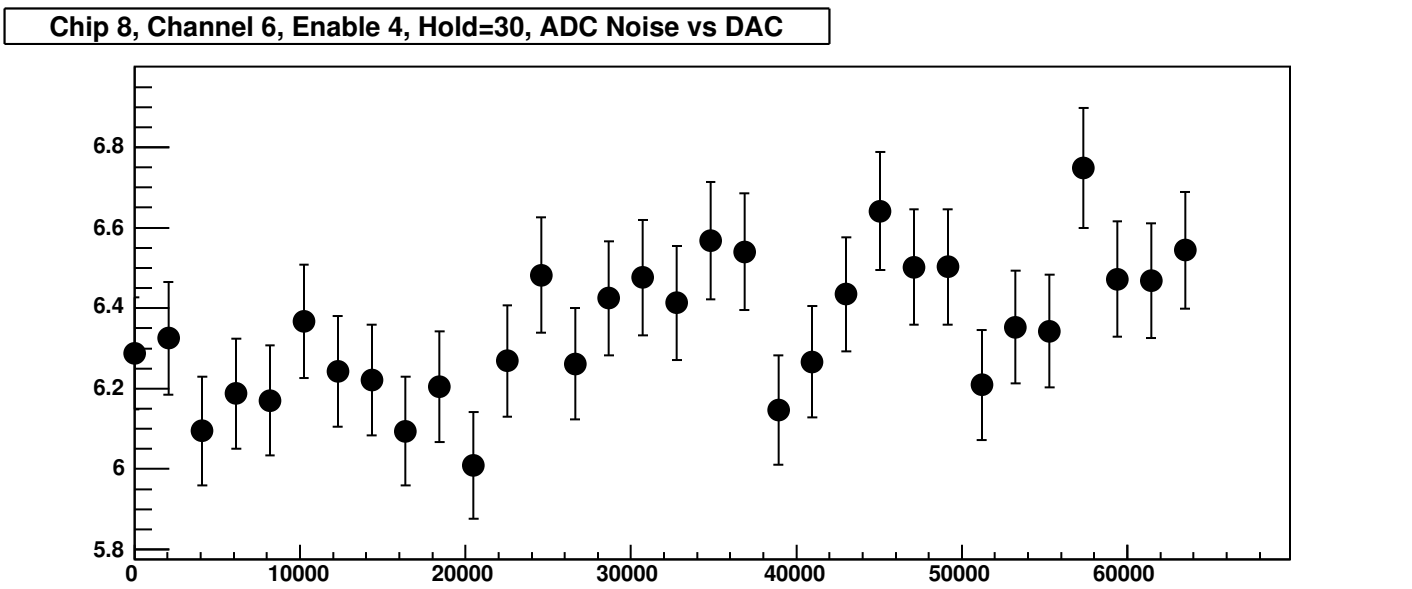

Chip 8, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

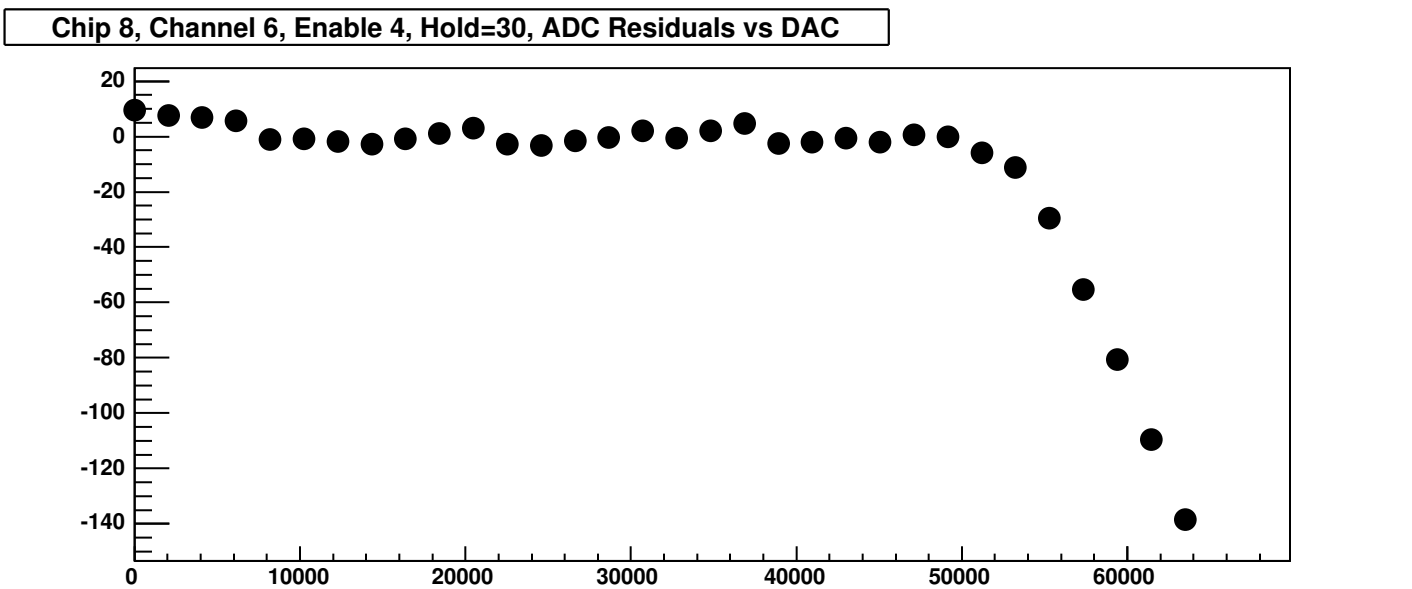

Chip 8, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2116 / 15
p0 1301 ± 0.1143
p1 -0.0004001 ± 4.638e-06
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

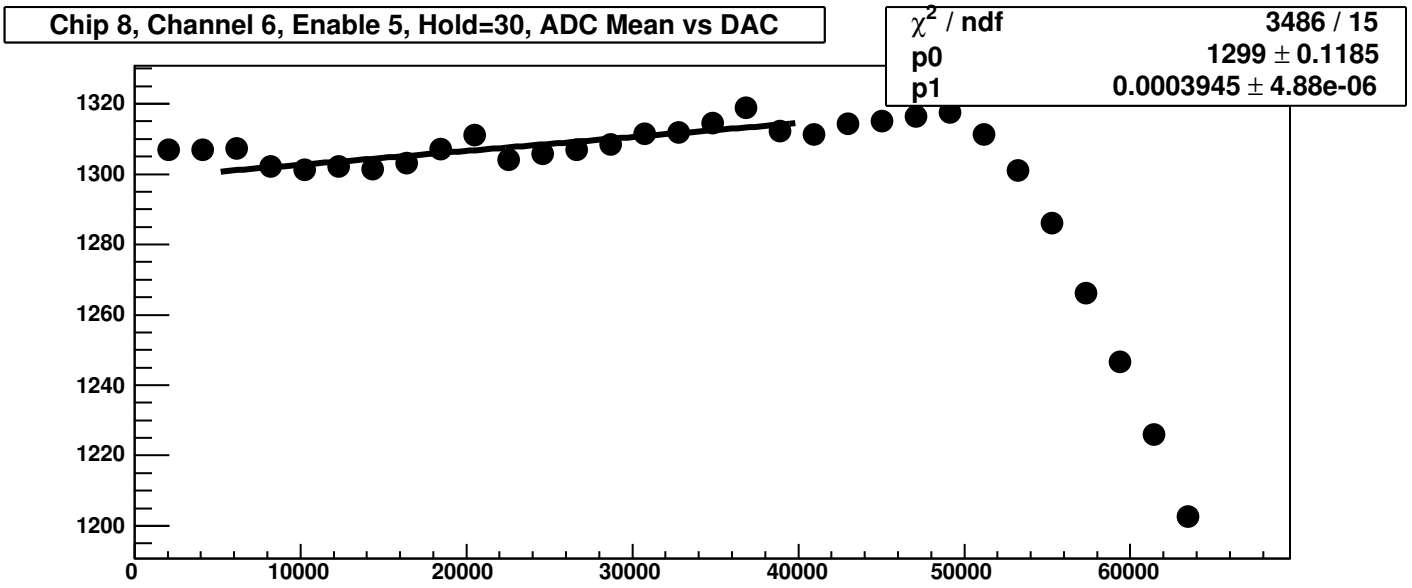

Chip 8, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

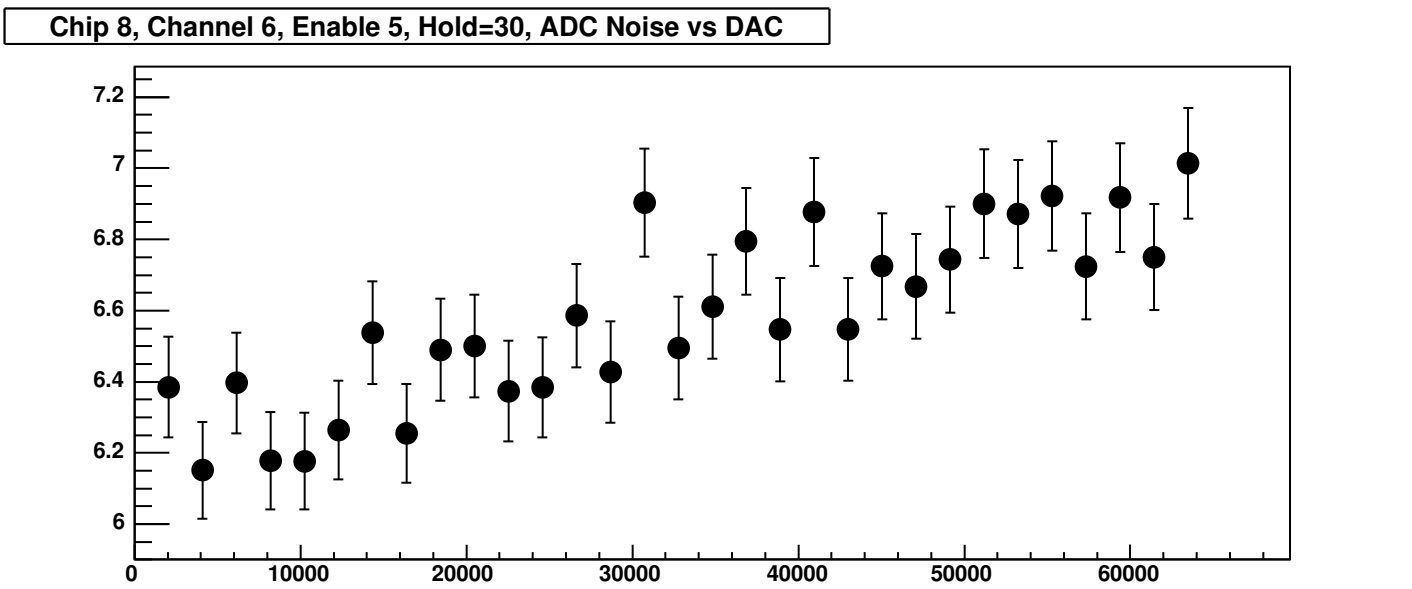

Chip 8, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

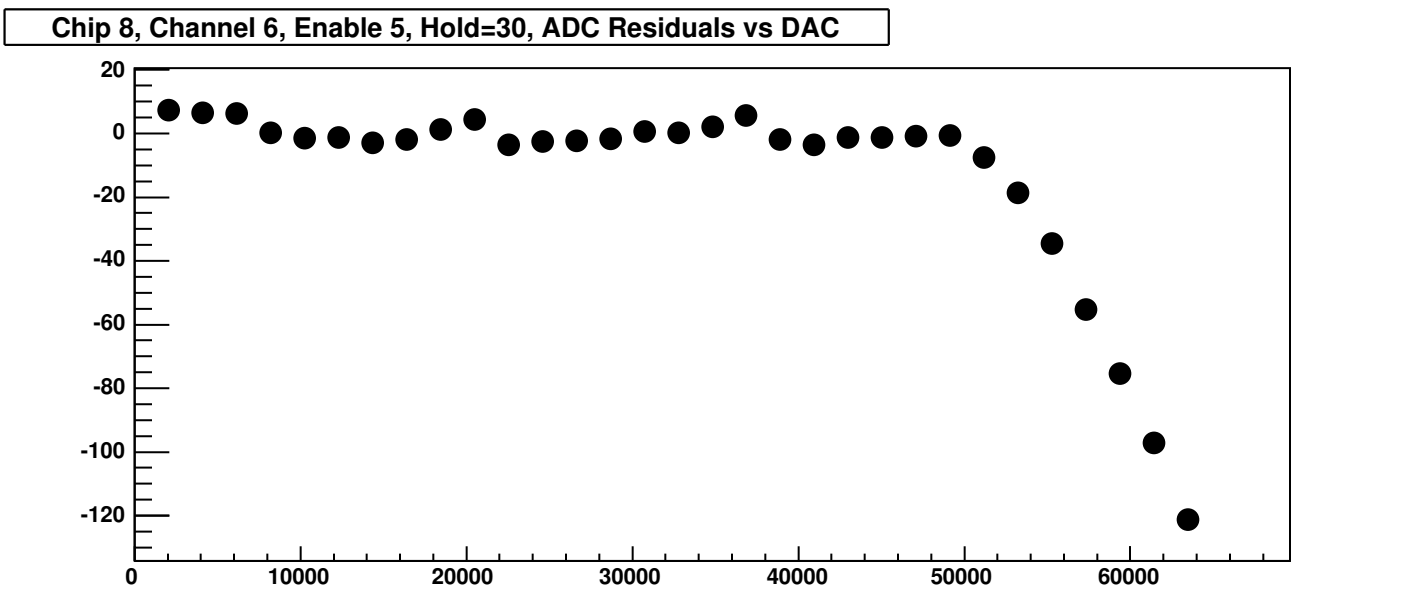

Chip 8, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ^2 / ndf 2961 / 15
p0 1301 ± 0.1162
p1 0.0001304 ± 4.76e-06
1320
1300
1280
1260
1240
1220
1200
1180
1160
0
10000
20000
30000
40000
50000
60000

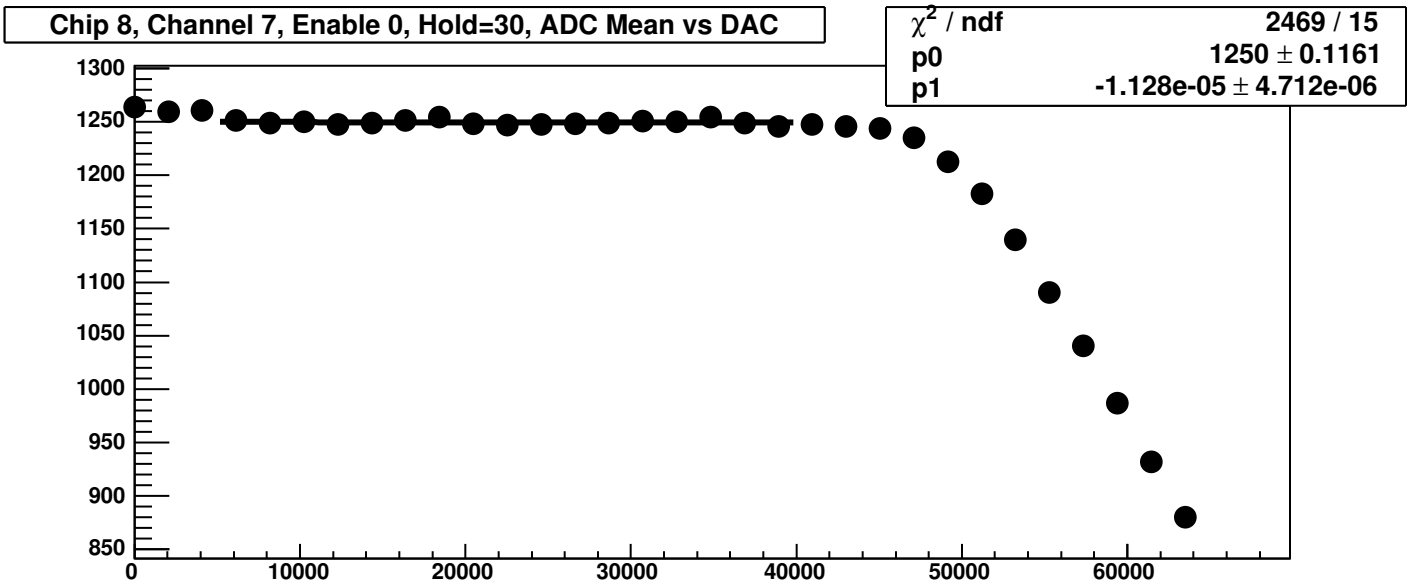

Chip 8, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

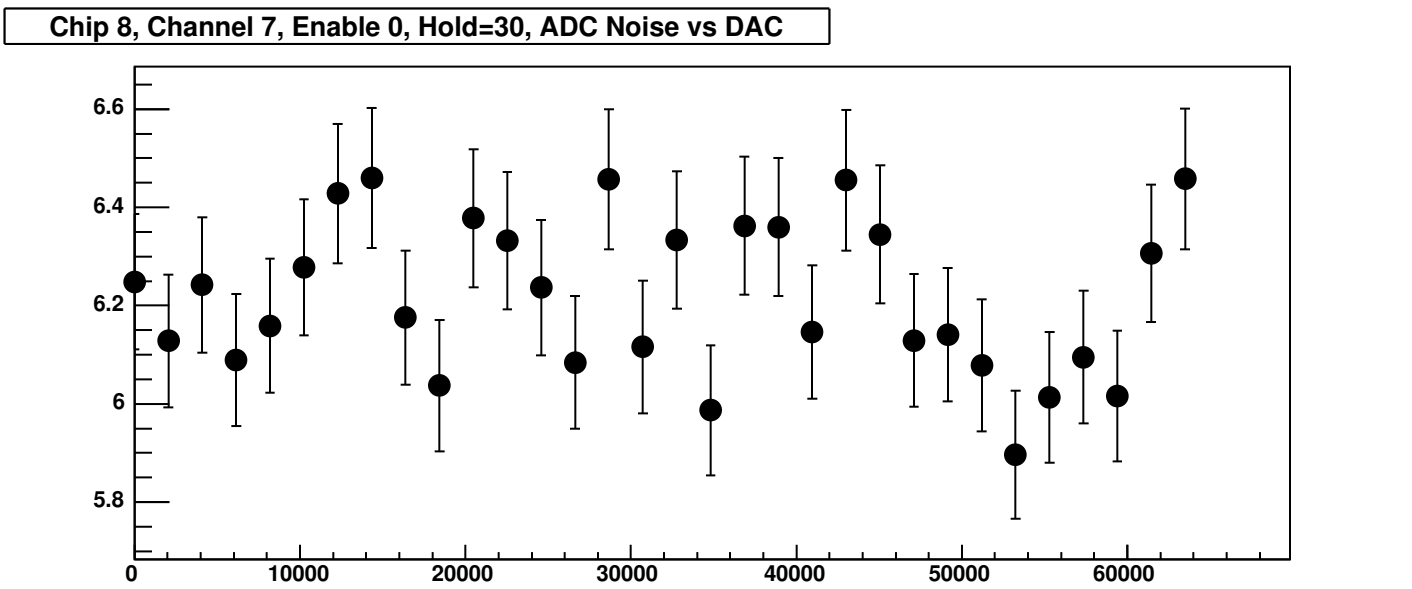

Chip 8, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

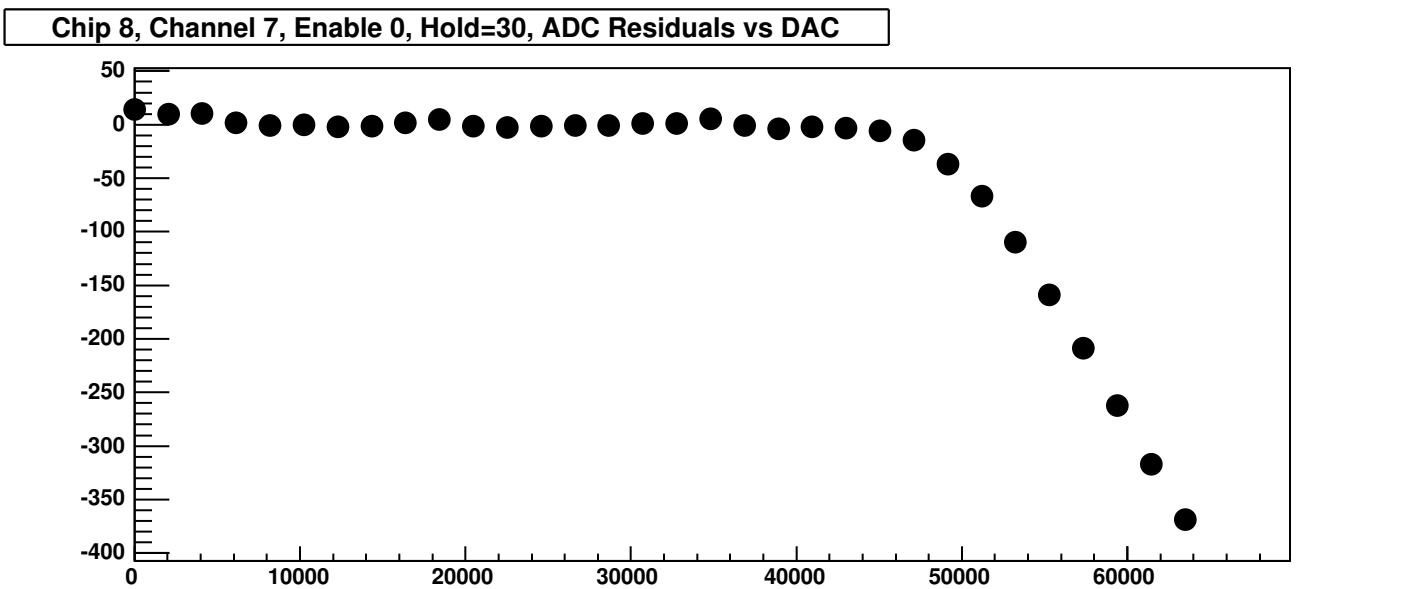

Chip 8, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3486 / 15
p0 1299 ± 0.1185
p1 0.0003945 ± 4.88e-06
1320
1300
1280
1260
1240
1220
1200
0
10000
20000
30000
40000
50000
60000

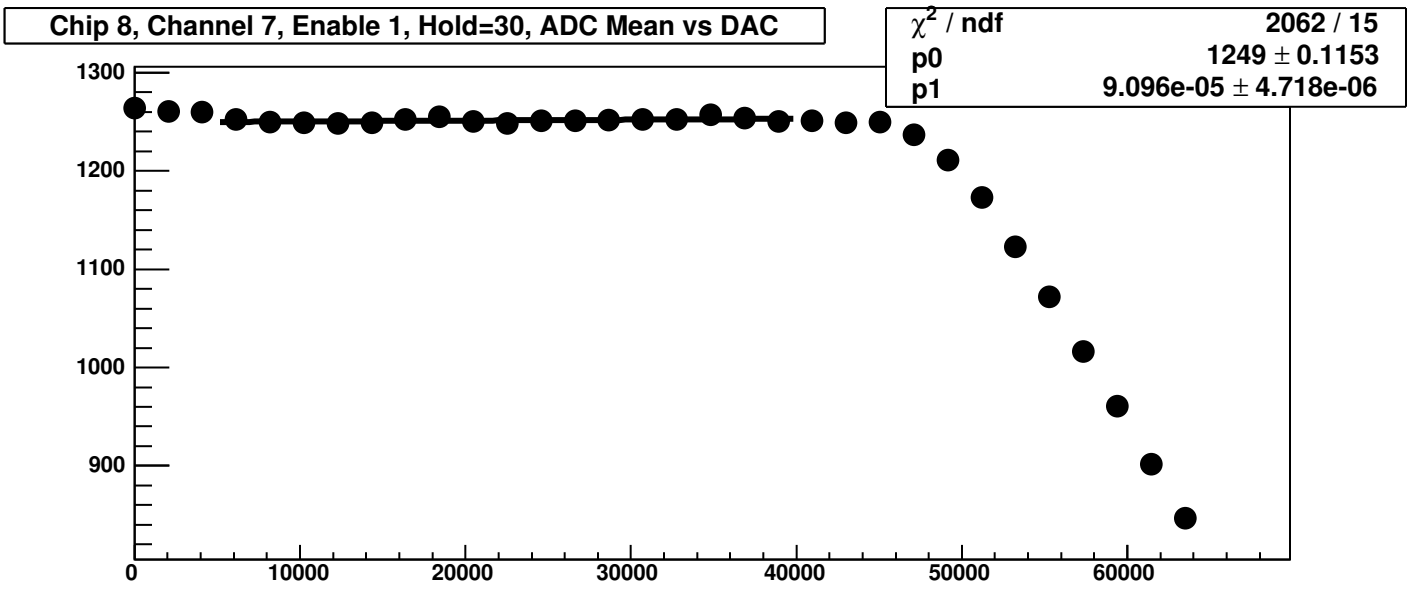

Chip 8, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

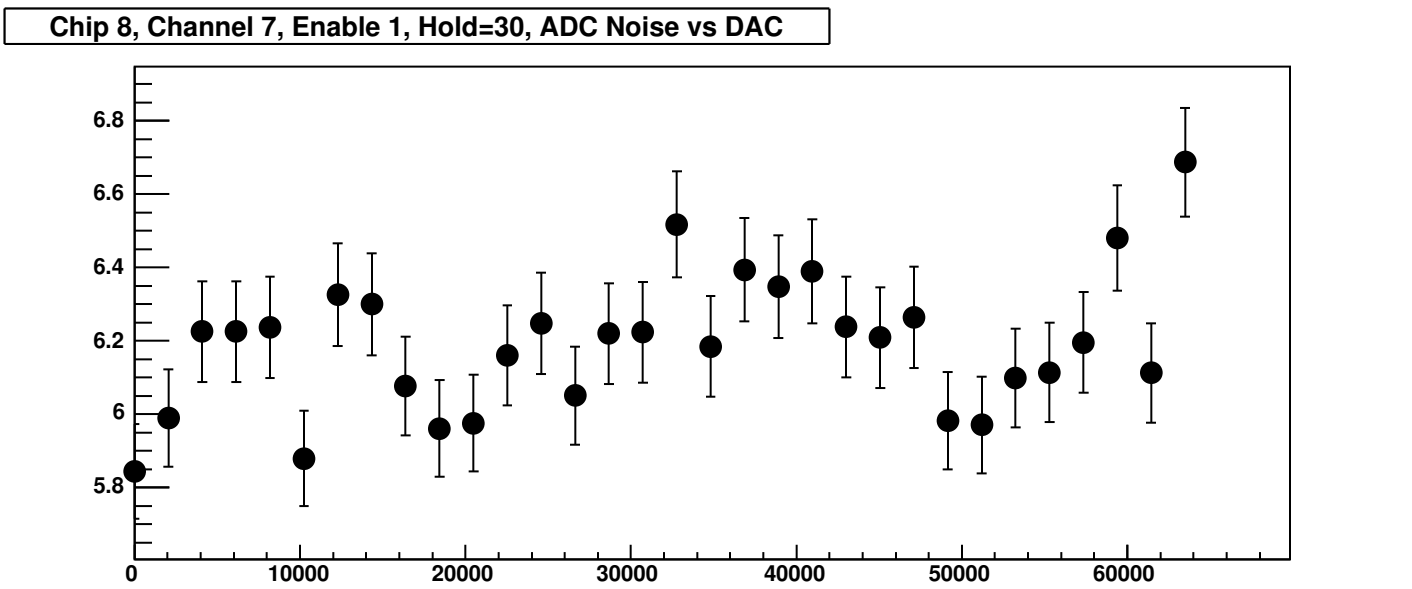

Chip 8, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

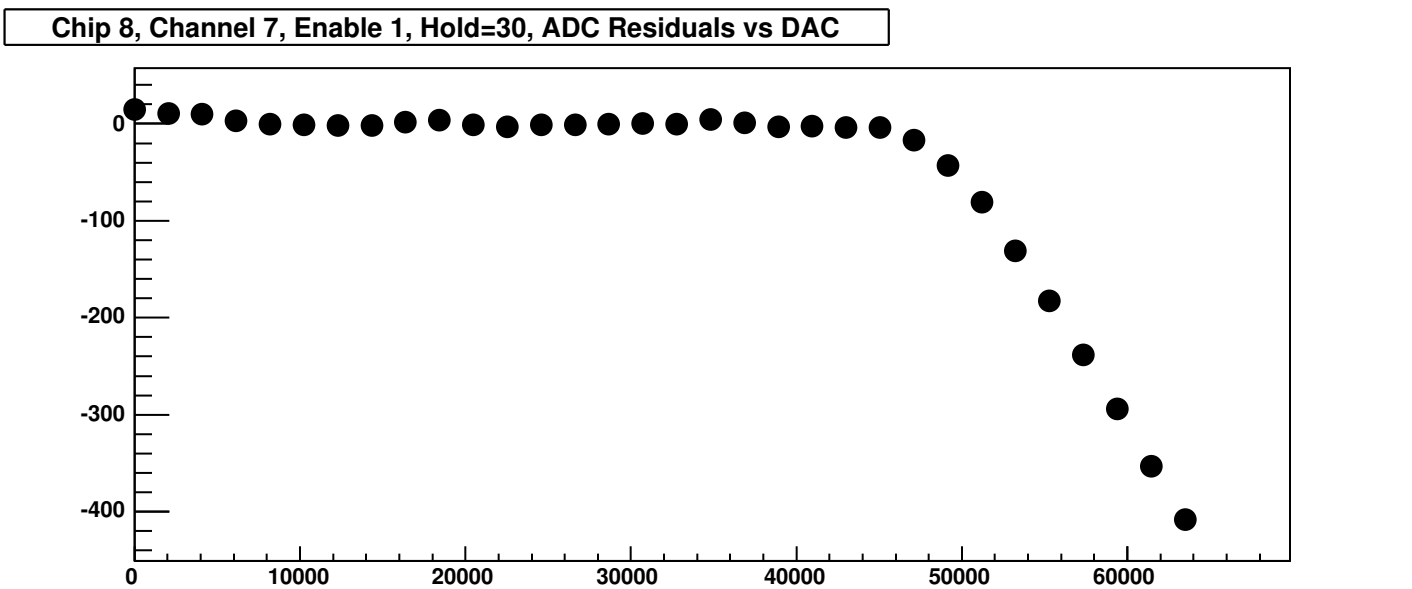

Chip 8, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2469 / 15
p0 1250 ± 0.1161
p1 -1.128e-05 ± 4.712e-06
1300
1250
1200
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

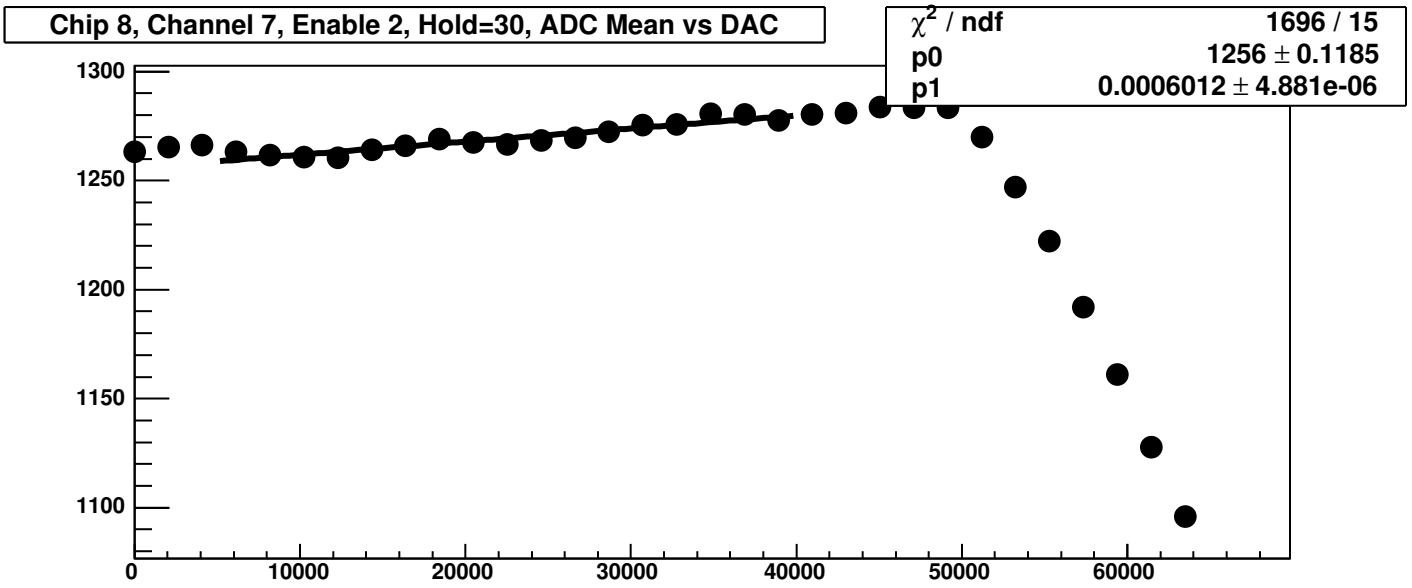

Chip 8, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

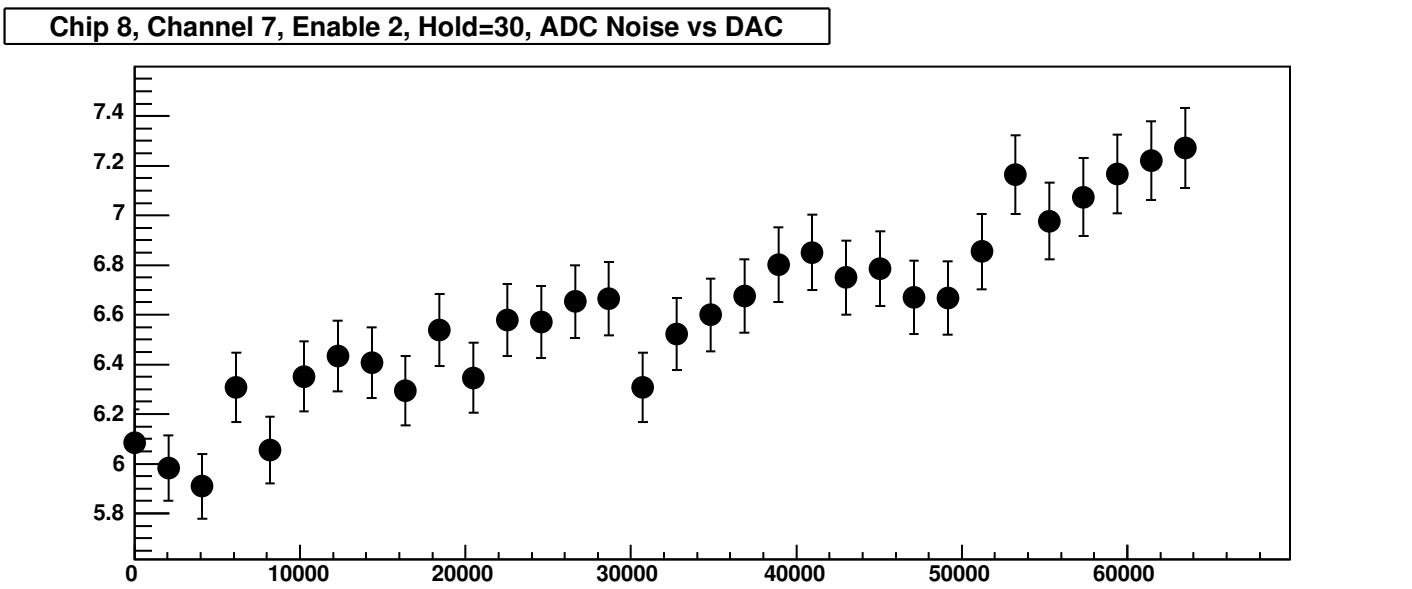

Chip 8, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

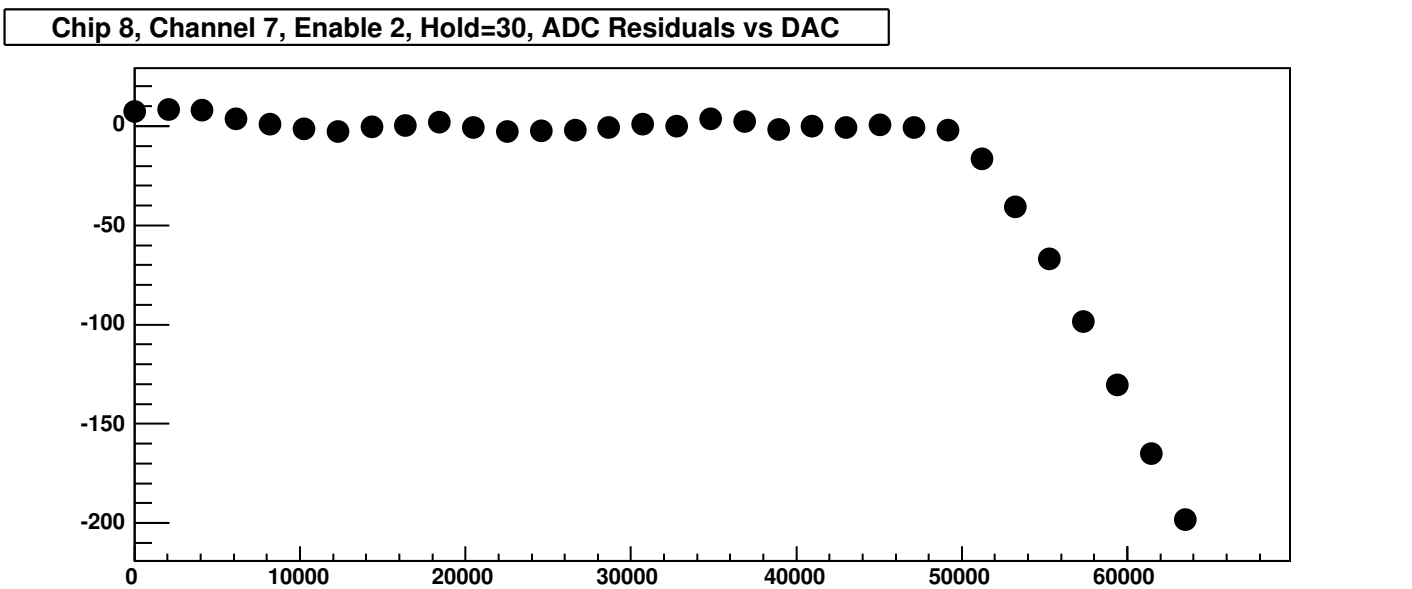

Chip 8, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2062 / 15
p0 1249 ± 0.1153
p1 9.096e-05 ± 4.718e-06
1300
1200
1100
1000
900
0
10000
20000
30000
40000
50000
60000

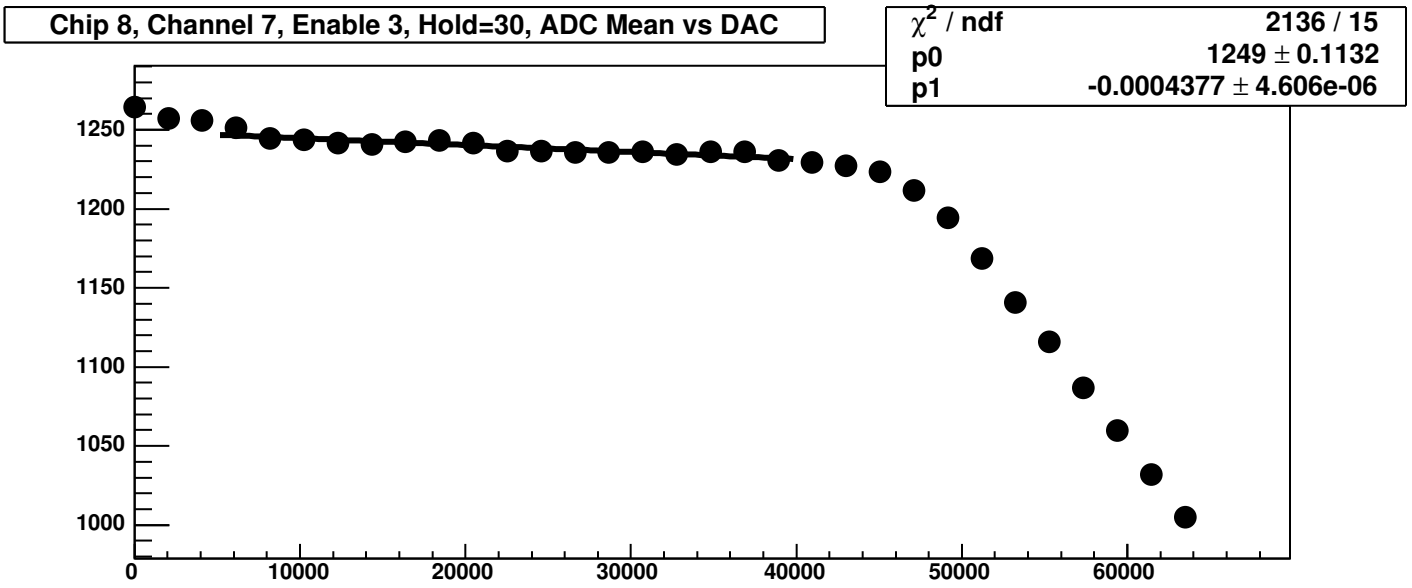

Chip 8, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

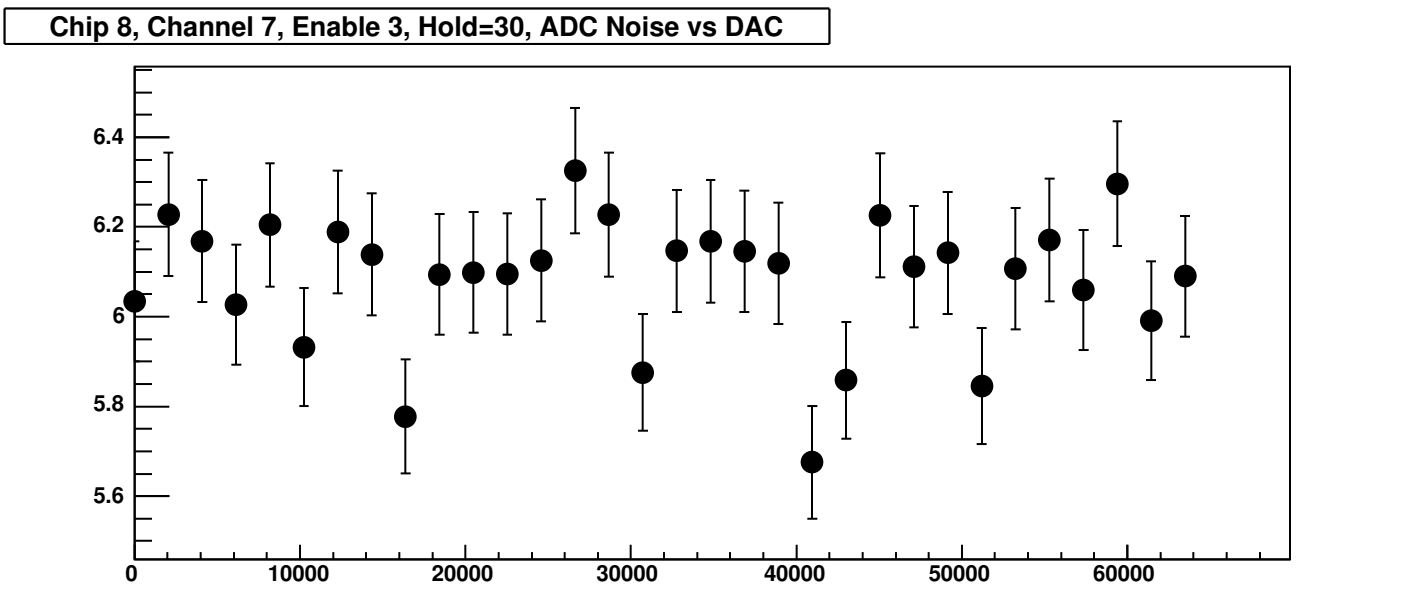

Chip 8, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

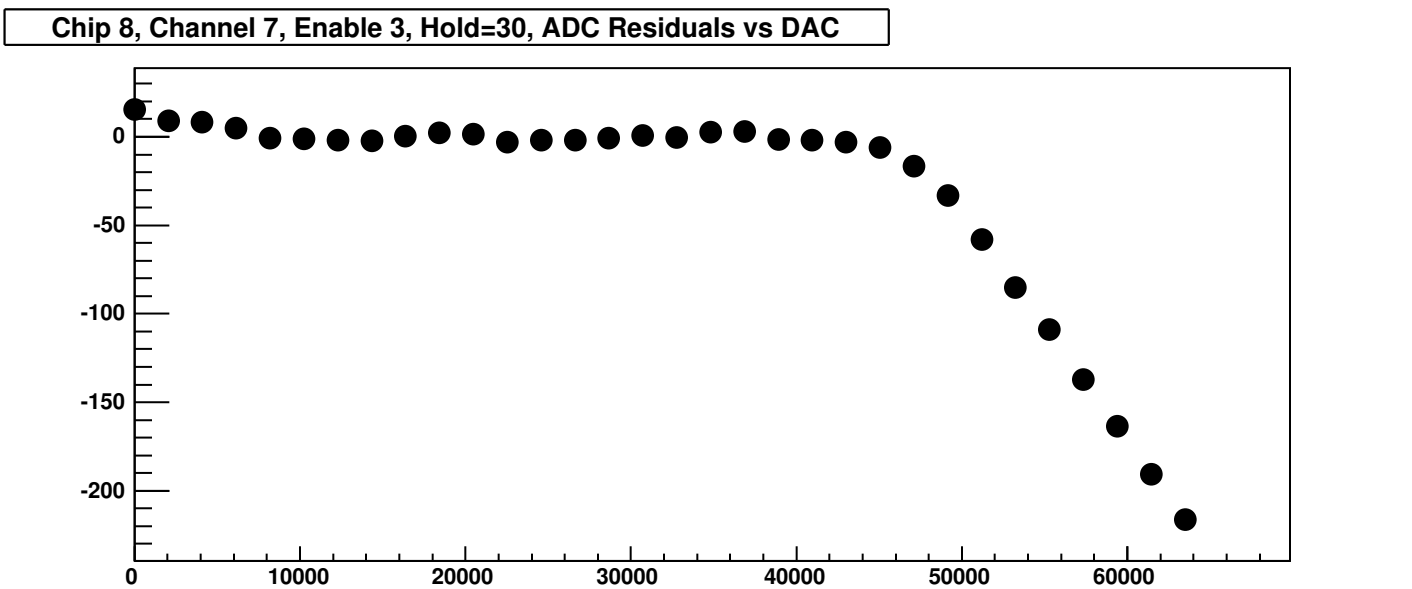

Chip 8, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1696 / 15
p0 1256 ± 0.1185
p1 0.0006012 ± 4.881e-06
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

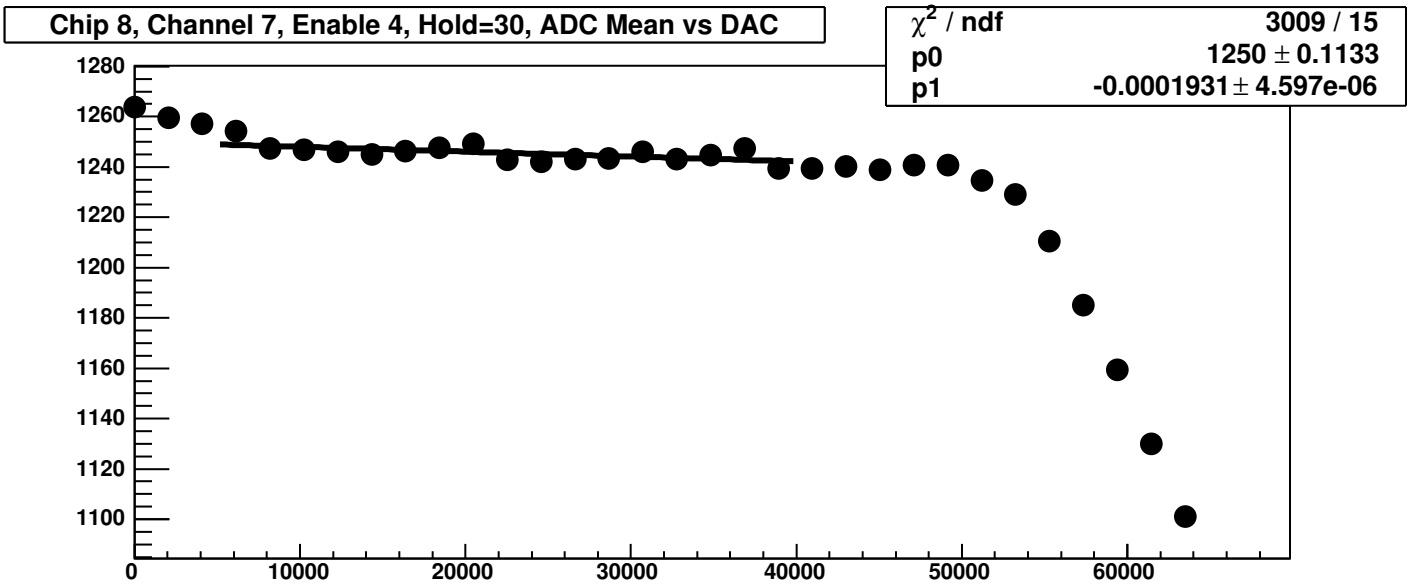

Chip 8, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

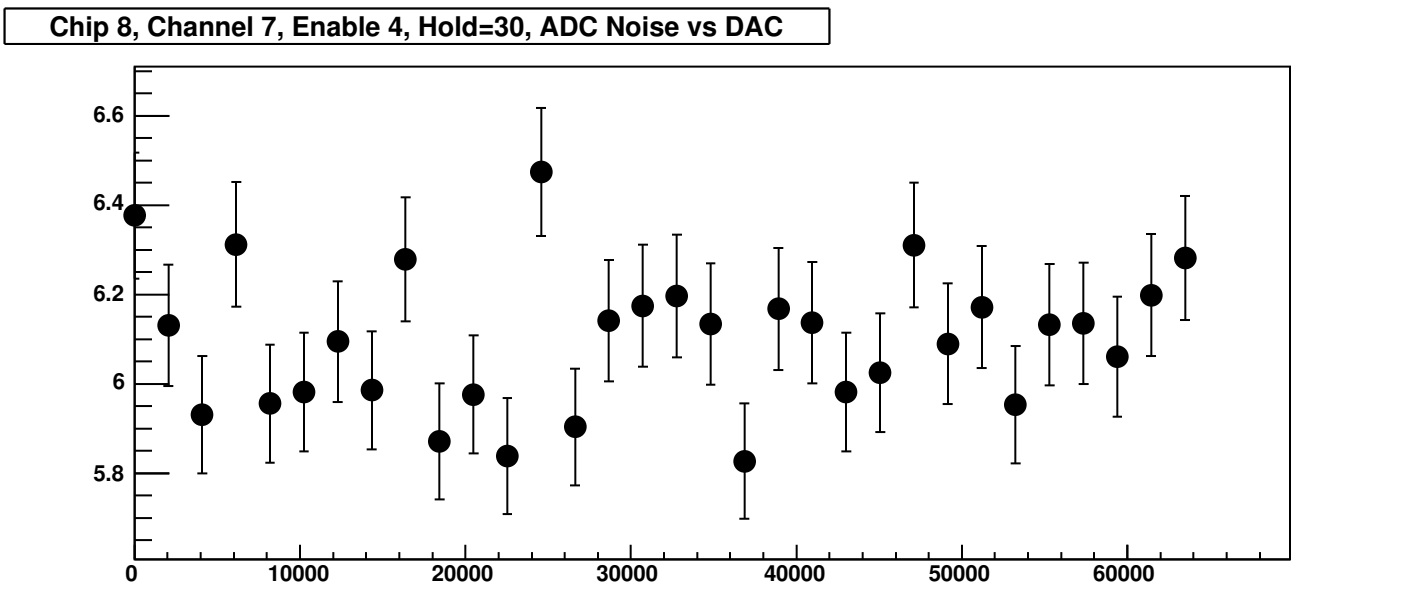

Chip 8, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

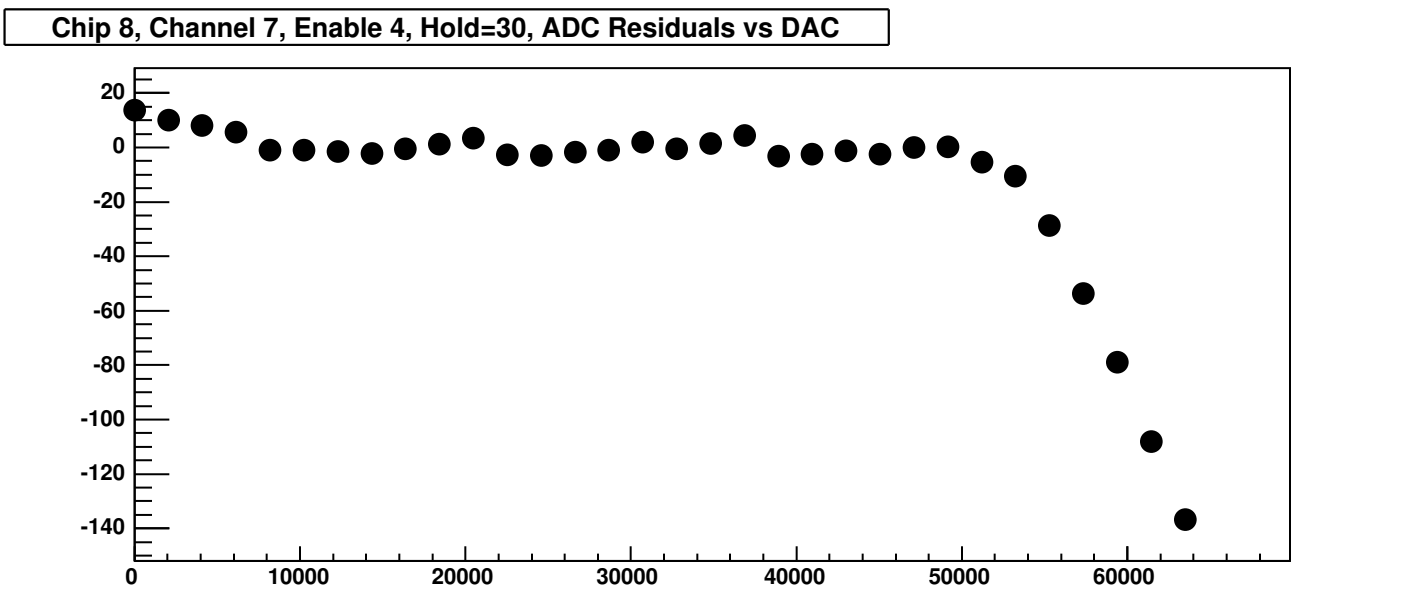

Chip 8, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2136 / 15
p0 1249 ± 0.1132
p1 -0.0004377 ± 4.606e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

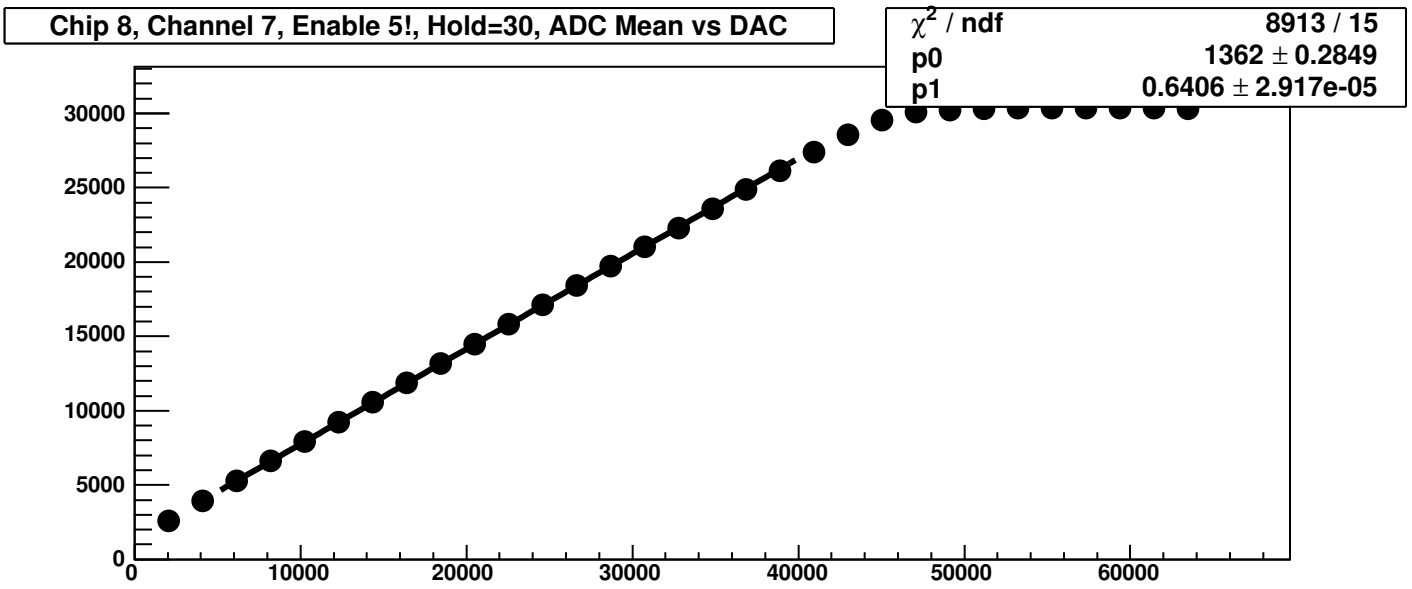

Chip 8, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

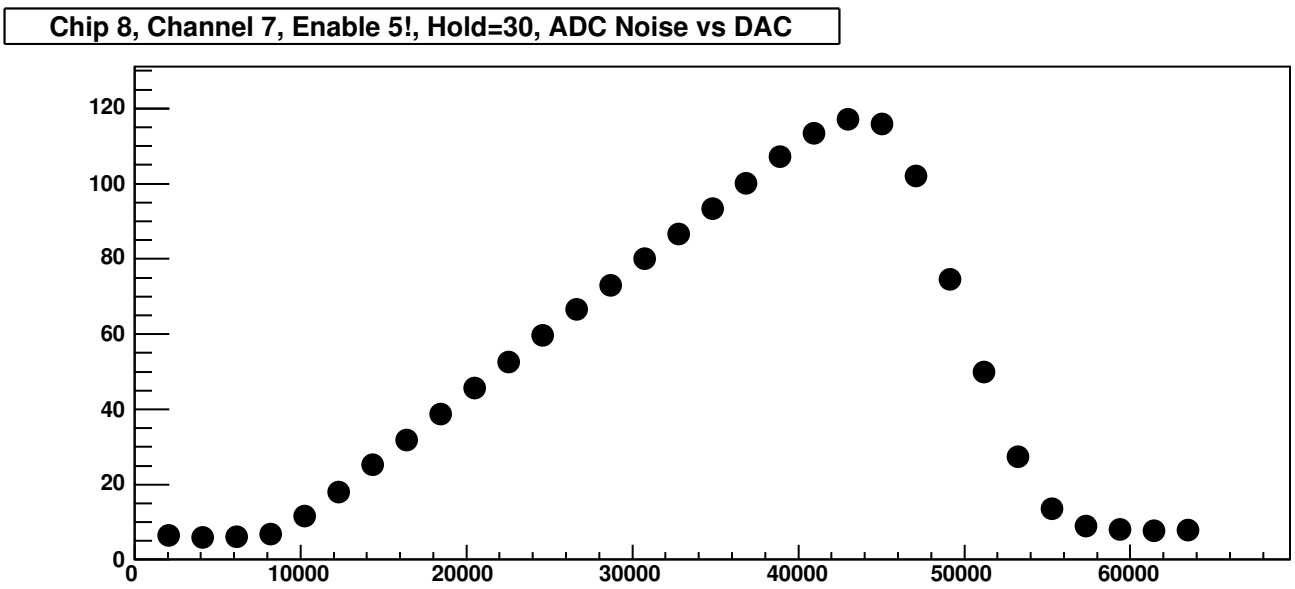

Chip 8, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

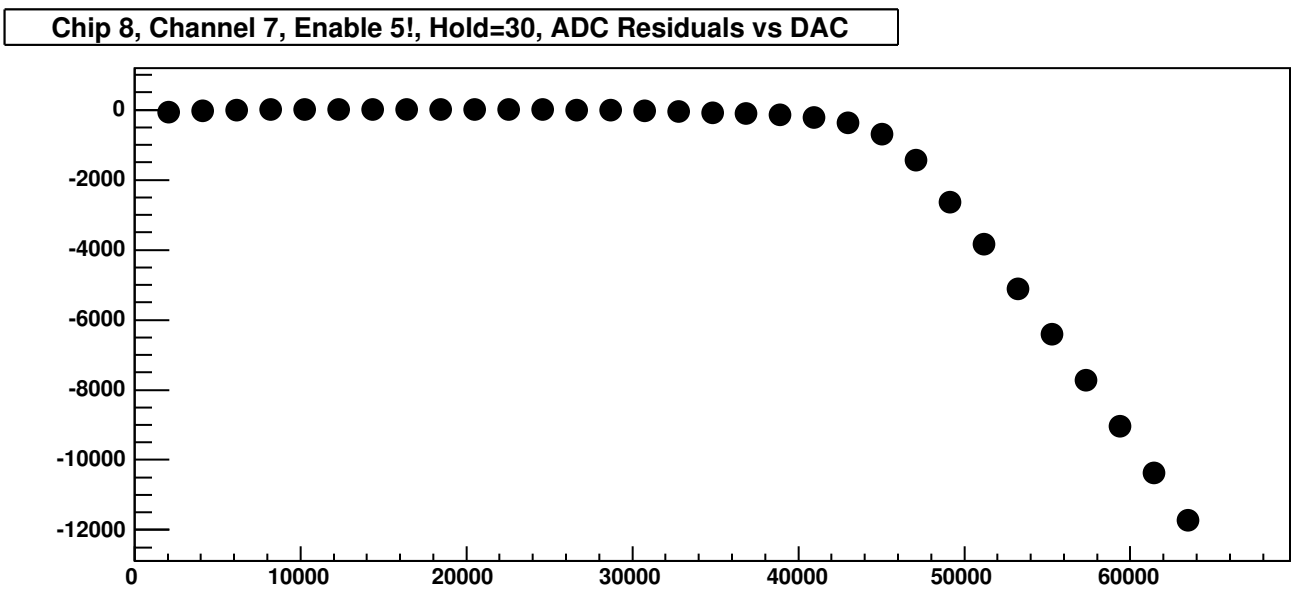

Chip 8, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 3009 / 15
p0 1250 ± 0.1133
p1 -0.0001931 ± 4.597e-06
1280
1260
1240
1220
1200
1180
1160
1140
1120
1100
0
10000
20000
30000
40000
50000
60000

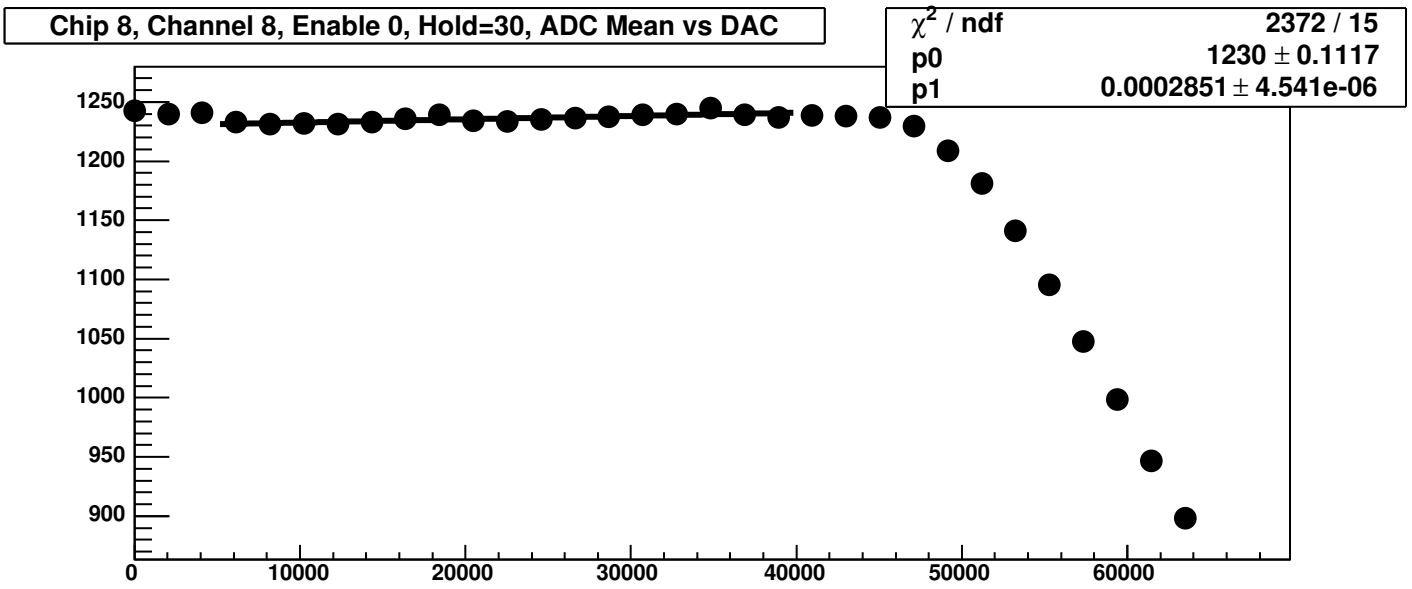

Chip 8, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

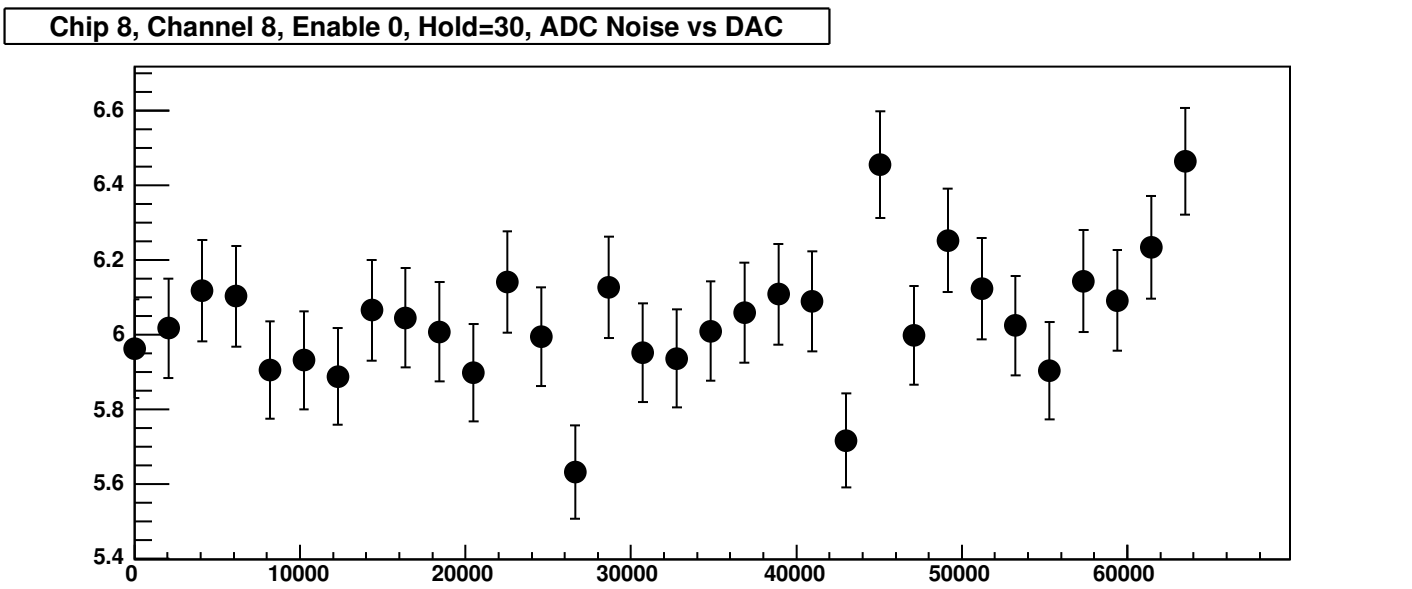

Chip 8, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

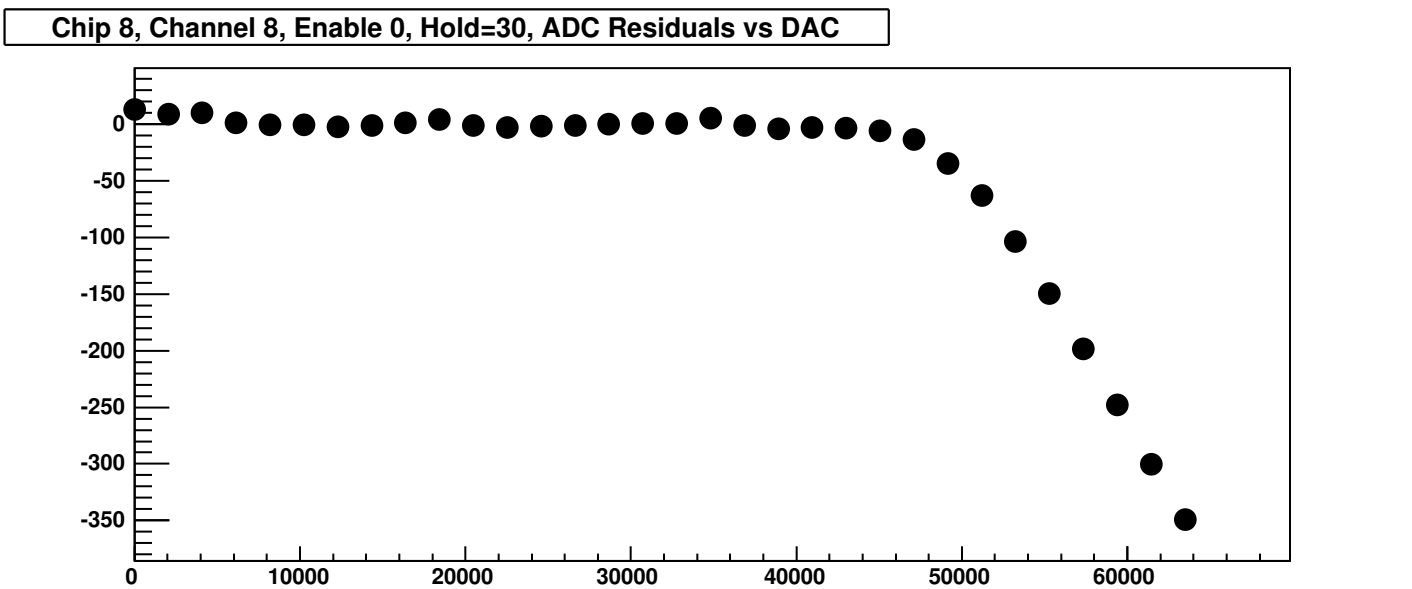

Chip 8, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 8913 / 15
p0 1362 ± 0.2849
p1 0.6406 ± 2.917e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

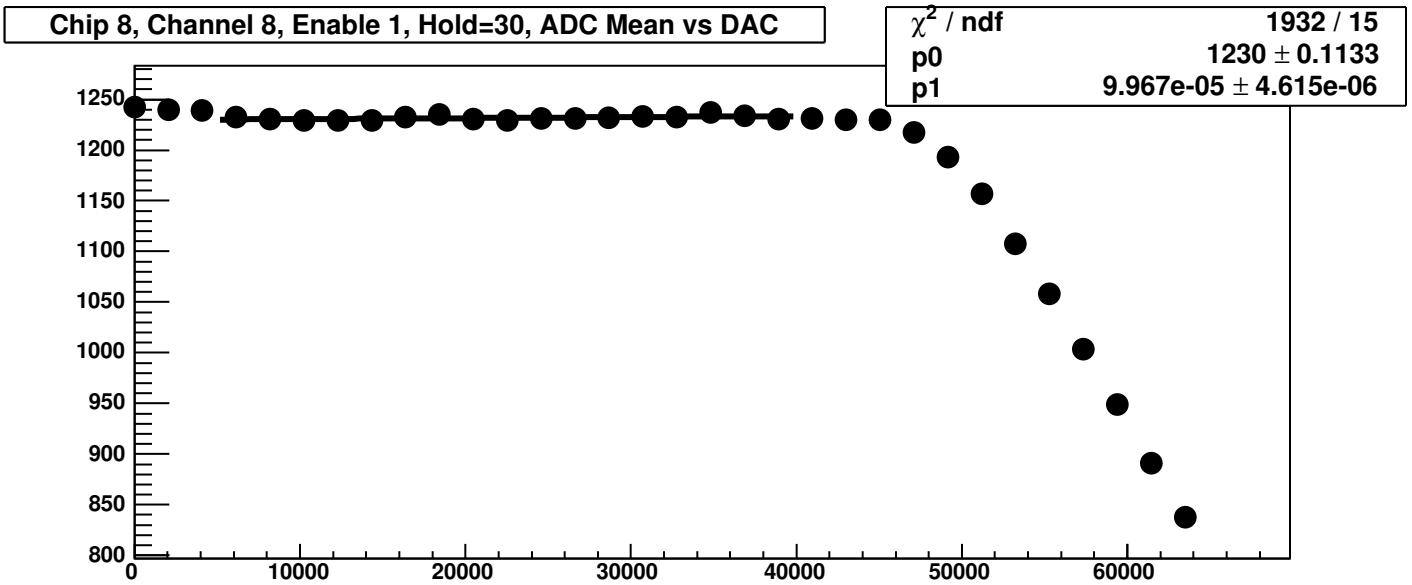

Chip 8, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

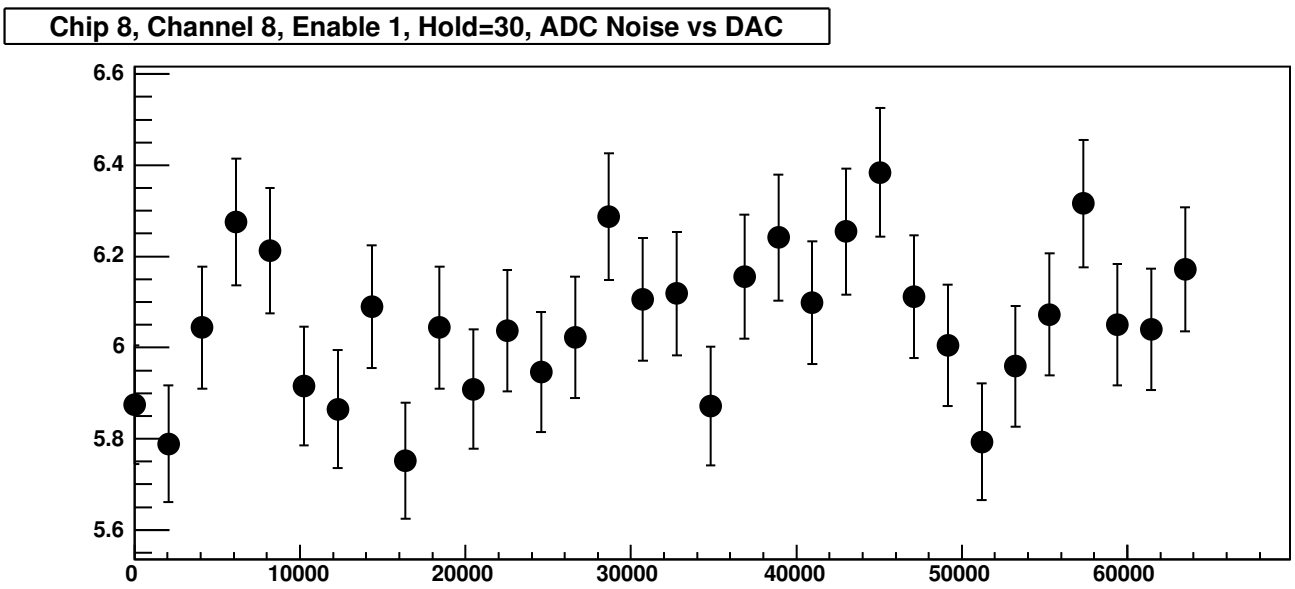

Chip 8, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

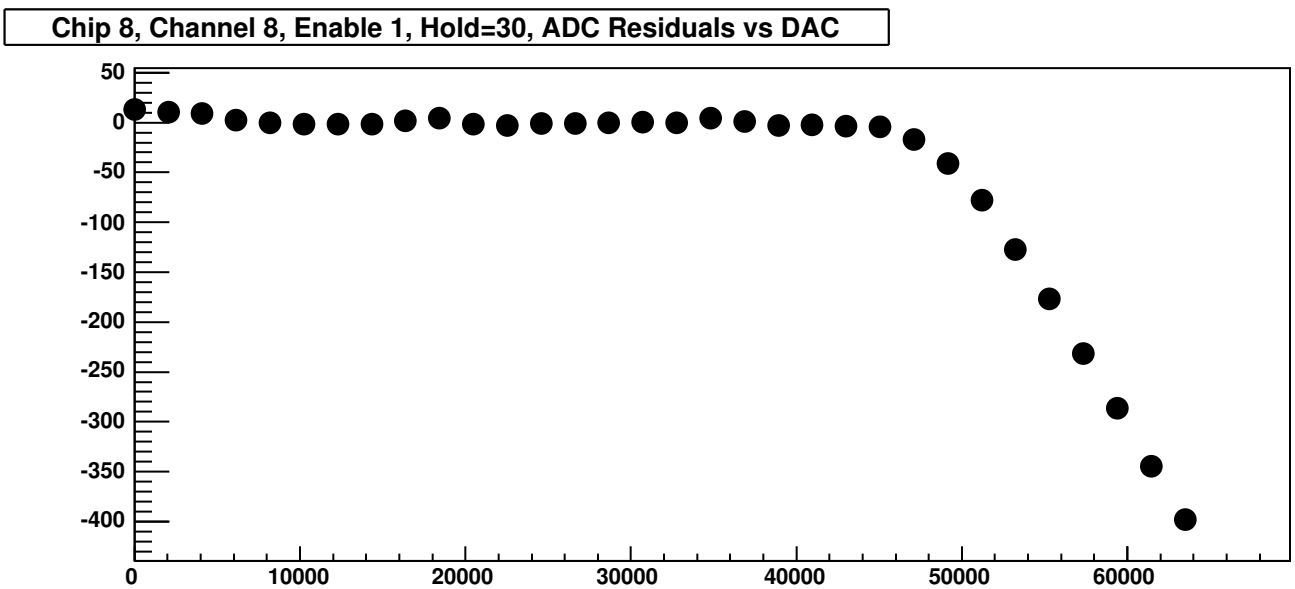

Chip 8, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2372 / 15
p0 1230 ± 0.1117
p1 0.0002851 ± 4.541e-06
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

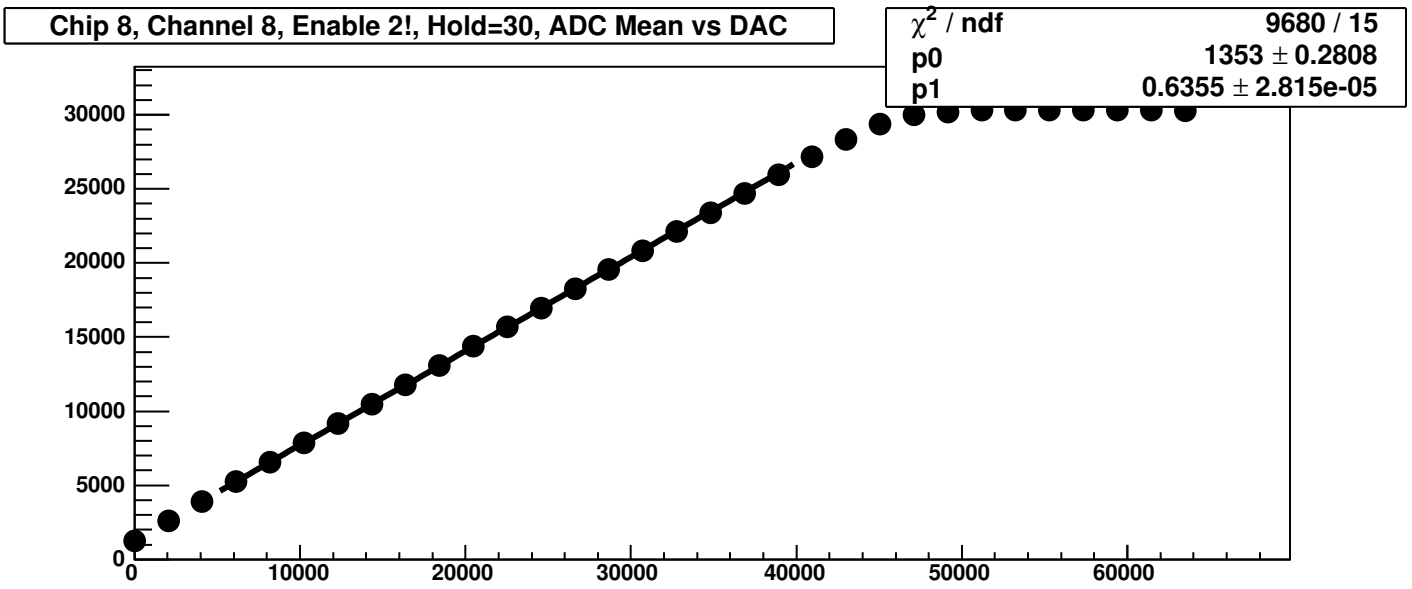

Chip 8, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

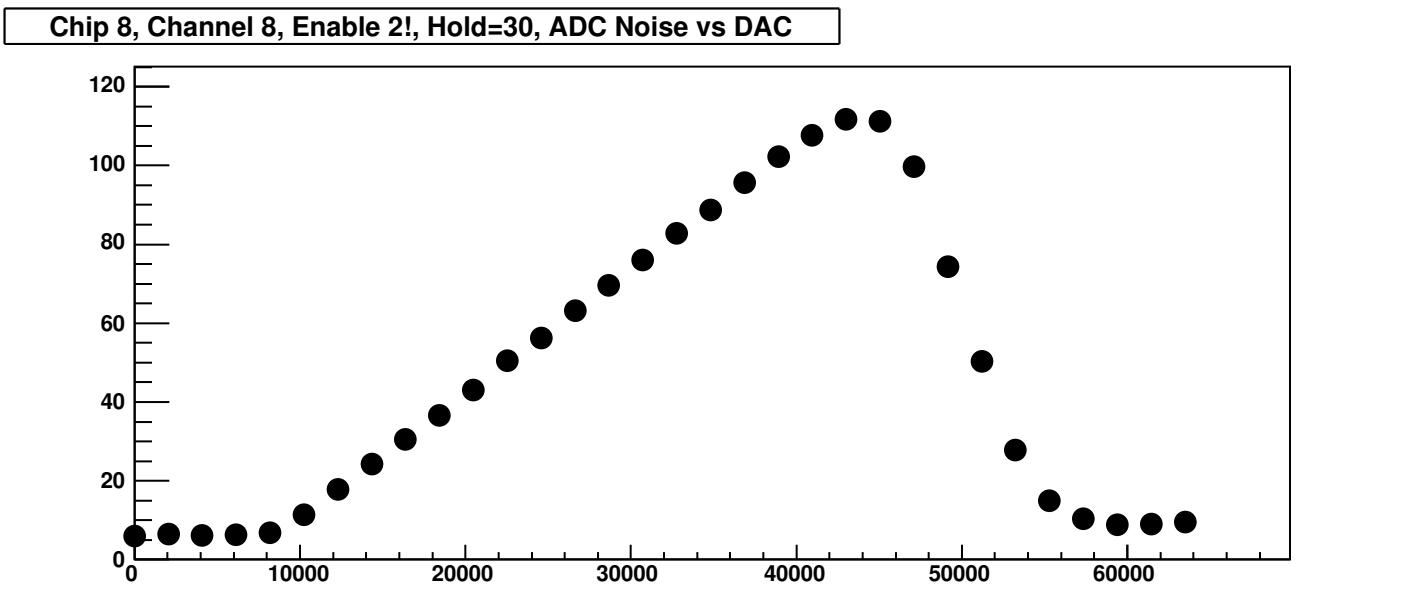

Chip 8, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

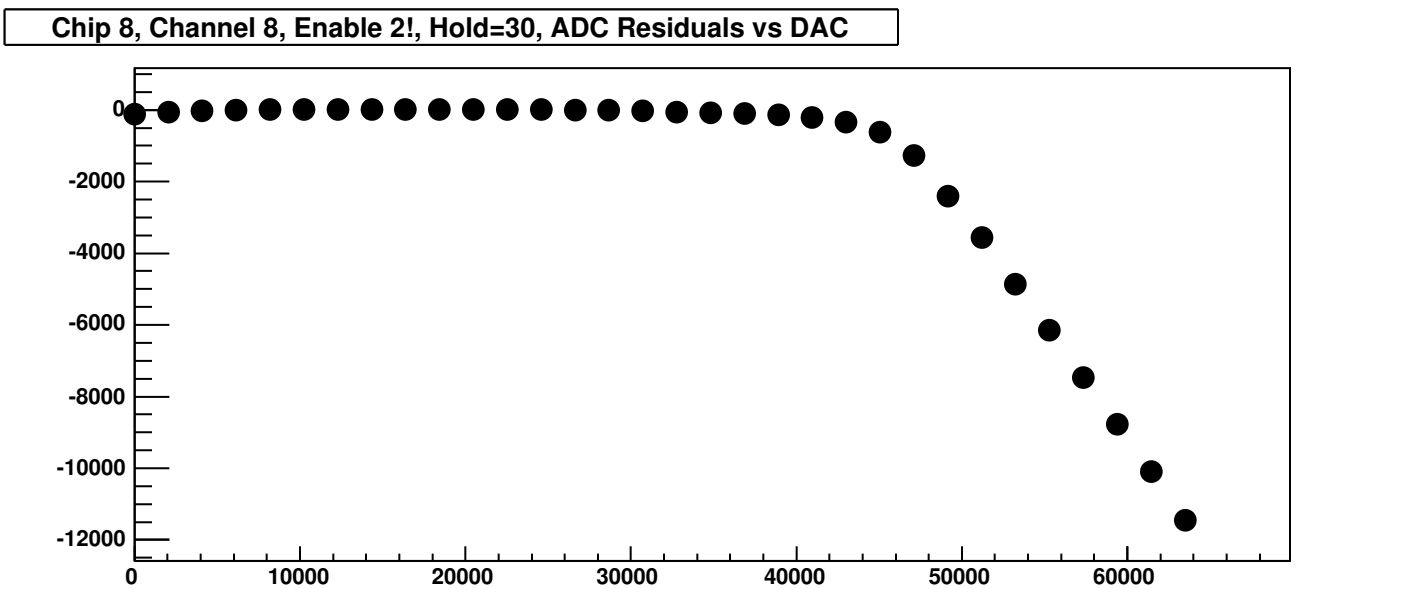

Chip 8, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1932 / 15
p0 1230 ± 0.1133
p1 9.967e-05 ± 4.615e-06
1250
1200
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

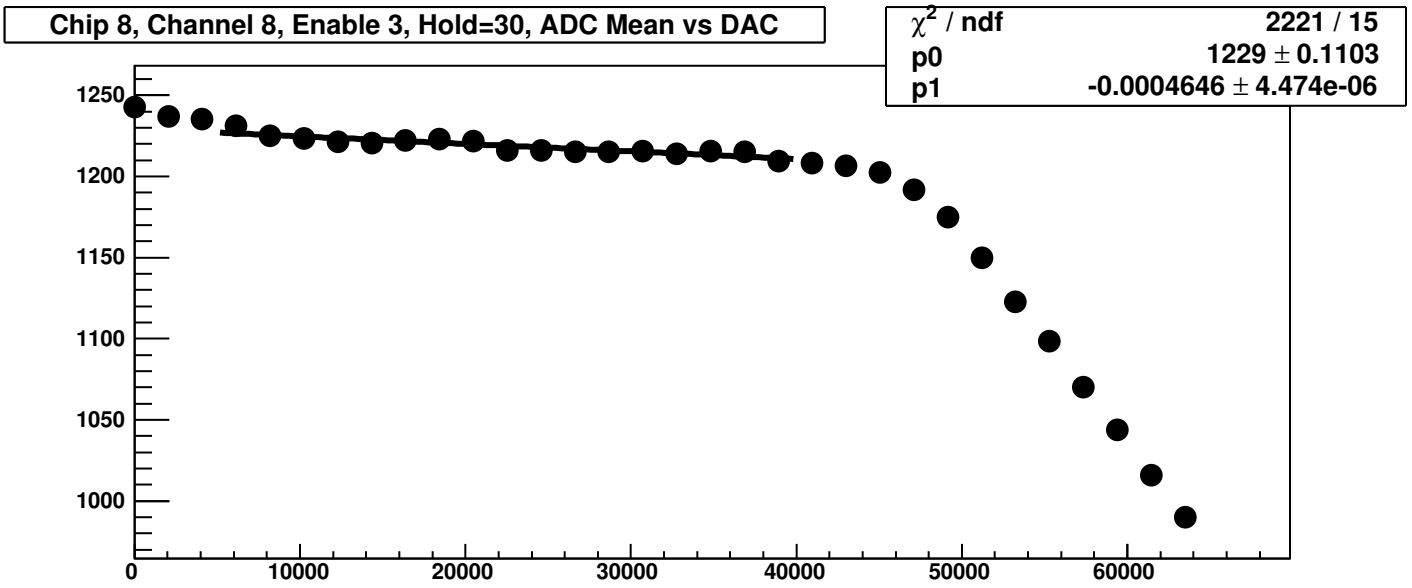

Chip 8, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

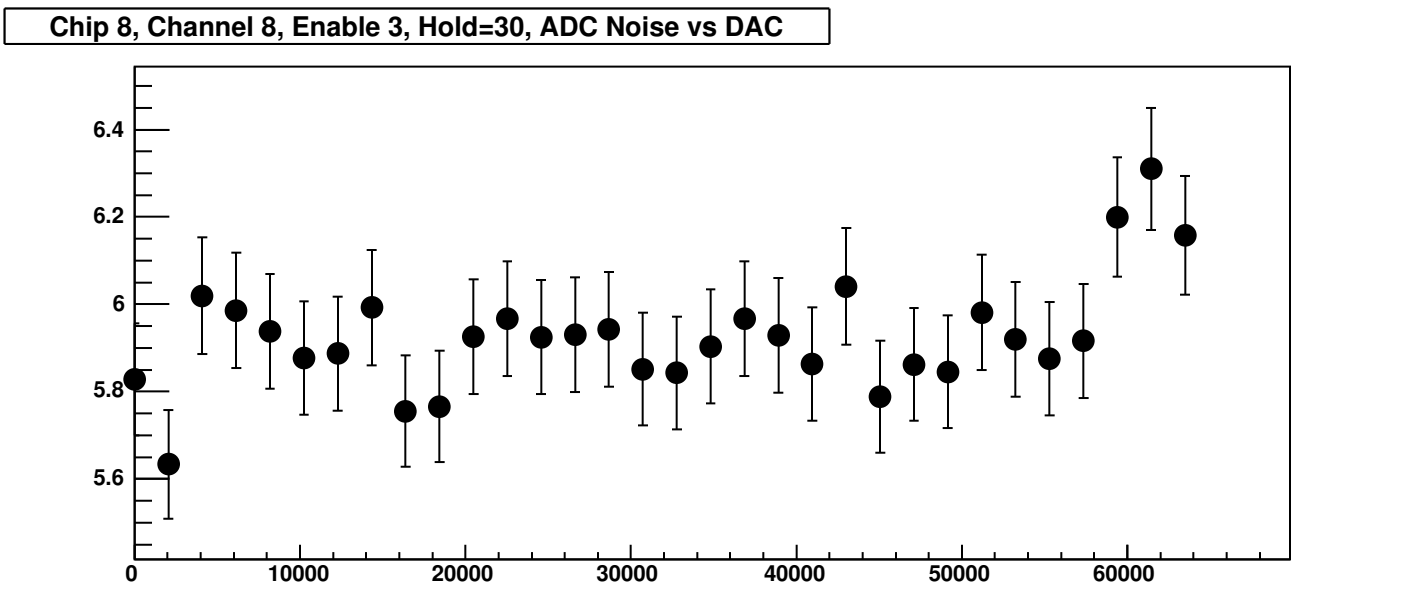

Chip 8, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

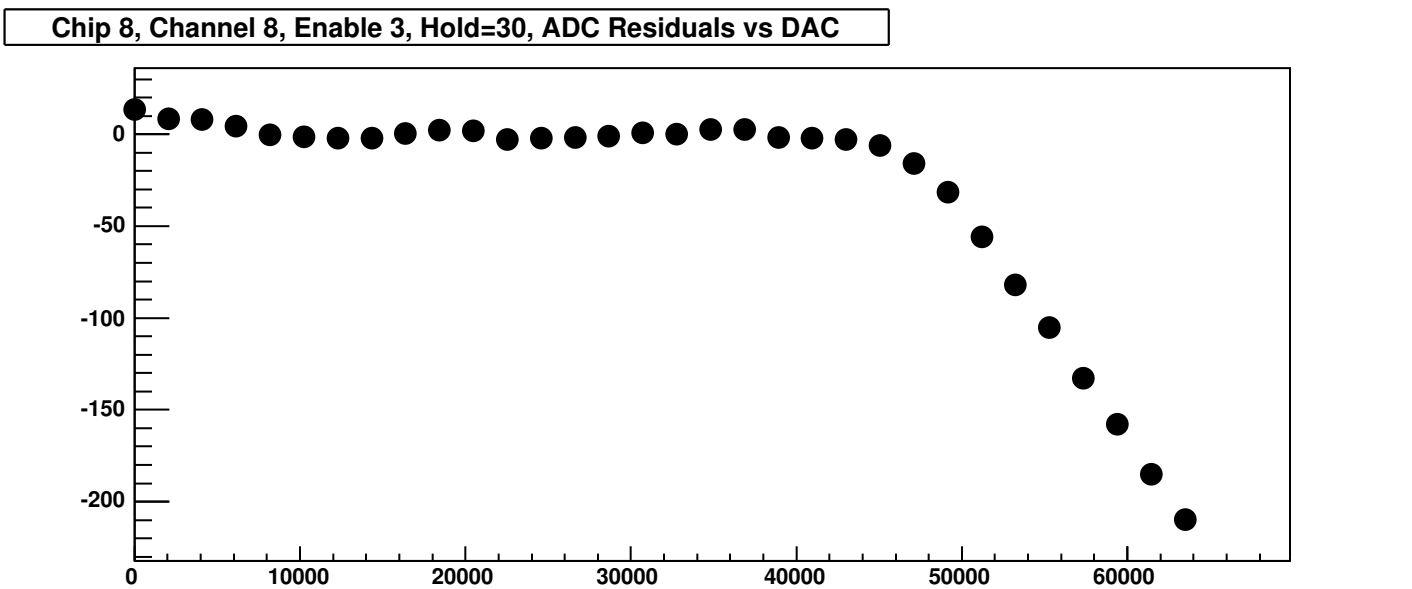

Chip 8, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 9680 / 15
p0 1353 ± 0.2808
p1 0.6355 ± 2.815e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

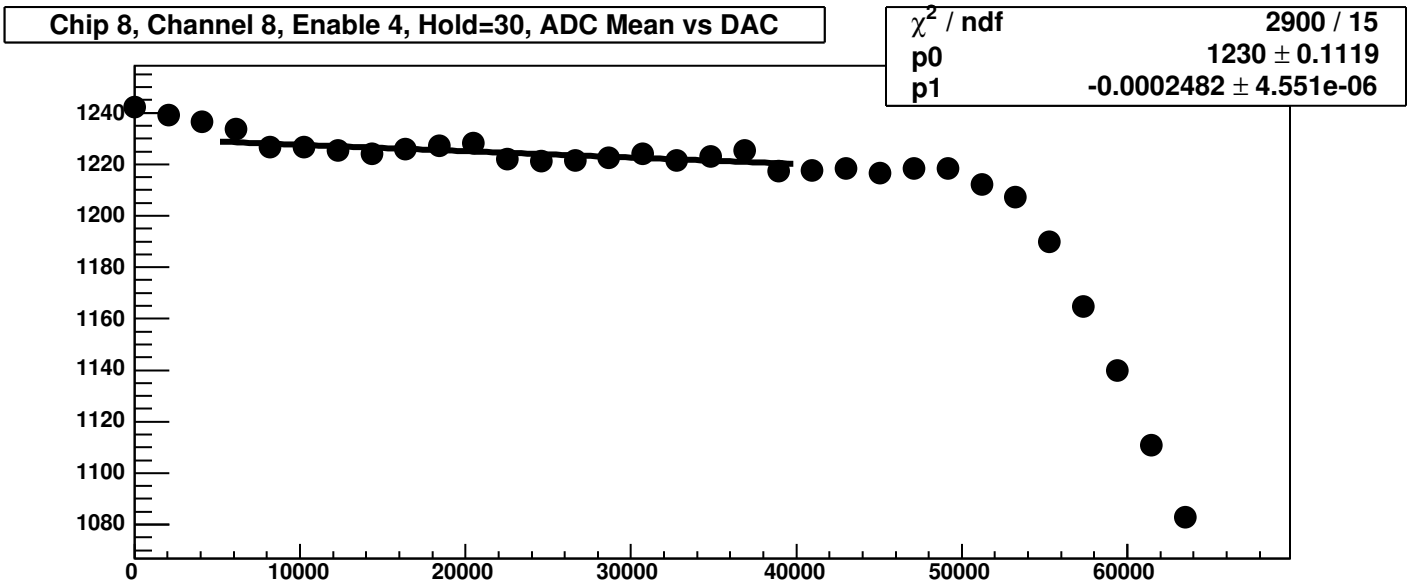

Chip 8, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

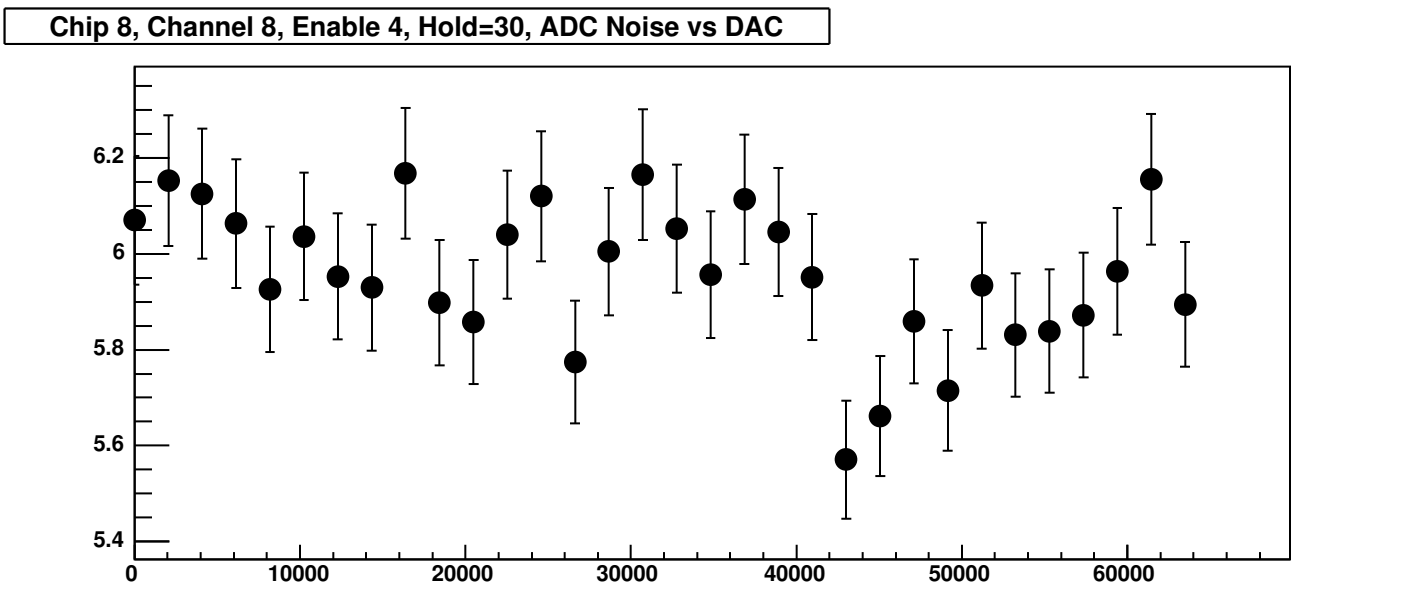

Chip 8, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

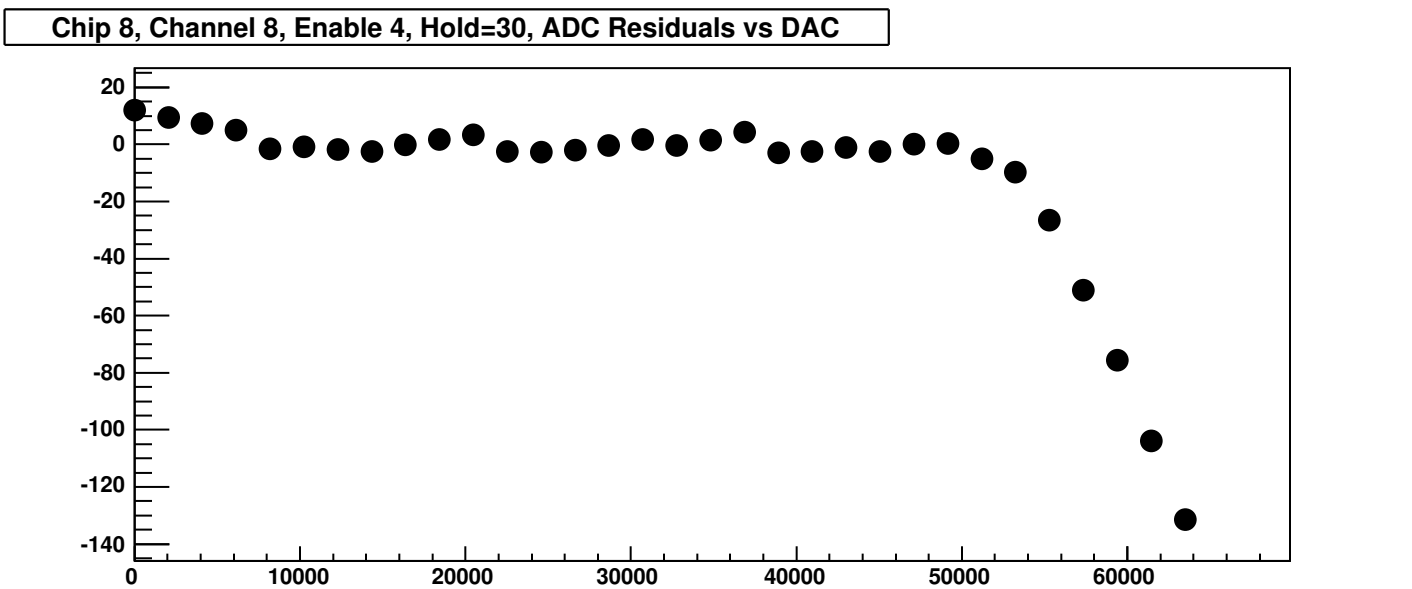

Chip 8, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2221 / 15
p0 1229 ± 0.1103
p1 -0.0004646 ± 4.474e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

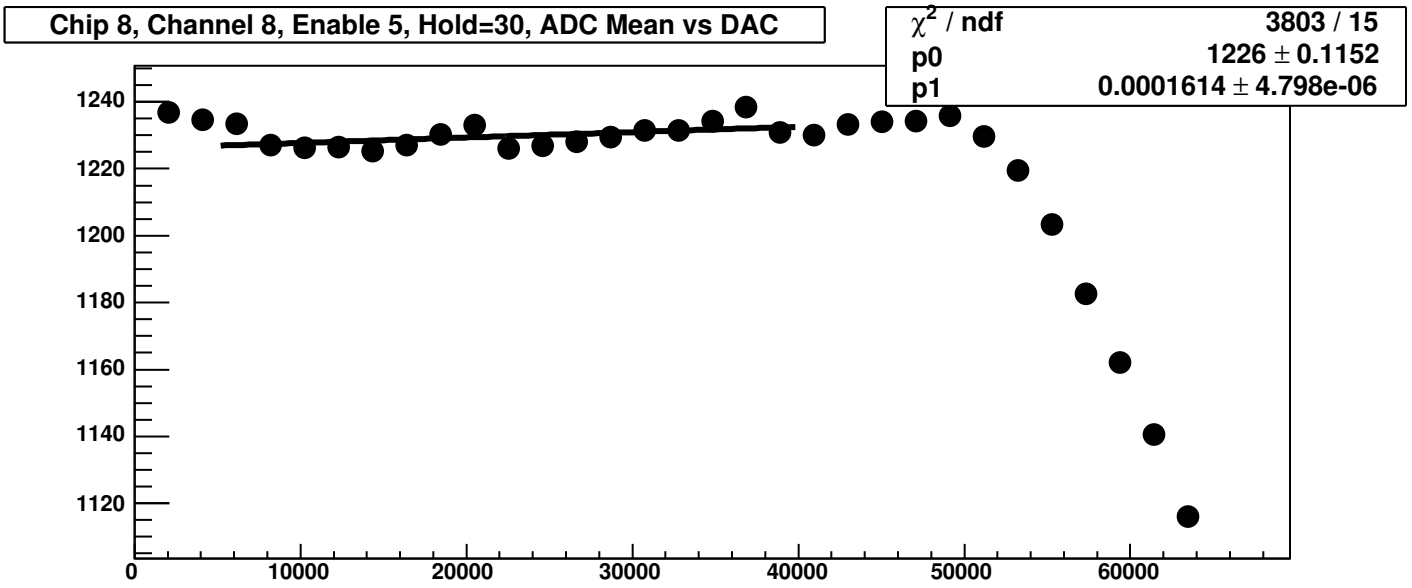

Chip 8, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

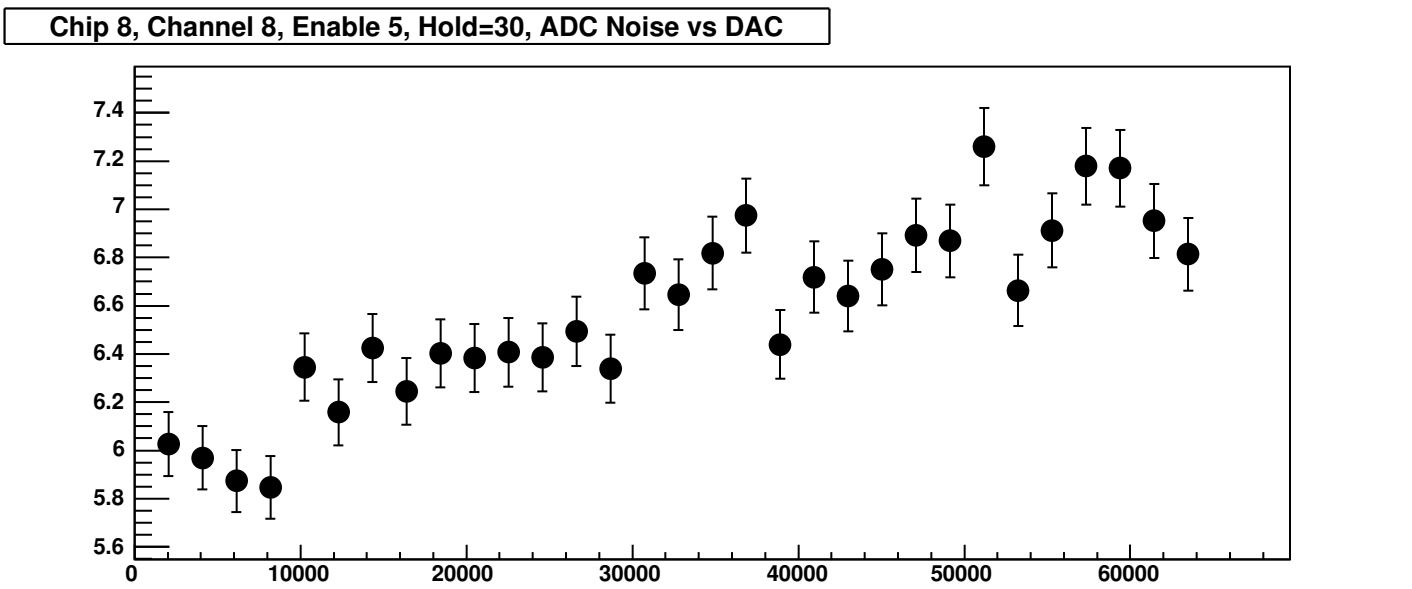

Chip 8, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

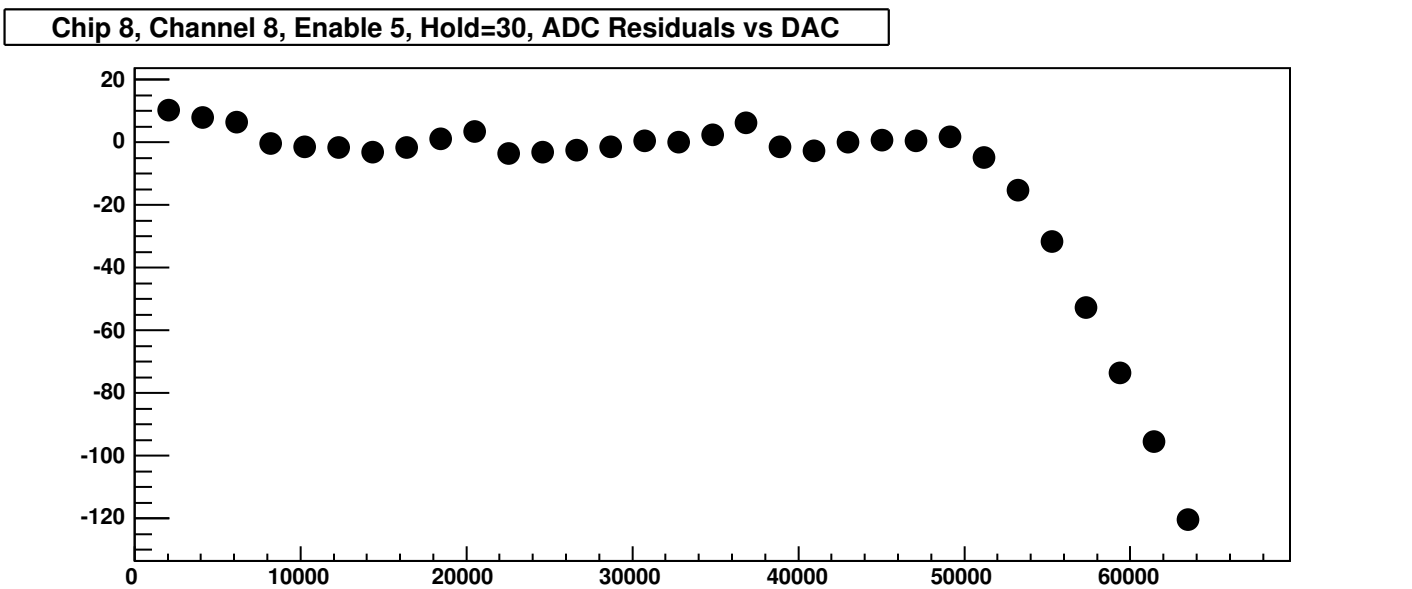

Chip 8, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 2900 / 15
p0 1230 ± 0.1119
p1 -0.0002482 ± 4.551e-06
1240
1220
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

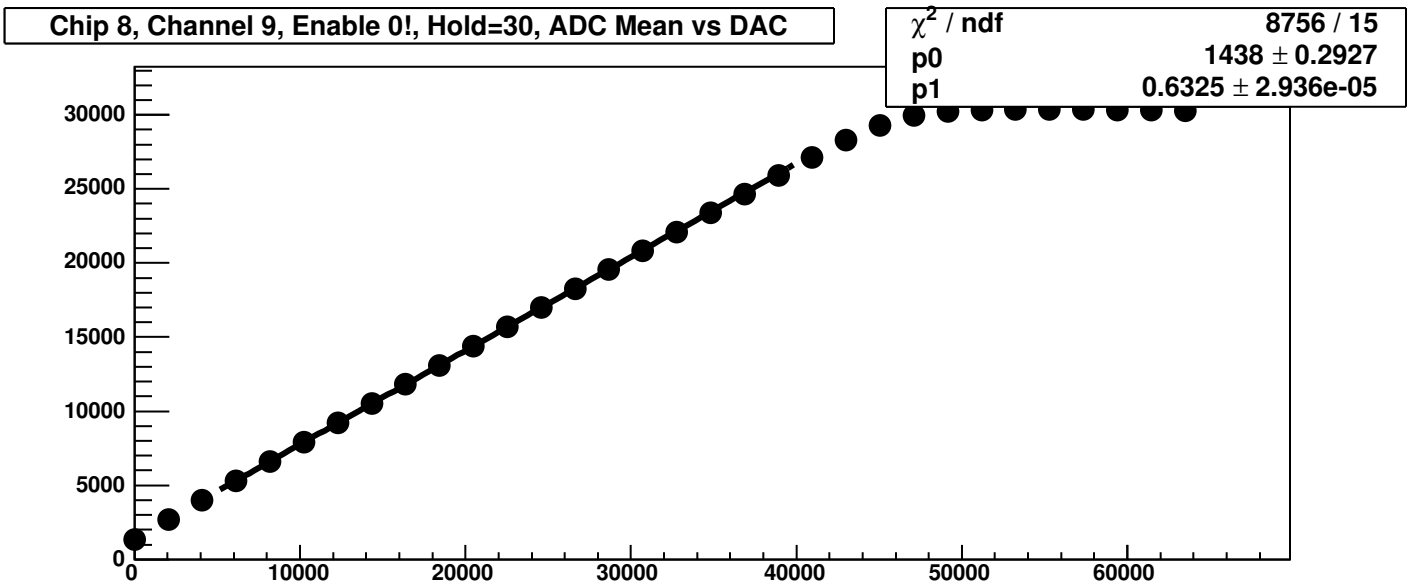

Chip 8, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

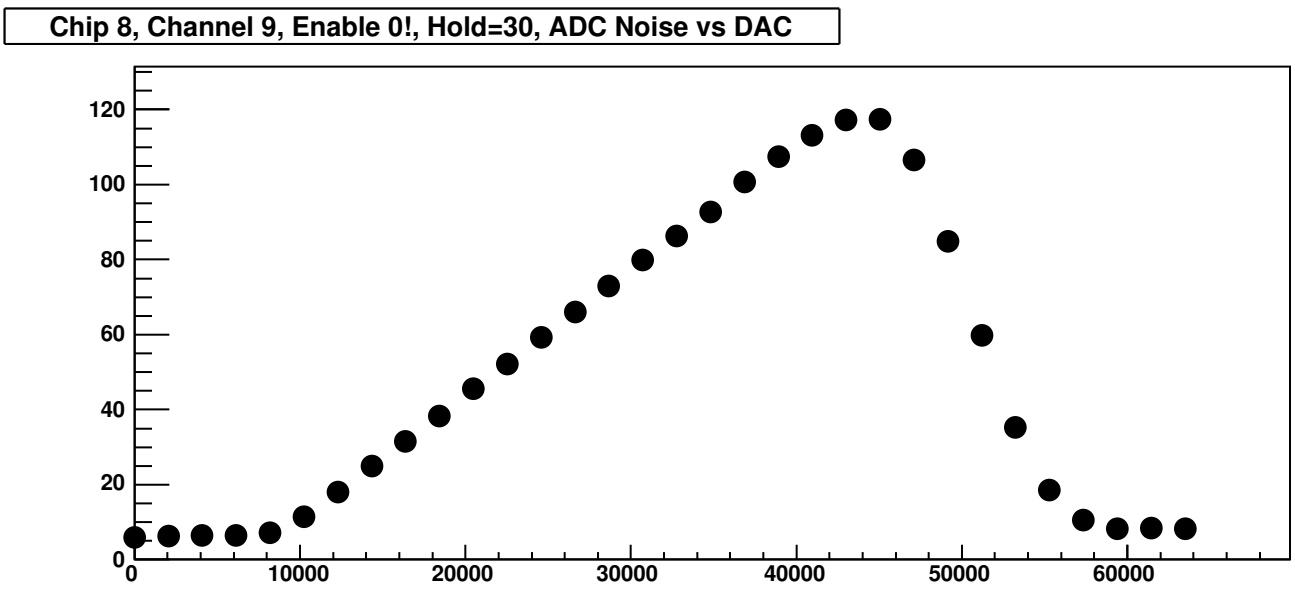

Chip 8, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

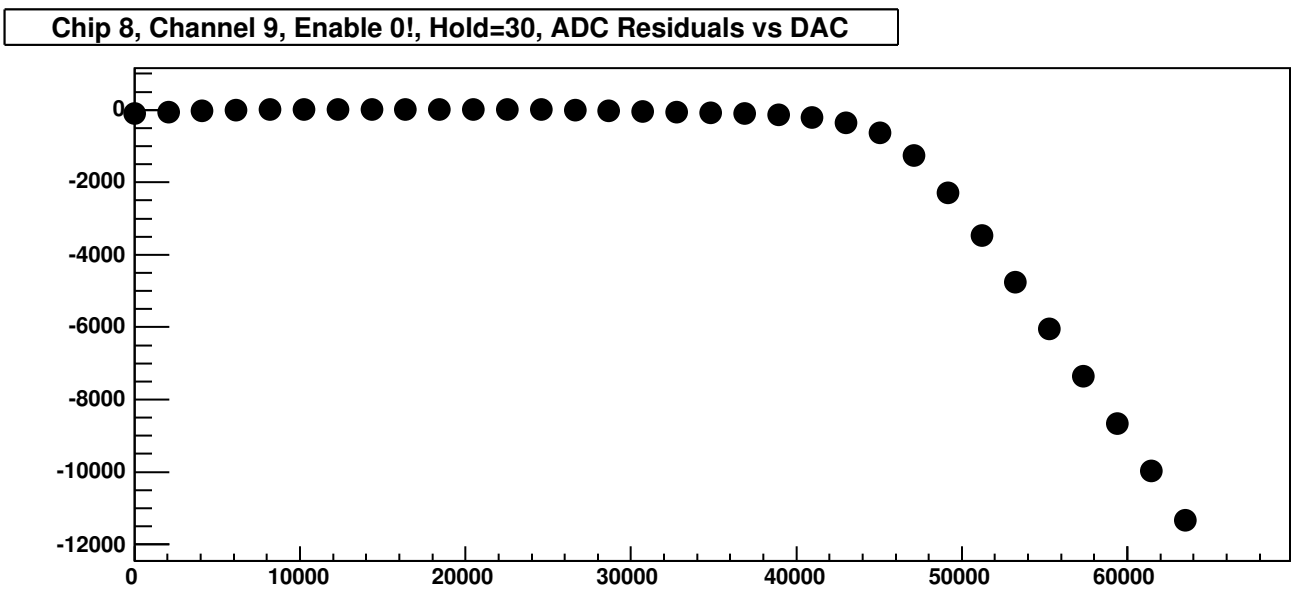

Chip 8, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3803 / 15
p0 1226 ± 0.1152
p1 0.0001614 ± 4.798e-06
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

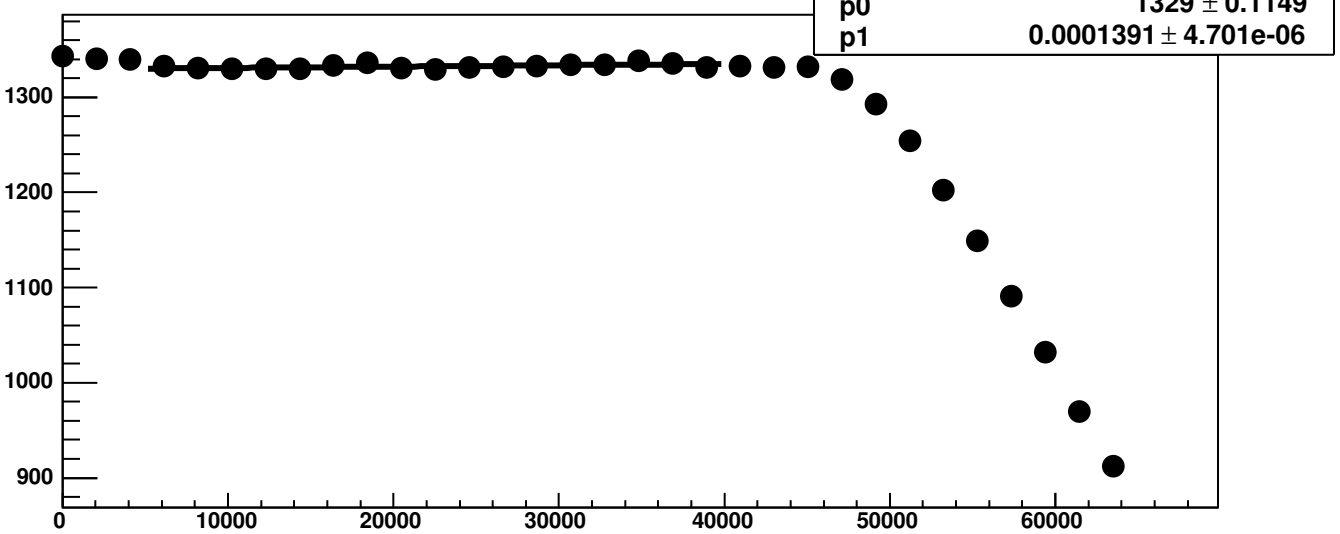

Chip 8, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 8756 / 15
p0 1438 ± 0.2927
p1 0.6325 ± 2.936e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

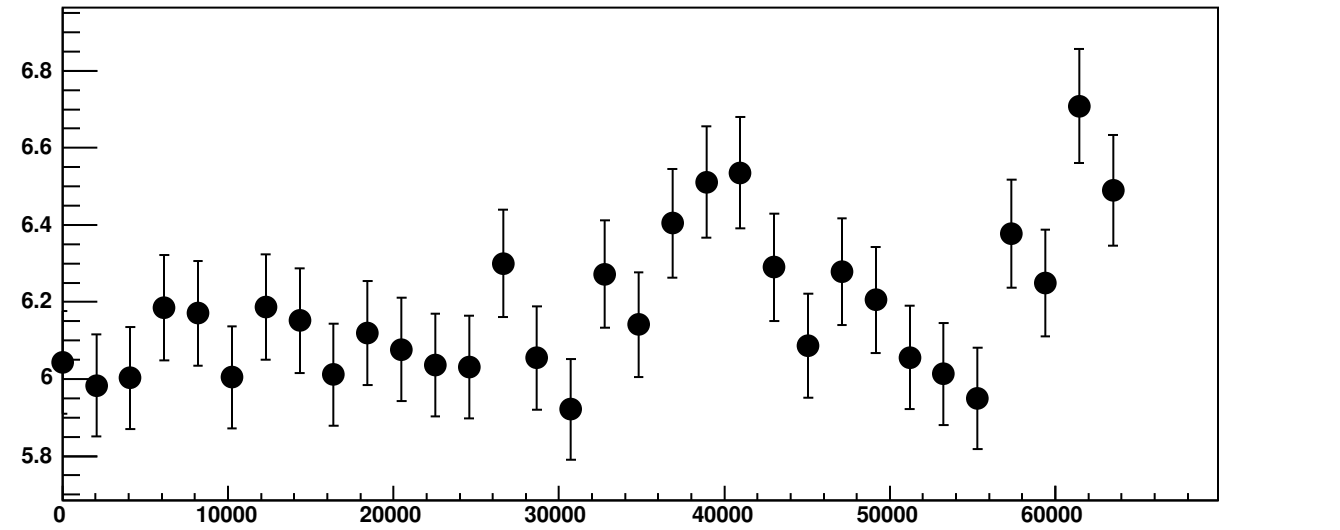

Chip 8, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

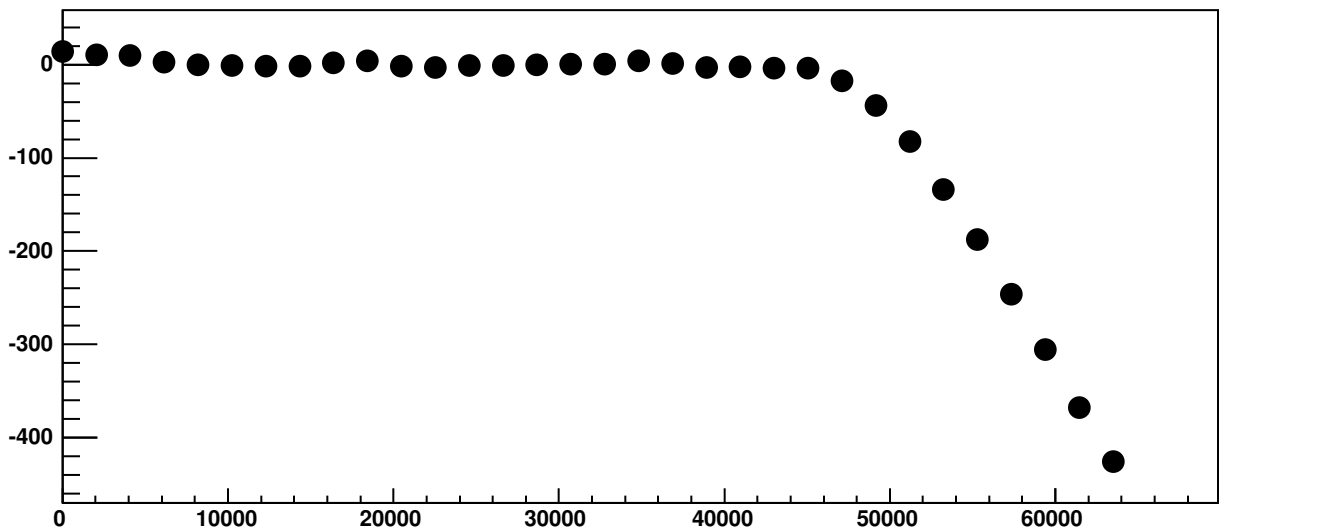
Chip 8, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

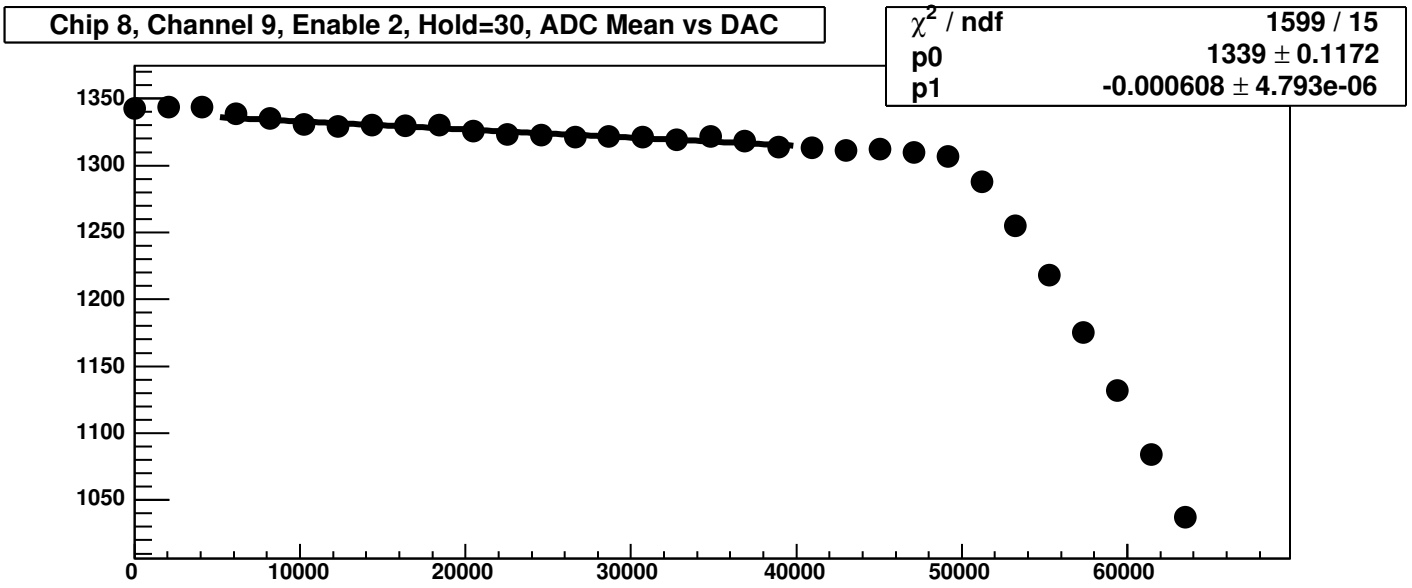


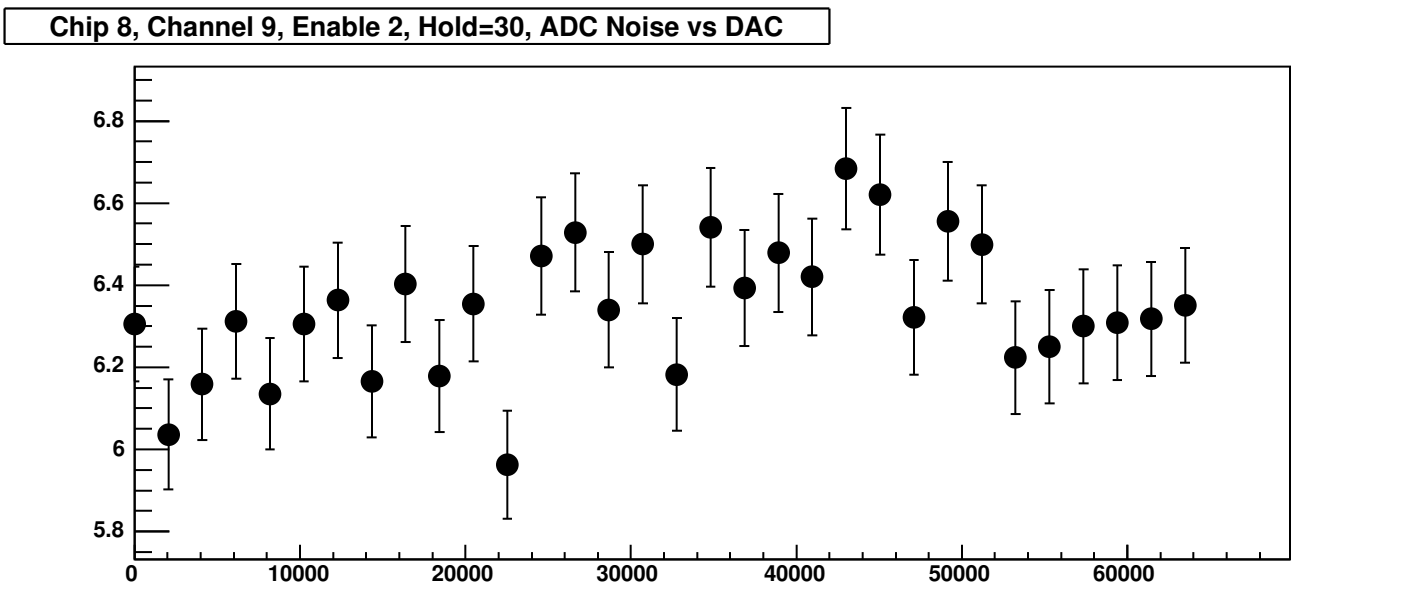
Chip 8, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC

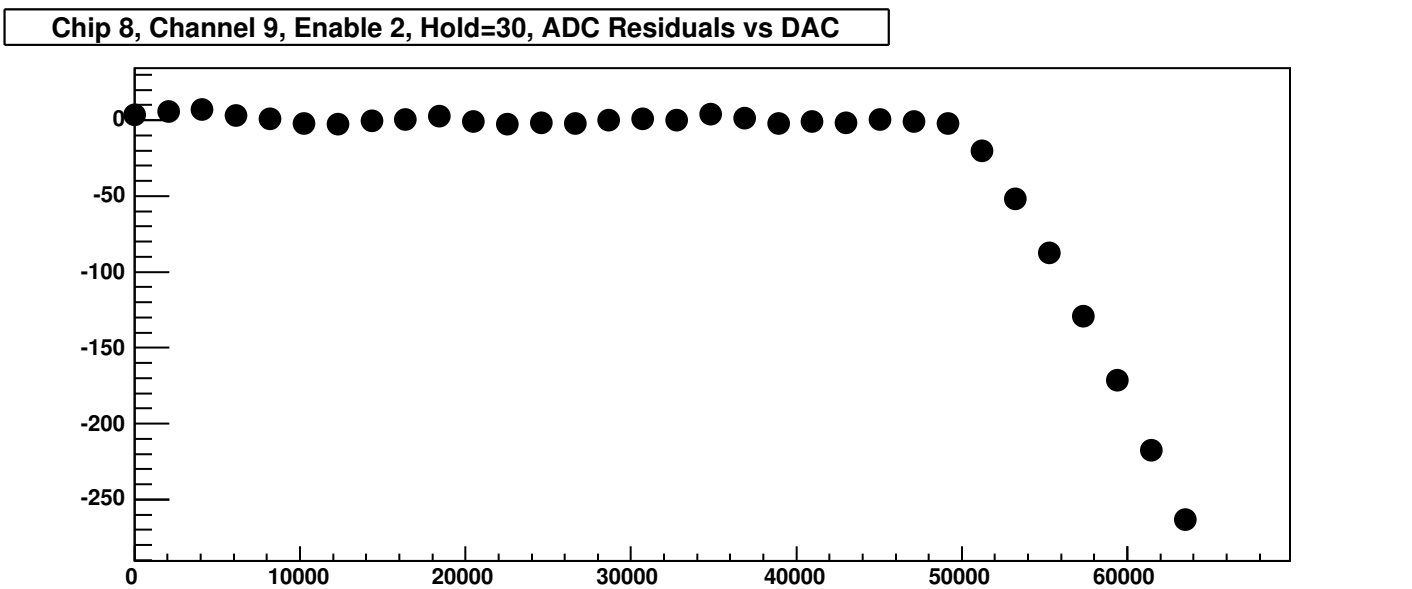


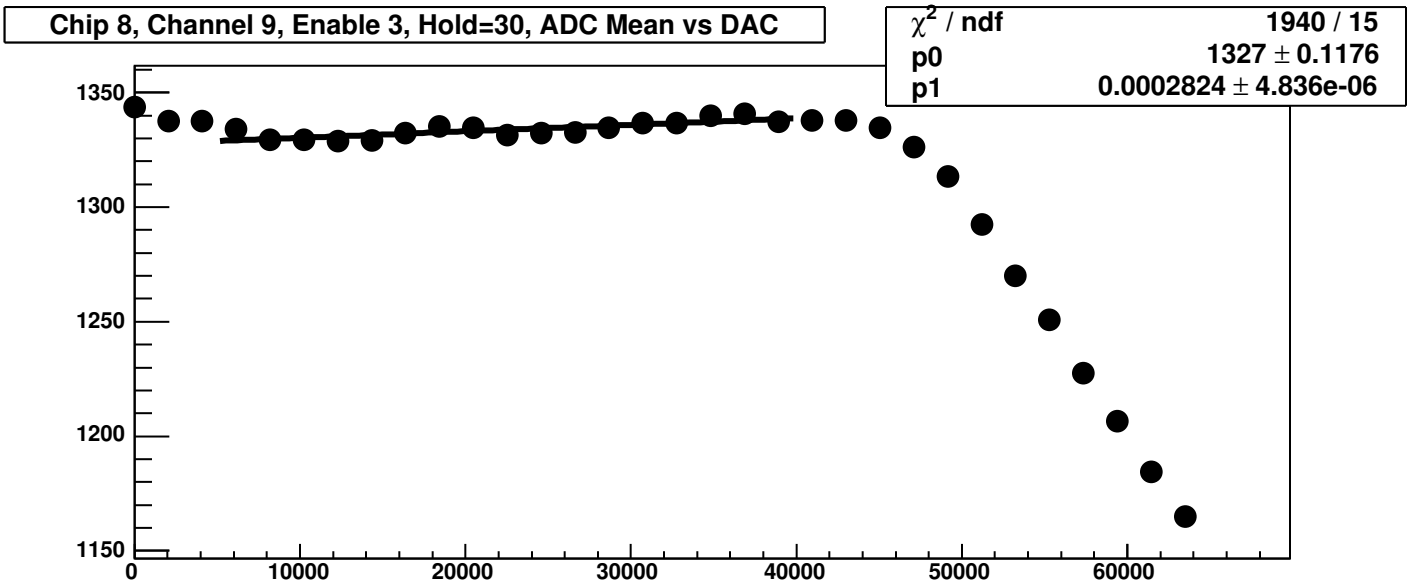
Chip 8, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

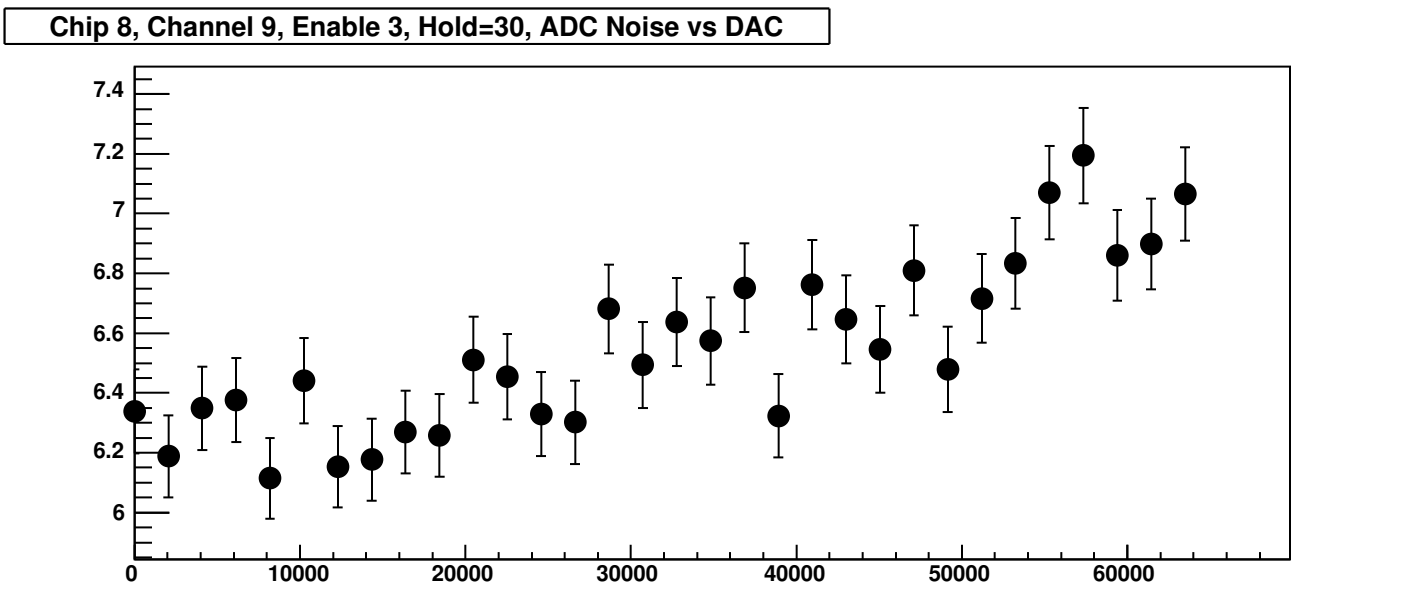


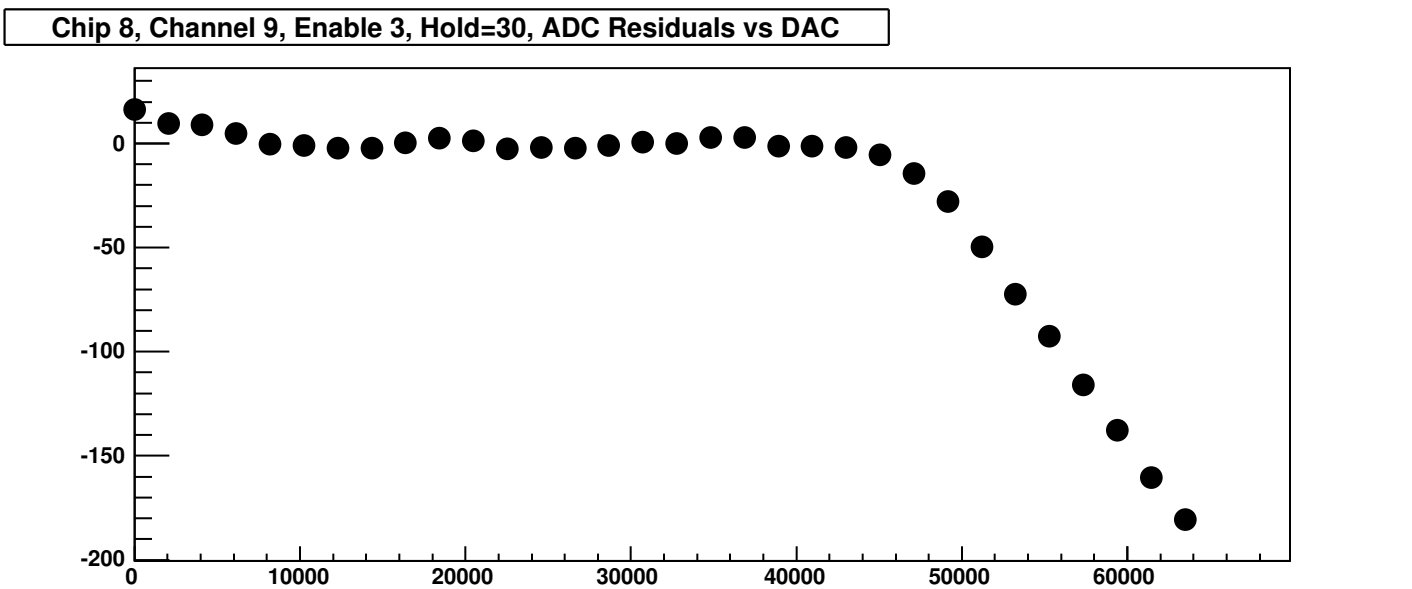

Chip 8, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1599 / 15
p0 1339 ± 0.1172
p1 -0.000608 ± 4.793e-06
1350
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

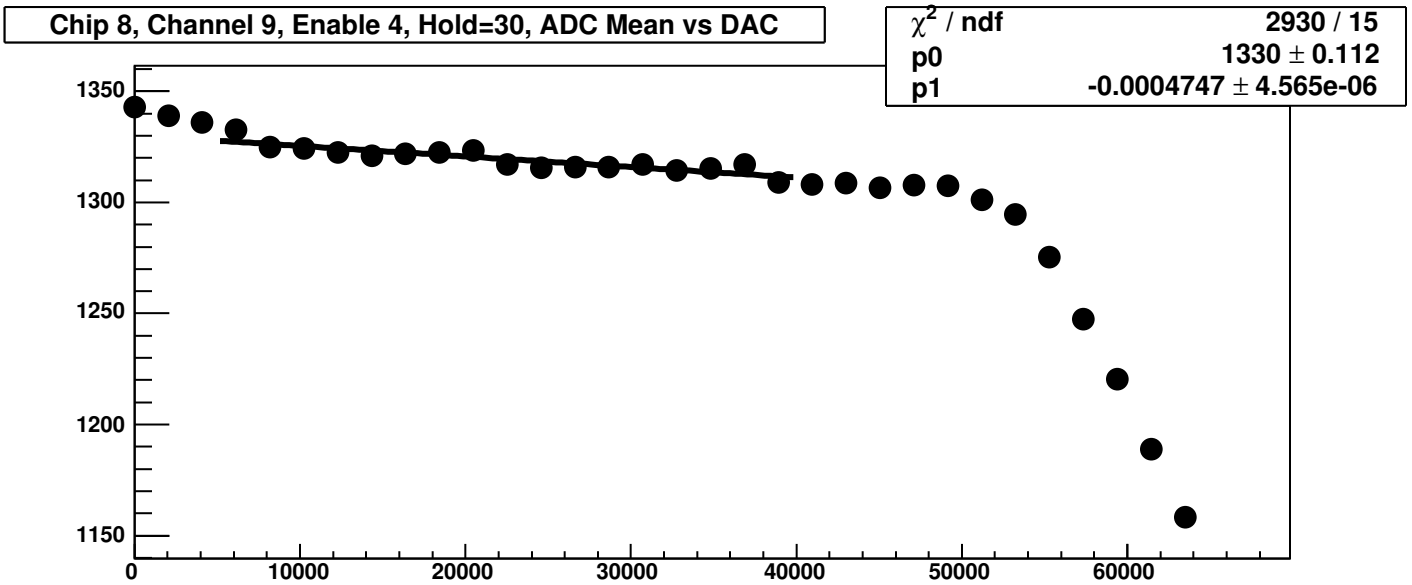

Chip 8, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

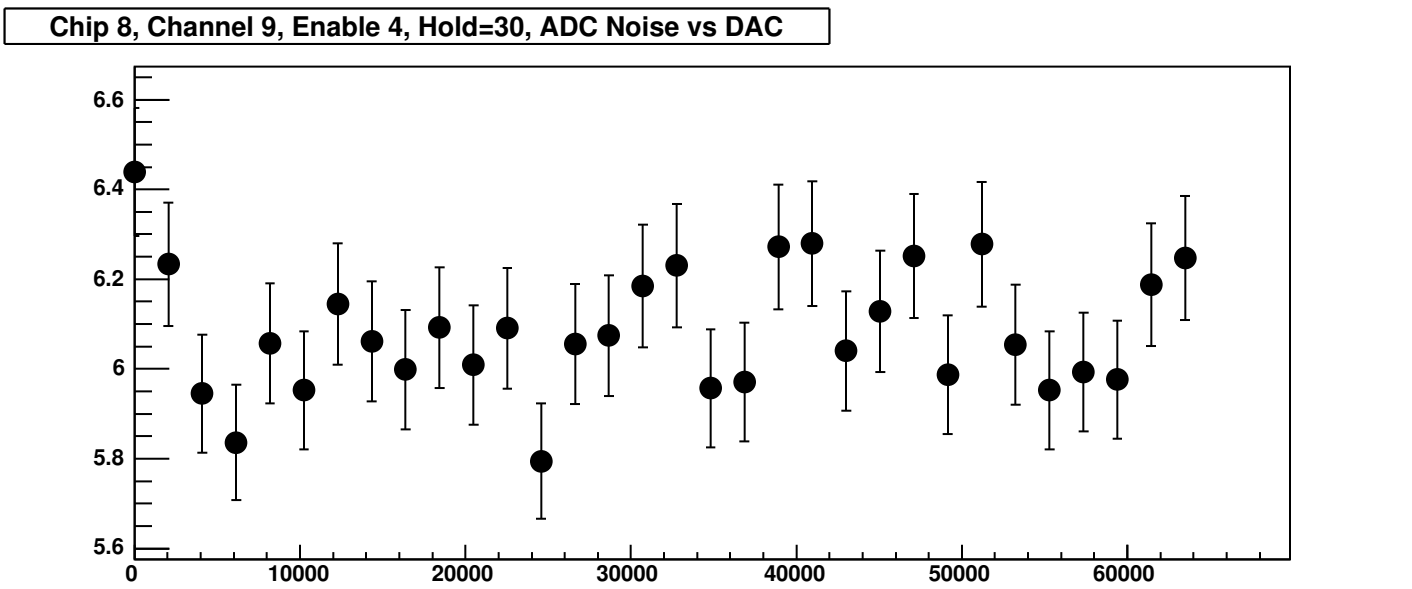

Chip 8, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

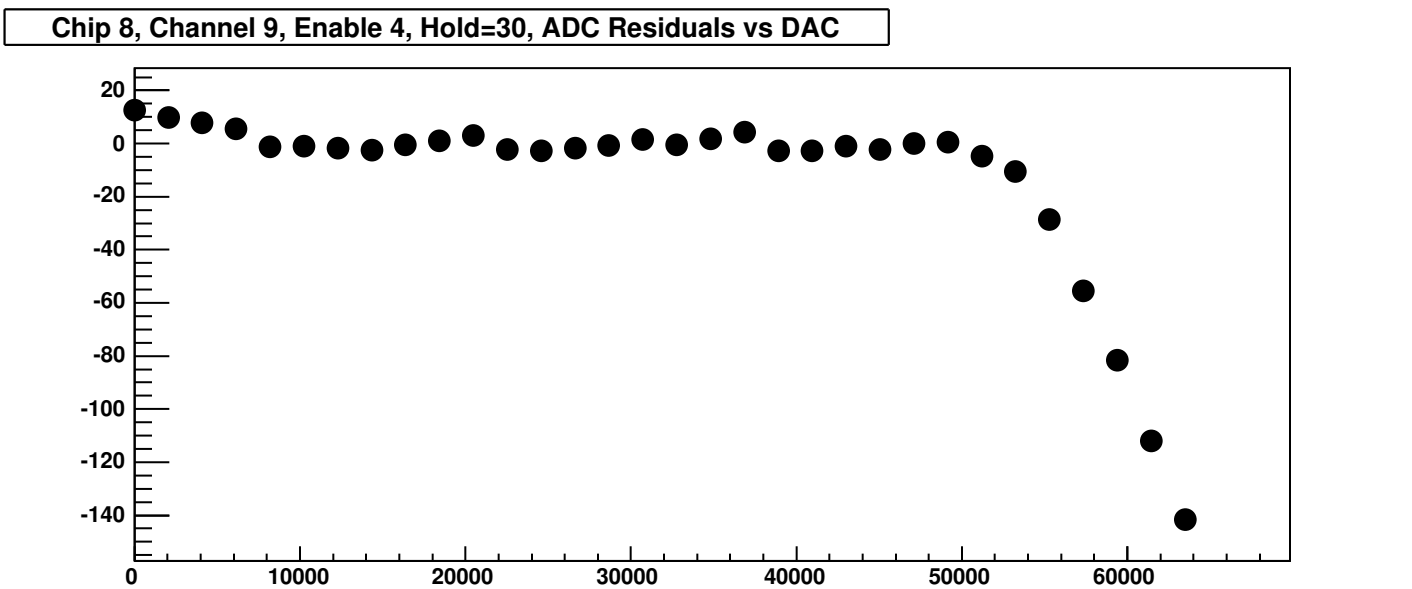

Chip 8, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 1940 / 15
p0 1327 ± 0.1176
p1 0.0002824 ± 4.836e-06
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

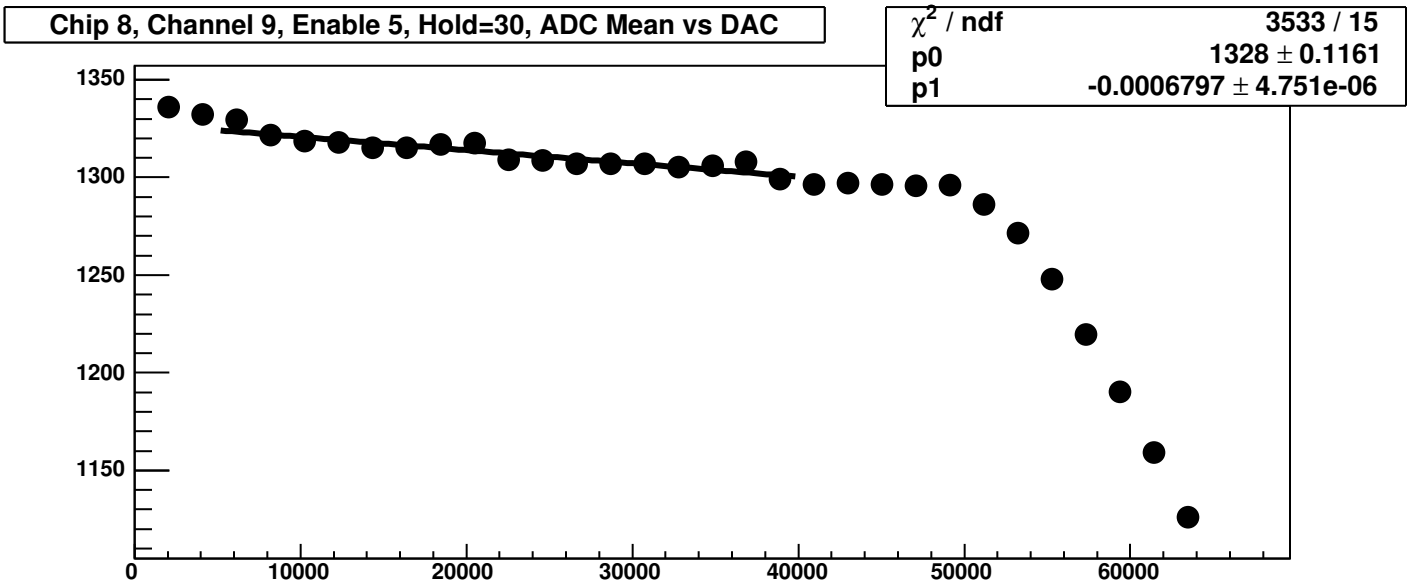

Chip 8, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

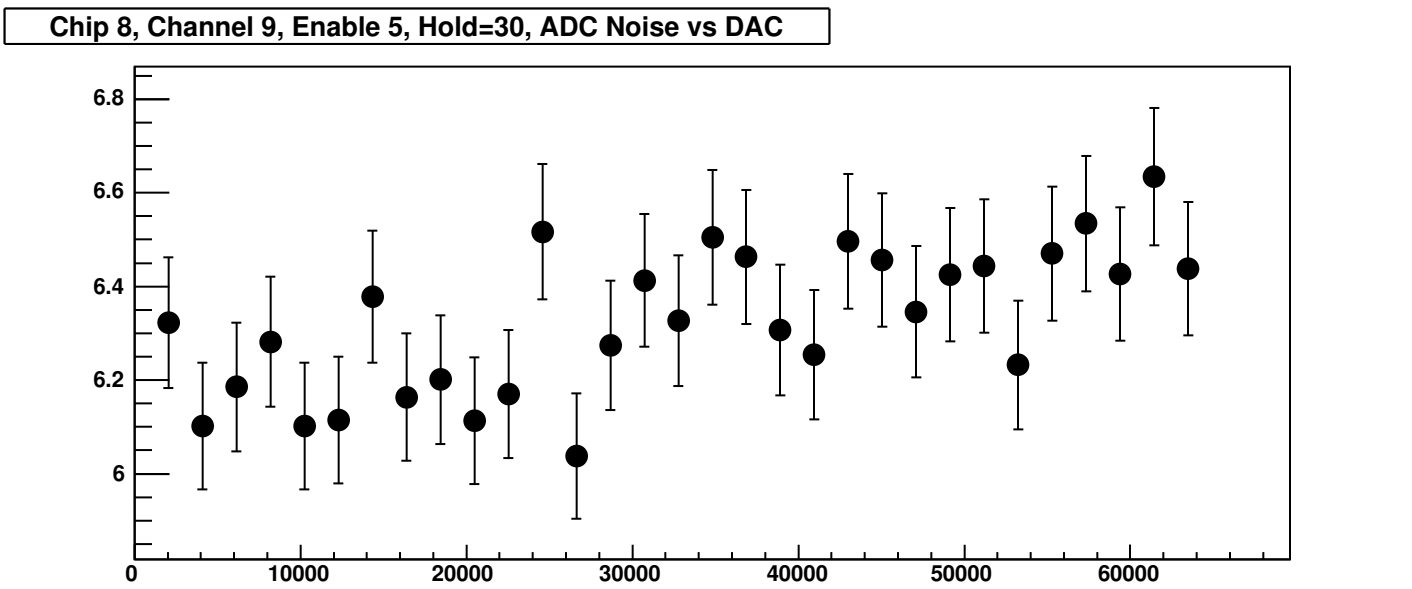

Chip 8, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

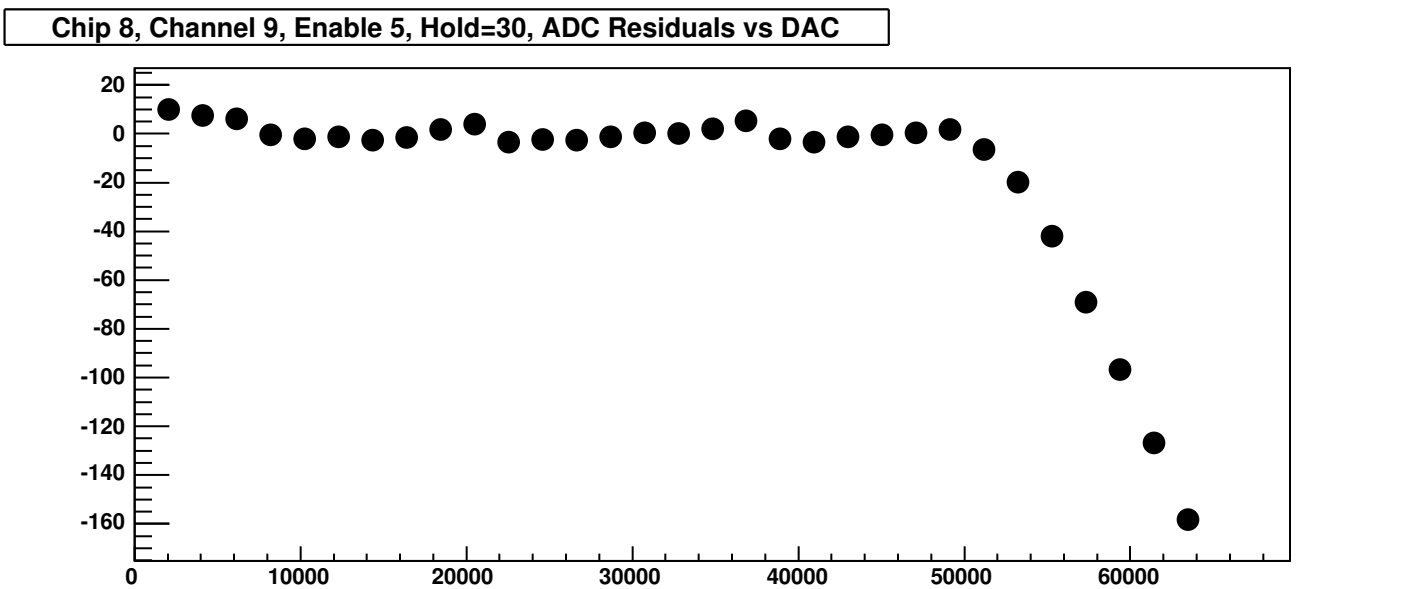

Chip 8, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 2930 / 15
p0 1330 ± 0.112
p1 -0.0004747 ± 4.565e-06
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

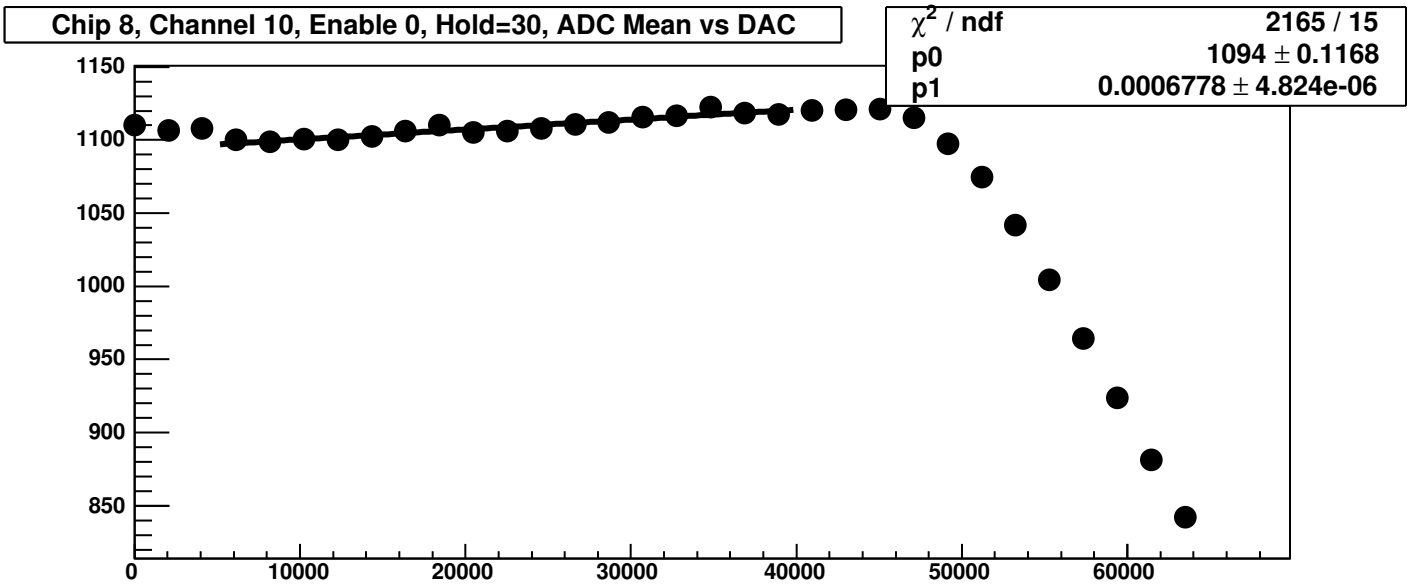

Chip 8, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

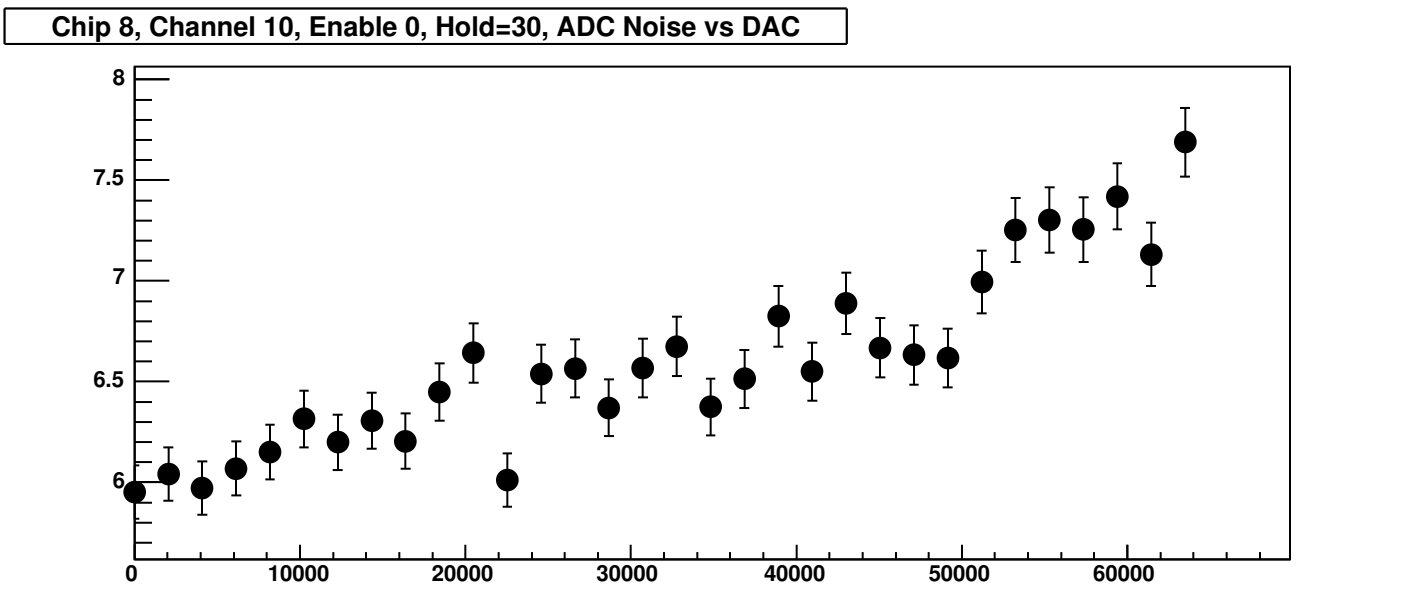

Chip 8, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

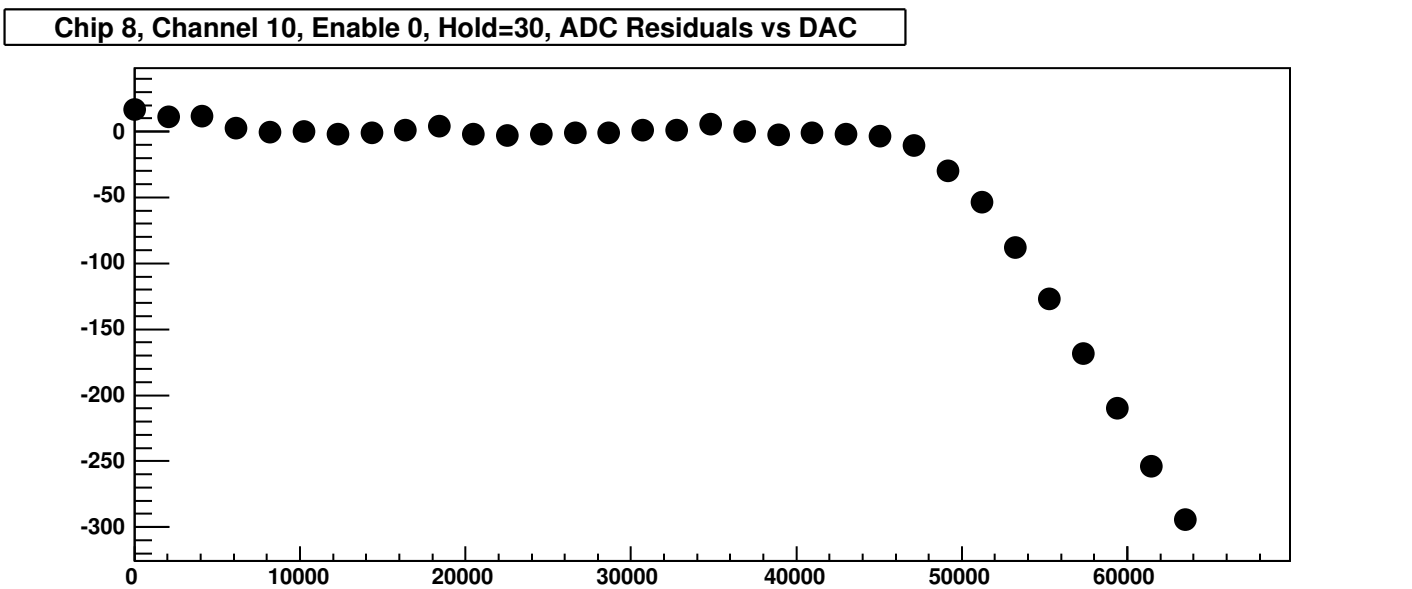

Chip 8, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3533 / 15
p0 1328 ± 0.1161
p1 -0.0006797 ± 4.751e-06
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

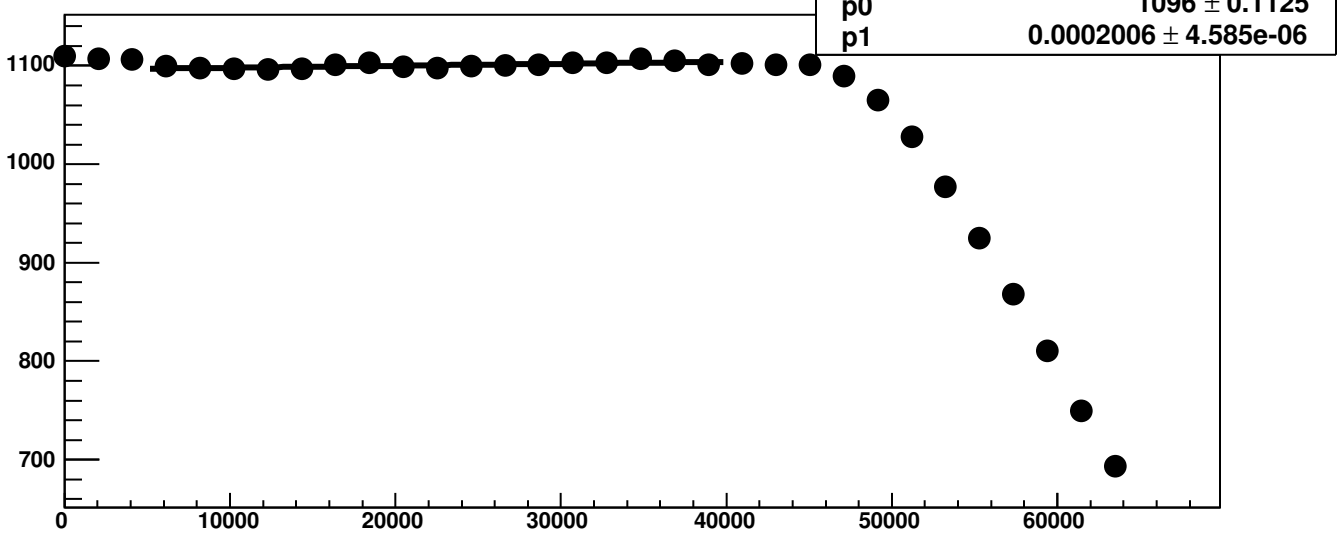

Chip 8, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2165 / 15
p0 1094 ± 0.1168
p1 0.0006778 ± 4.824e-06
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

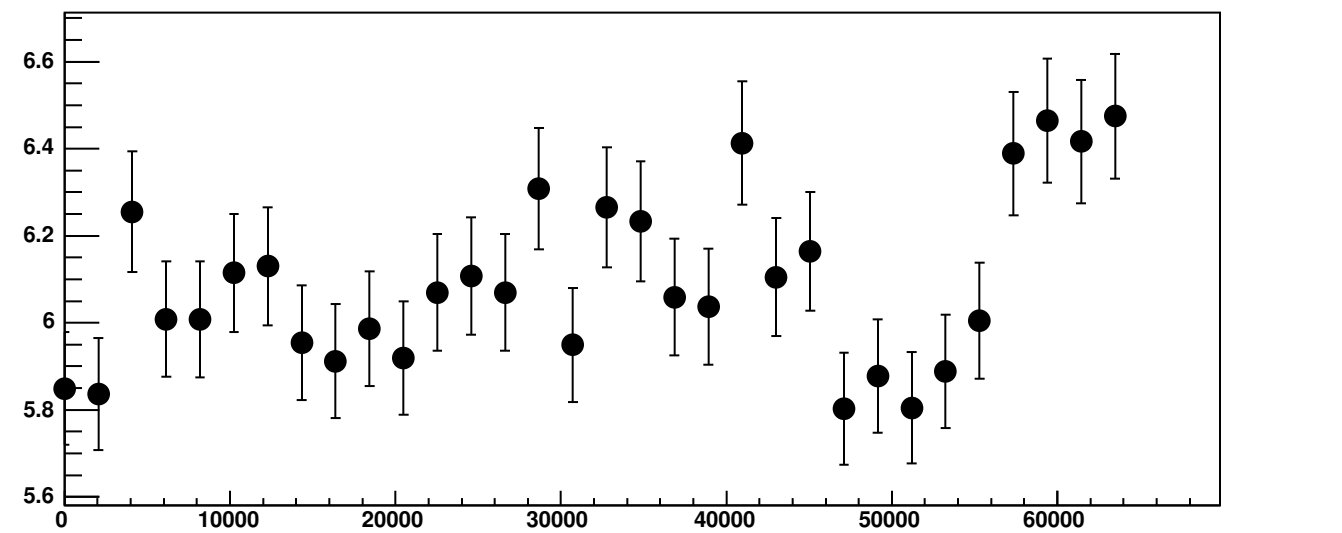

Chip 8, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
8
7.5
7
6.5
6
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

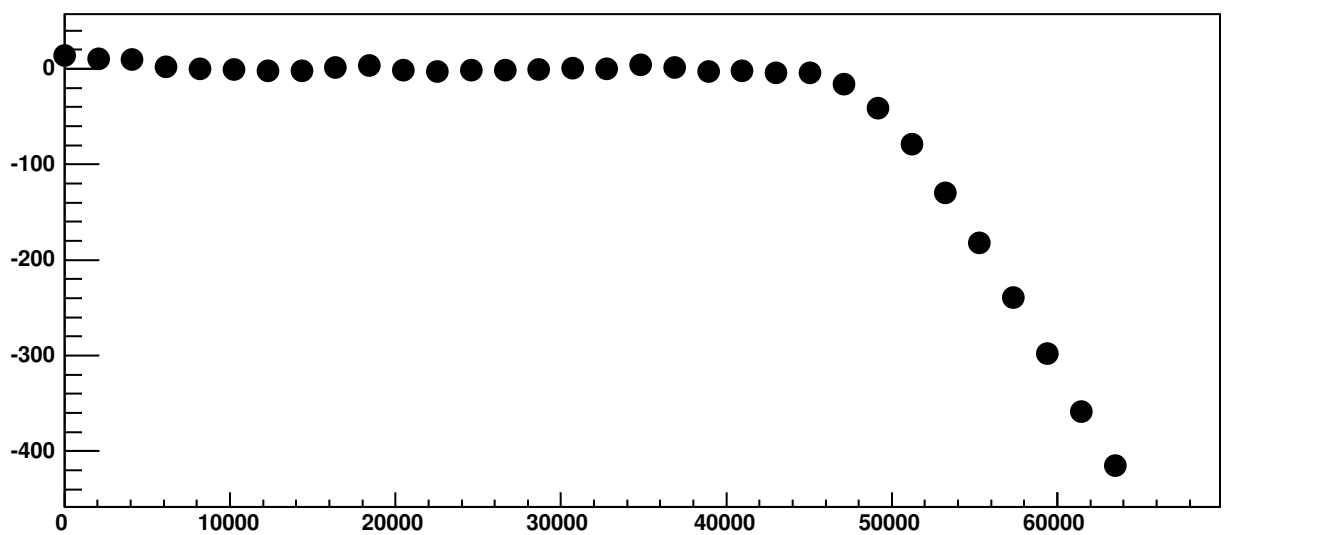
Chip 8, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

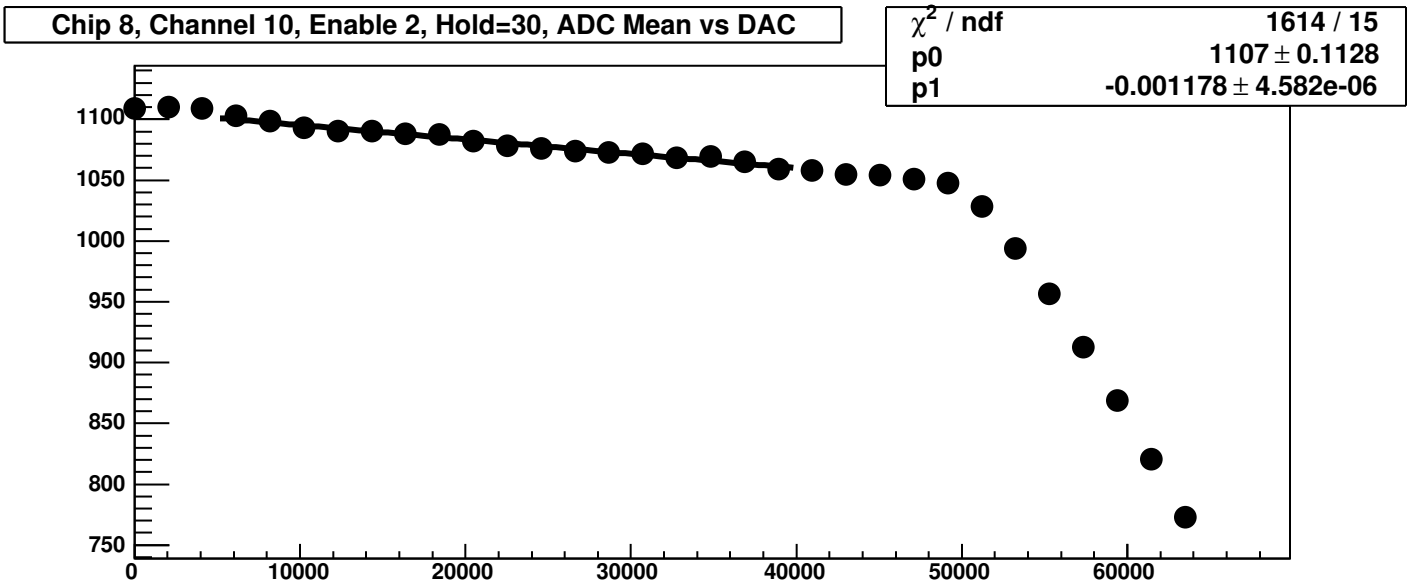


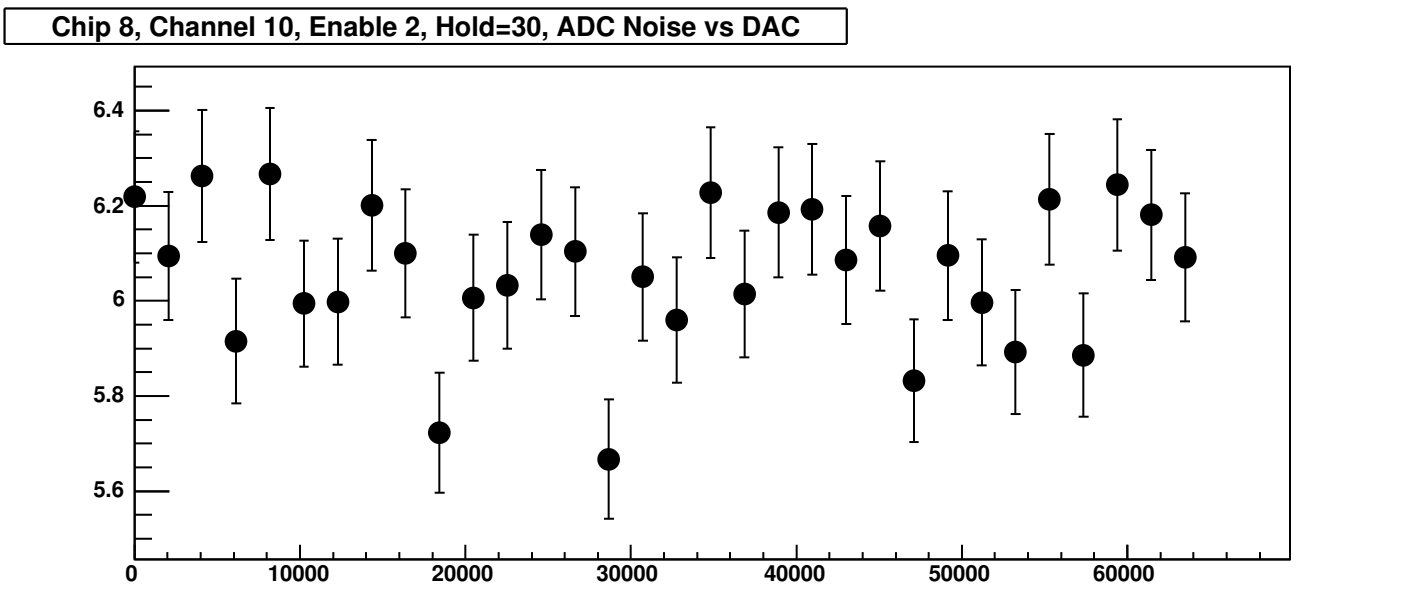
Chip 8, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC

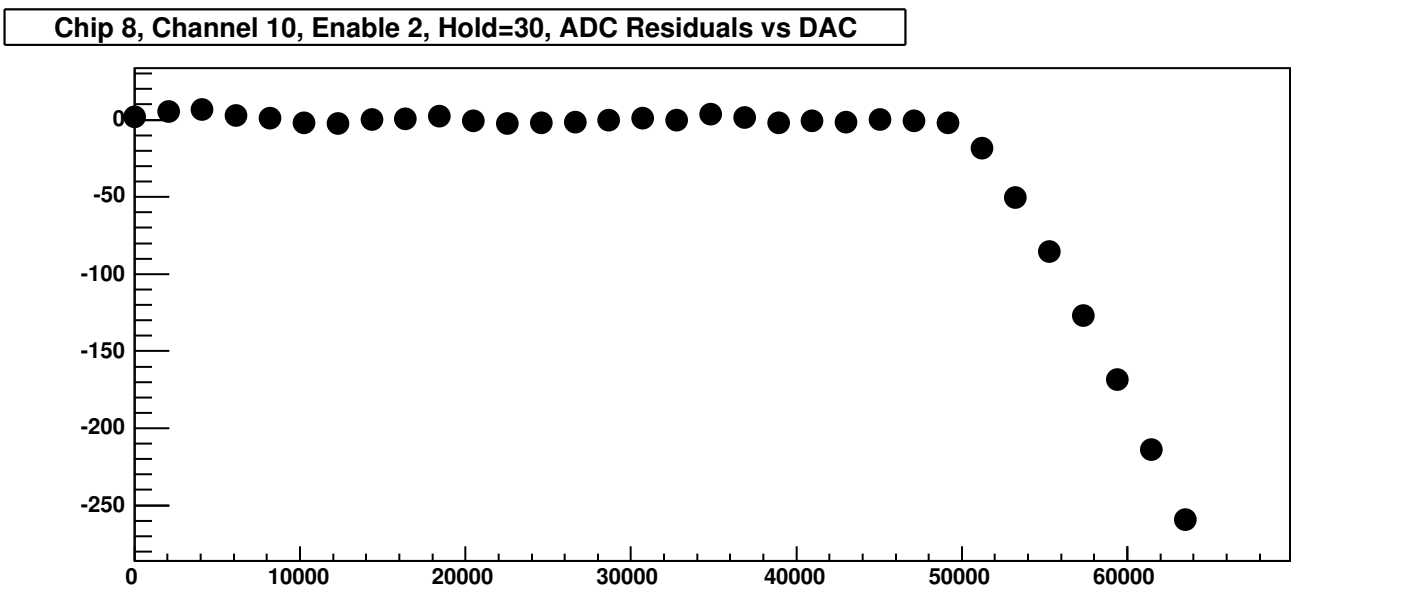


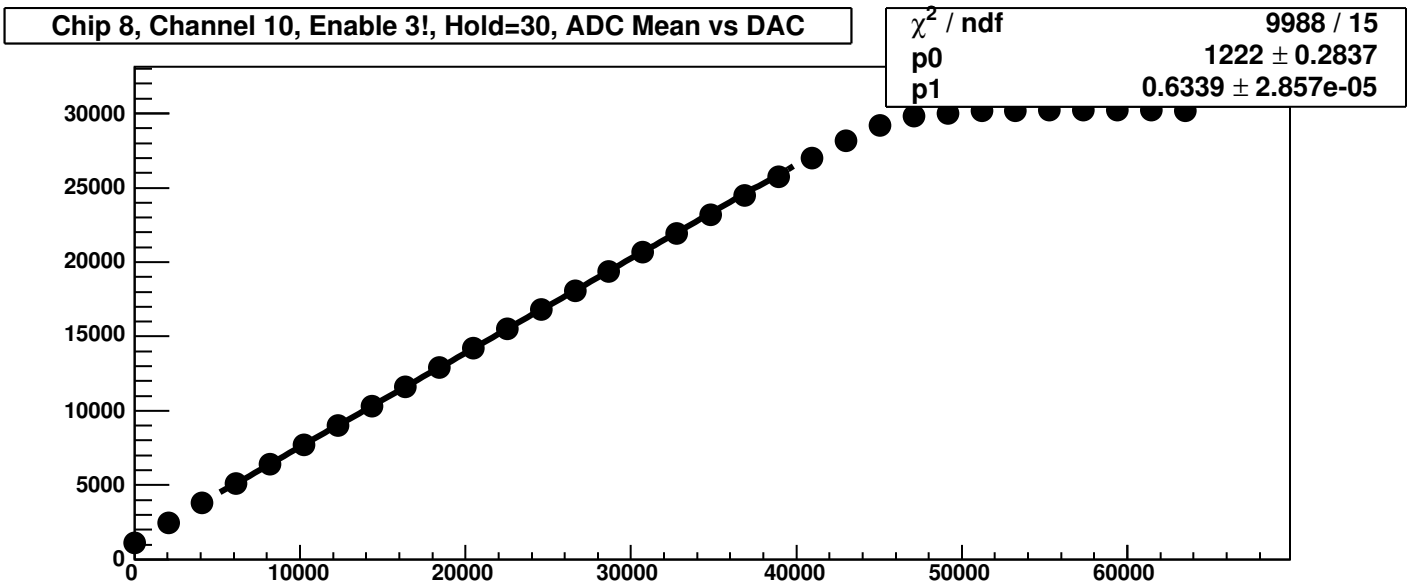
Chip 8, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC

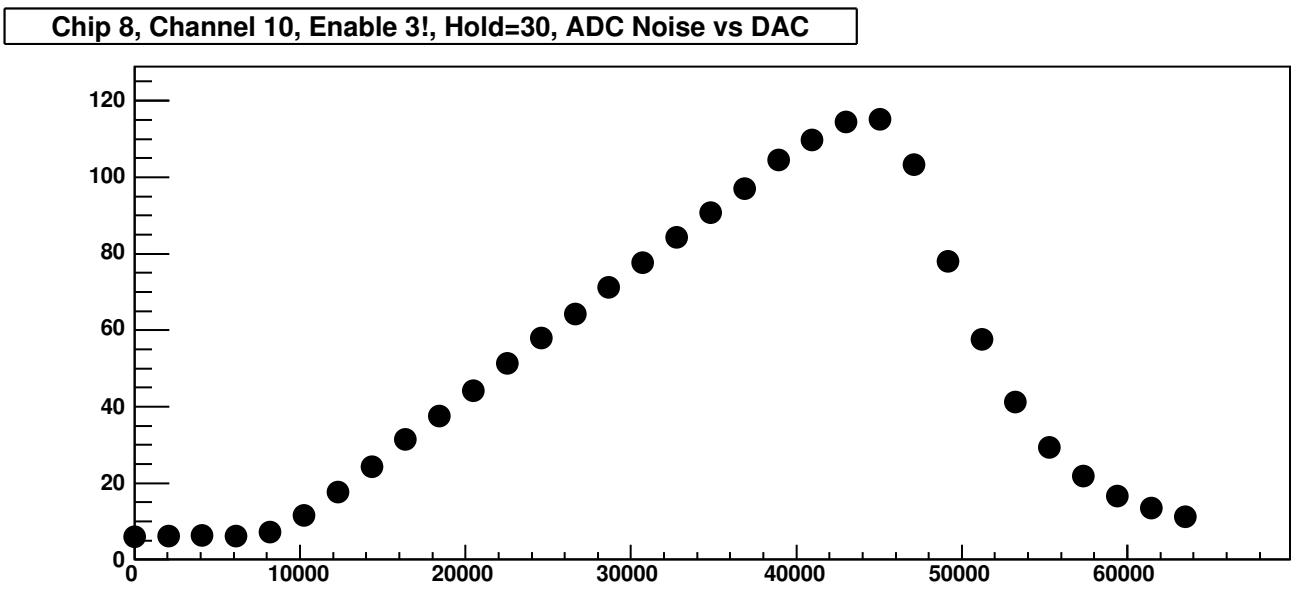


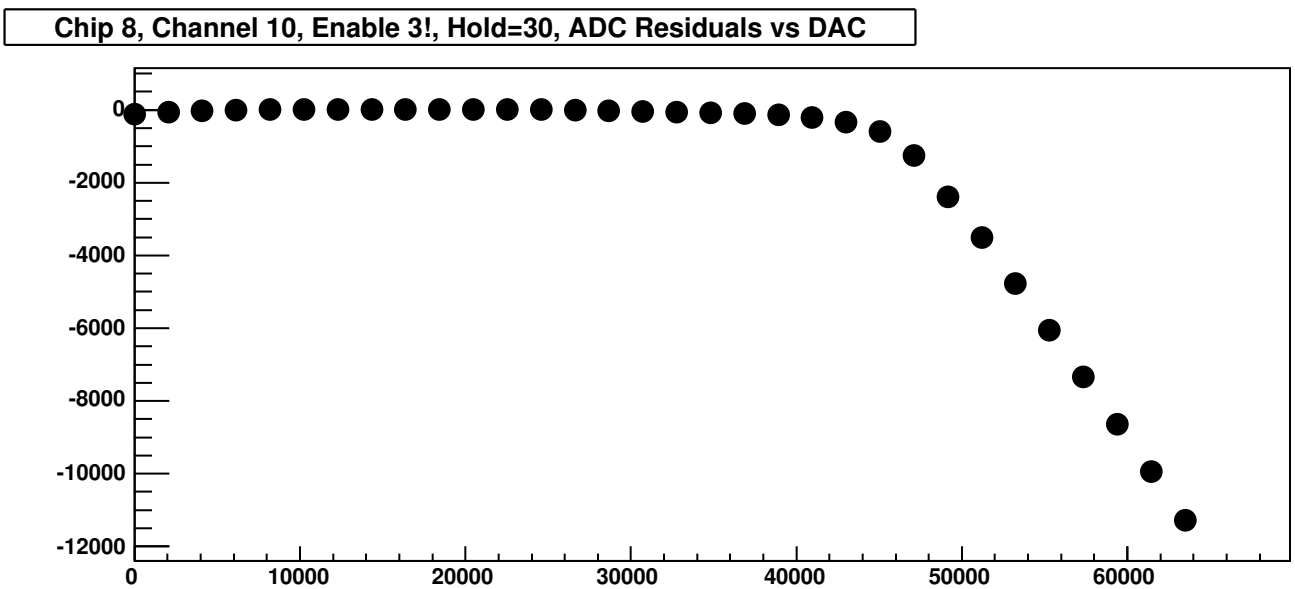

Chip 8, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1614 / 15
p0 1107 ± 0.1128
p1 -0.001178 ± 4.582e-06
1100
1050
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

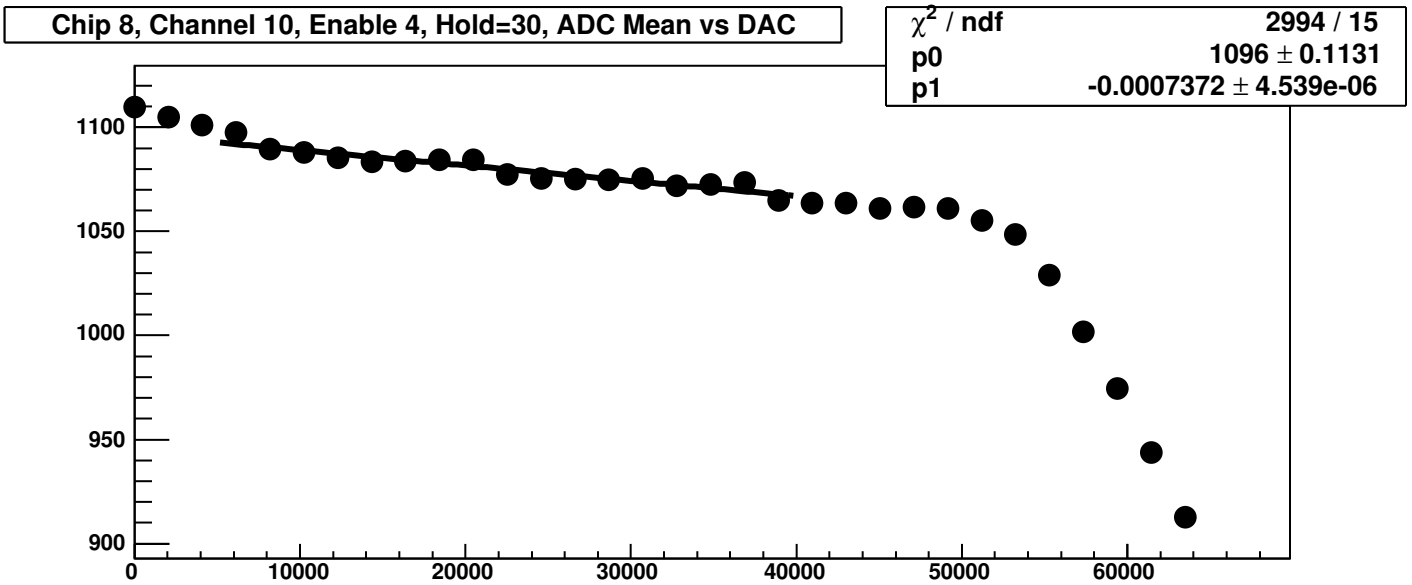

Chip 8, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

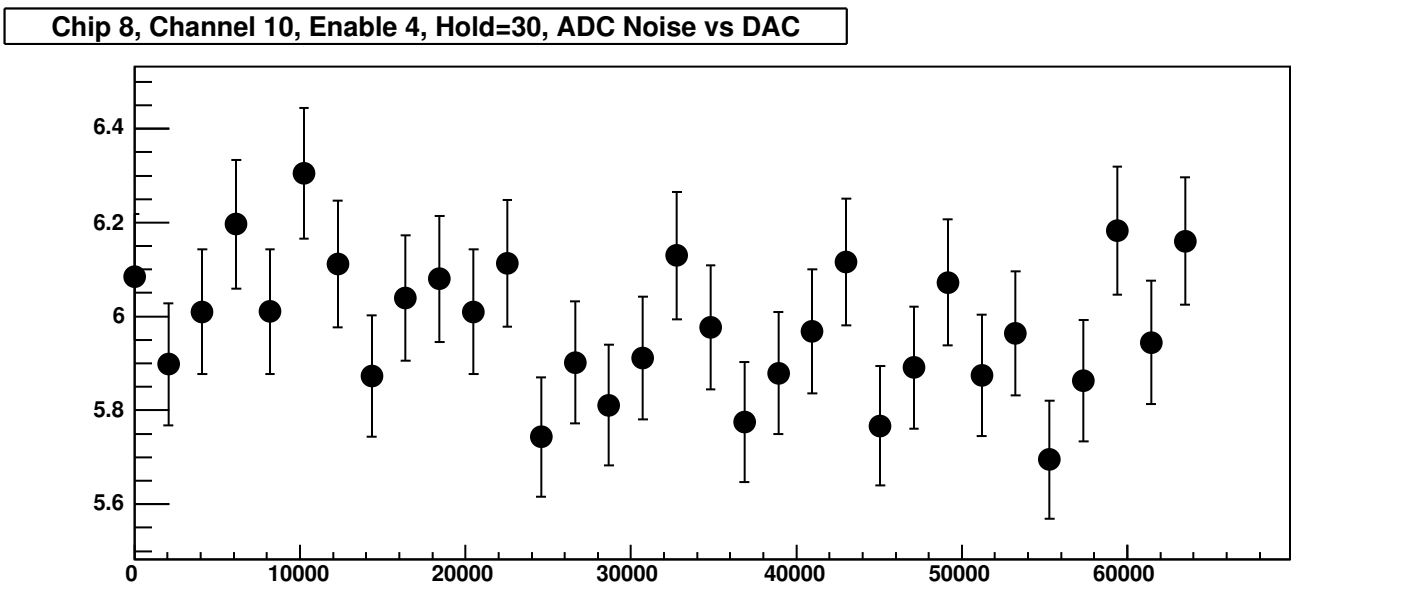

Chip 8, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

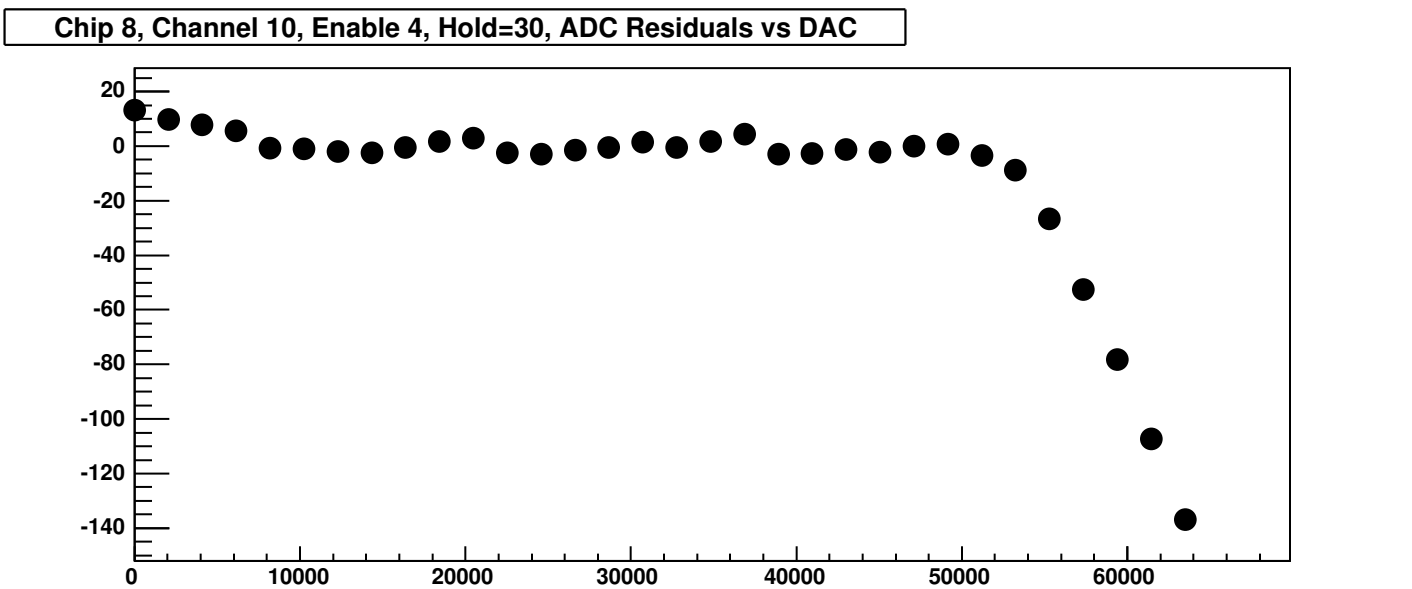

Chip 8, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 9988 / 15
p0 1222 ± 0.2837
p1 0.6339 ± 2.857e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

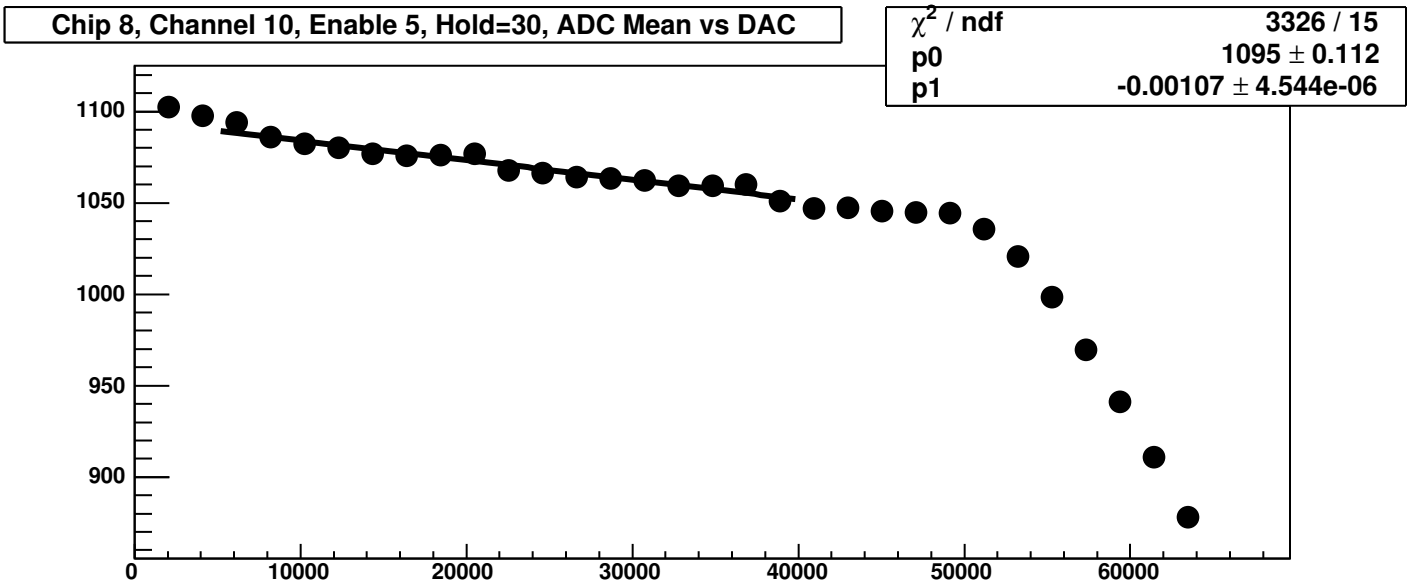

Chip 8, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

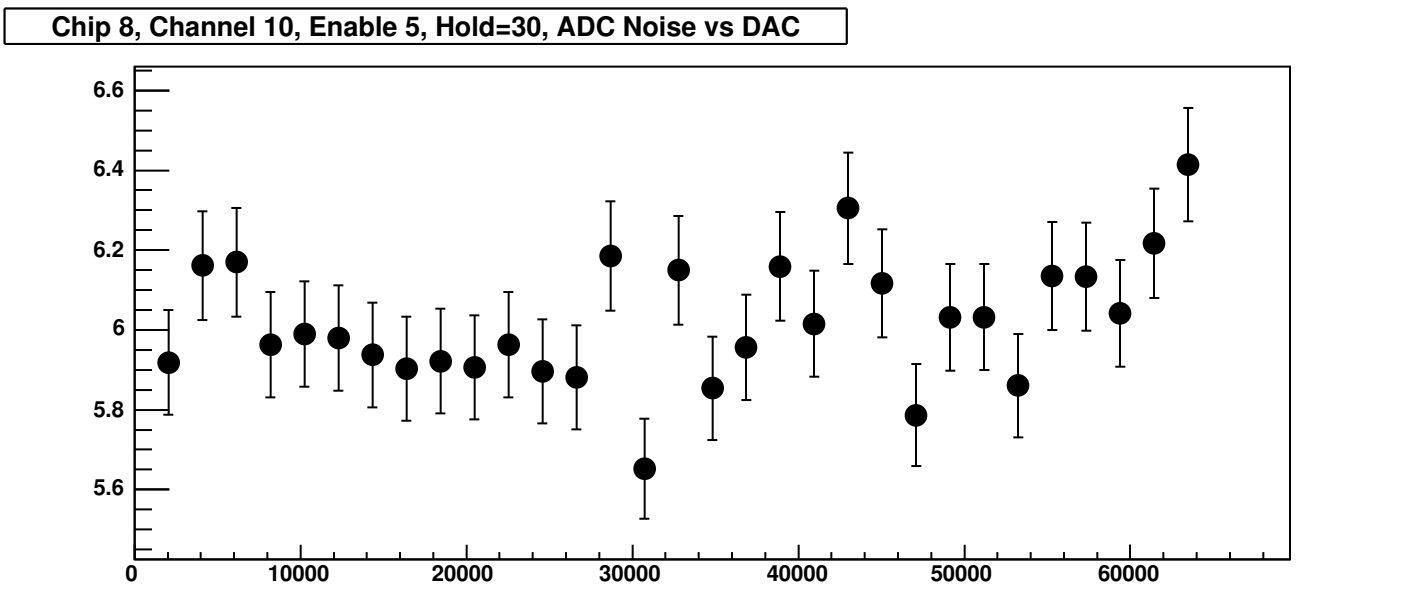

Chip 8, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

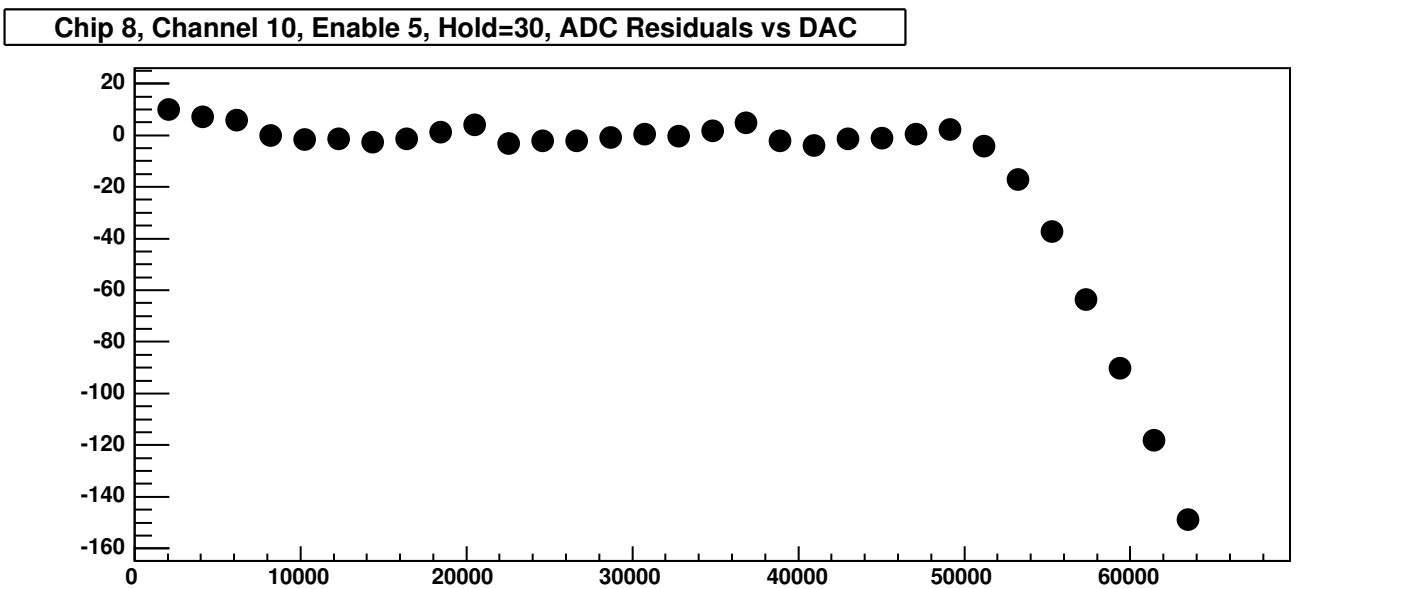

Chip 8, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 2994 / 15
p0 1096 ± 0.1131
p1 -0.0007372 ± 4.539e-06
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

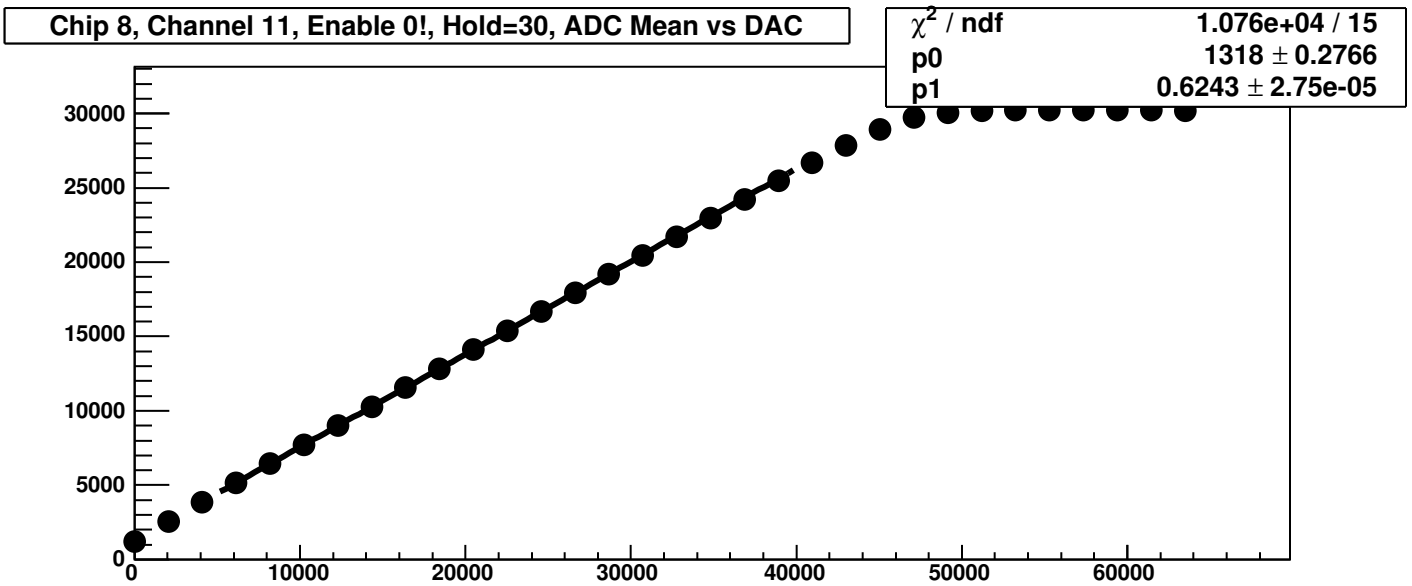

Chip 8, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

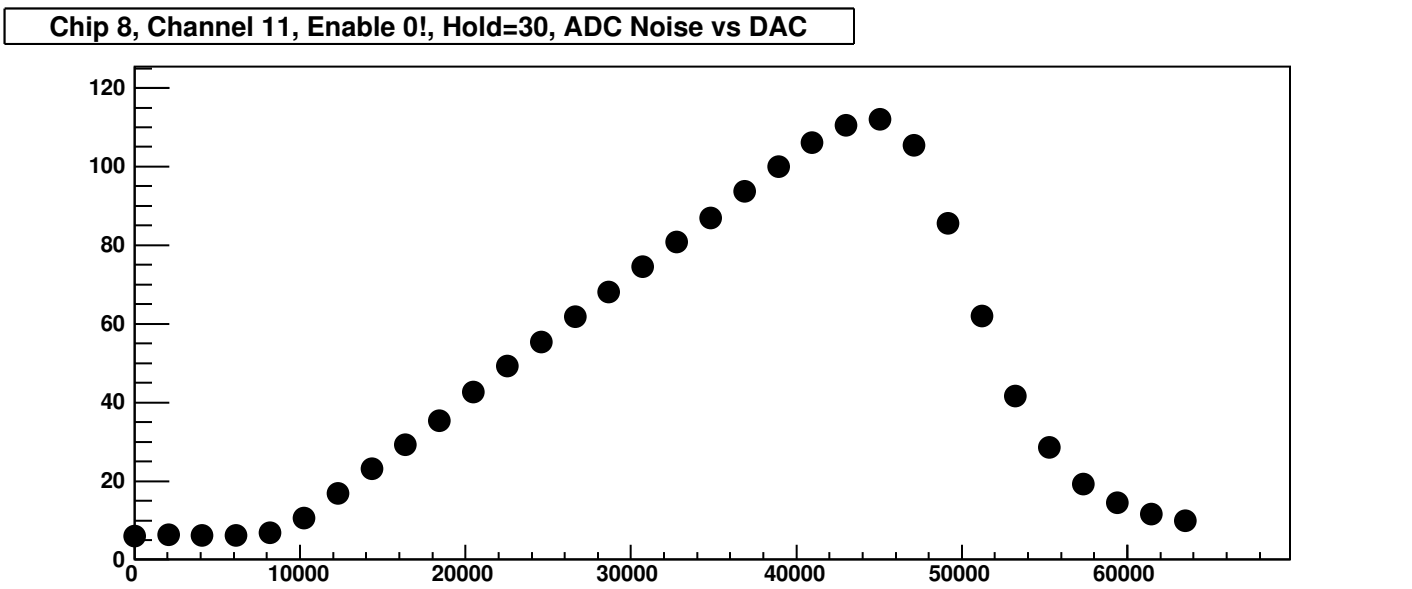

Chip 8, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

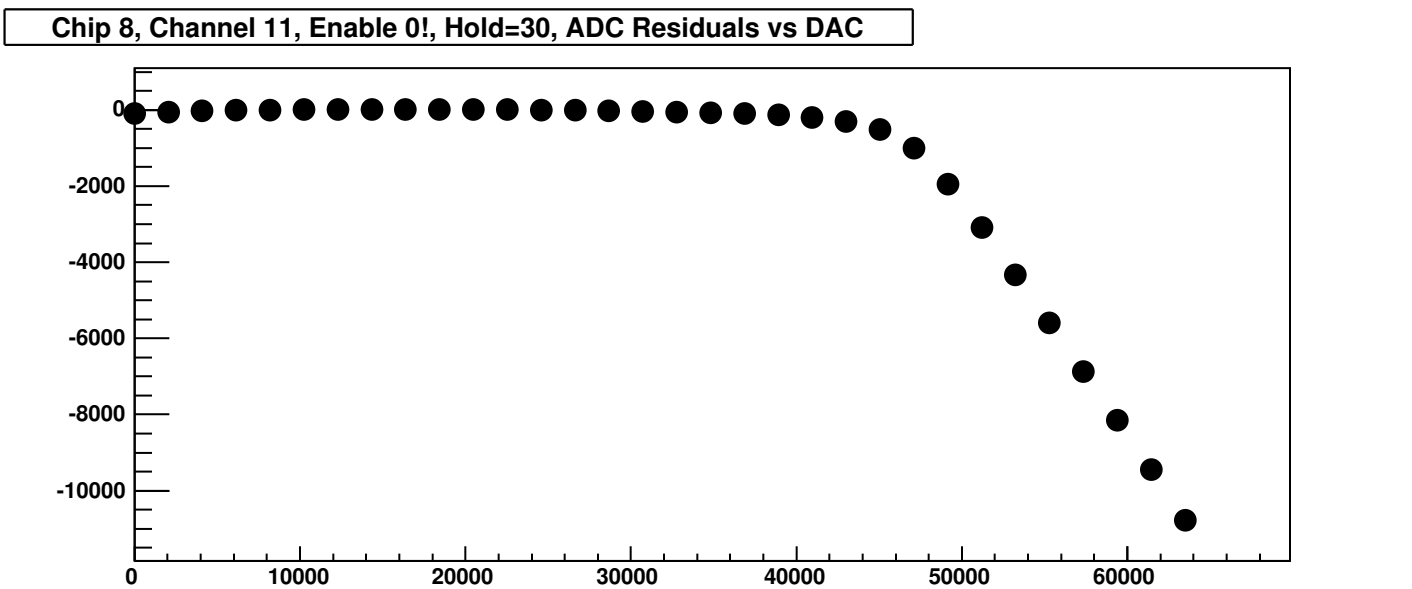

Chip 8, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3326 / 15
p0 1095 ± 0.112
p1 -0.00107 ± 4.544e-06
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

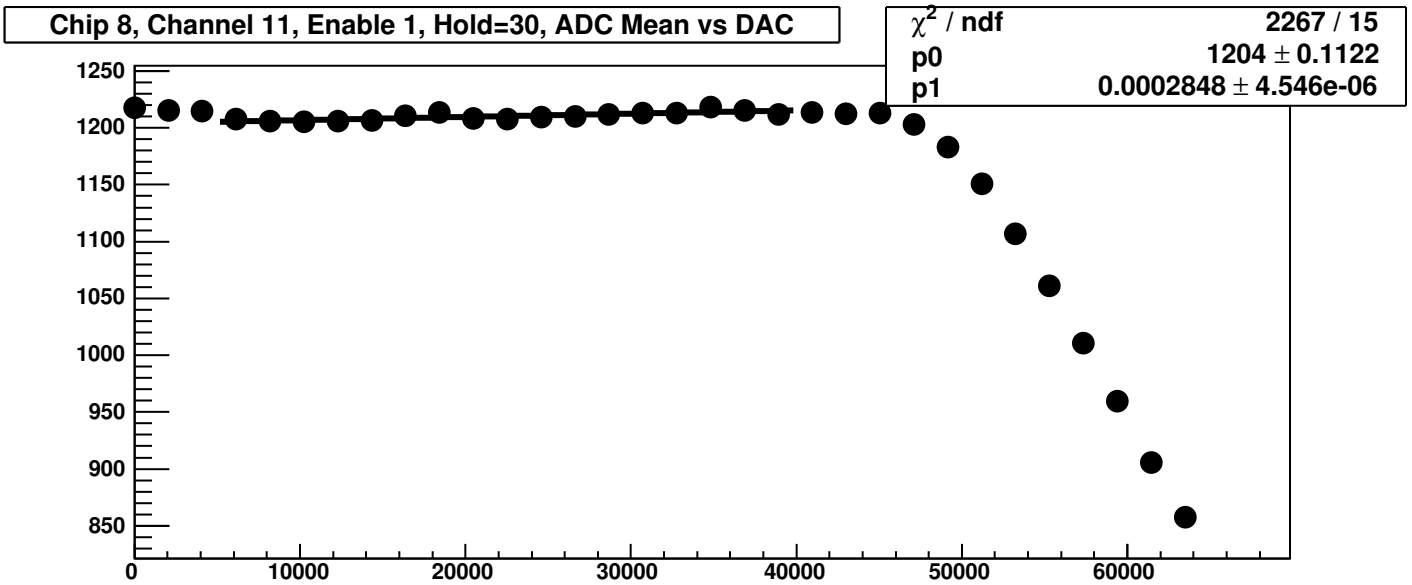

Chip 8, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

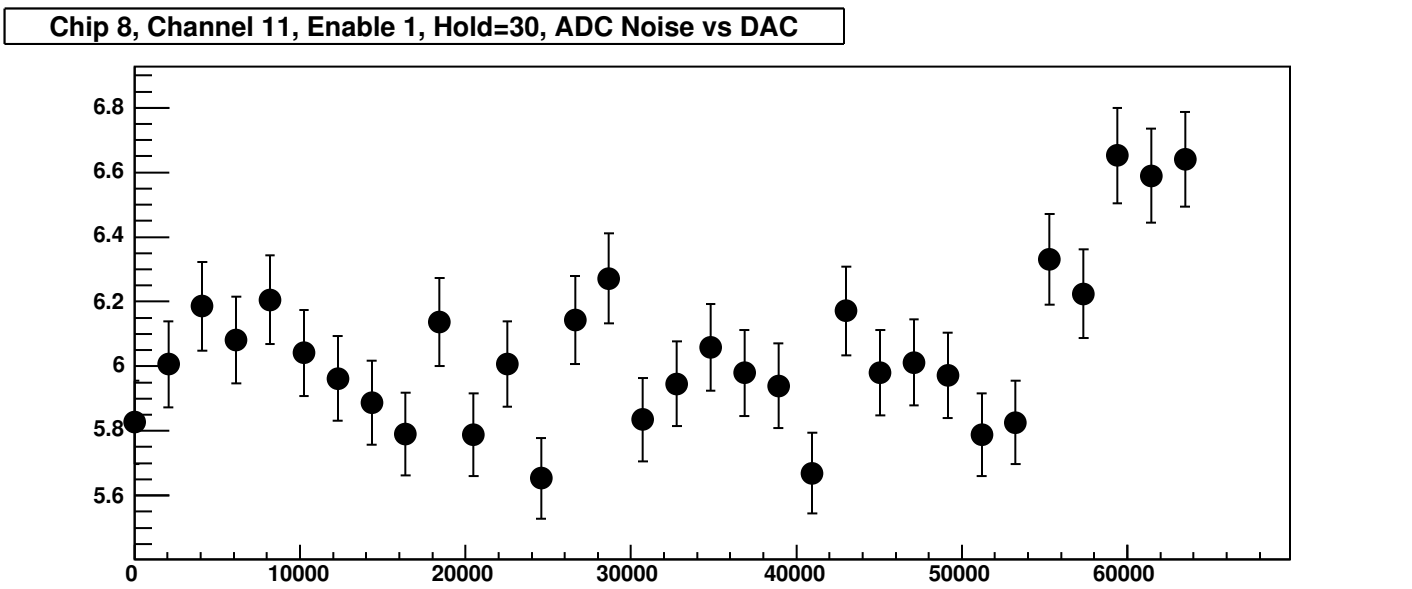

Chip 8, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

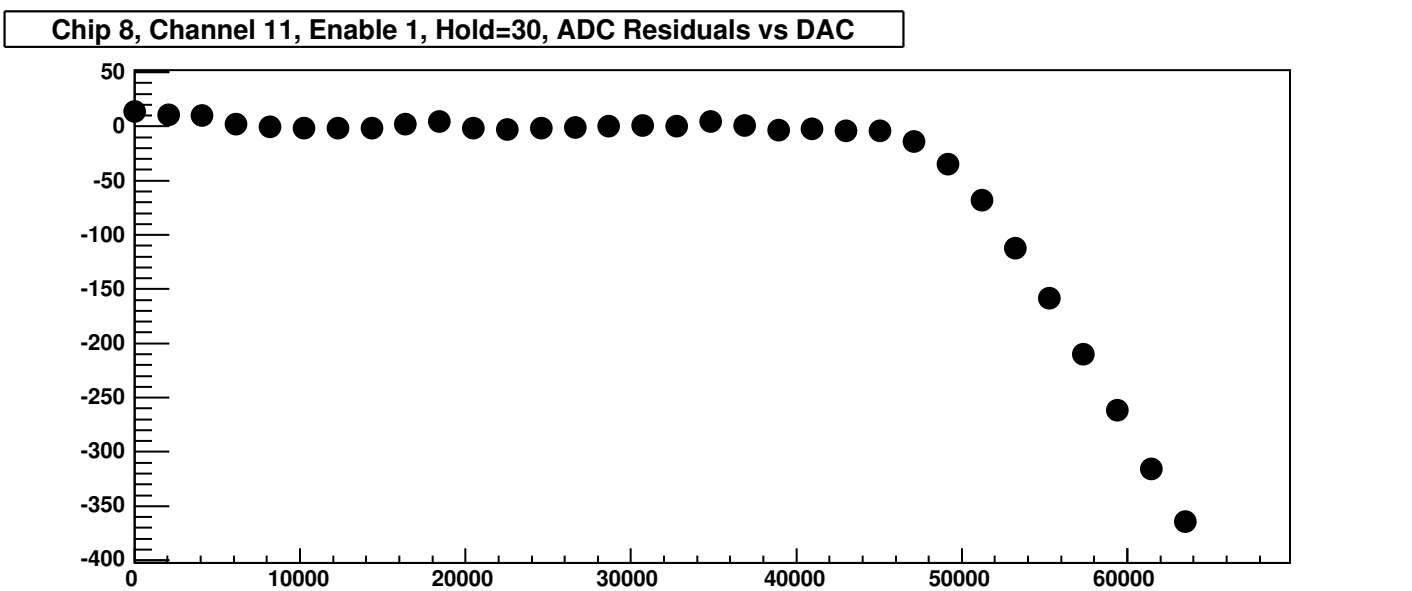

Chip 8, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.076e+04 / 15
p0 1318 ± 0.2766
p1 0.6243 ± 2.75e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

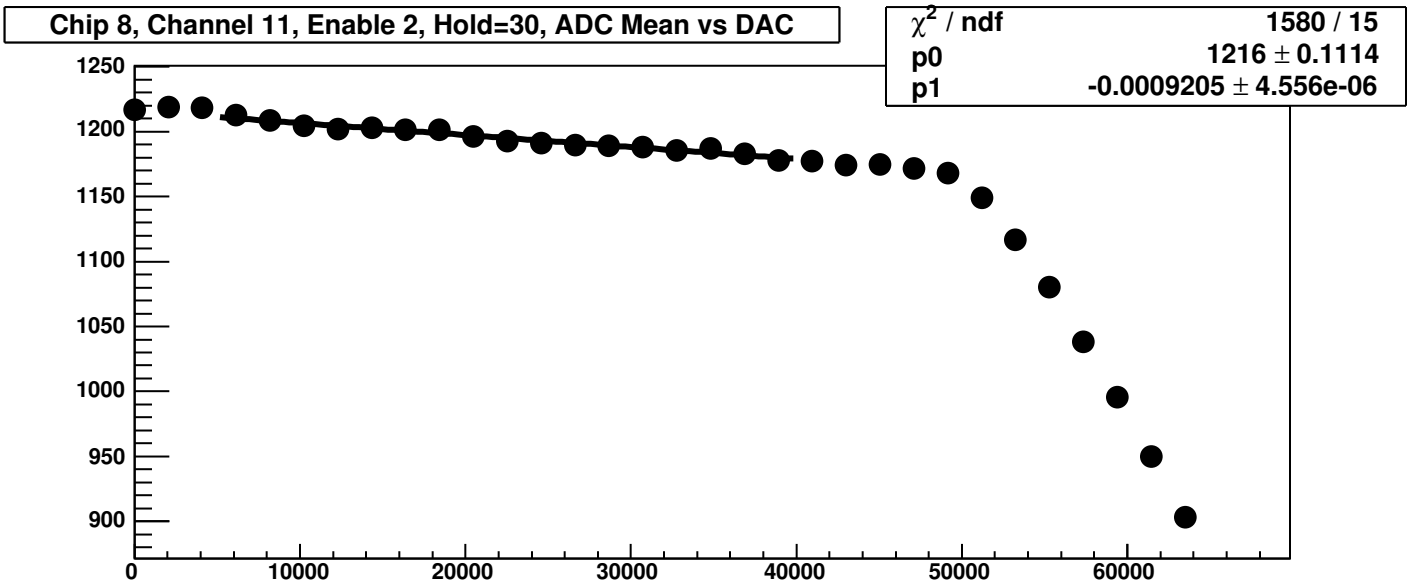

Chip 8, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

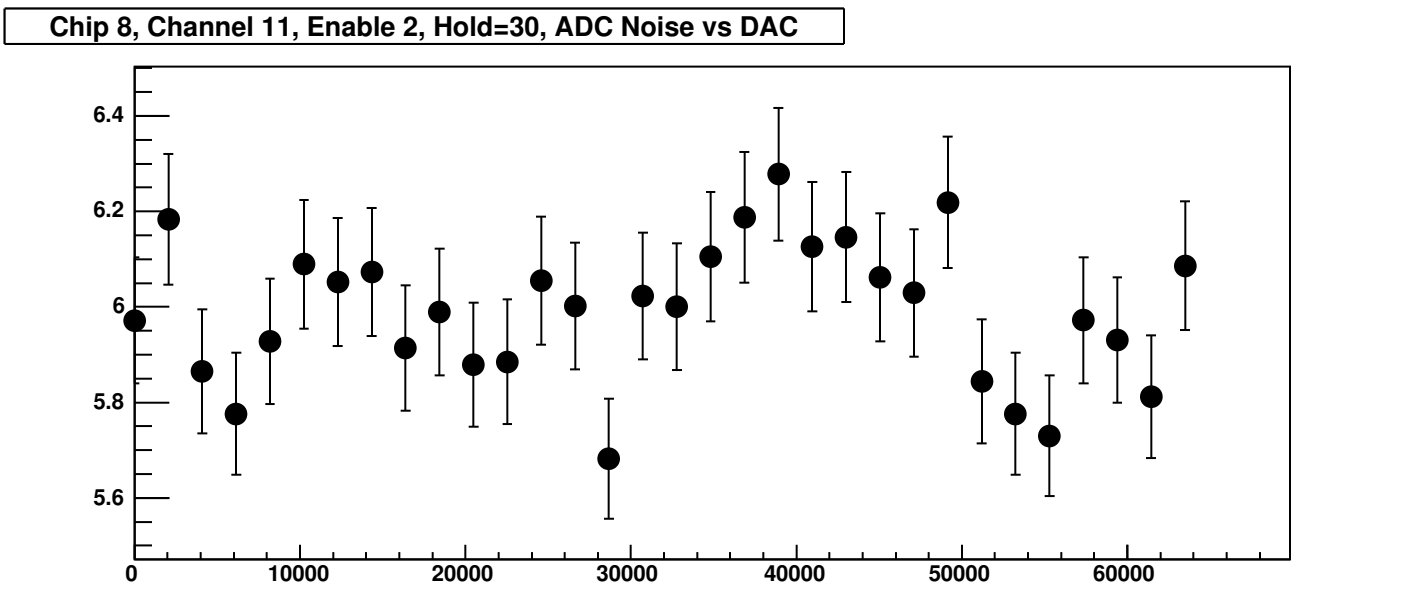

Chip 8, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

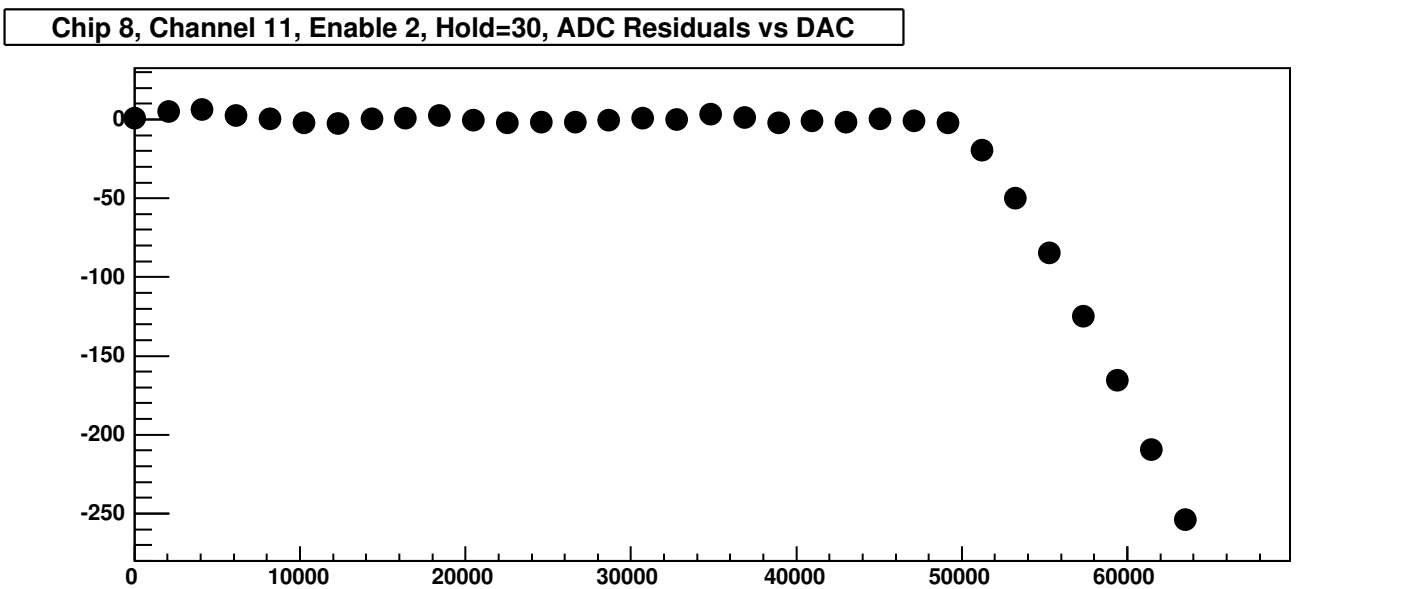

Chip 8, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2267 / 15
p0 1204 ± 0.1122
p1 0.0002848 ± 4.546e-06
1250
1200
1150
1100
1050
1000
950
900
850
0
10000
20000
30000
40000
50000
60000

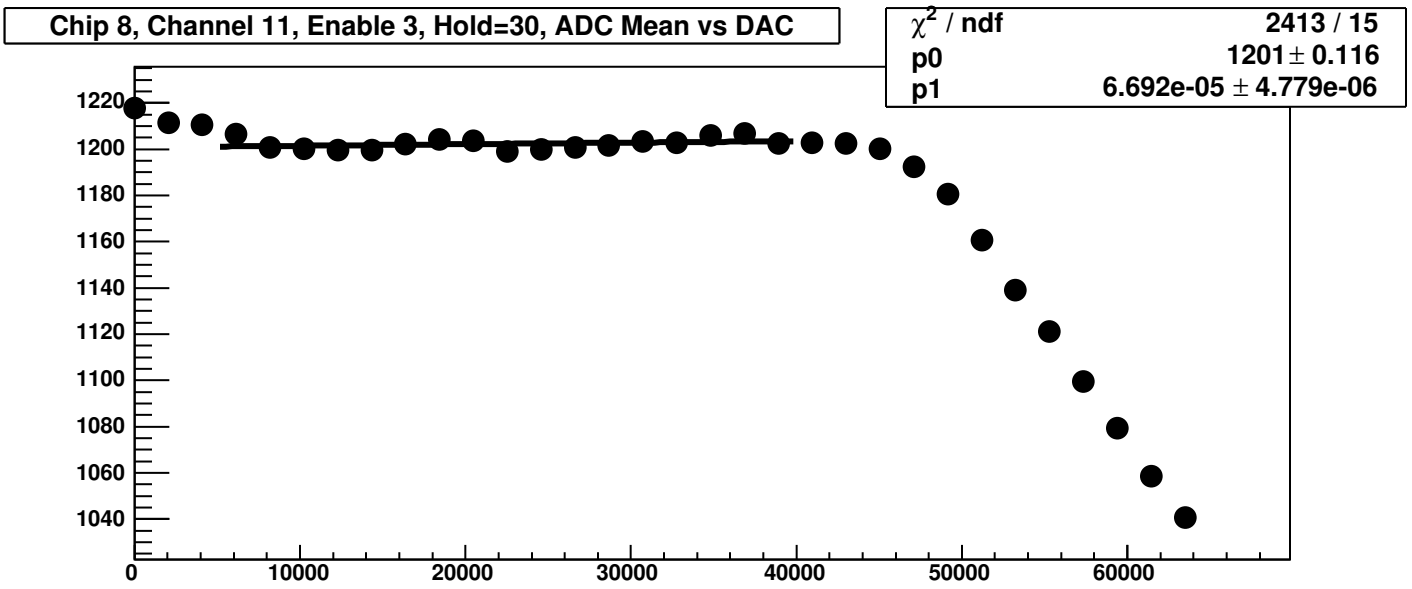

Chip 8, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

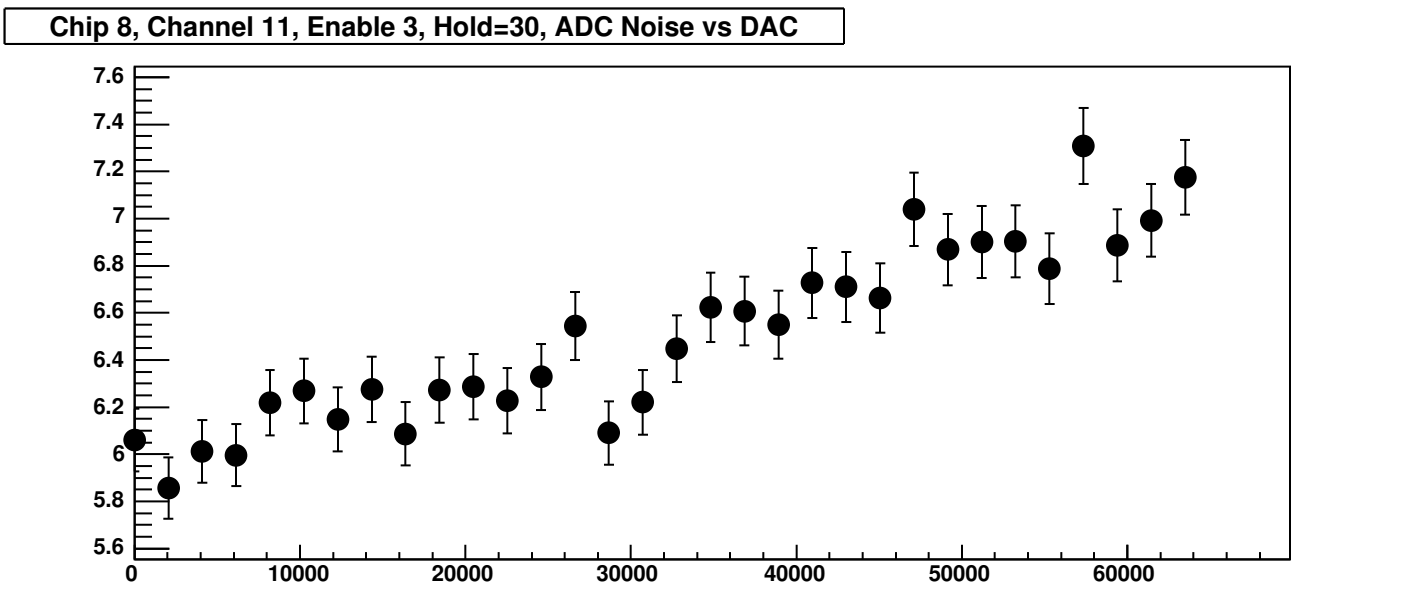

Chip 8, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

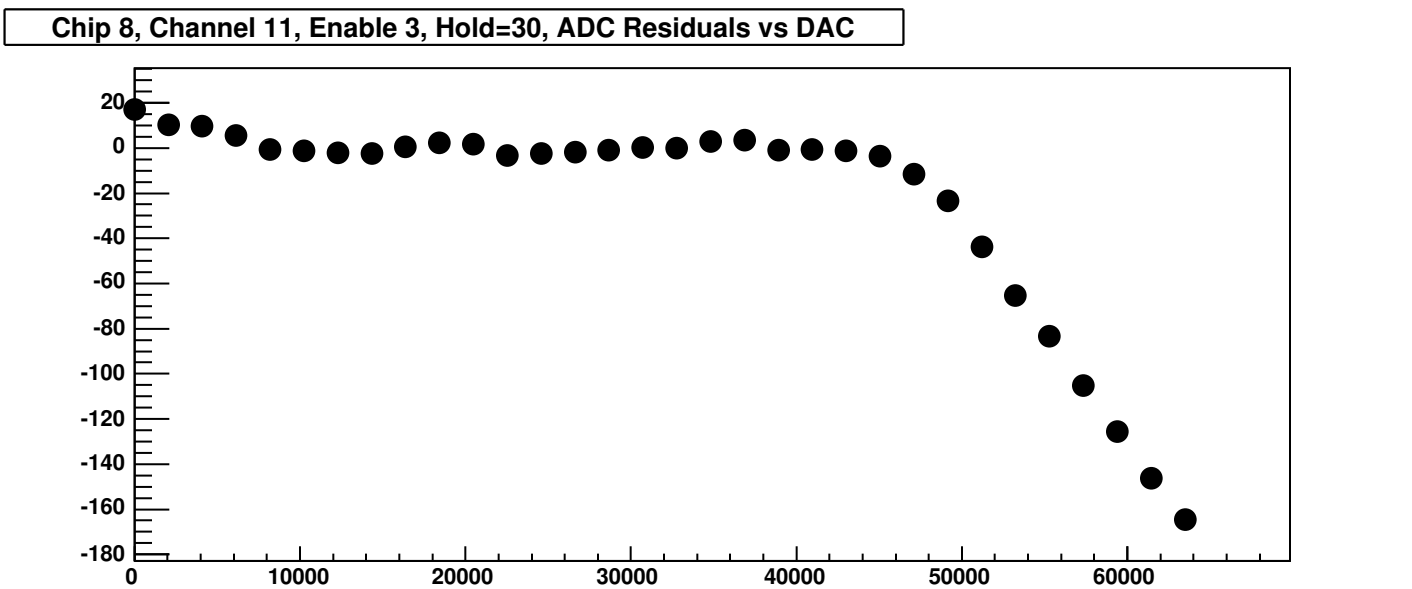

Chip 8, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1580 / 15
p0 1216 ± 0.1114
p1 -0.0009205 ± 4.556e-06
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

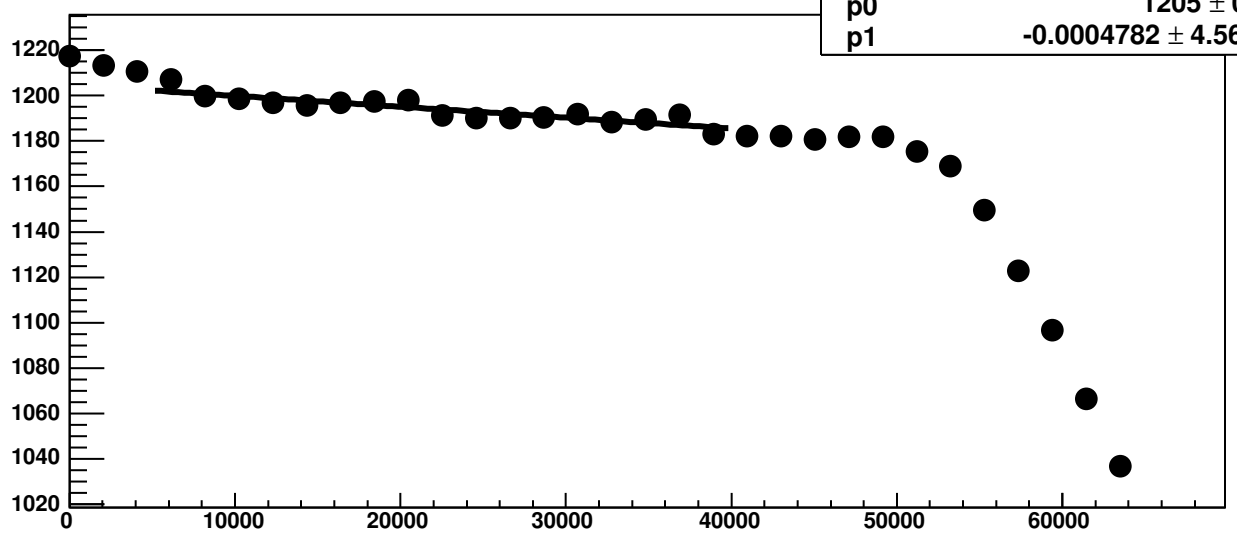

Chip 8, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2413 / 15
p0 1201 ± 0.116
p1 6.692e-05 ± 4.779e-06
1220
1200
1180
1160
1140
1120
1100
1080
1060
1040
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

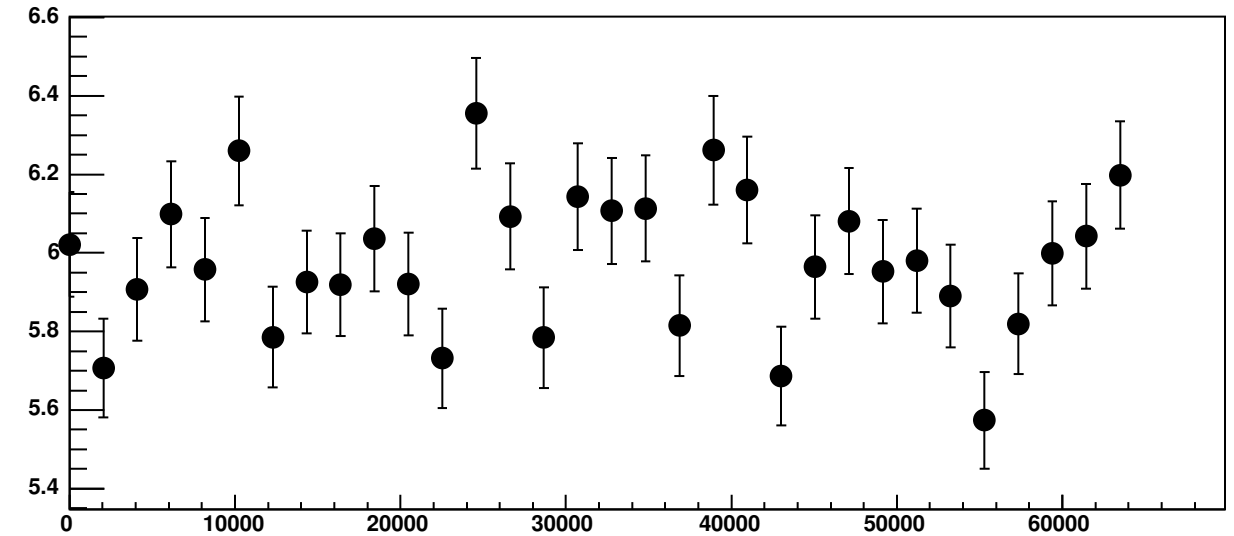

Chip 8, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

Chip 8, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

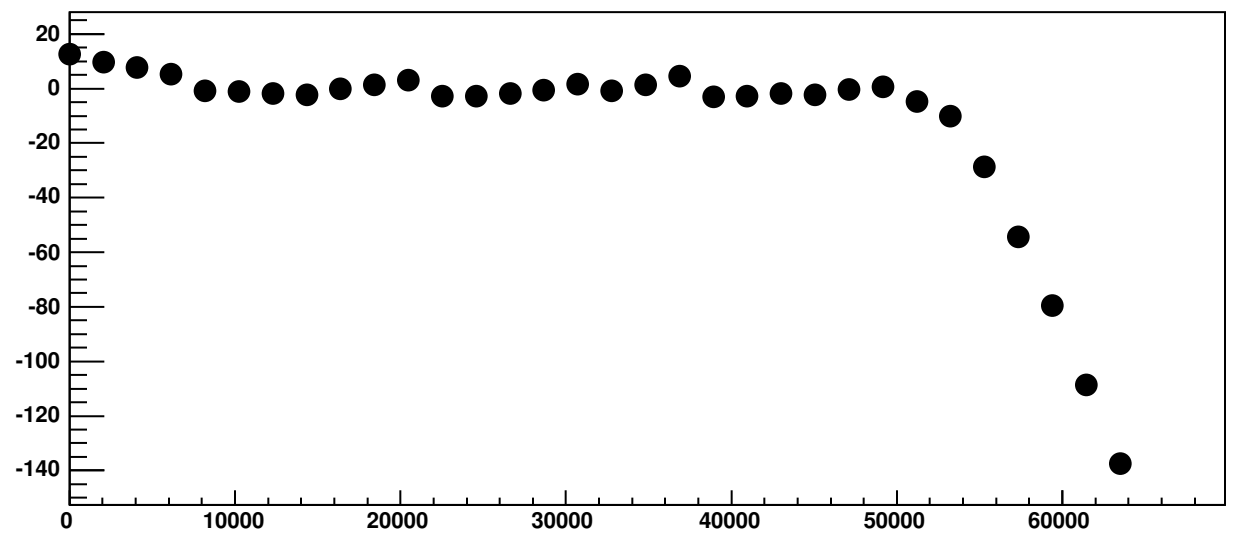
χ^2 / ndf	2984 / 15
p0	1205 ± 0.1122
p1	-0.0004782 ± 4.568e-06

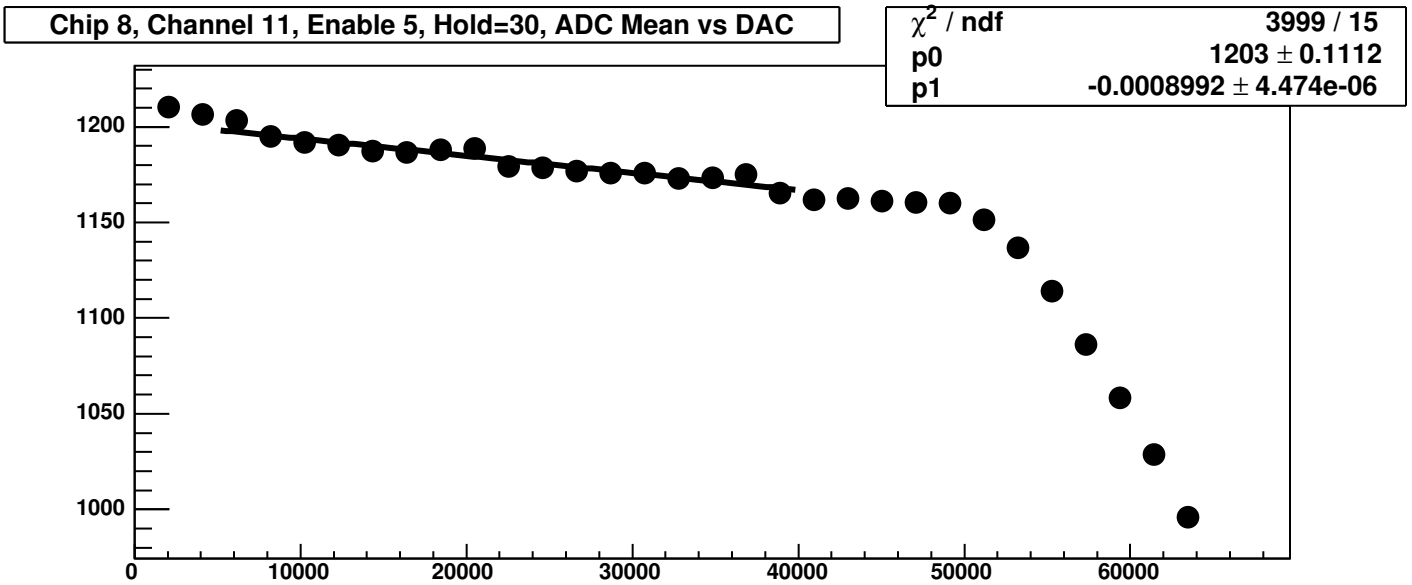


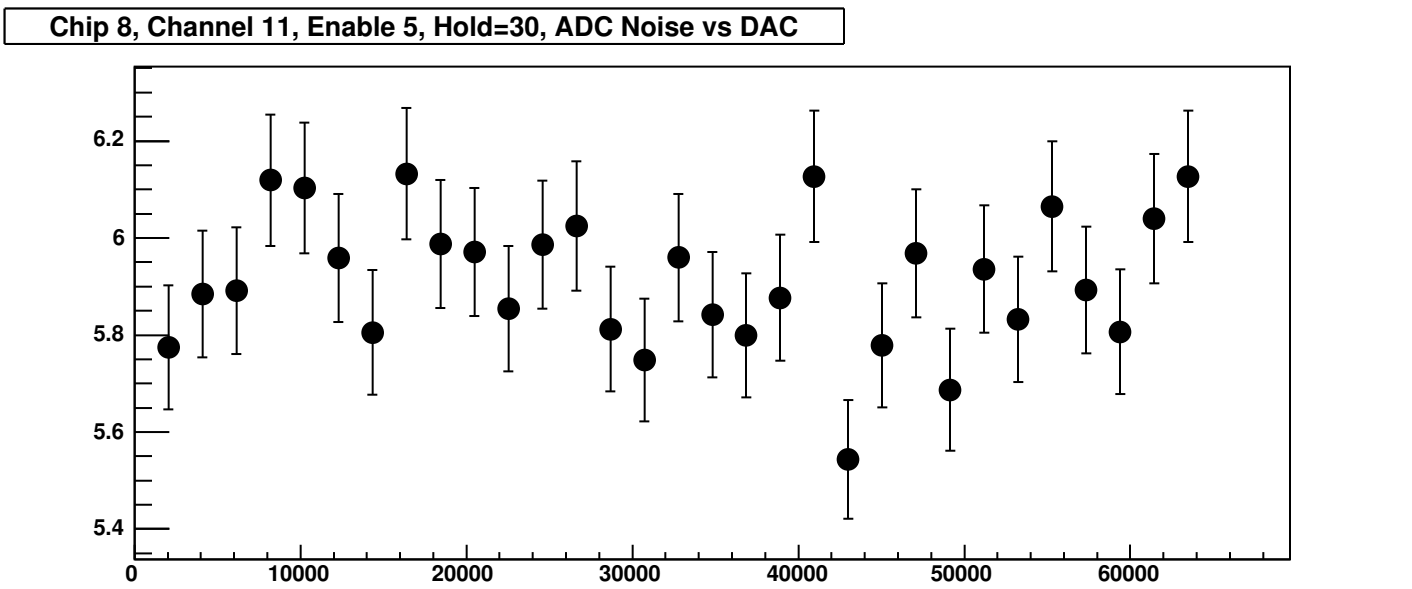
Chip 8, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC

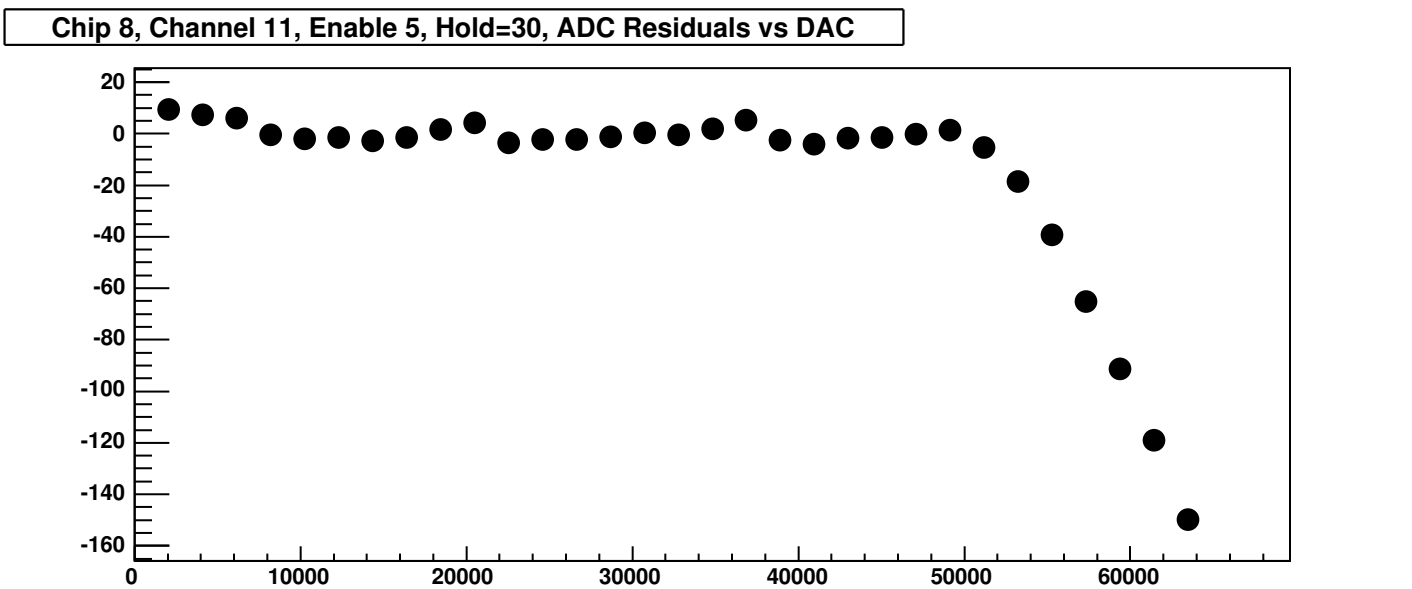


Chip 8, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC

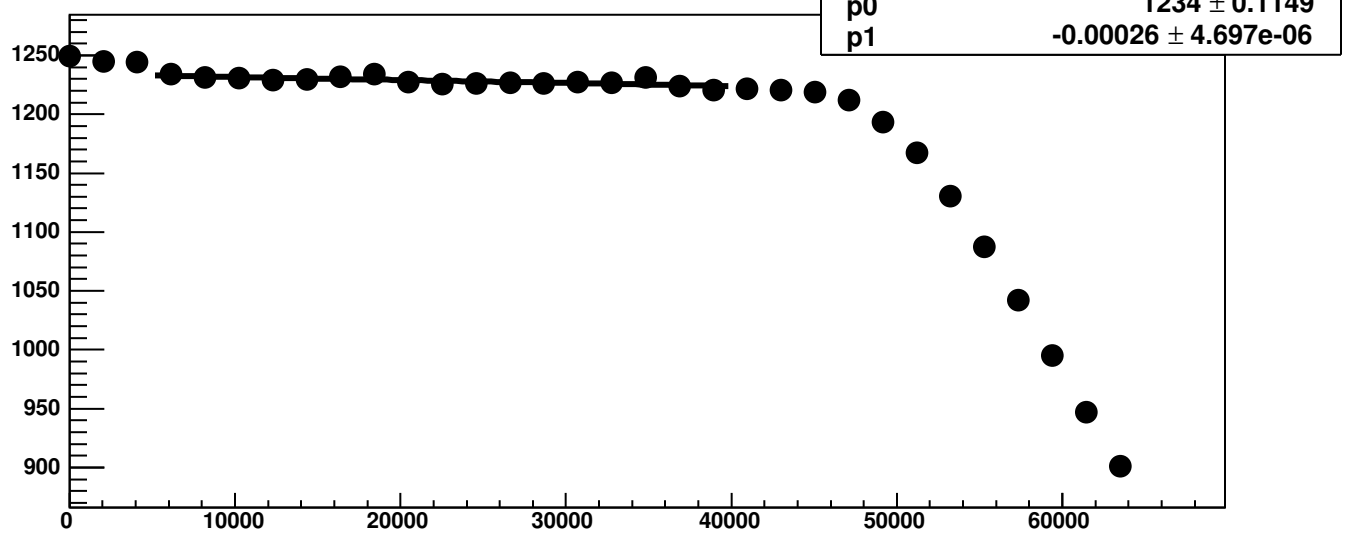



Chip 8, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3999 / 15
p0 1203 ± 0.1112
p1 -0.0008992 ± 4.474e-06
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

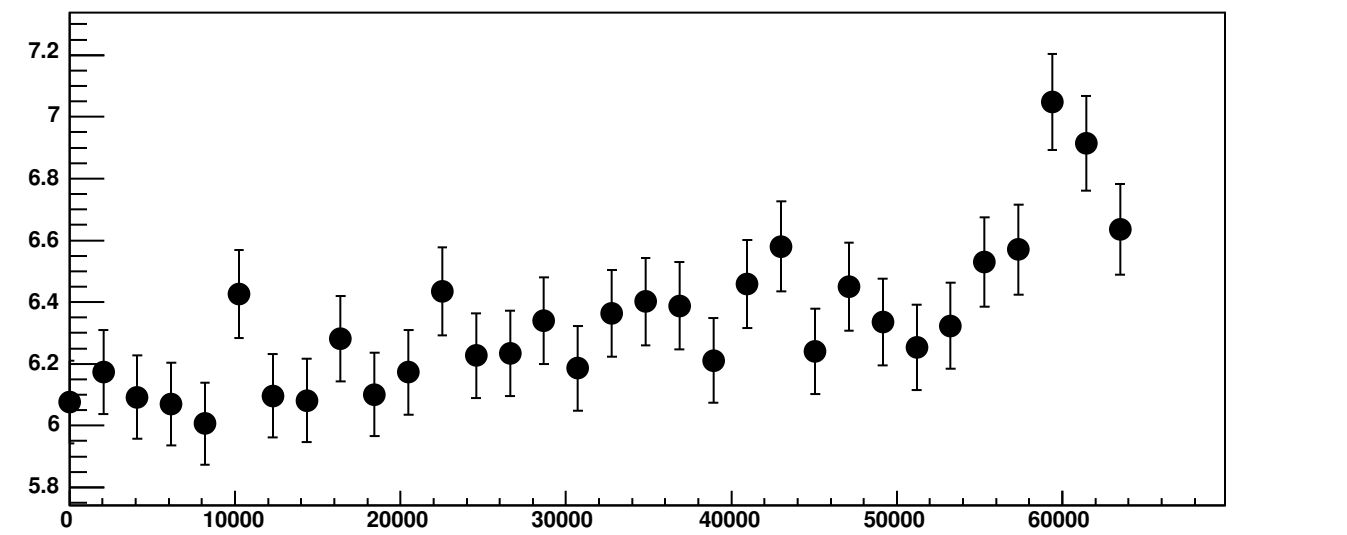

Chip 8, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

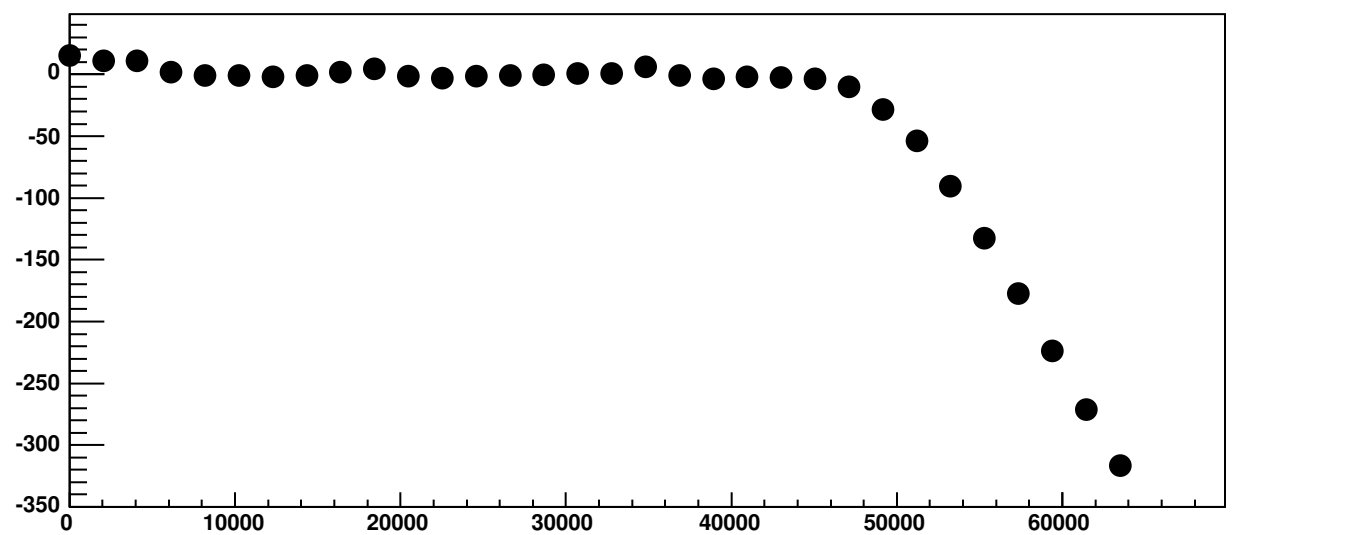
Chip 8, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

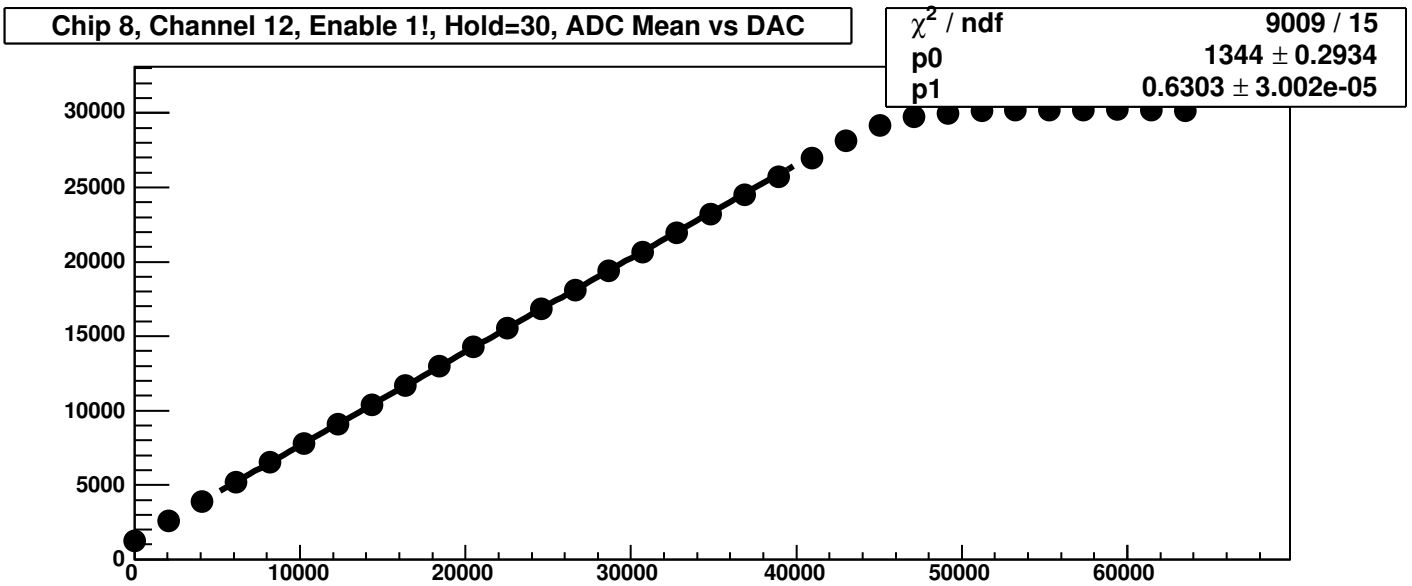


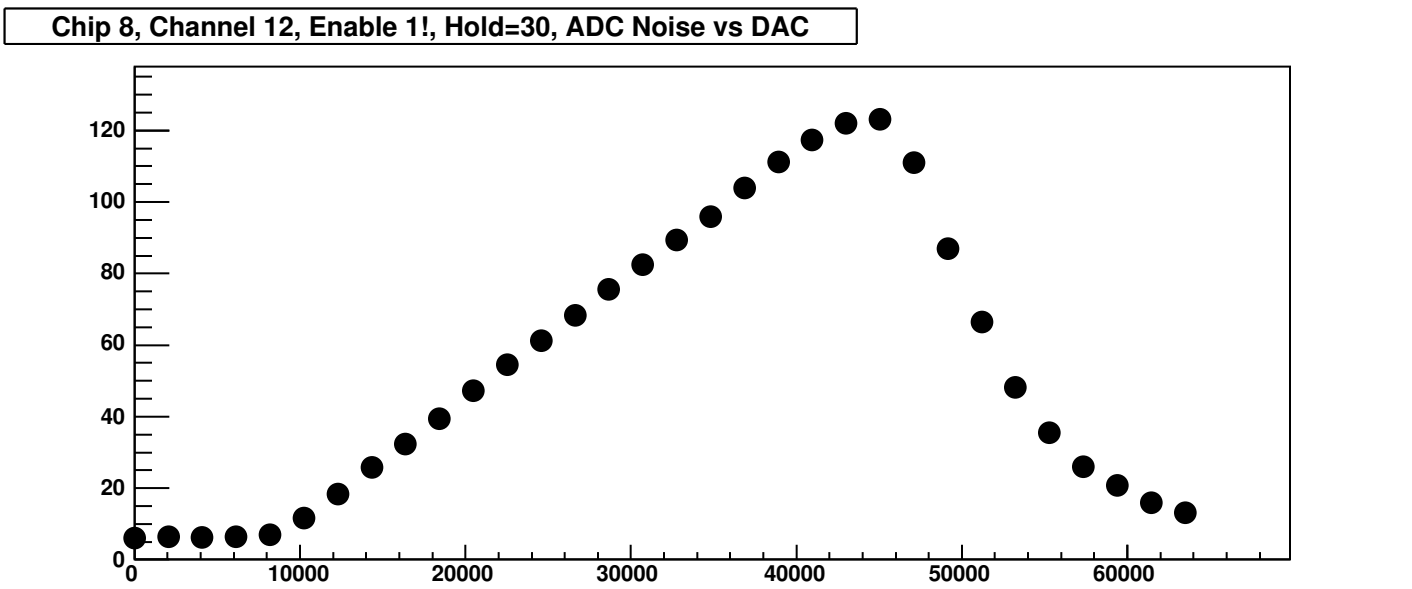
Chip 8, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC

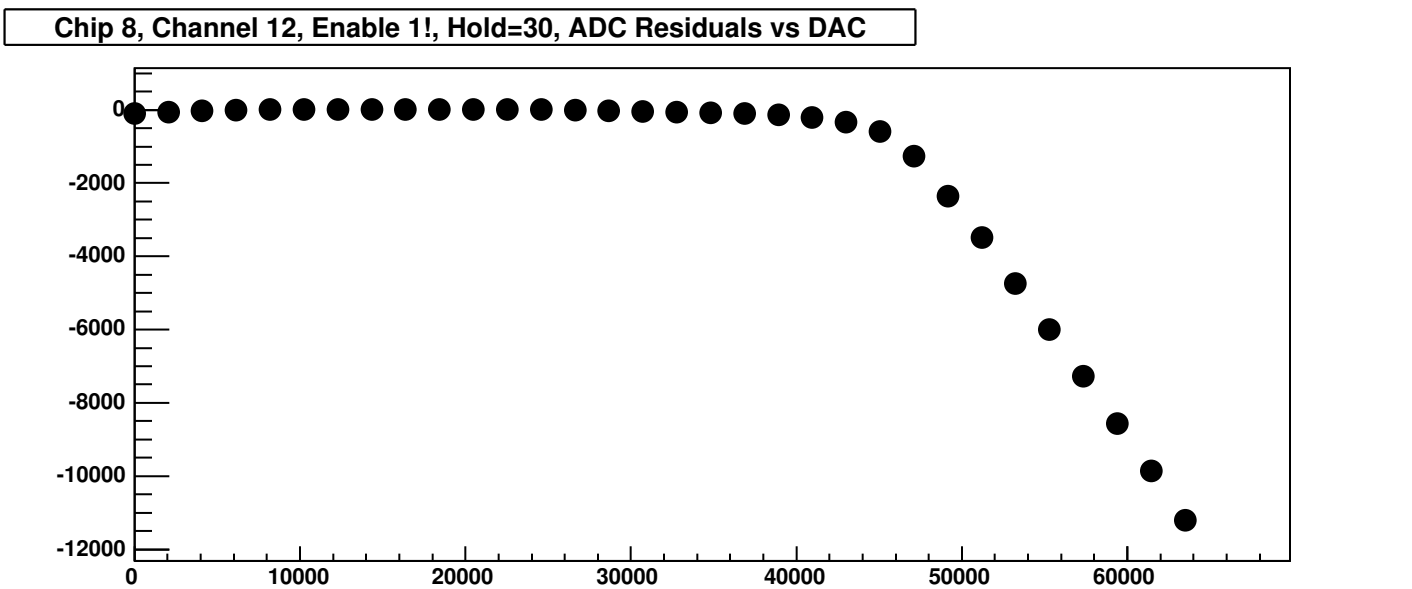


Chip 8, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC



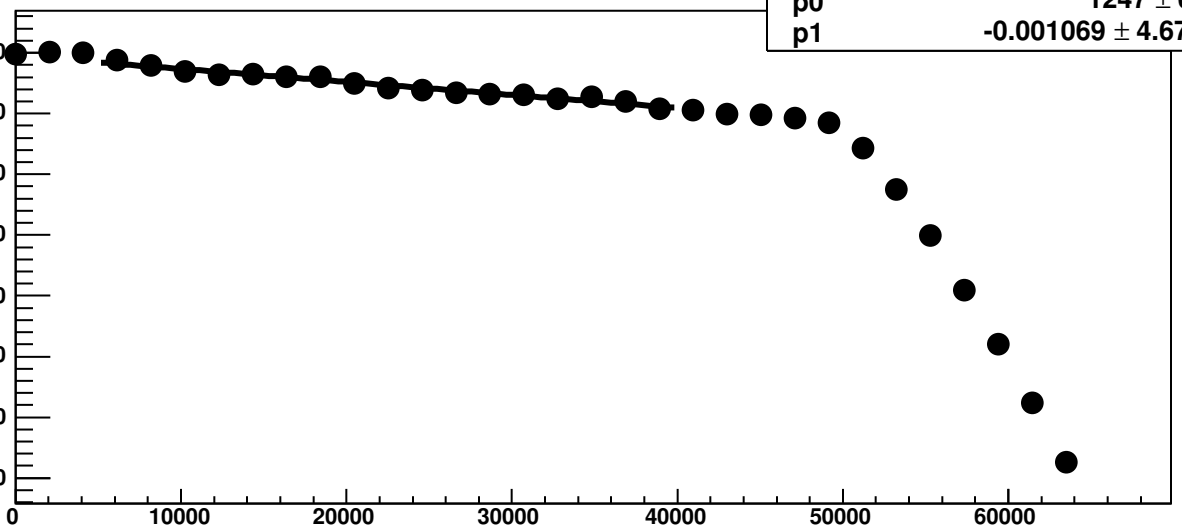

Chip 8, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 9009 / 15
p0 1344 ± 0.2934
p1 0.6303 ± 3.002e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

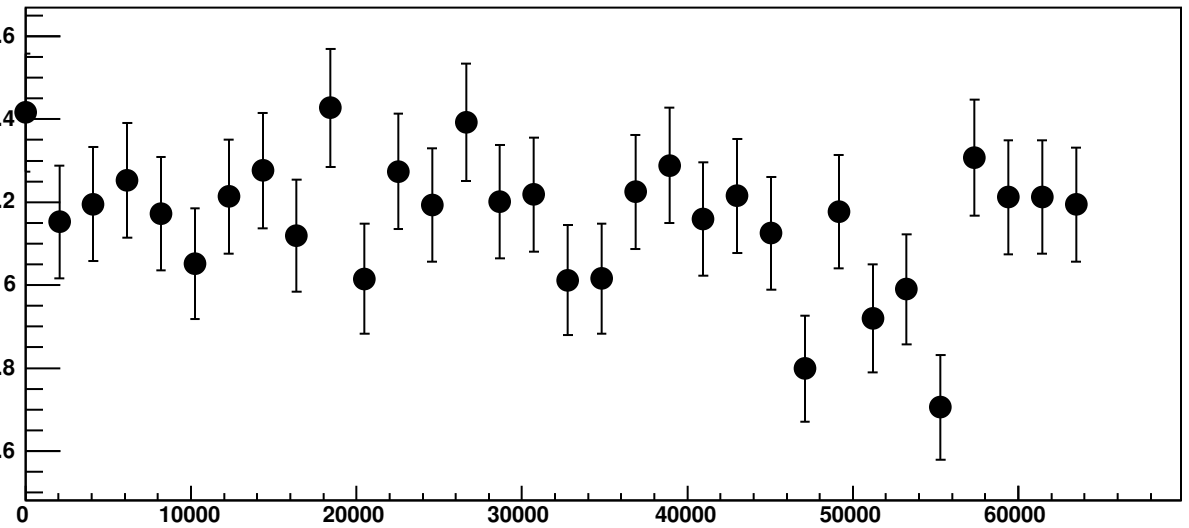

Chip 8, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

Chip 8, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC

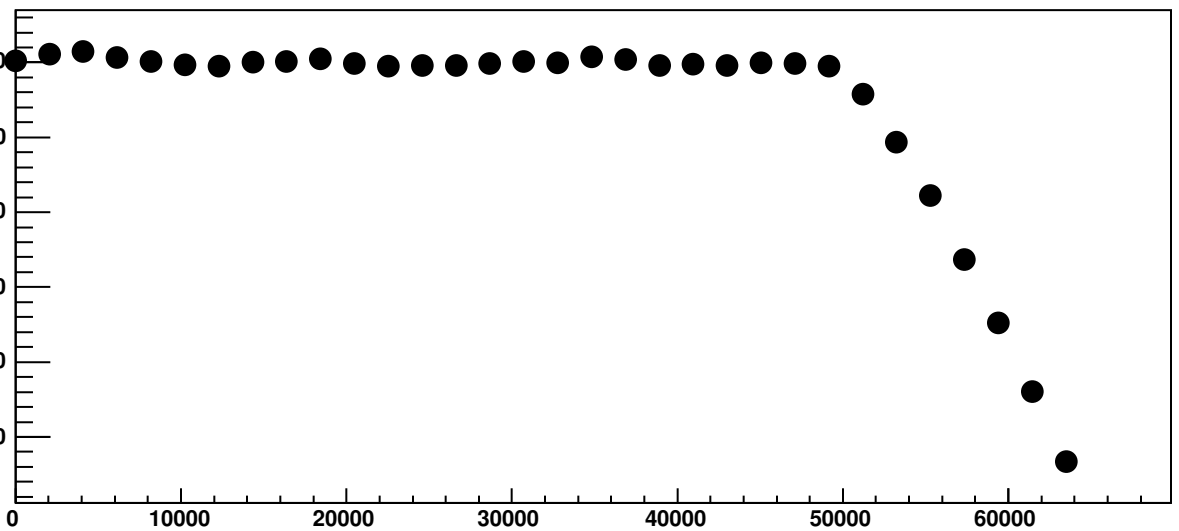
χ^2 / ndf	1689 / 15
p0	1247 ± 0.1153
p1	-0.001069 ± 4.672e-06



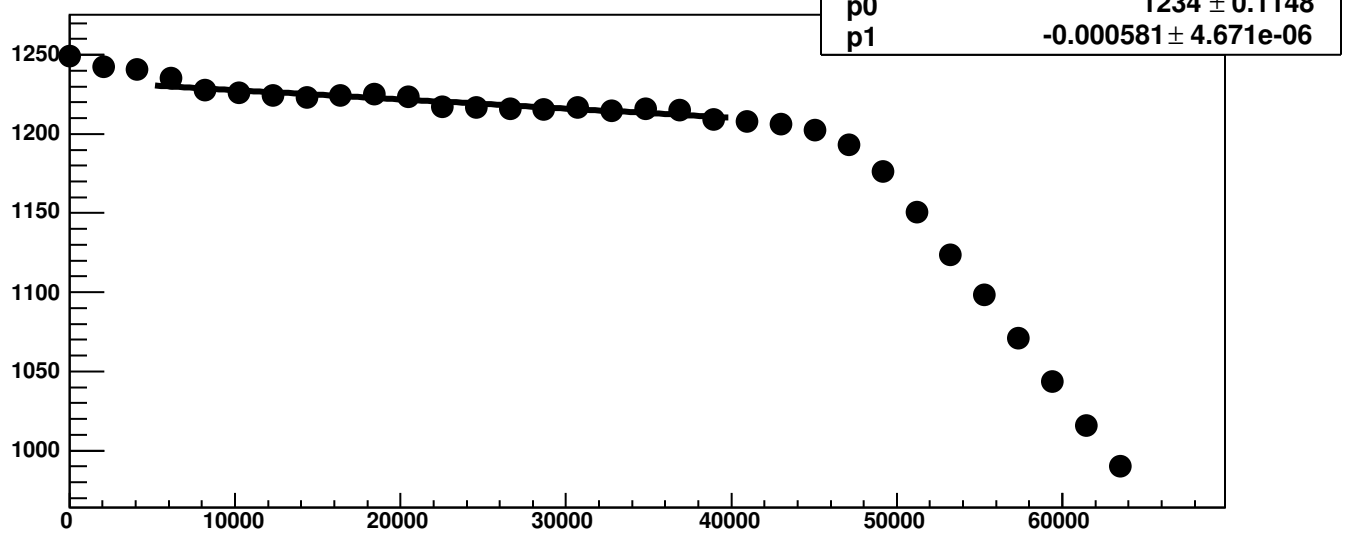
Chip 8, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



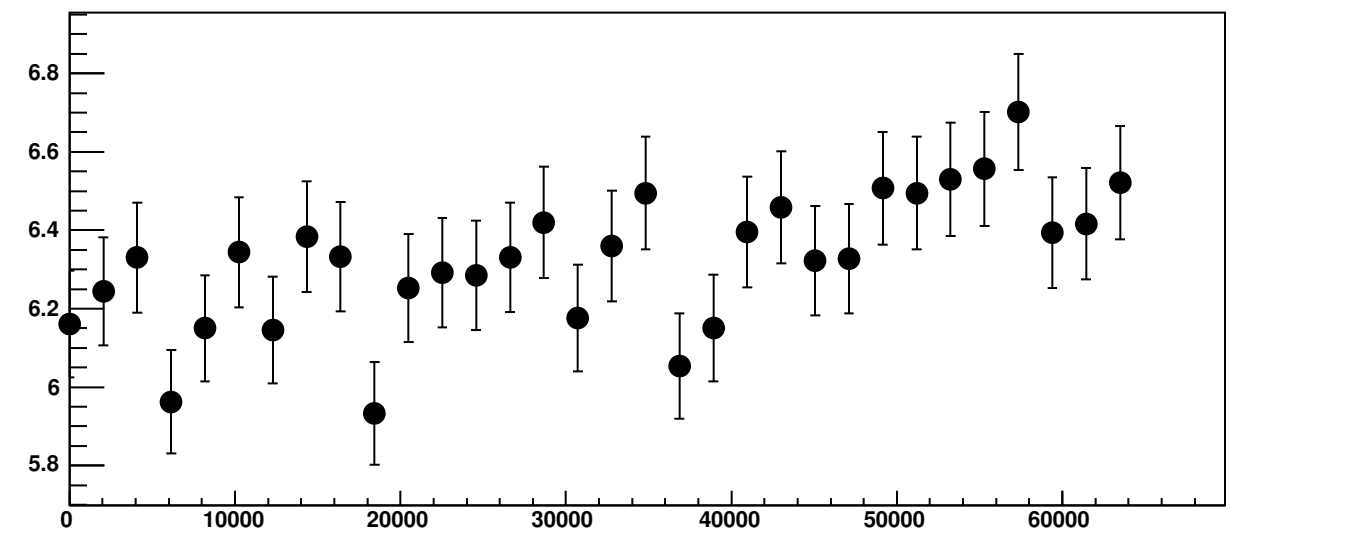
Chip 8, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC



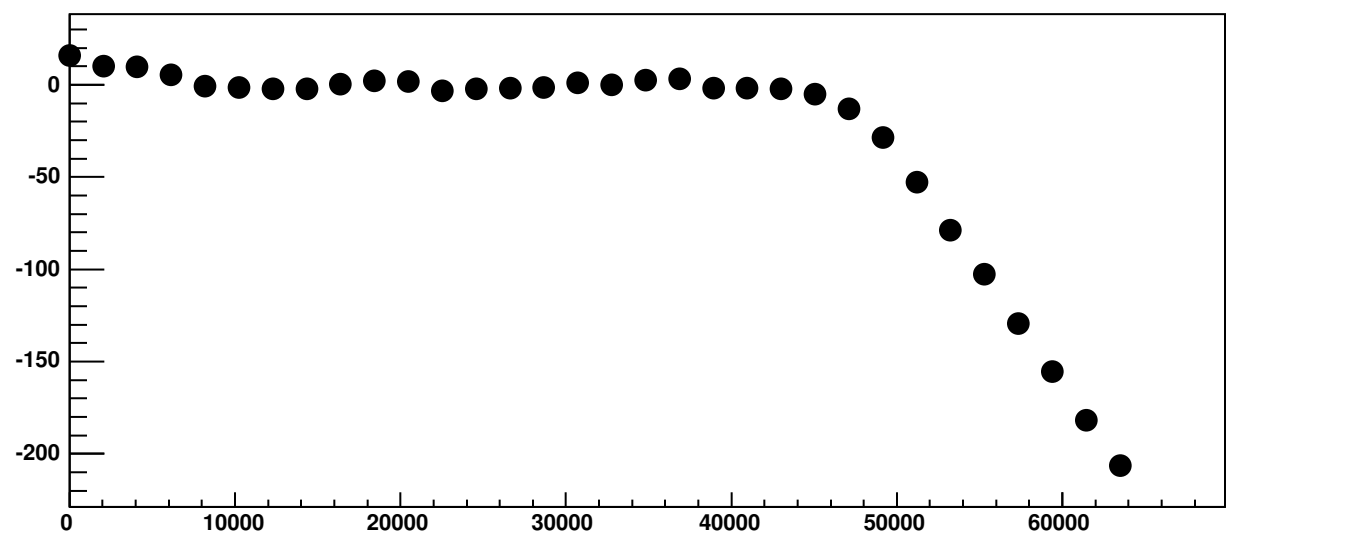
Chip 8, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

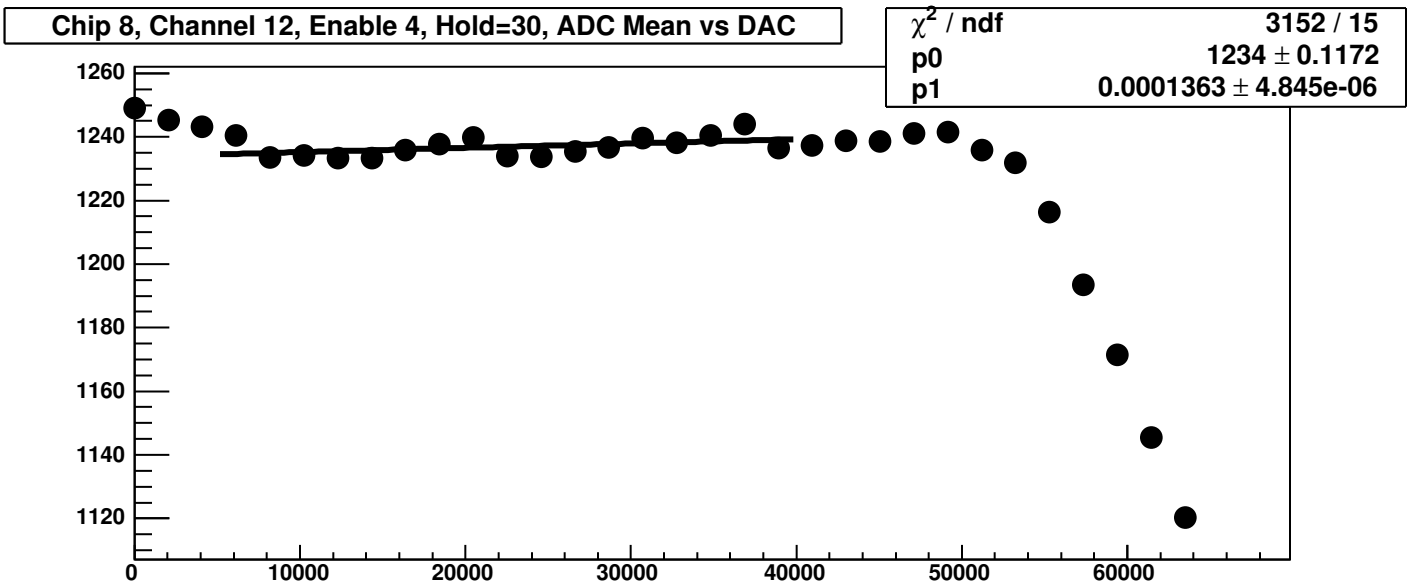


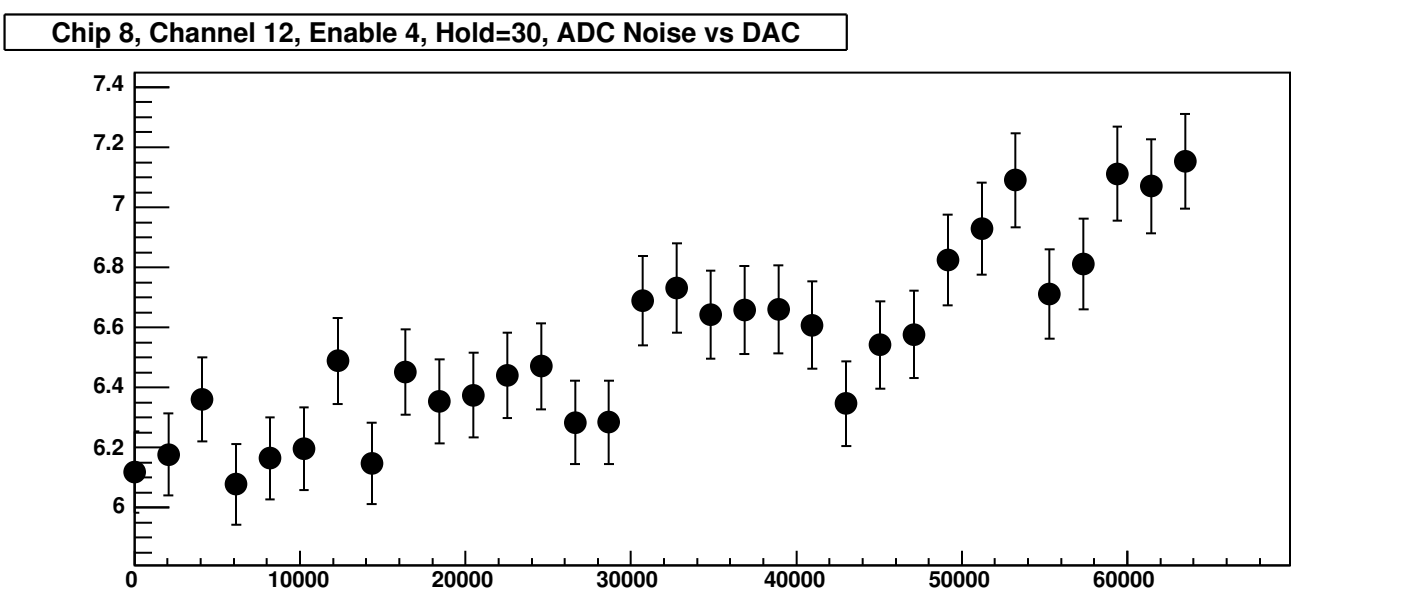
Chip 8, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC

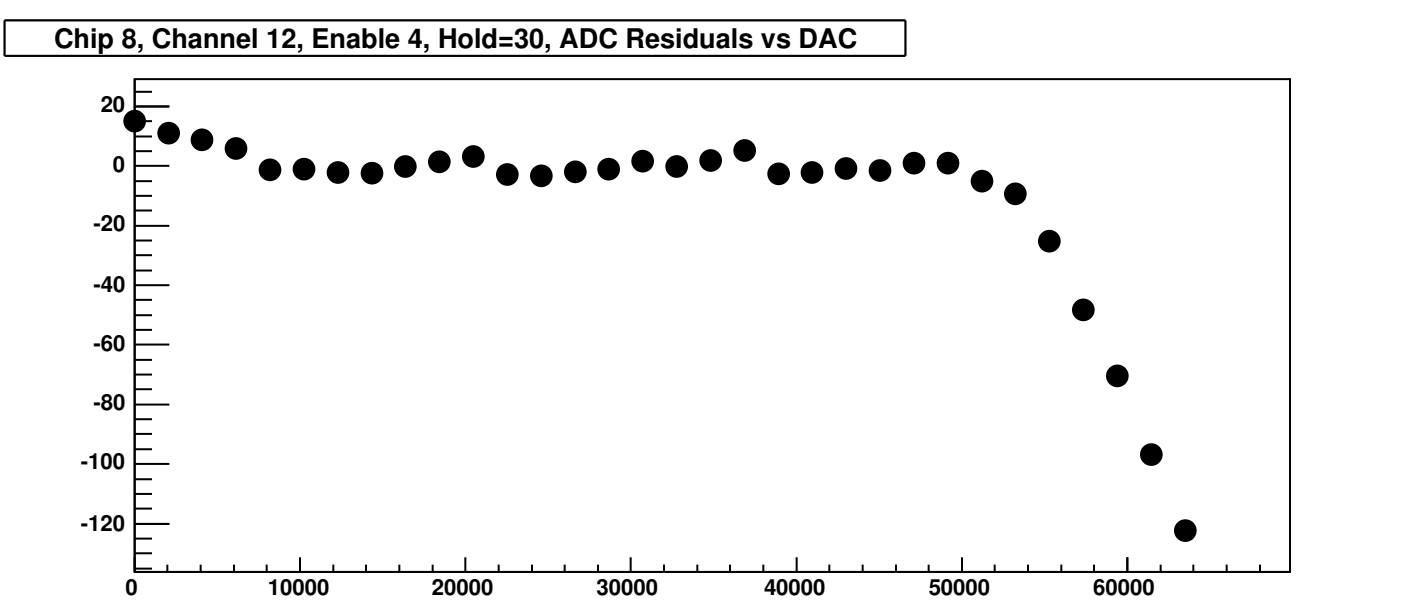


Chip 8, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC



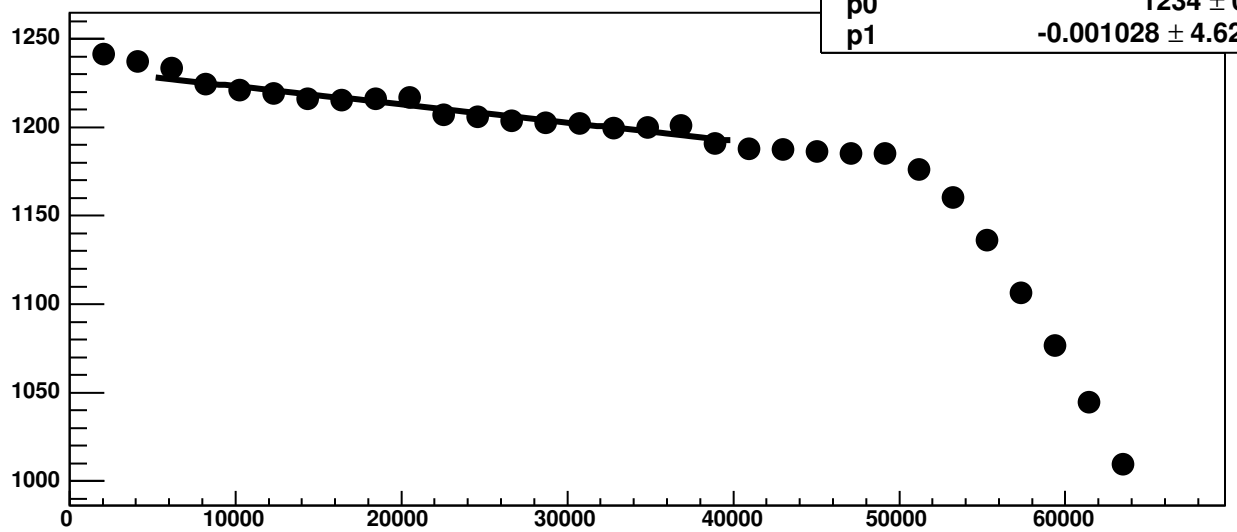

Chip 8, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 3152 / 15
p0 1234 ± 0.1172
p1 0.0001363 ± 4.845e-06
1260
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

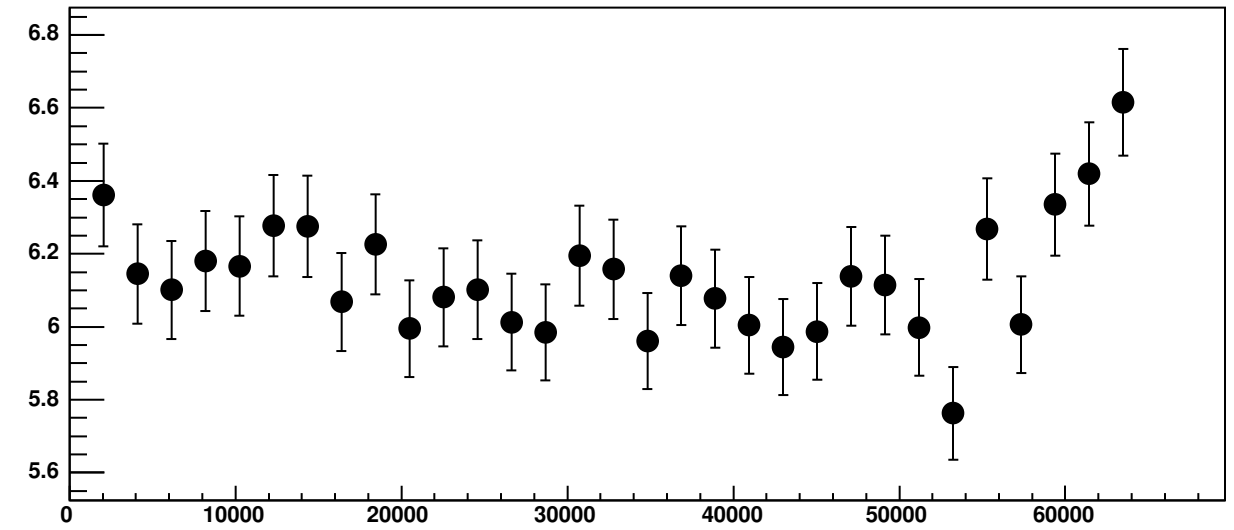

Chip 8, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

Chip 8, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC

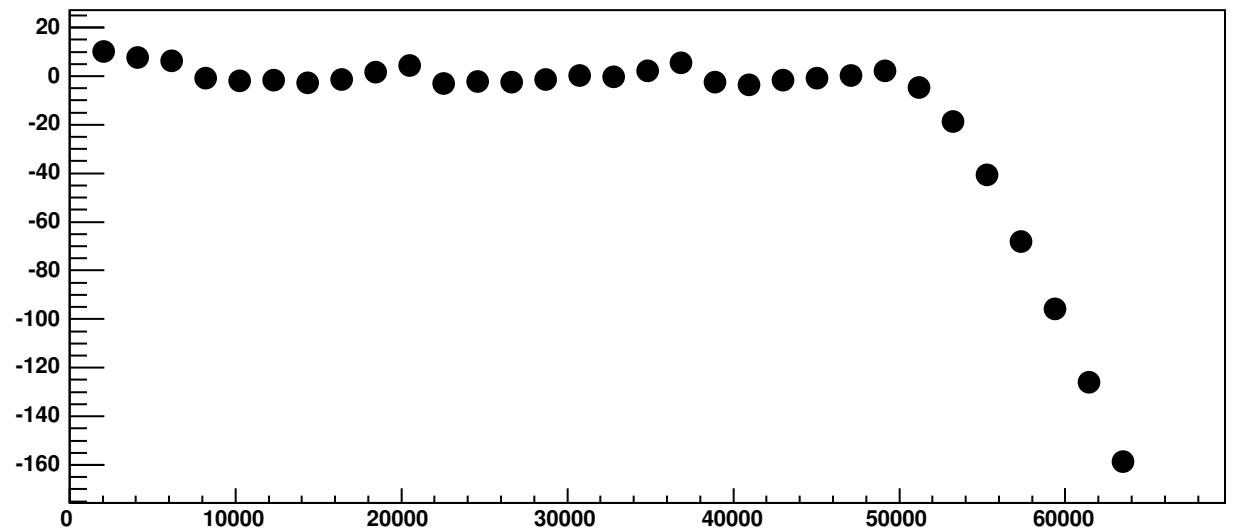
χ^2 / ndf	3876 / 15
p0	1234 ± 0.1146
p1	-0.001028 ± 4.627e-06

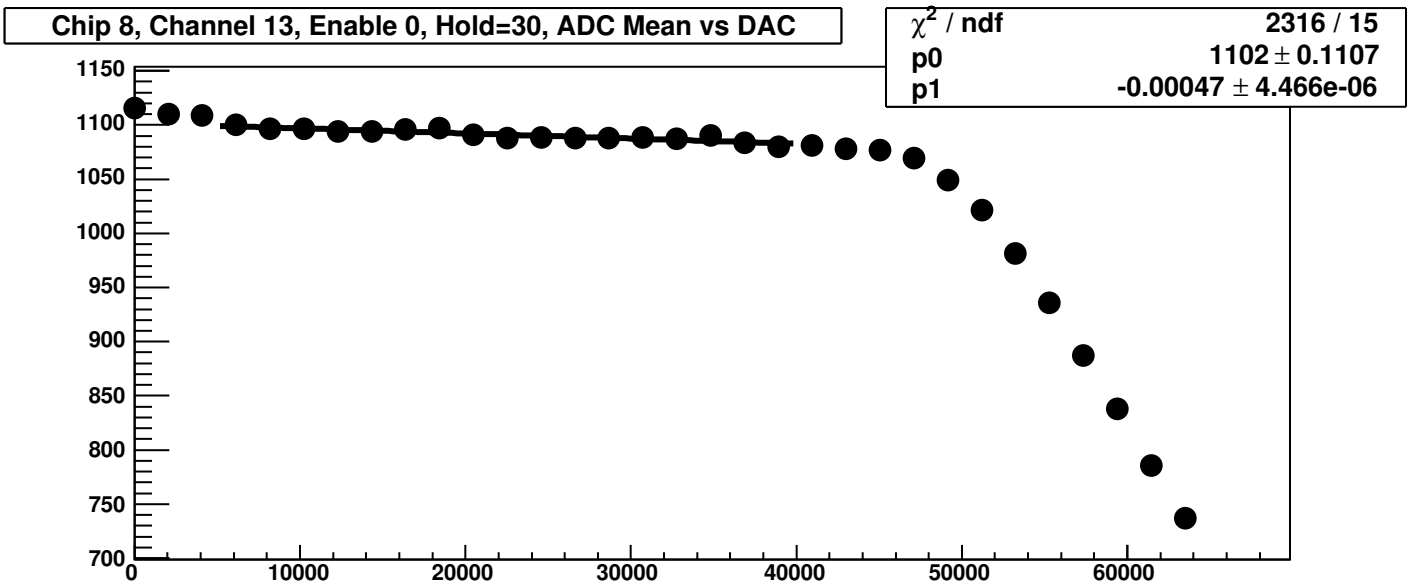


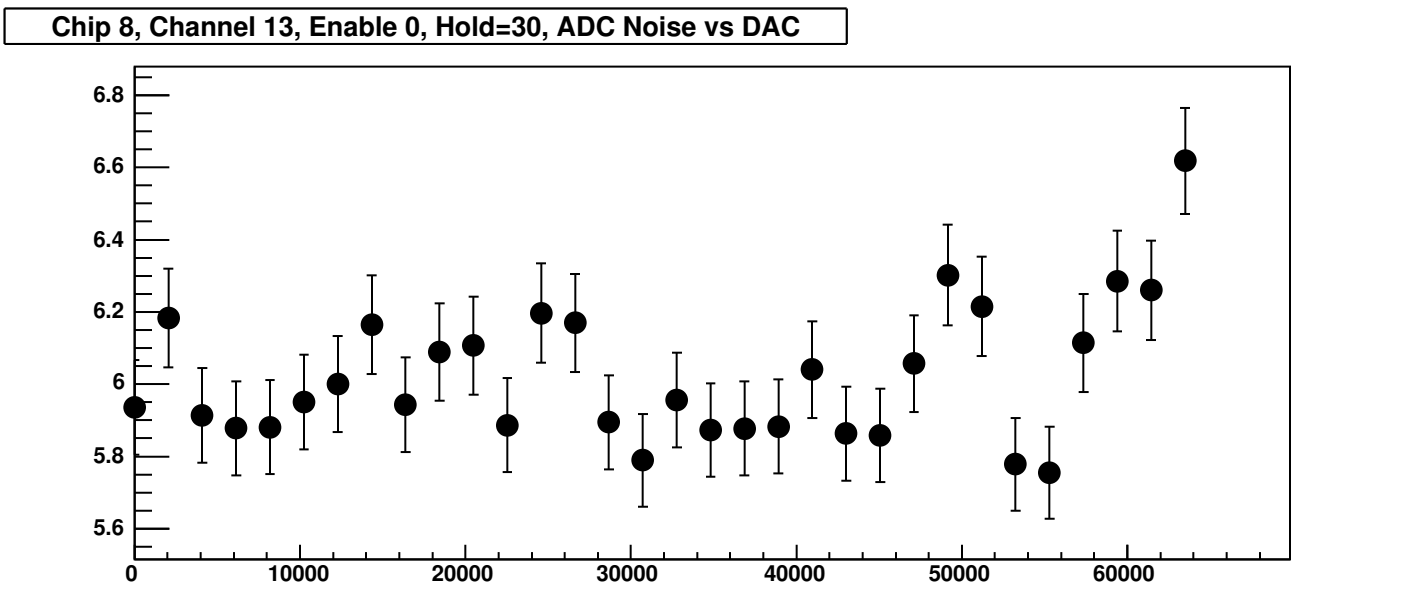
Chip 8, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC

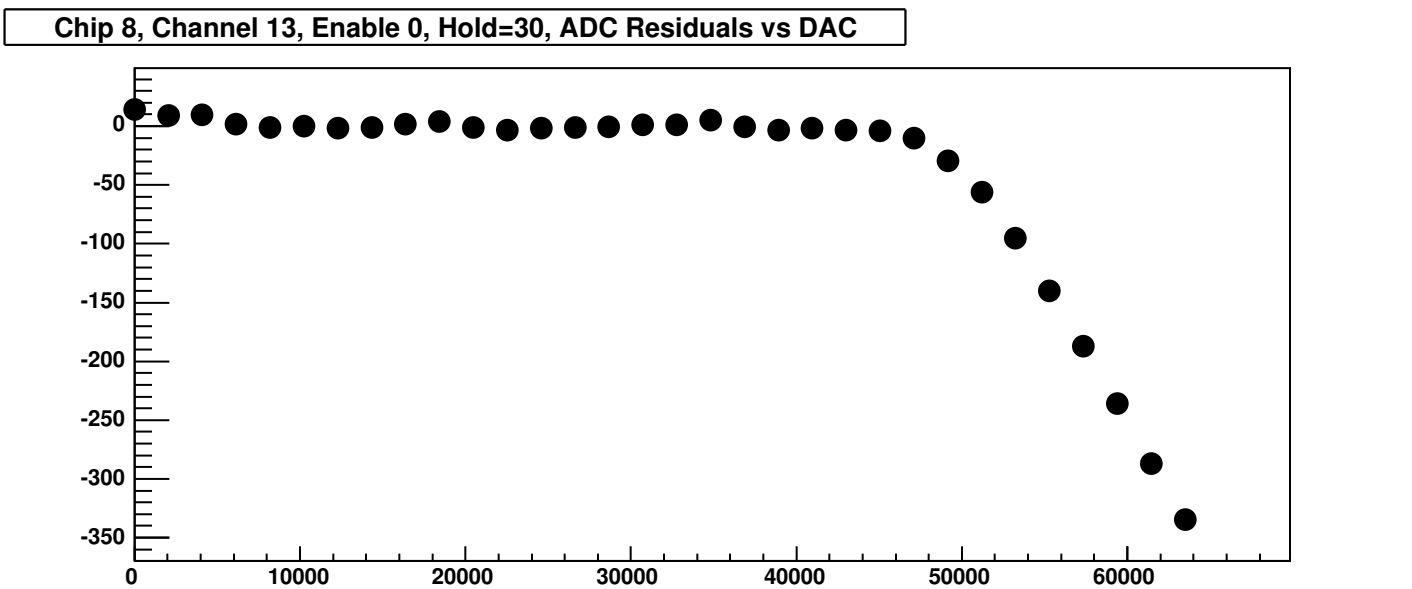


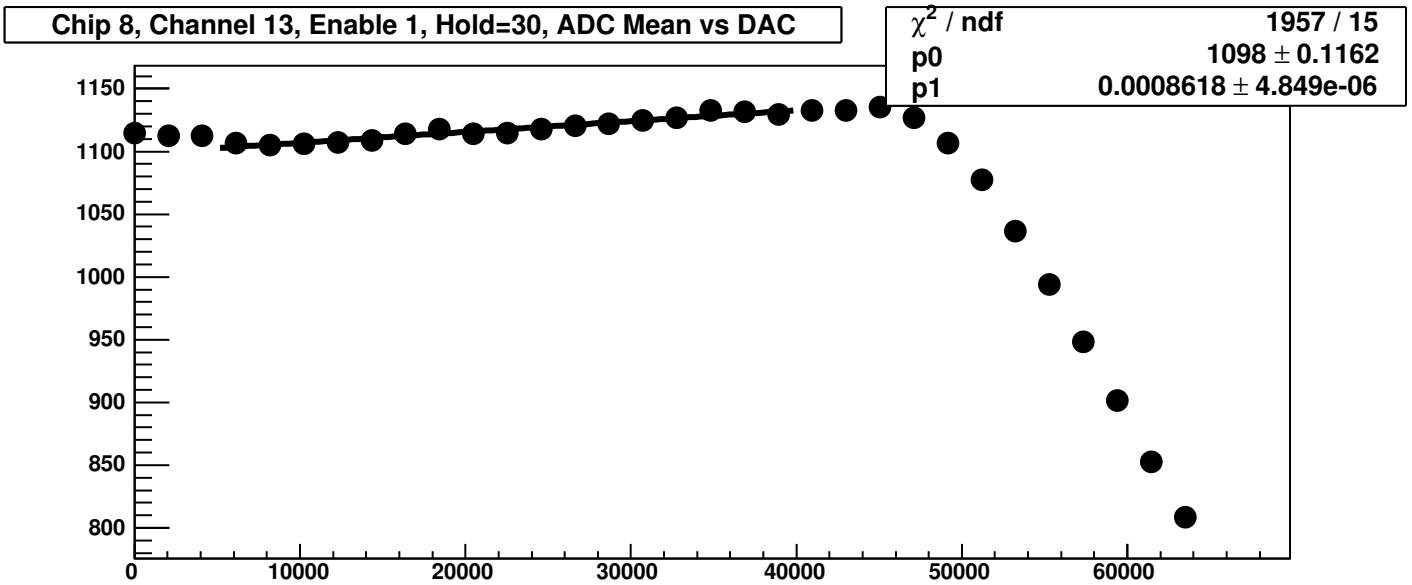
Chip 8, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

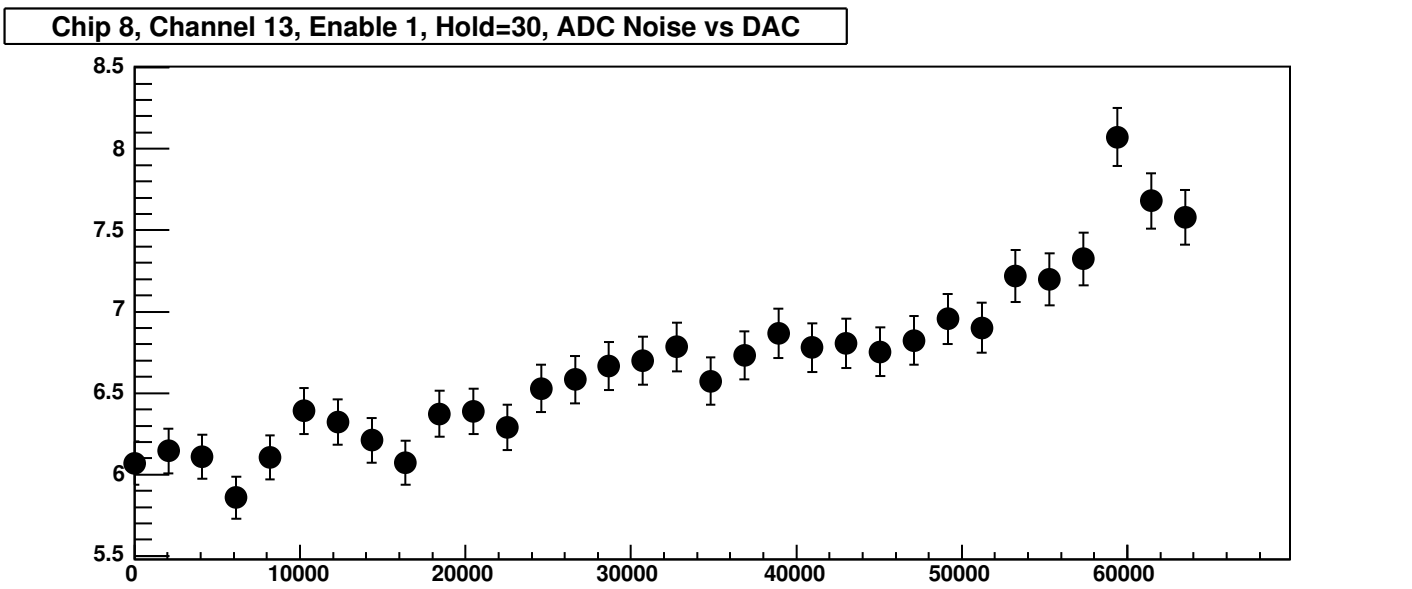


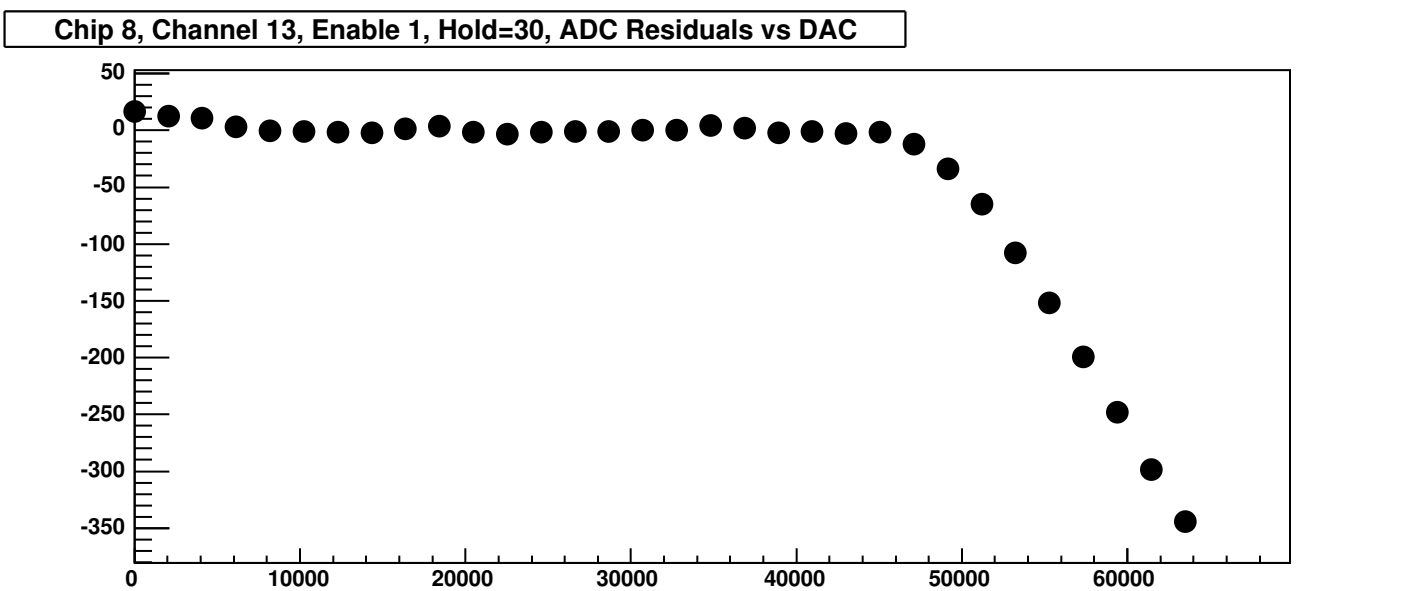

Chip 8, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2316 / 15
p0 1102 ± 0.1107
p1 -0.00047 ± 4.466e-06
1150
1100
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

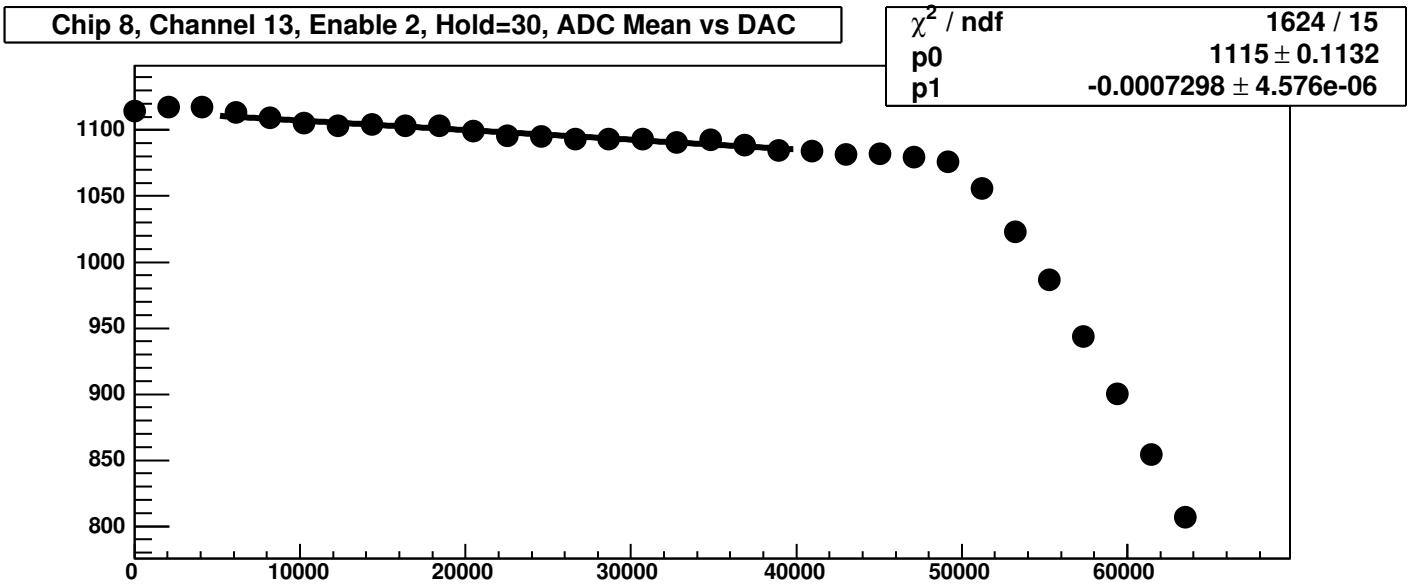

Chip 8, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

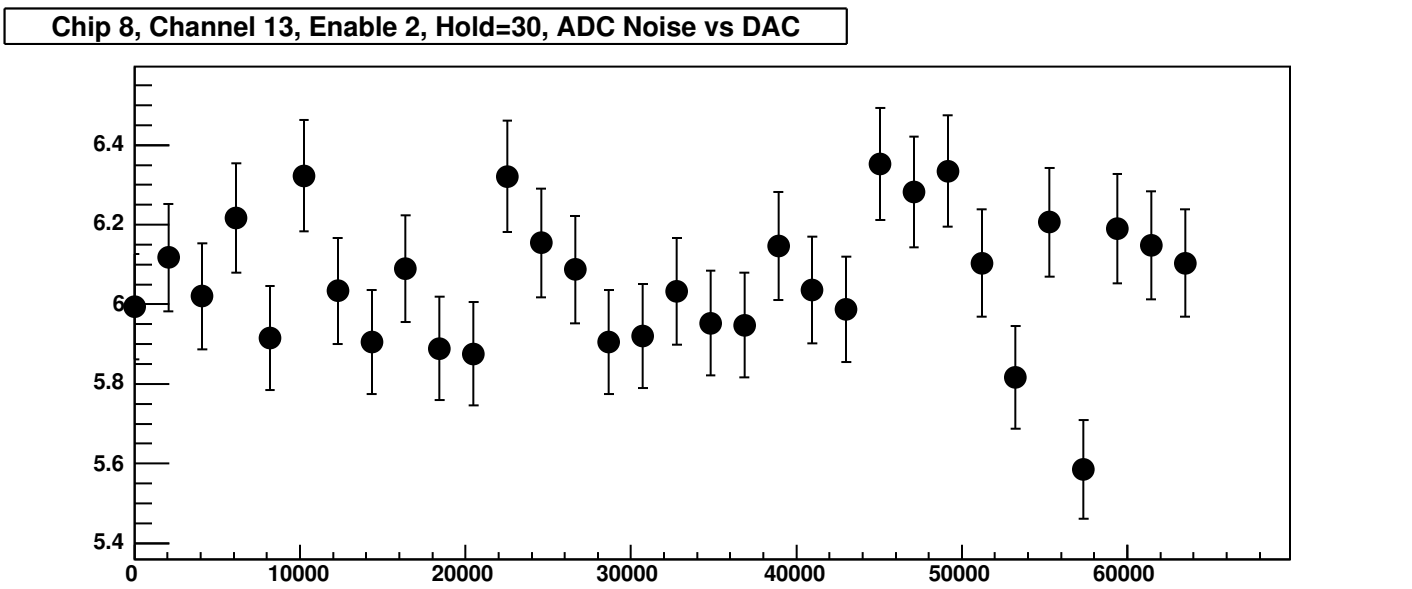

Chip 8, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

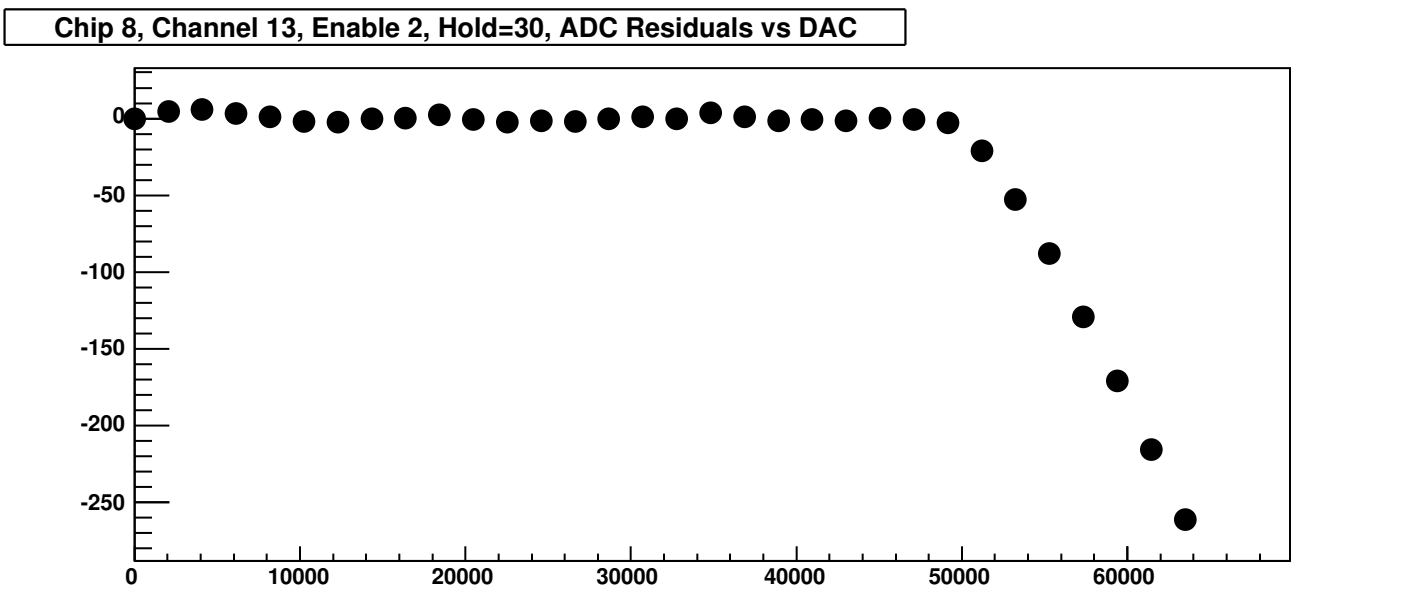

Chip 8, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1957 / 15
p0 1098 ± 0.1162
p1 0.0008618 ± 4.849e-06
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC
8.5
8
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000

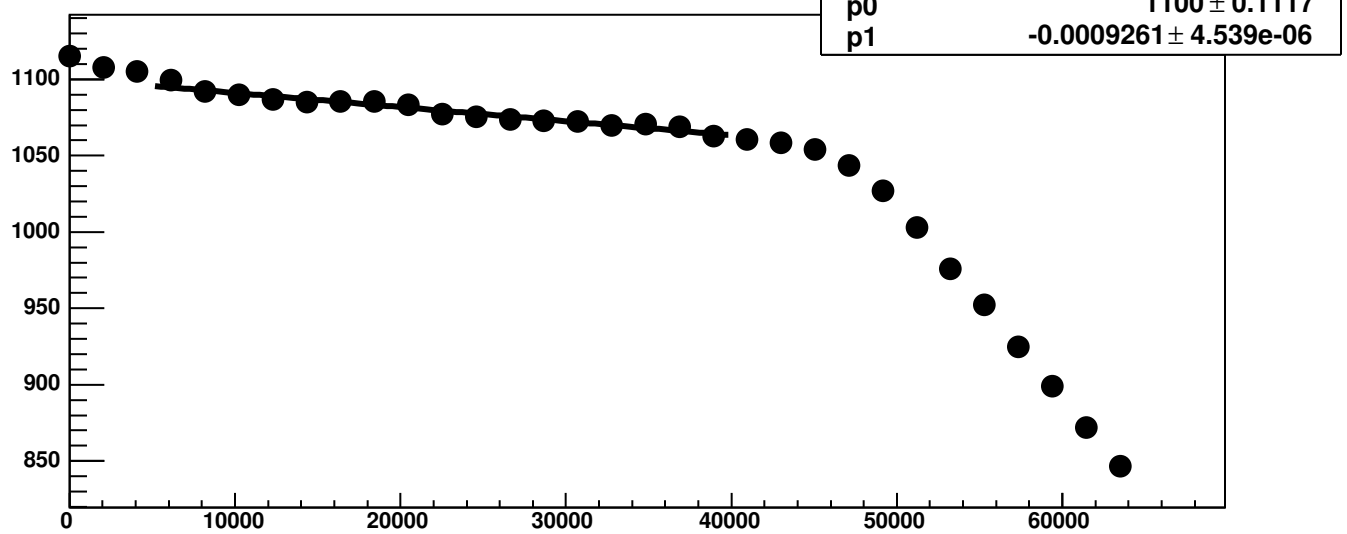

Chip 8, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1624 / 15
p0 1115 ± 0.1132
p1 -0.0007298 ± 4.576e-06
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

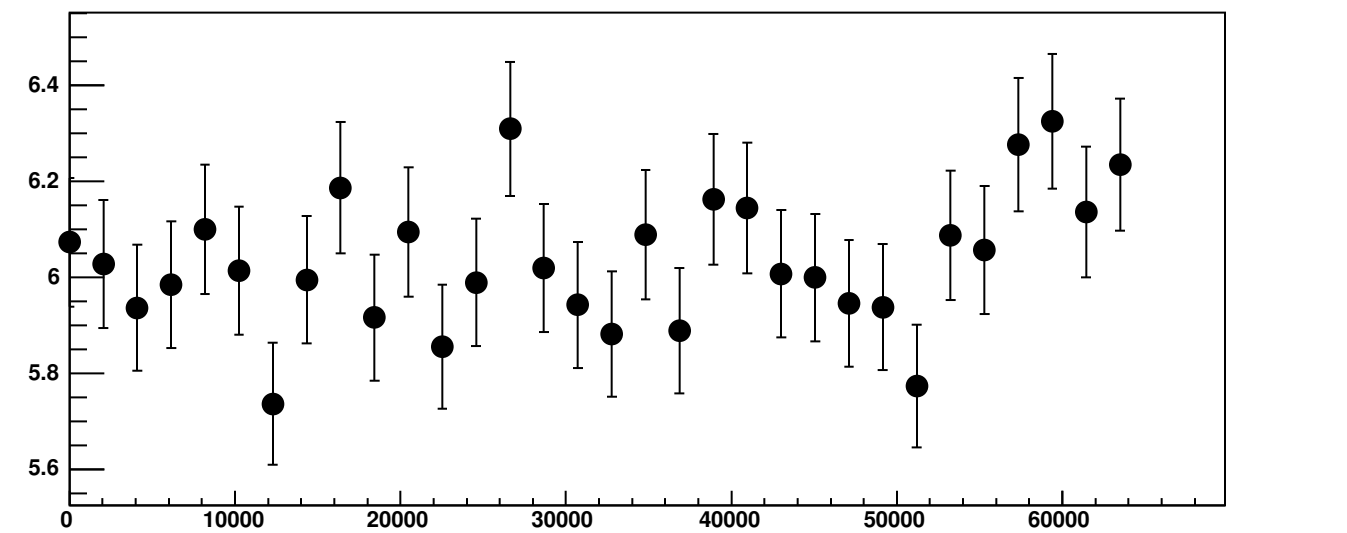

Chip 8, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

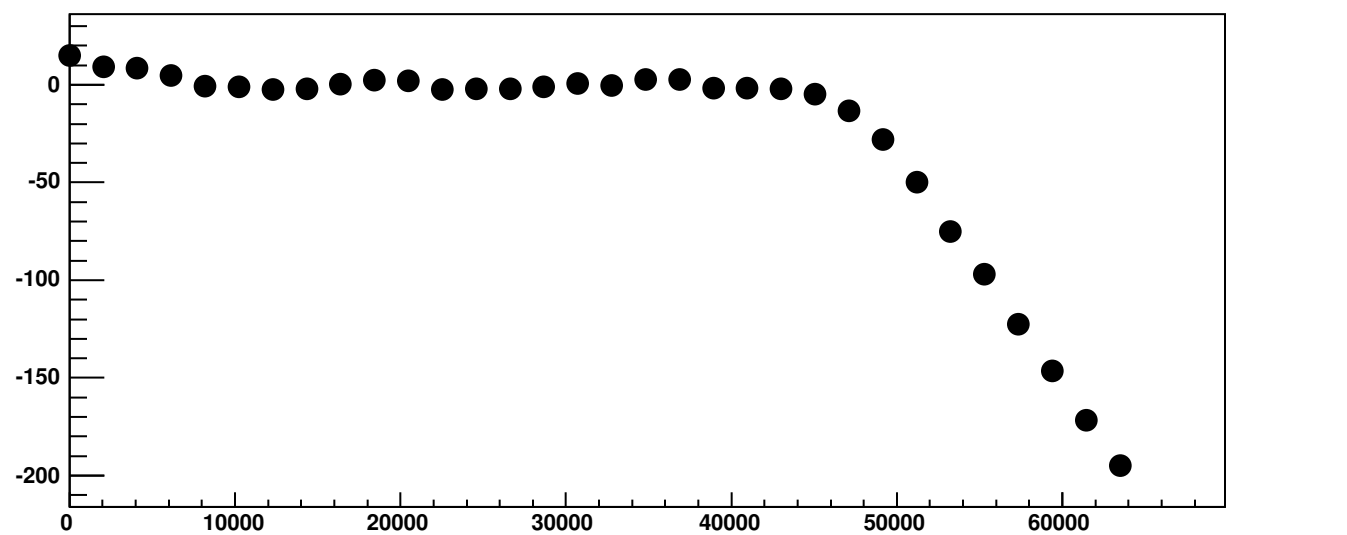
Chip 8, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC

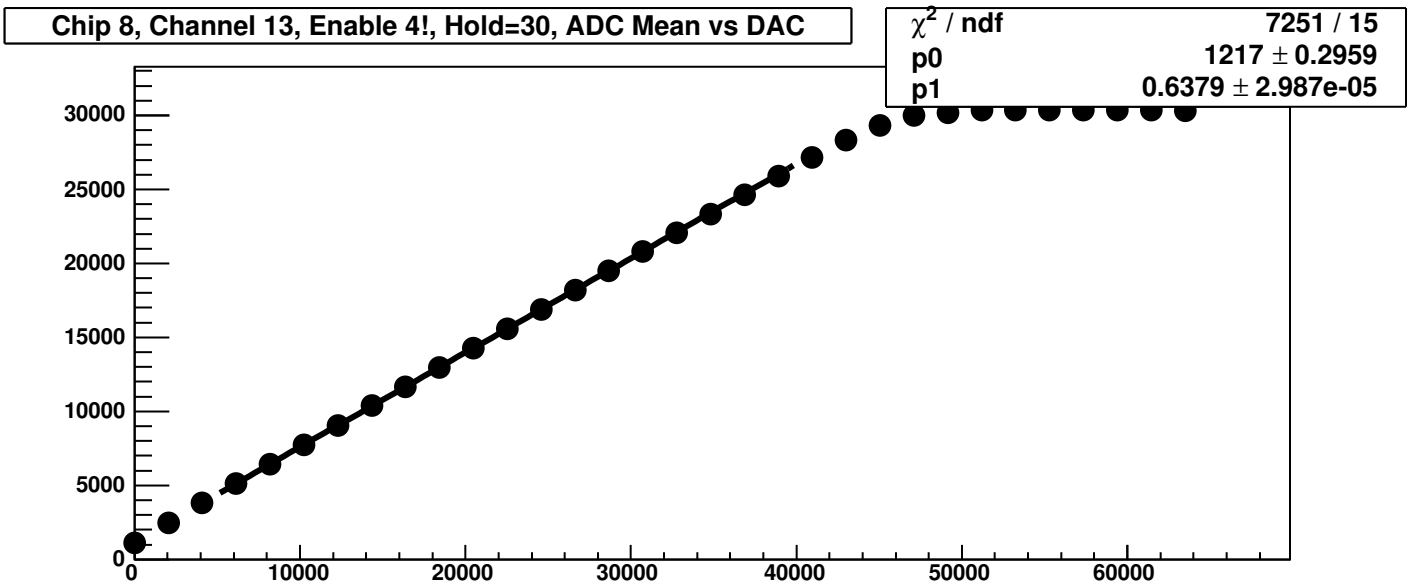


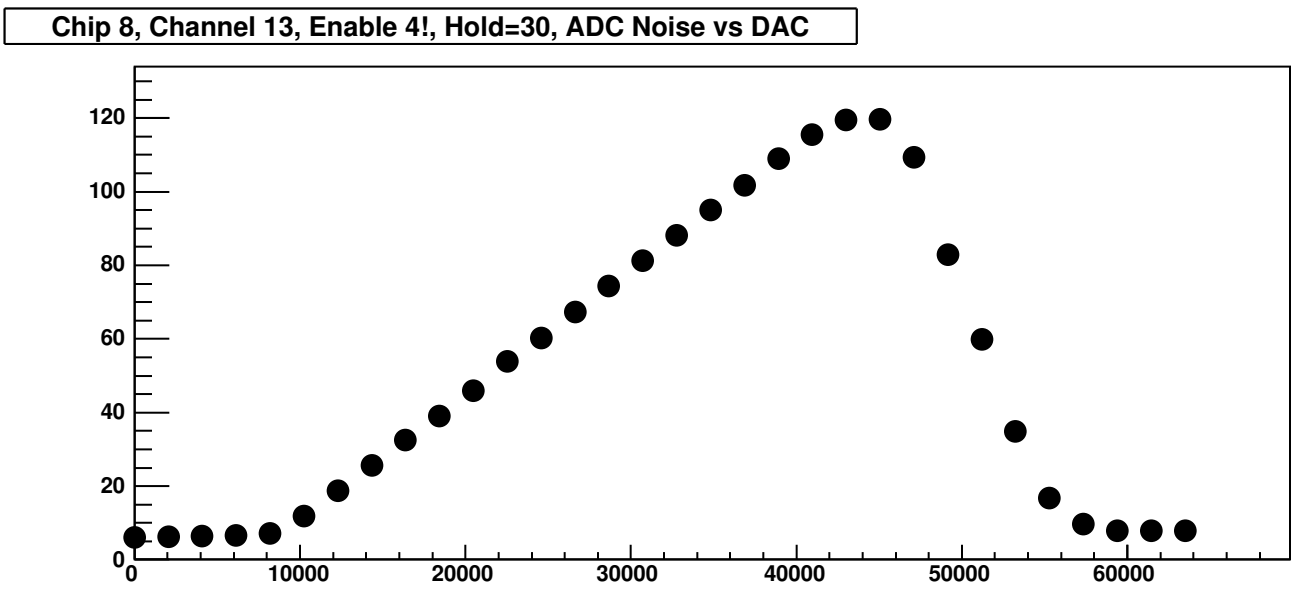
Chip 8, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC

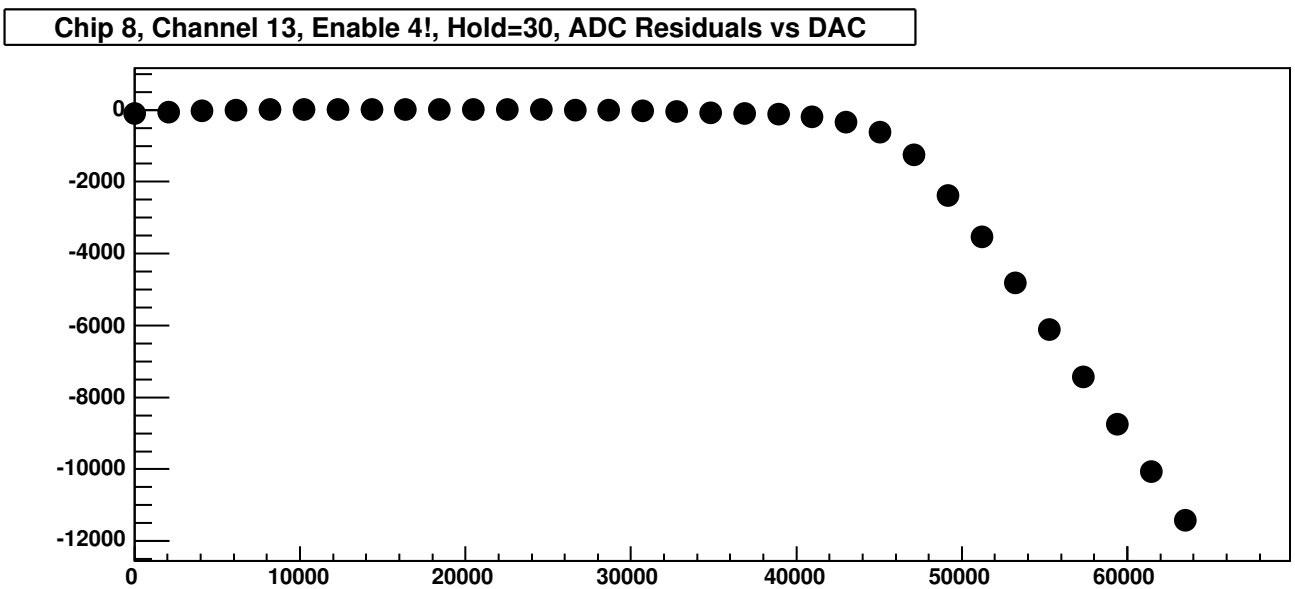


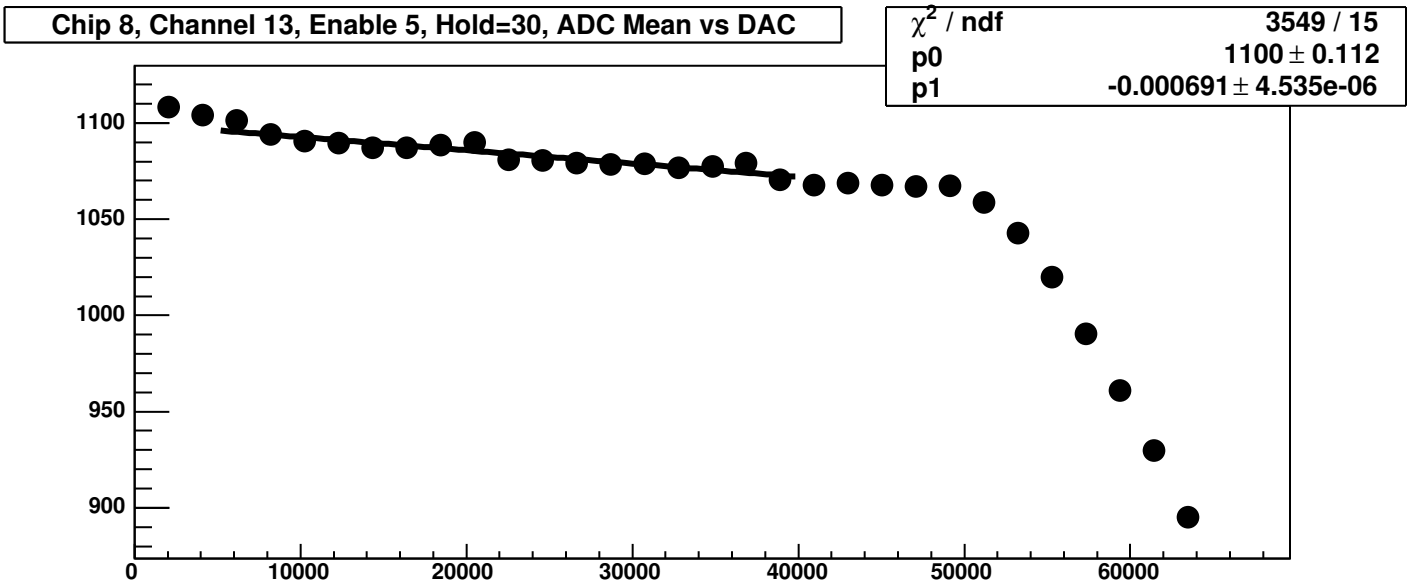
Chip 8, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC

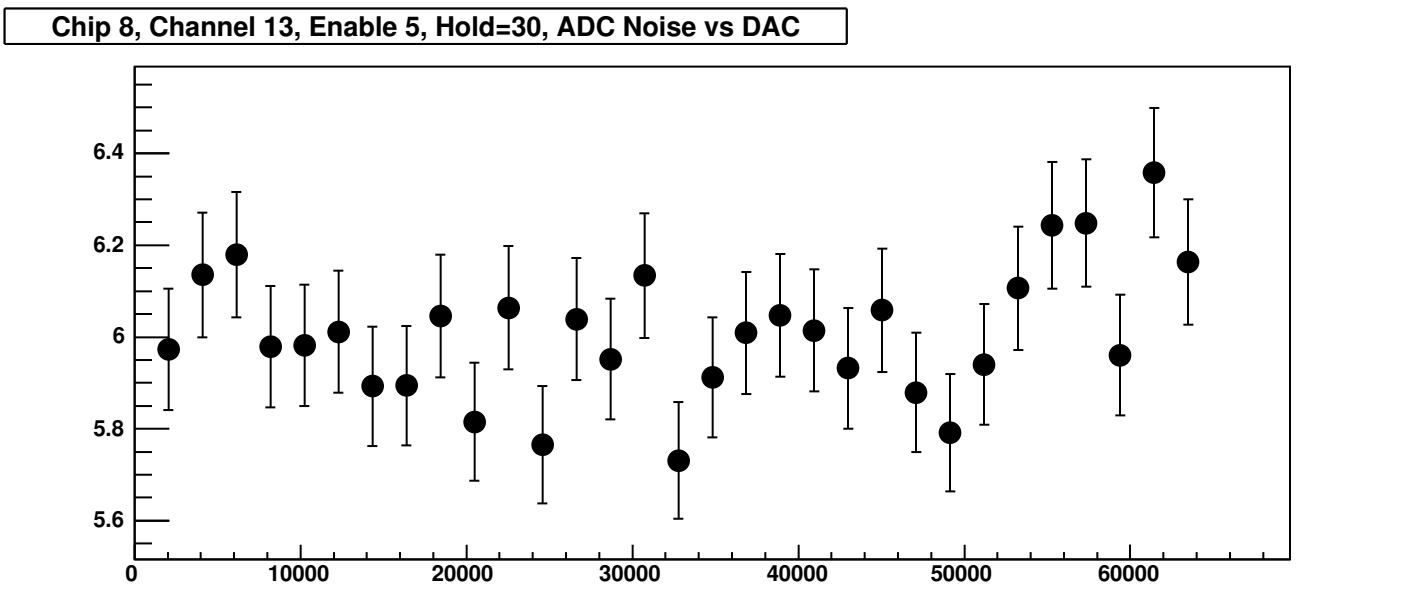


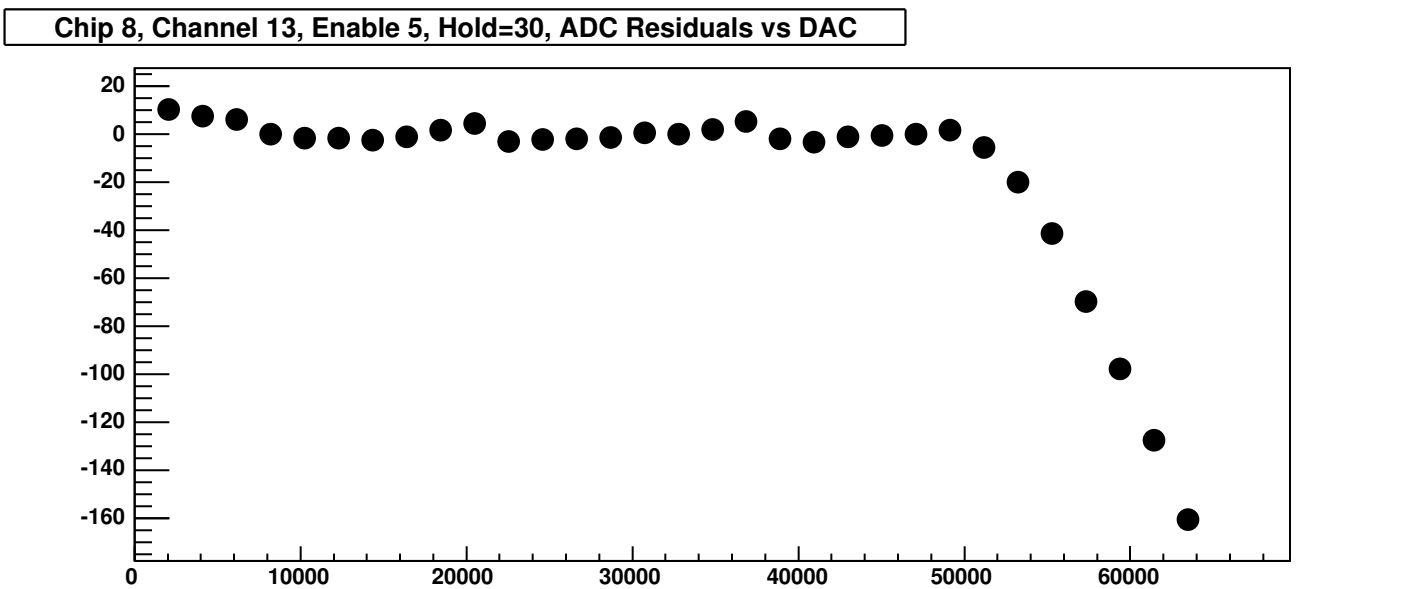

Chip 8, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 7251 / 15
p0 1217 ± 0.2959
p1 0.6379 ± 2.987e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

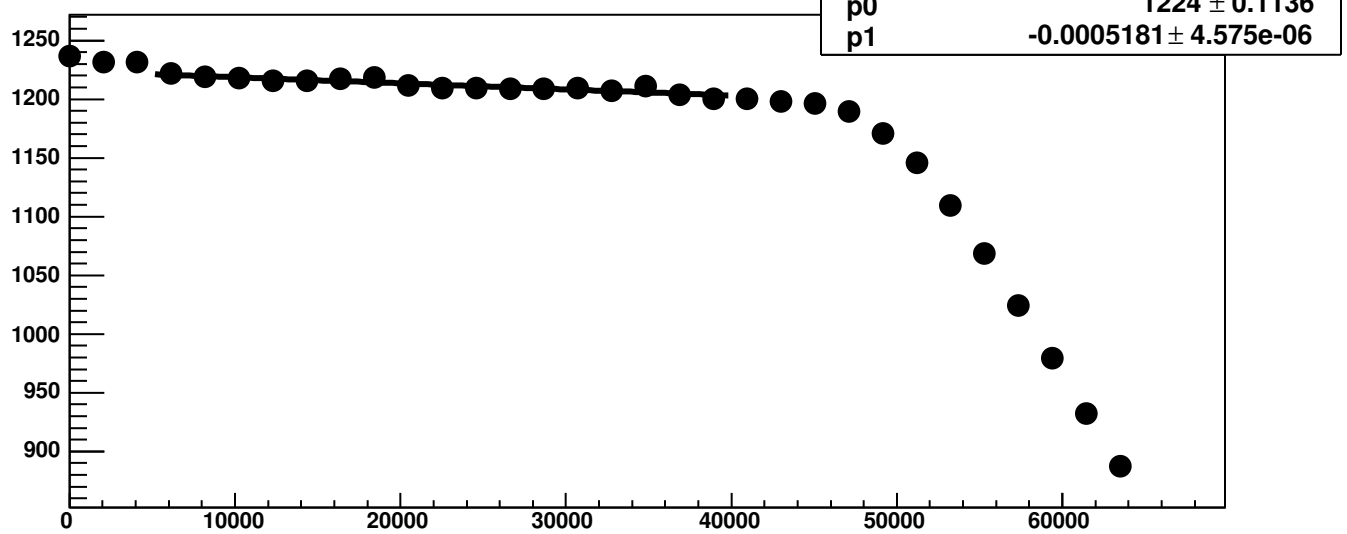

Chip 8, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3549 / 15
p0 1100 ± 0.112
p1 -0.000691 ± 4.535e-06
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

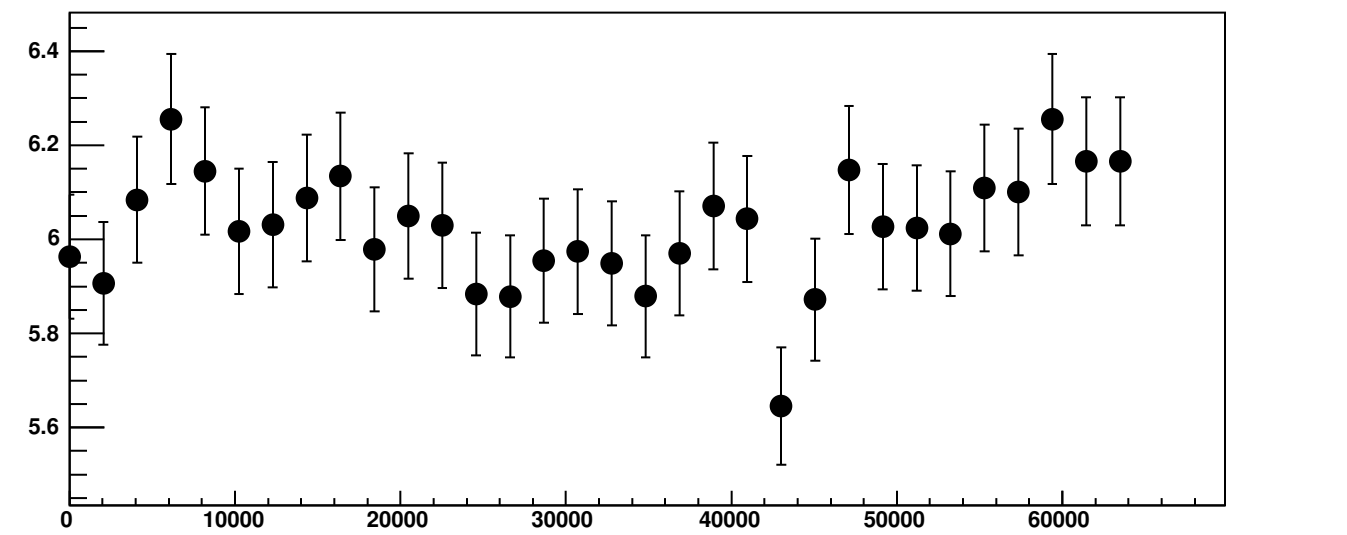

Chip 8, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

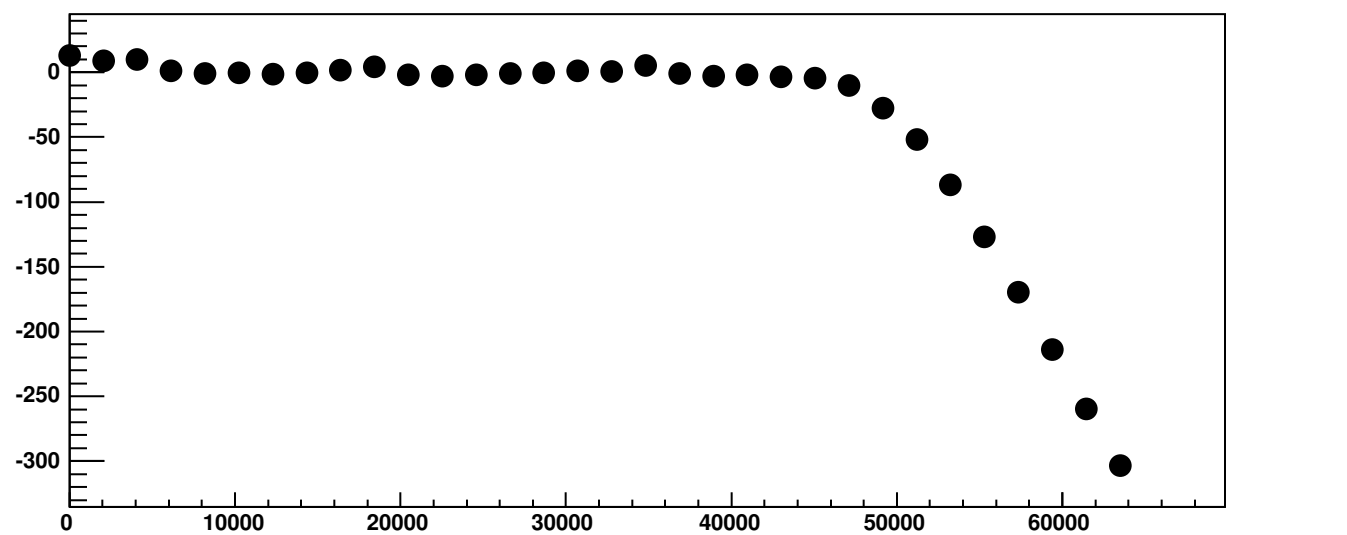
Chip 8, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC

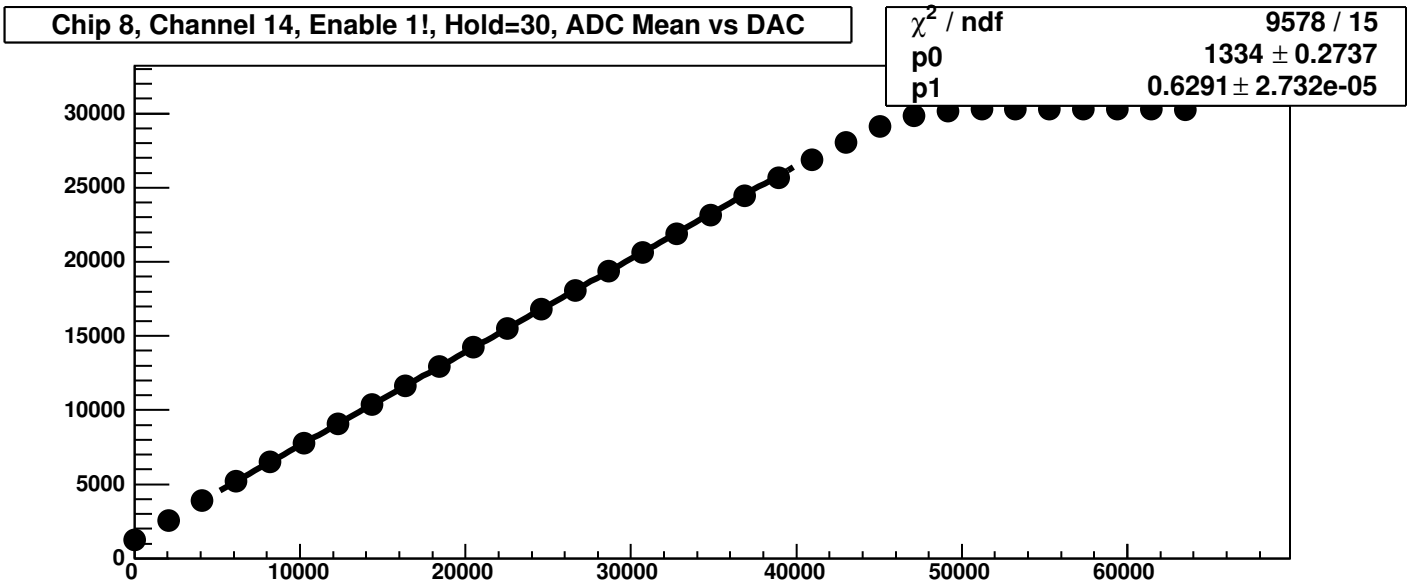


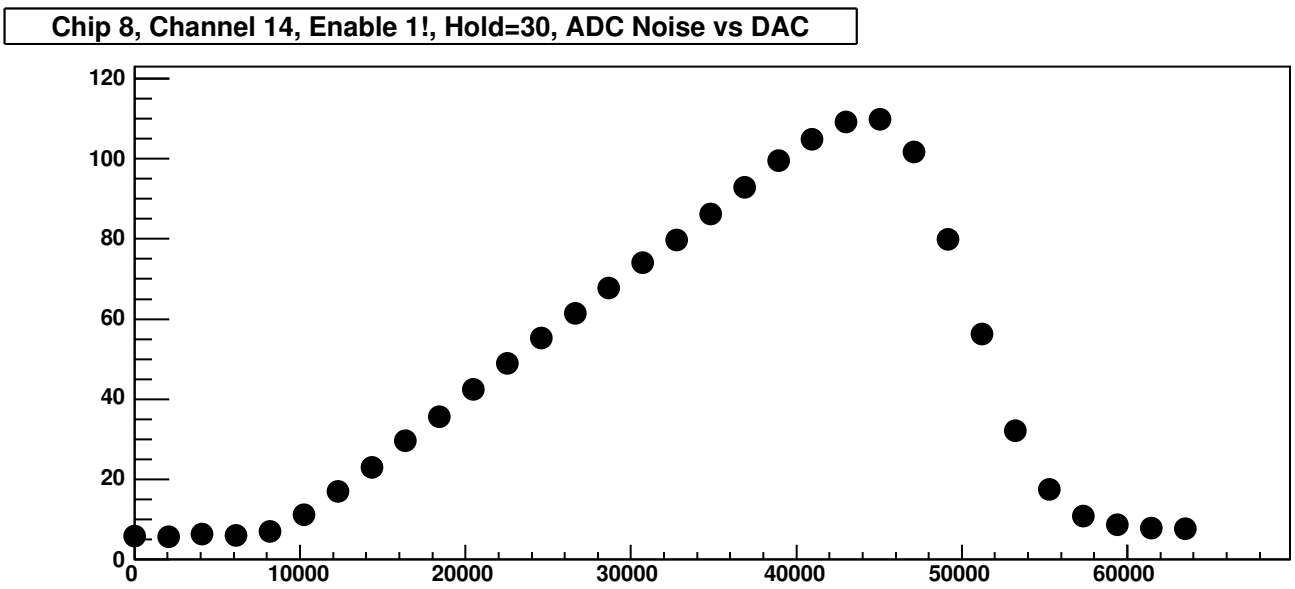
Chip 8, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC

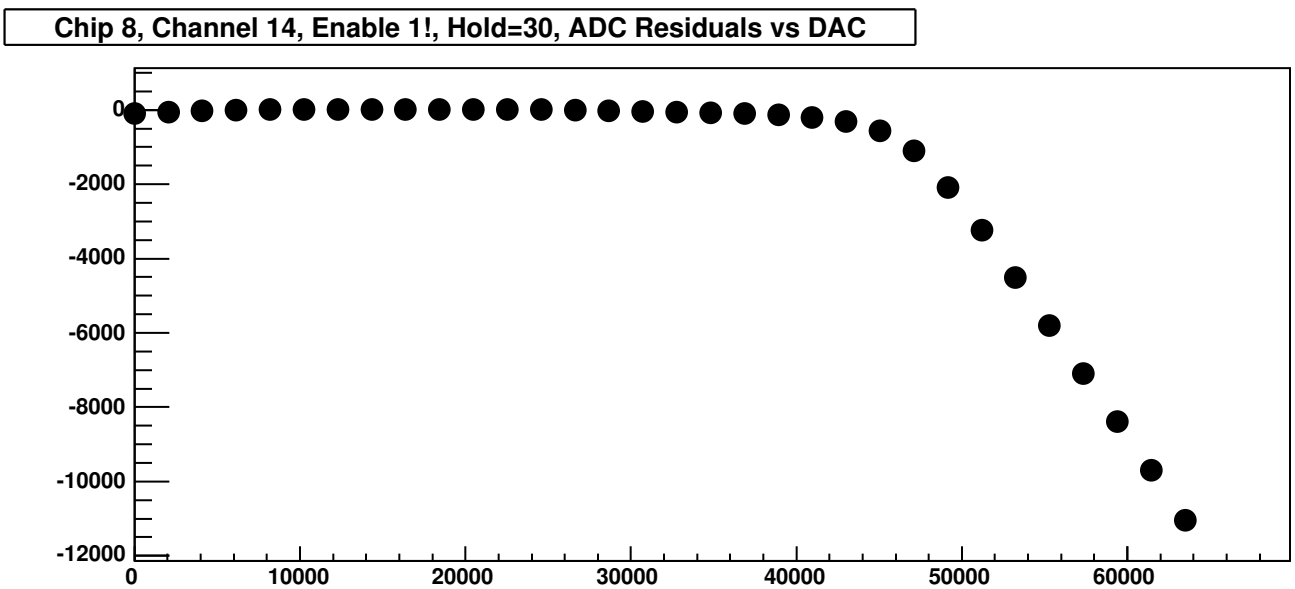


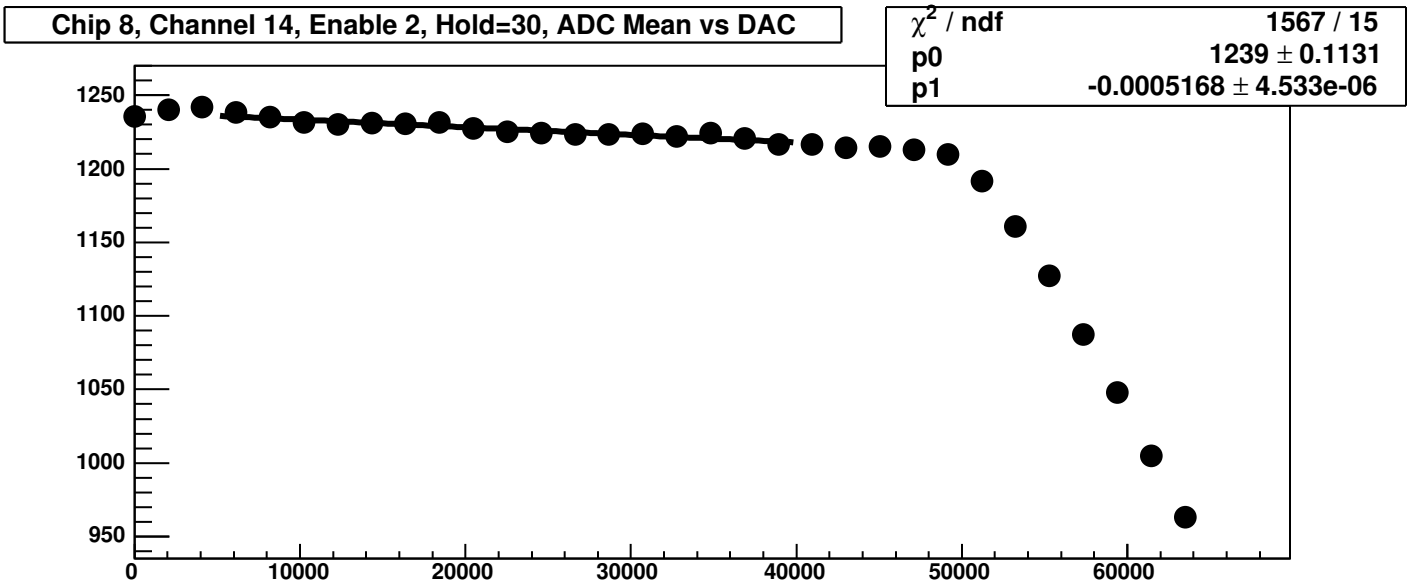
Chip 8, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC

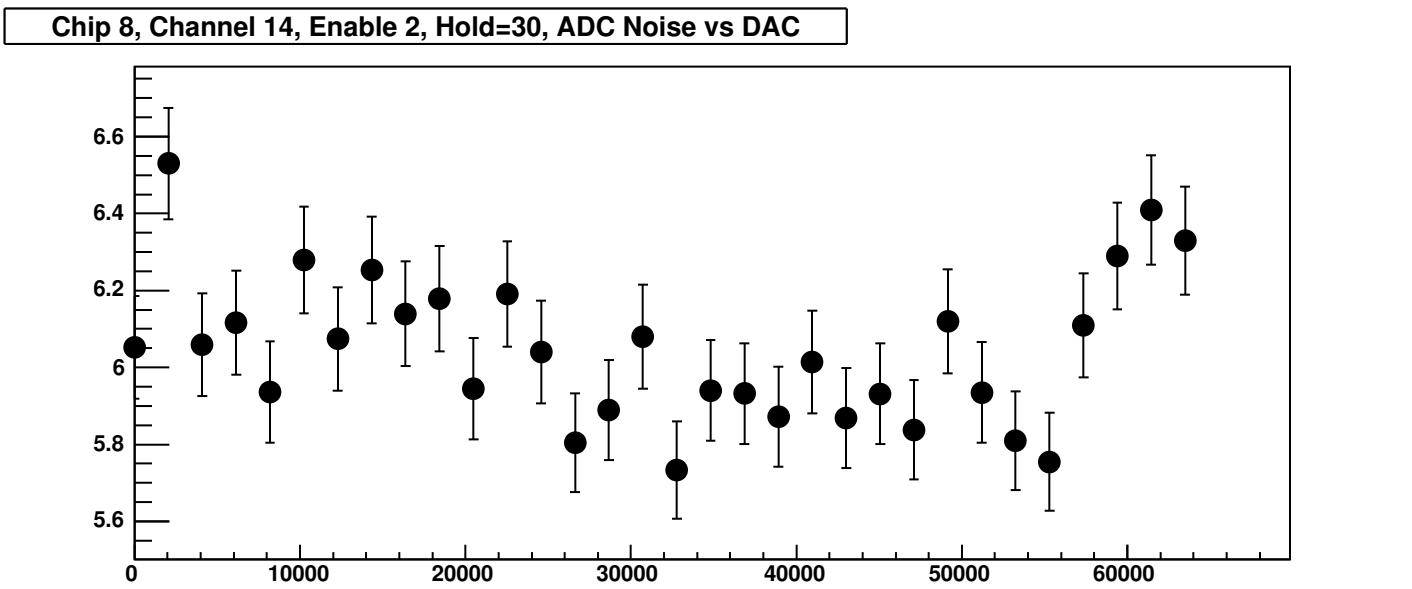


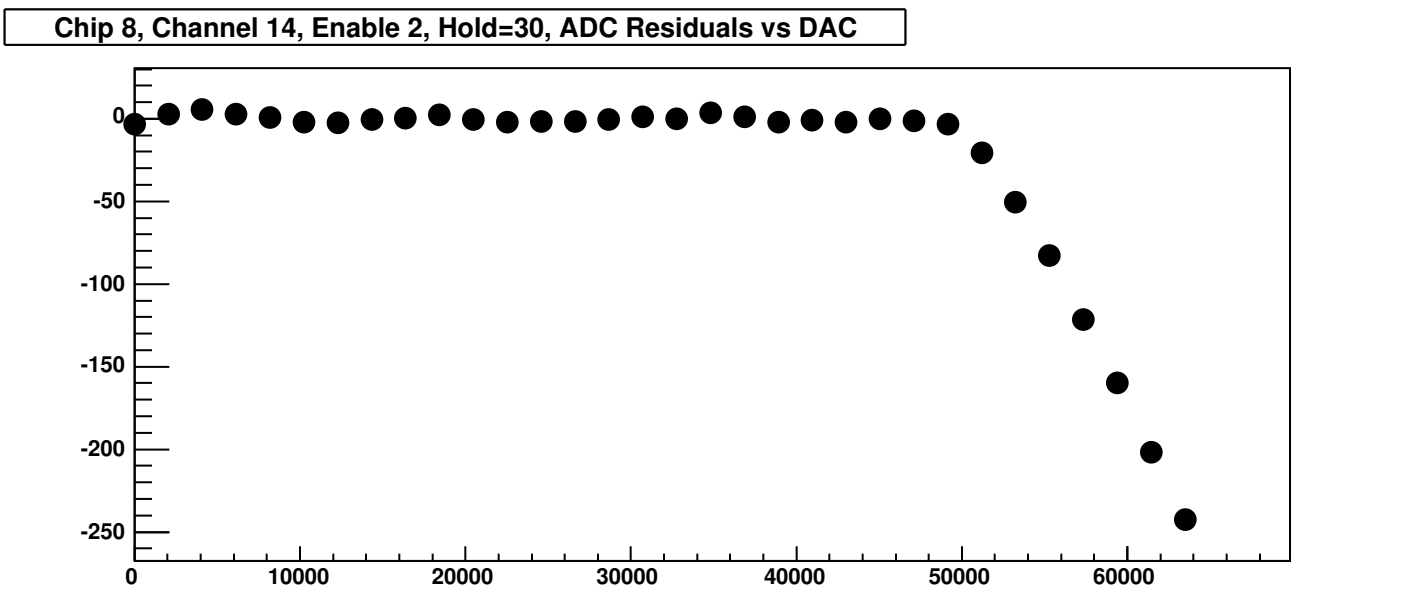

Chip 8, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 9578 / 15
p0 1334 ± 0.2737
p1 0.6291 ± 2.732e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

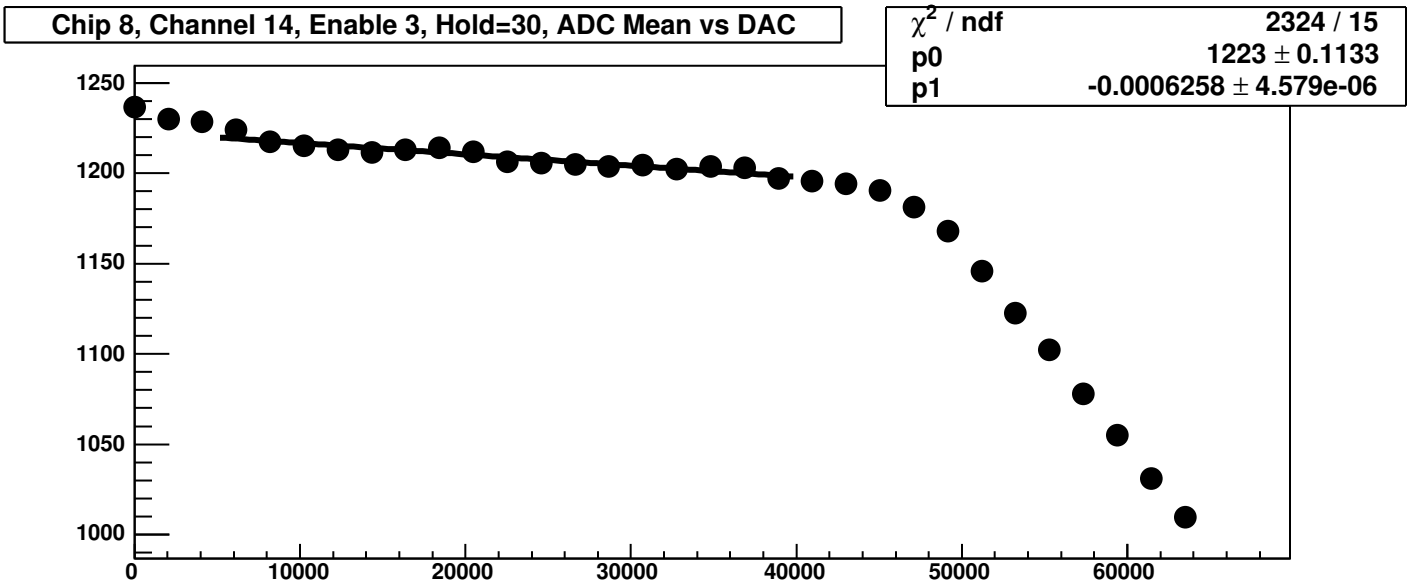

Chip 8, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

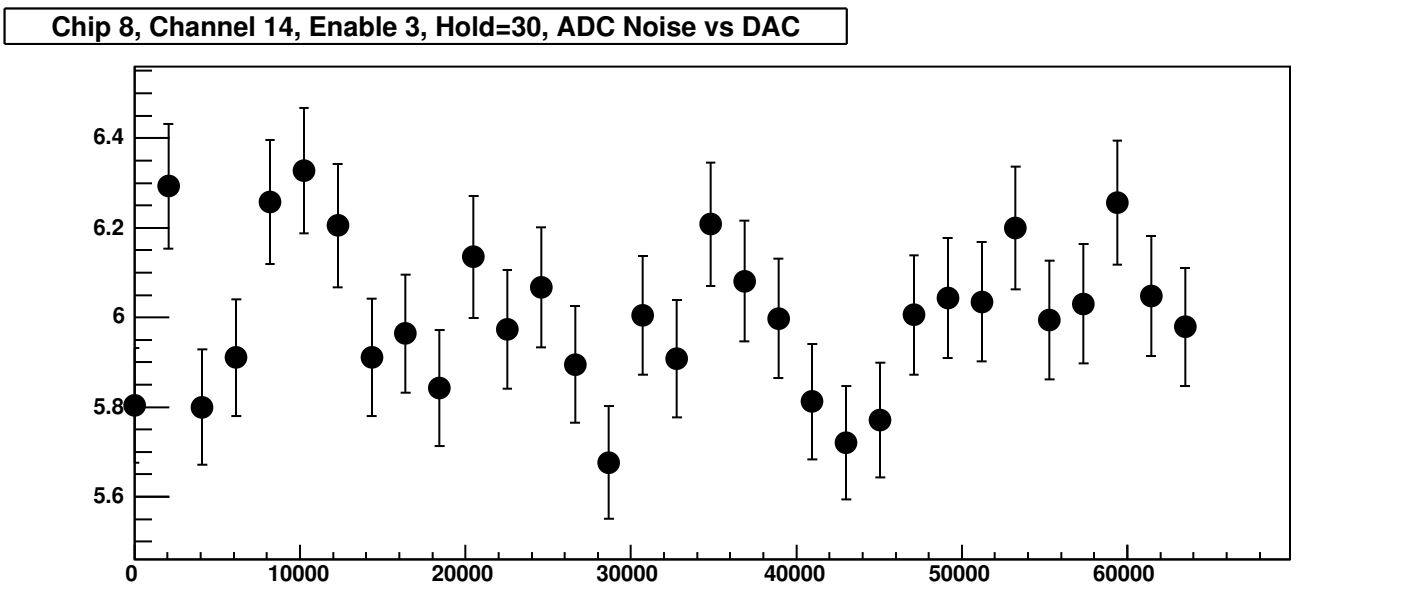

Chip 8, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

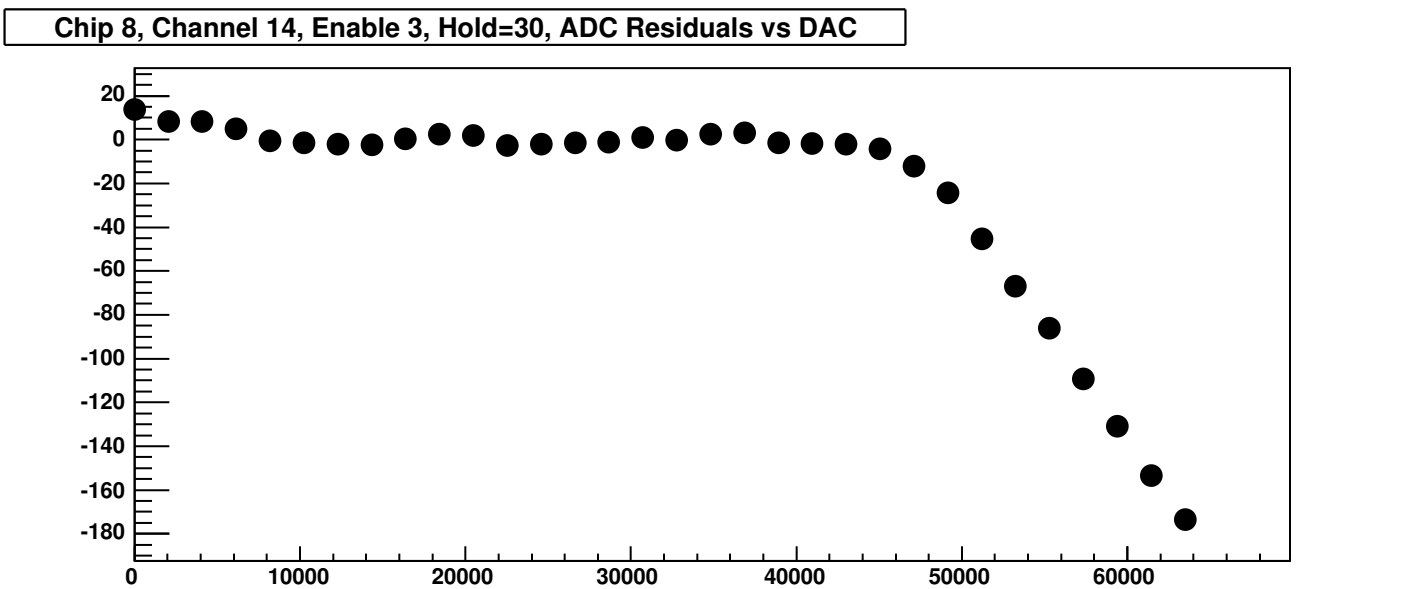

Chip 8, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1567 / 15
p0 1239 ± 0.1131
p1 -0.0005168 ± 4.533e-06
1250
1200
1150
1100
1050
1000
950
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

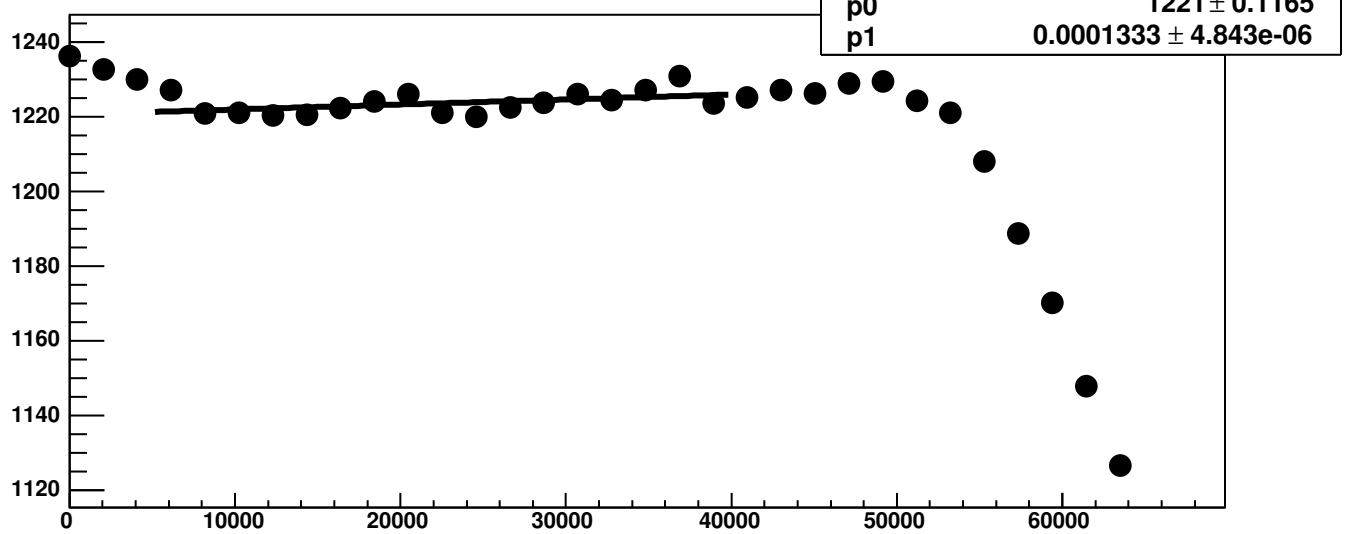

Chip 8, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2324 / 15
p0 1223 ± 0.1133
p1 -0.0006258 ± 4.579e-06
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

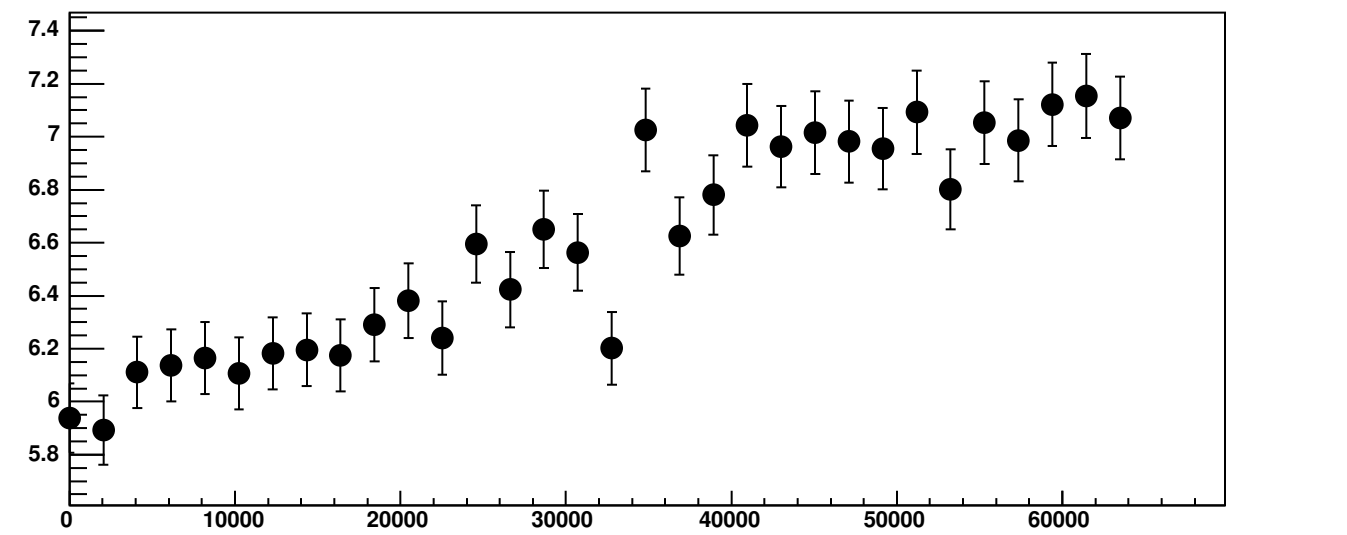

Chip 8, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

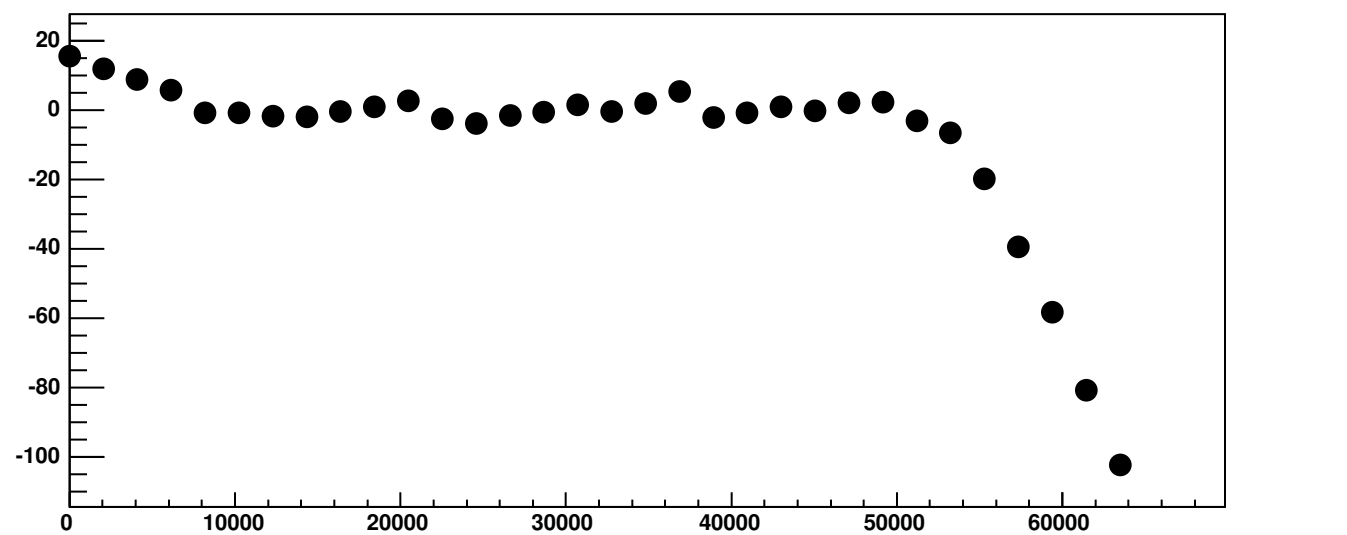
Chip 8, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC

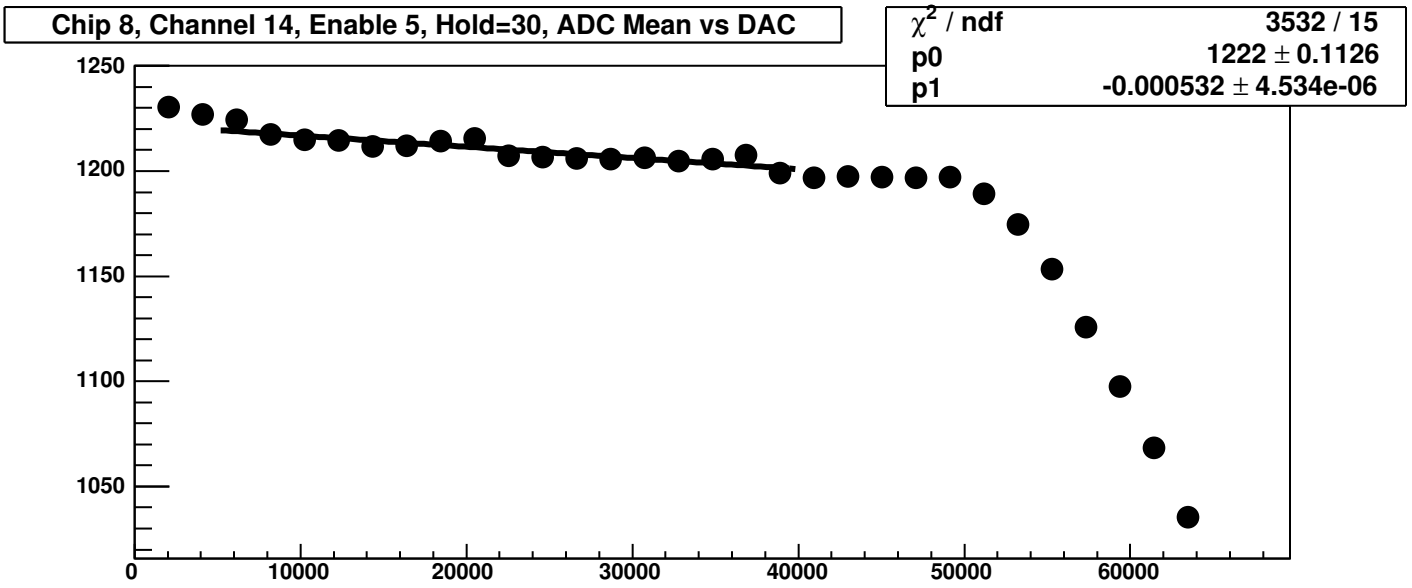


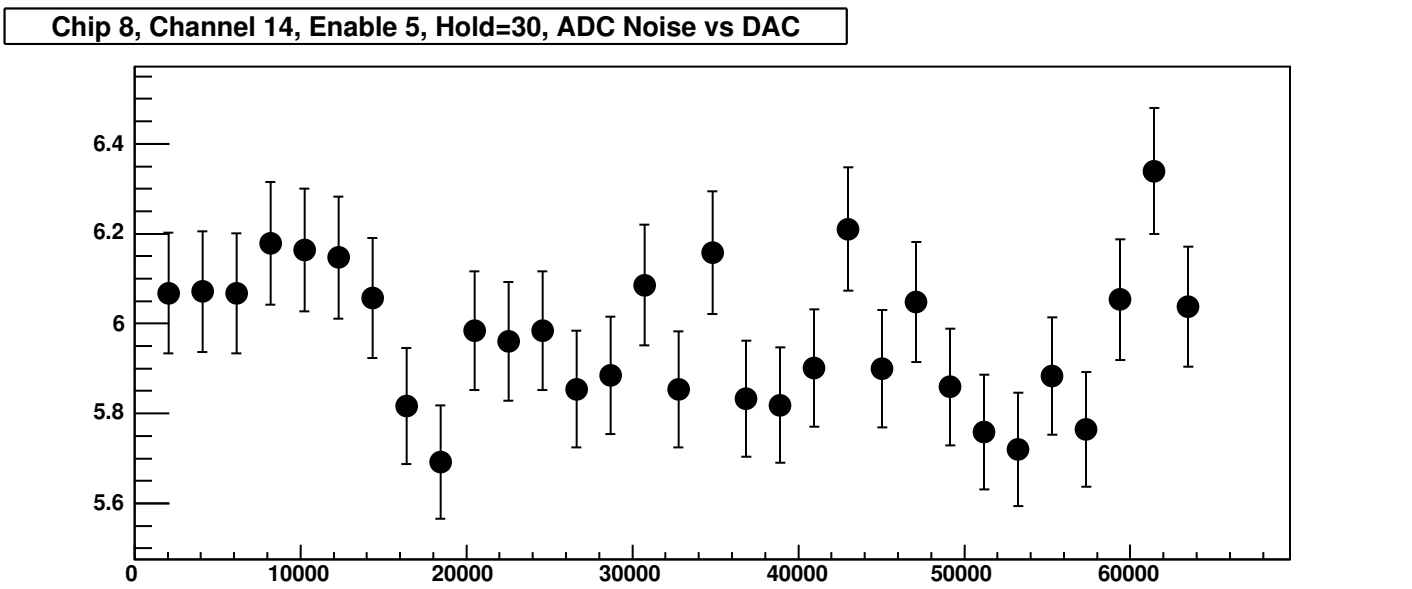
Chip 8, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC

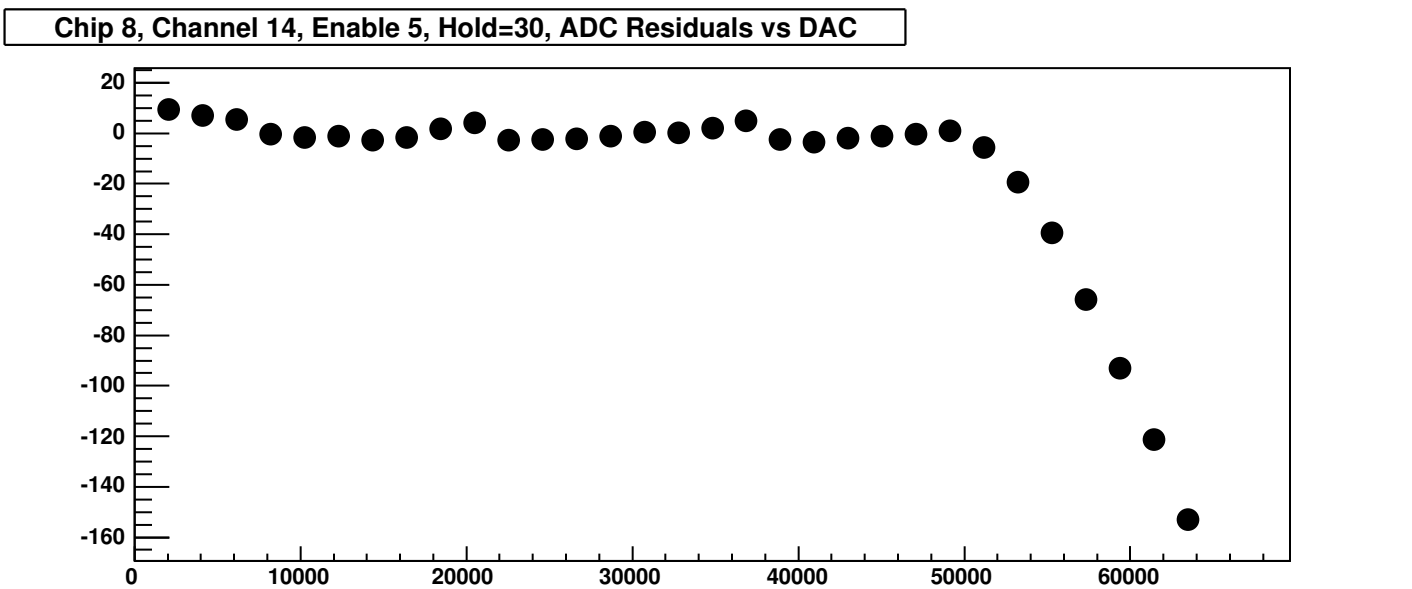


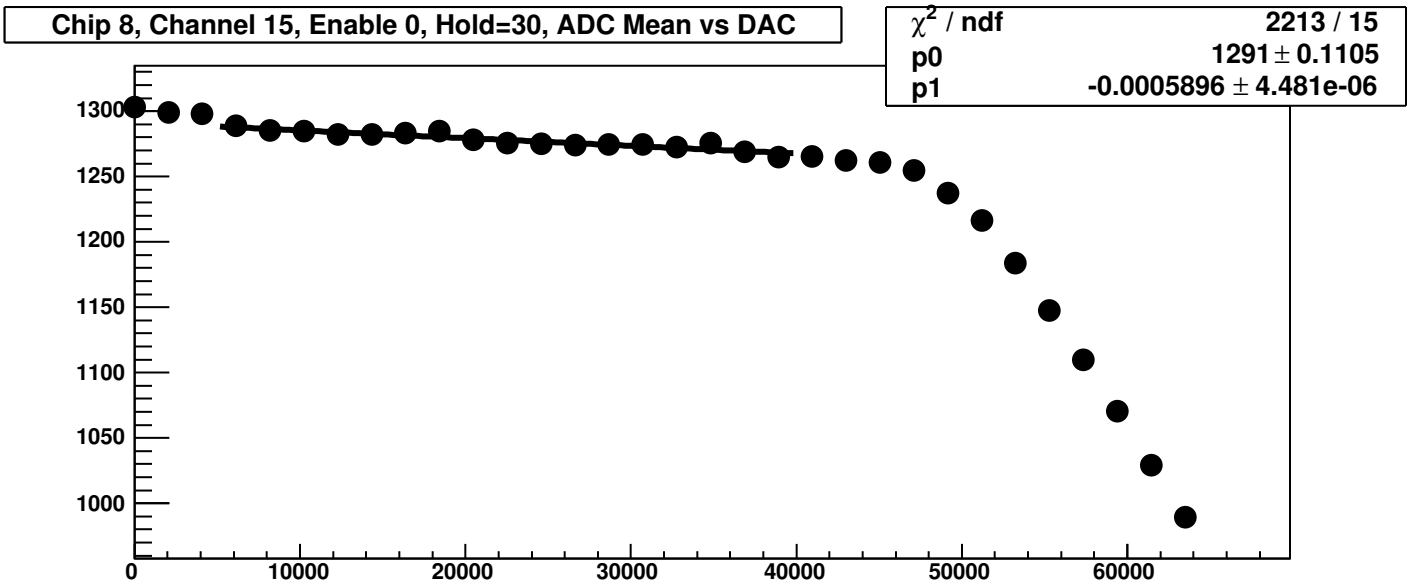
Chip 8, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC

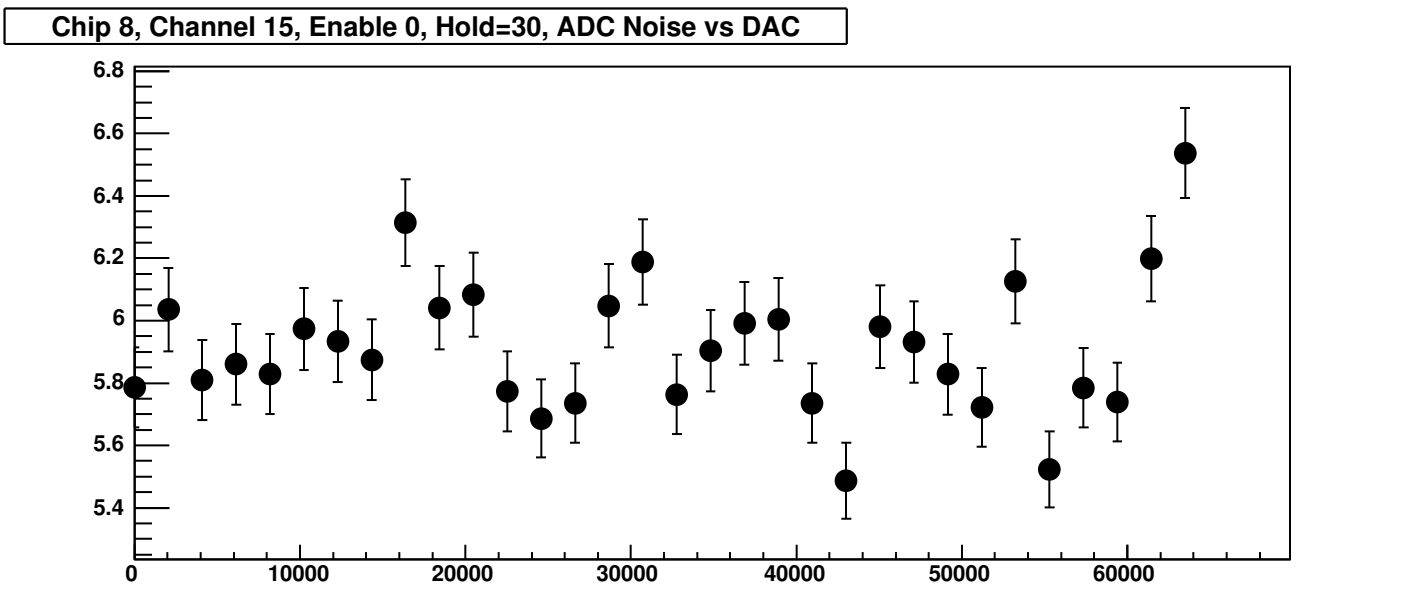


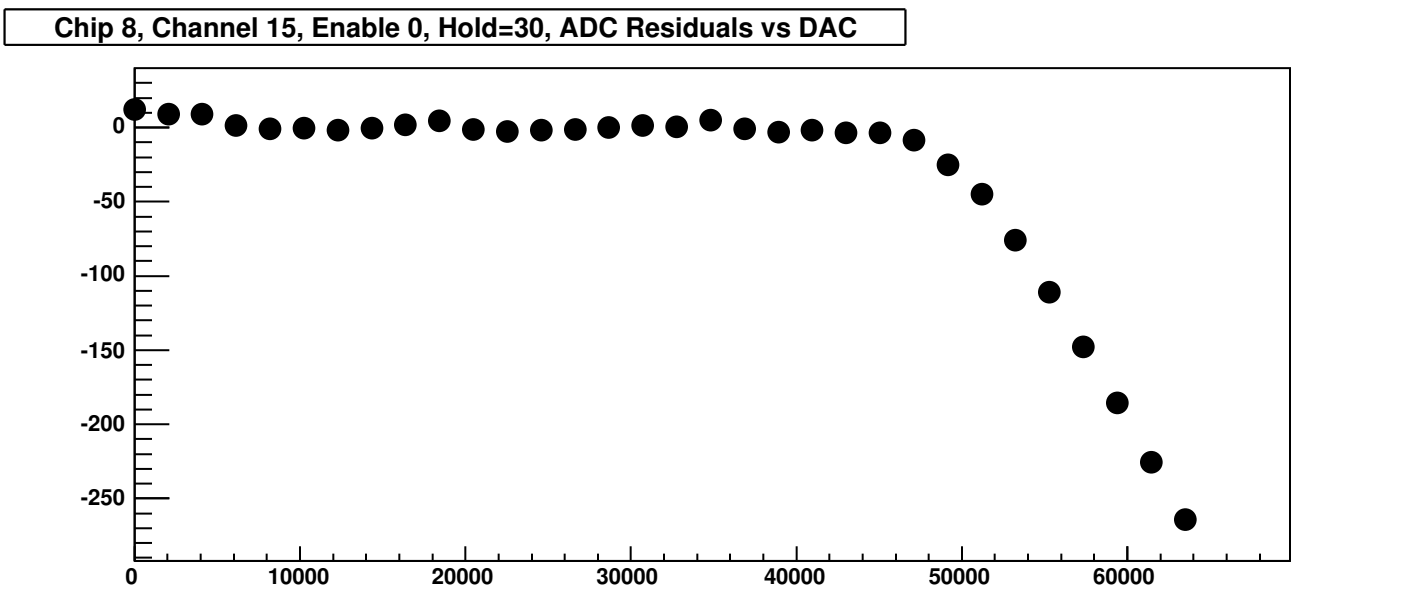

Chip 8, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3532 / 15
p0 1222 ± 0.1126
p1 -0.000532 ± 4.534e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

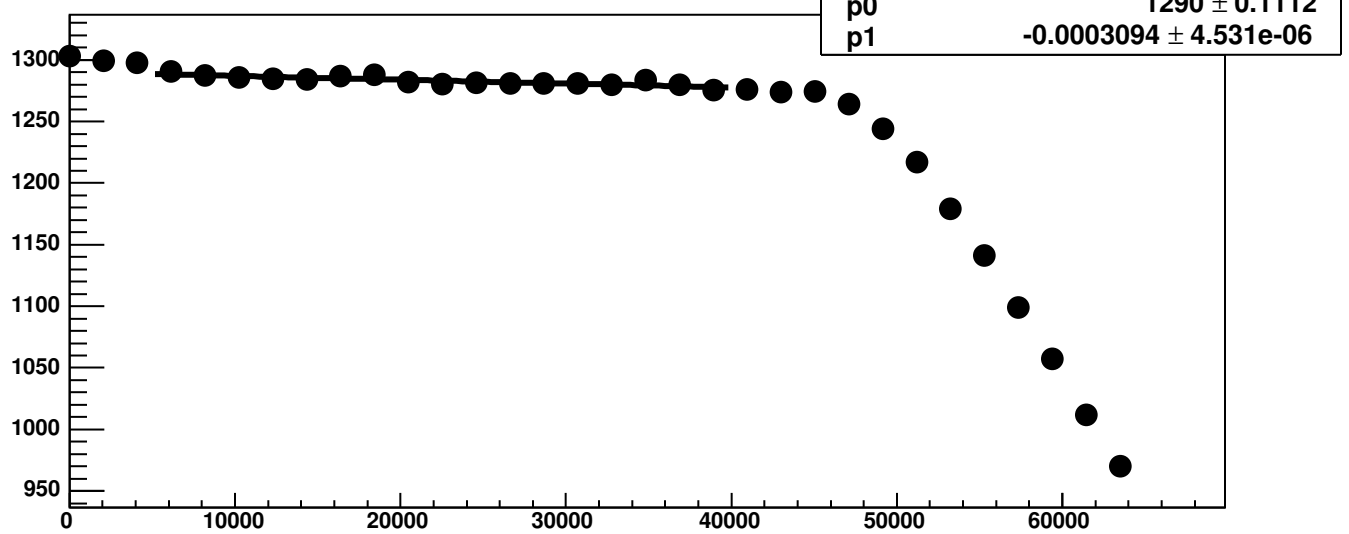

Chip 8, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2213 / 15
p0 1291 ± 0.1105
p1 -0.0005896 ± 4.481e-06
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

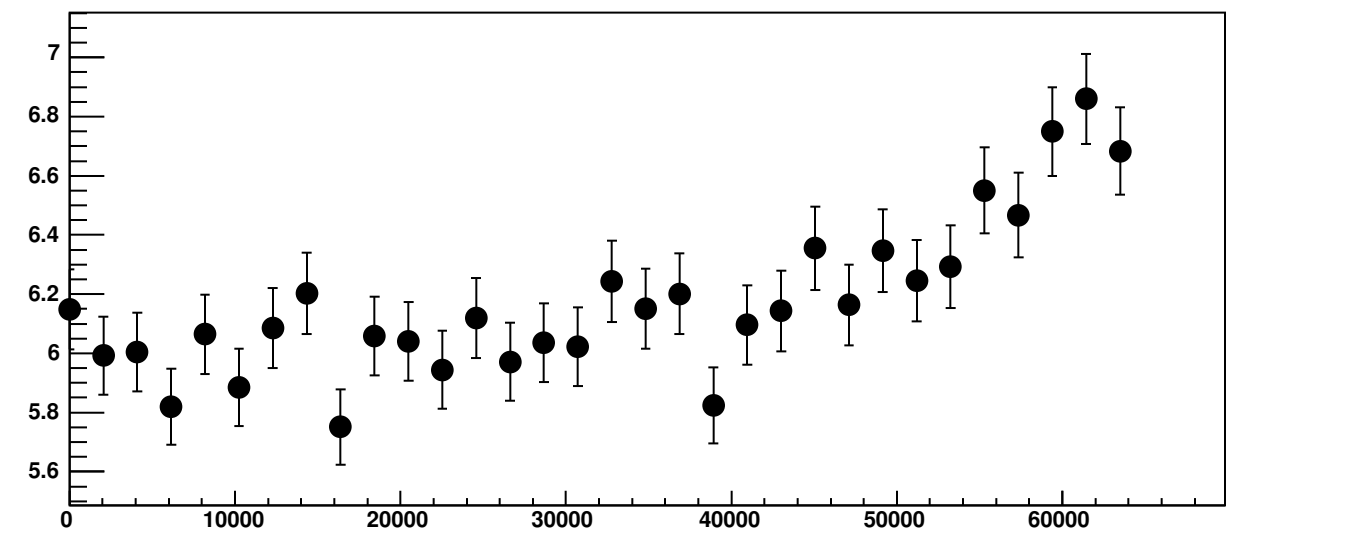

Chip 8, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

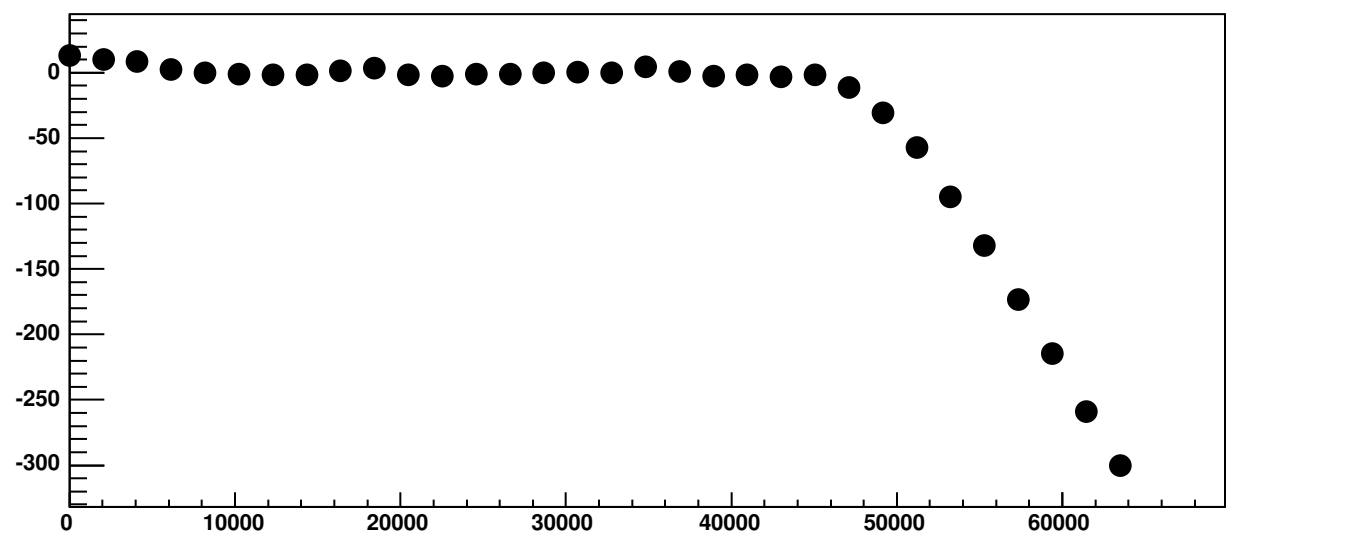
Chip 8, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

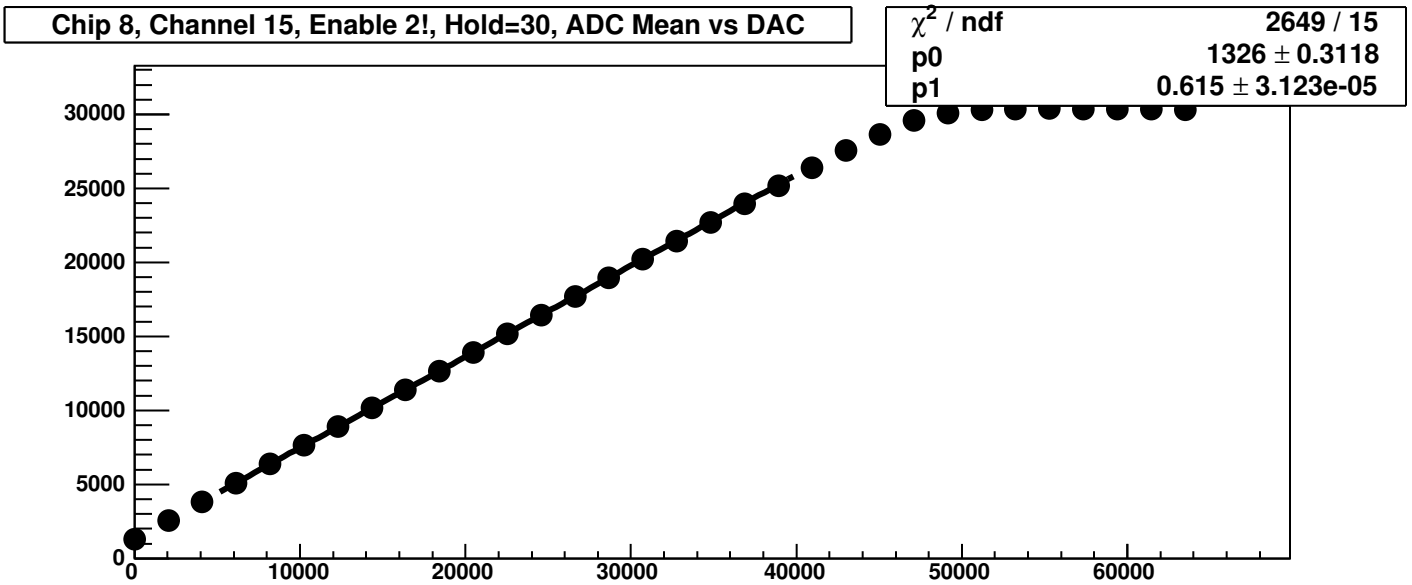


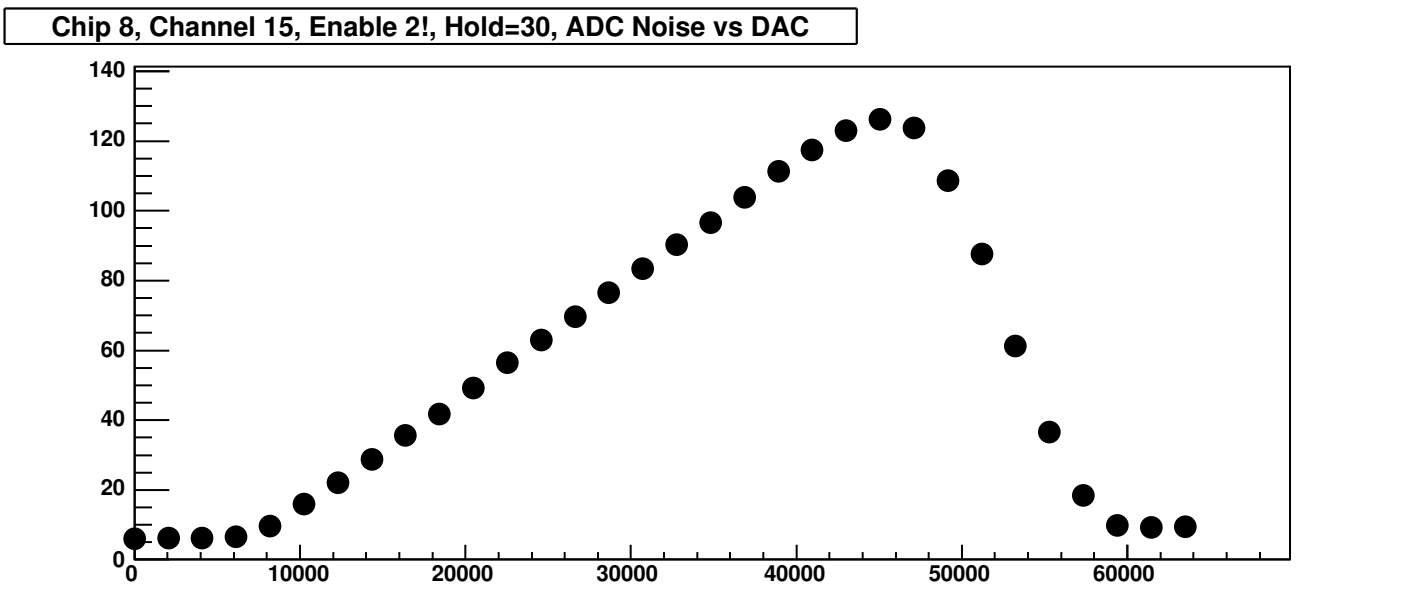
Chip 8, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC

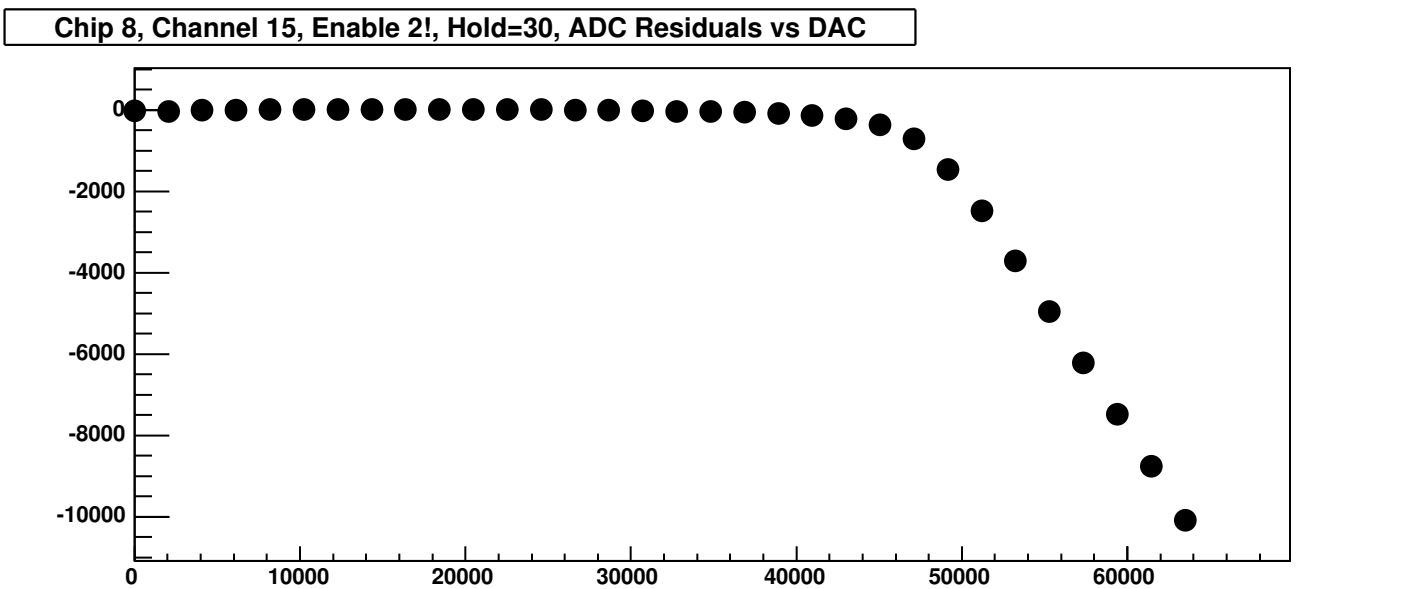


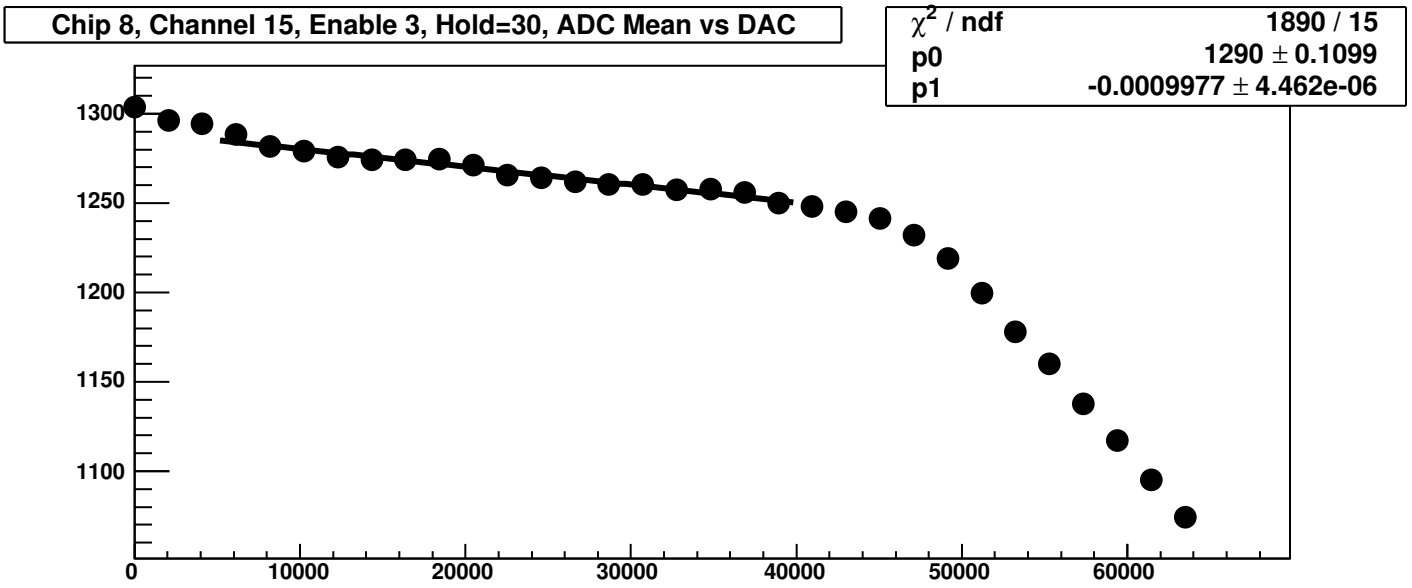
Chip 8, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

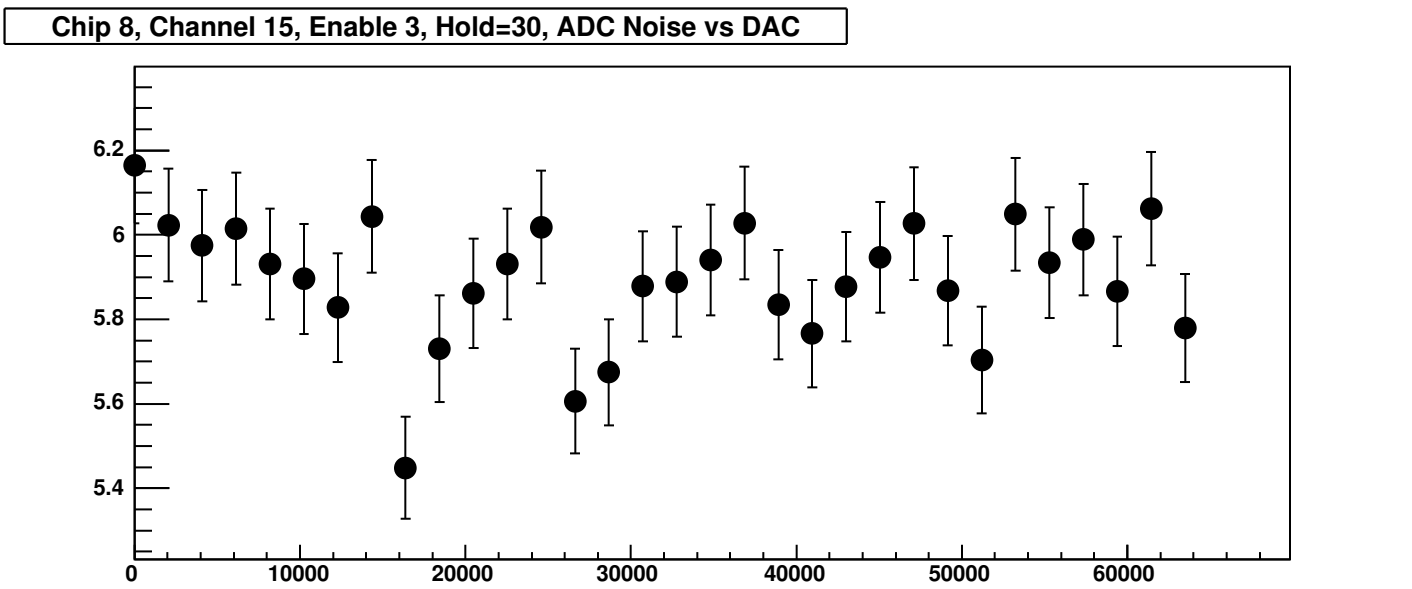


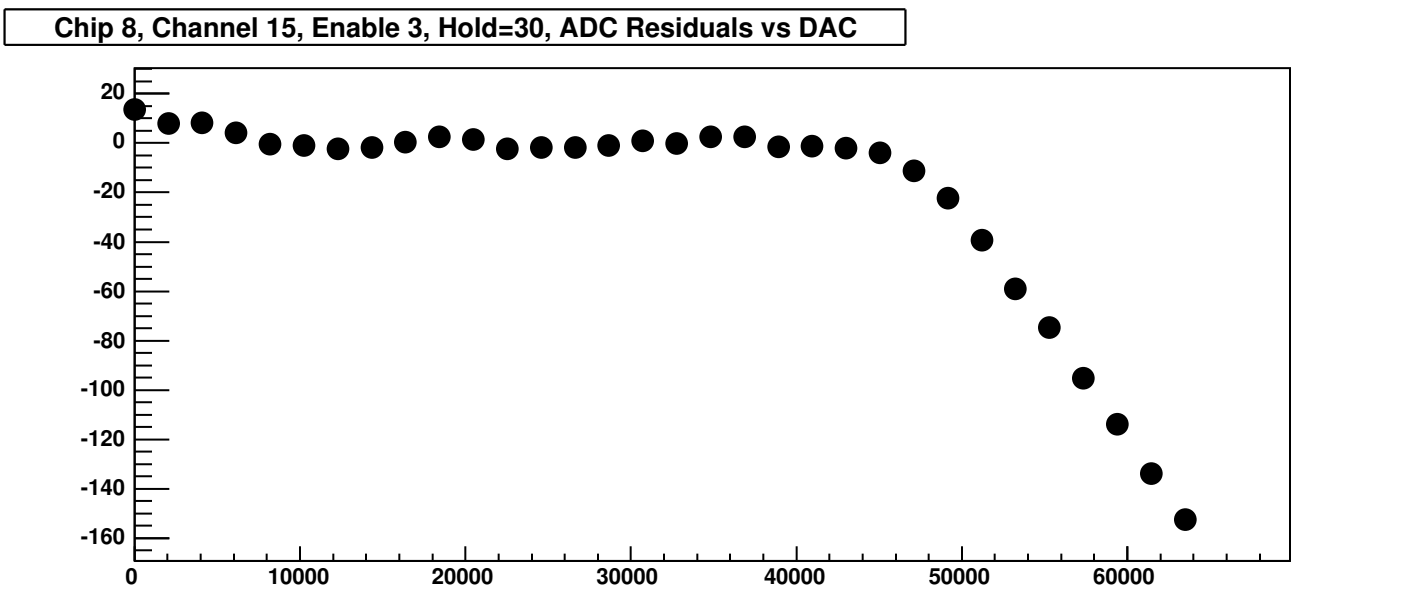

Chip 8, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 2649 / 15
p0 1326 ± 0.3118
p1 0.615 ± 3.123e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

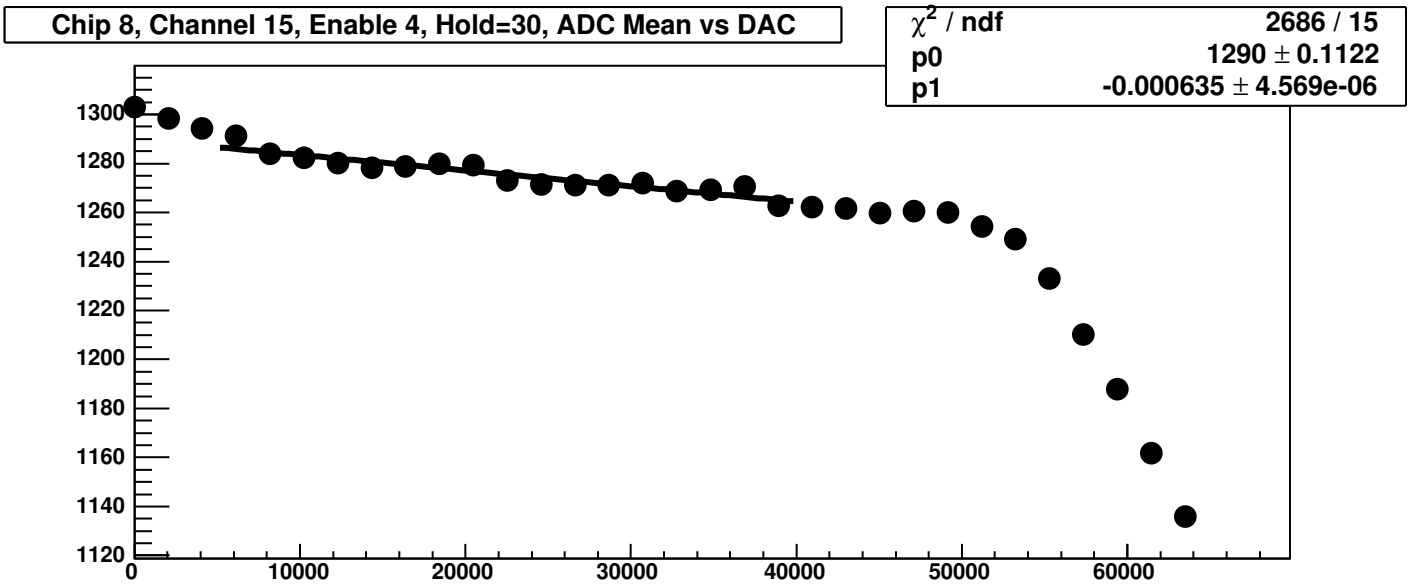

Chip 8, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

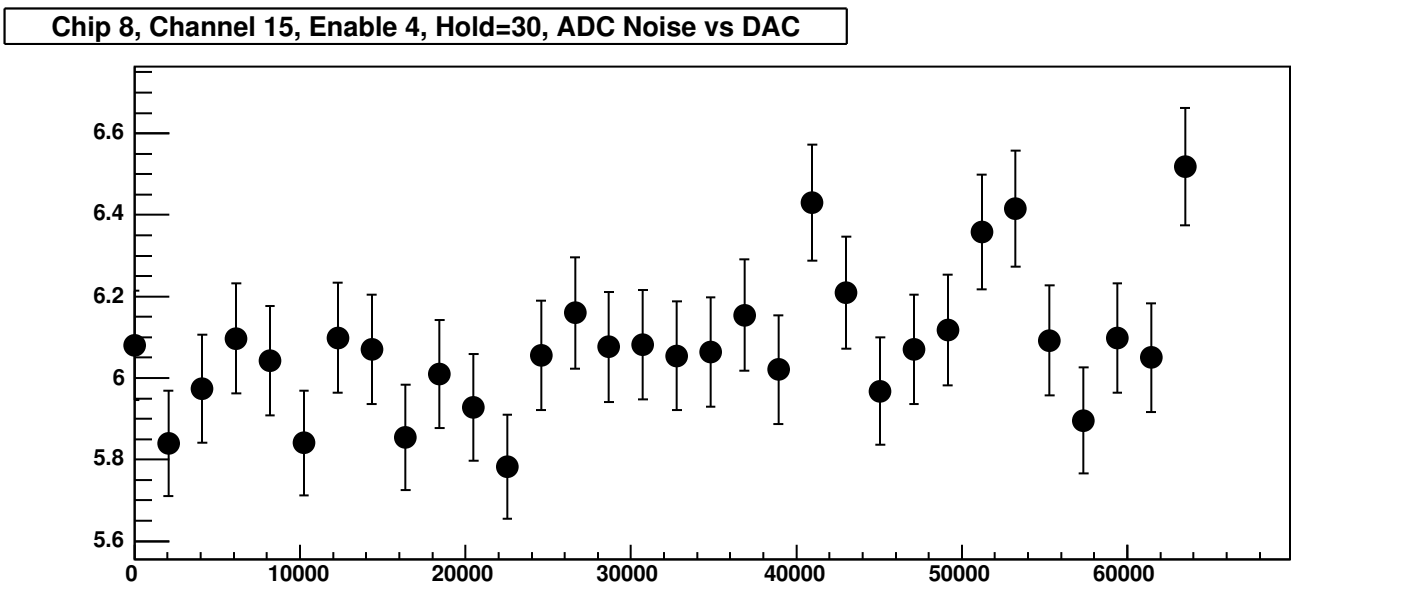

Chip 8, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

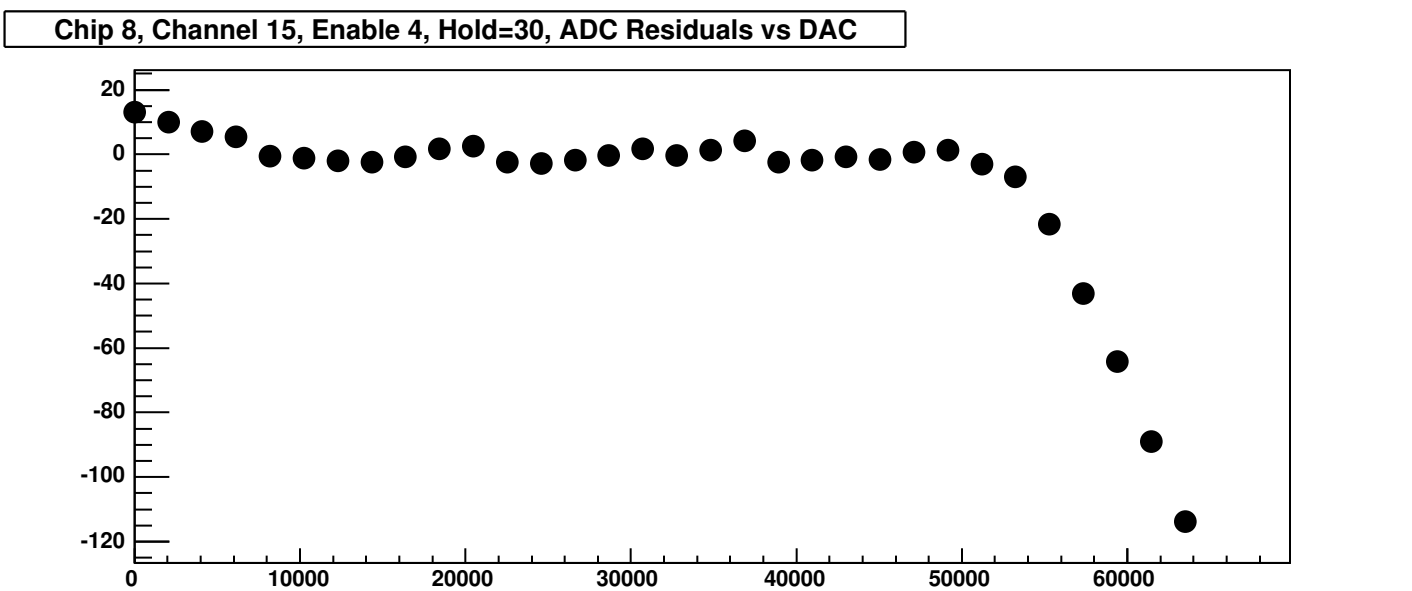

Chip 8, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 1890 / 15
p0 1290 ± 0.1099
p1 -0.0009977 ± 4.462e-06
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

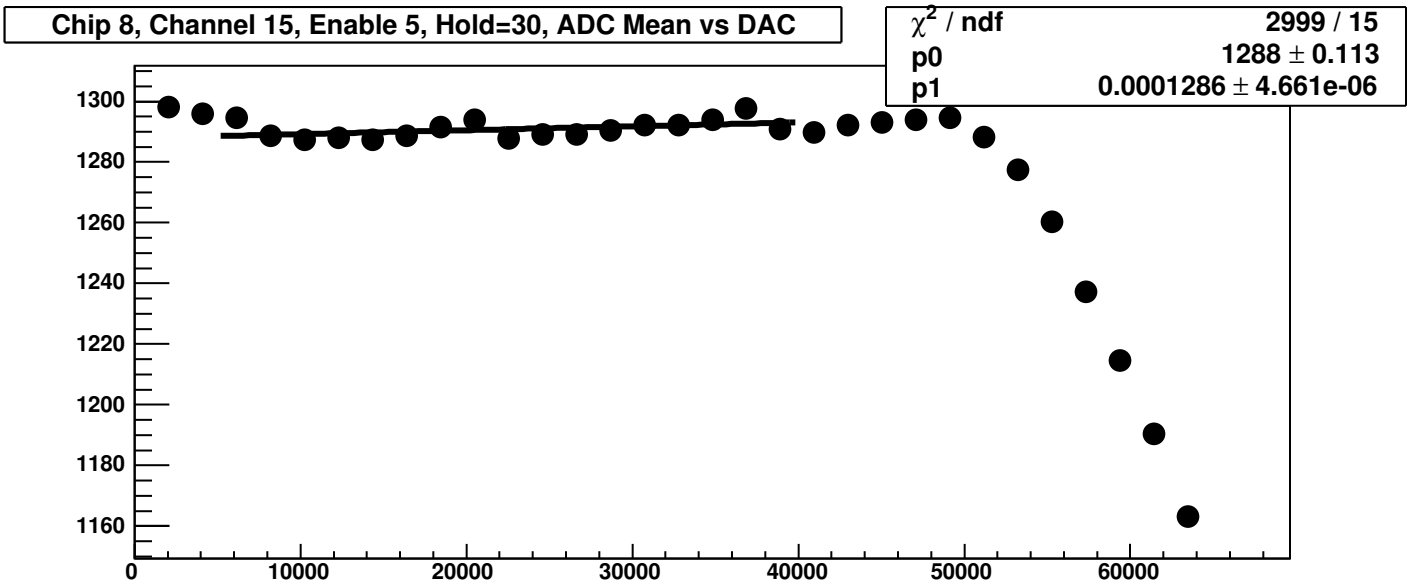

Chip 8, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

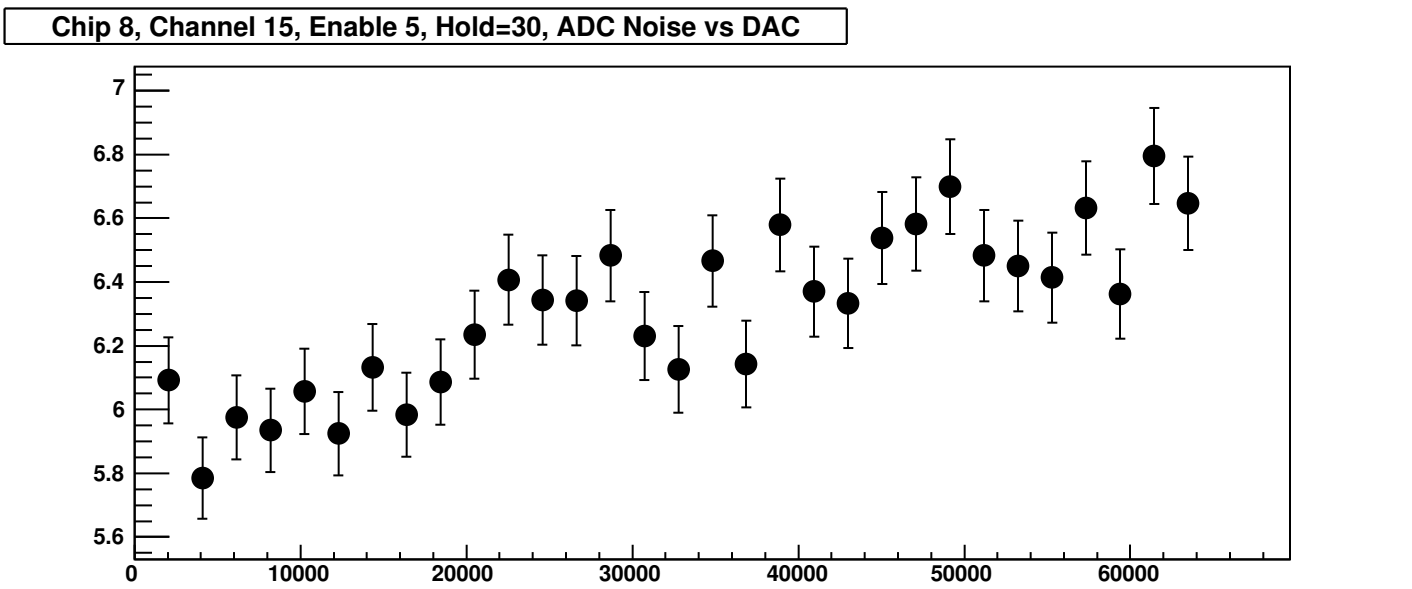

Chip 8, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

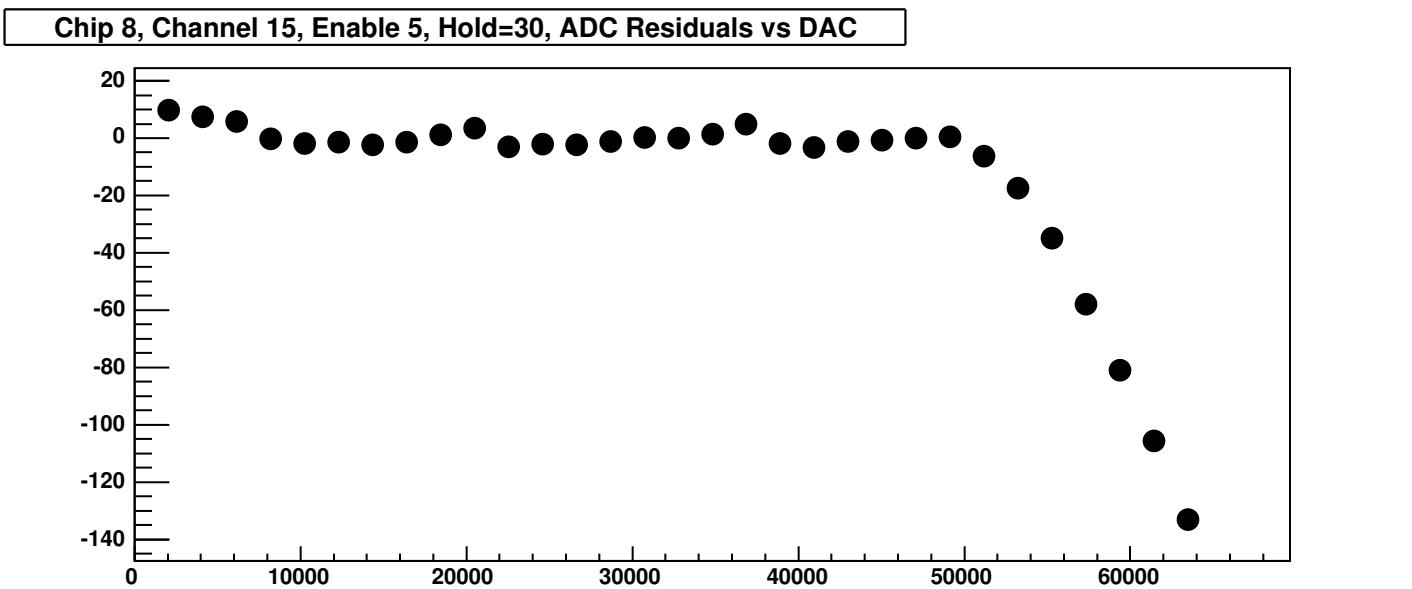

Chip 8, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 2686 / 15
p0 1290 ± 0.1122
p1 -0.000635 ± 4.569e-06
1300
1280
1260
1240
1220
1200
1180
1160
1140
1120
0
10000
20000
30000
40000
50000
60000

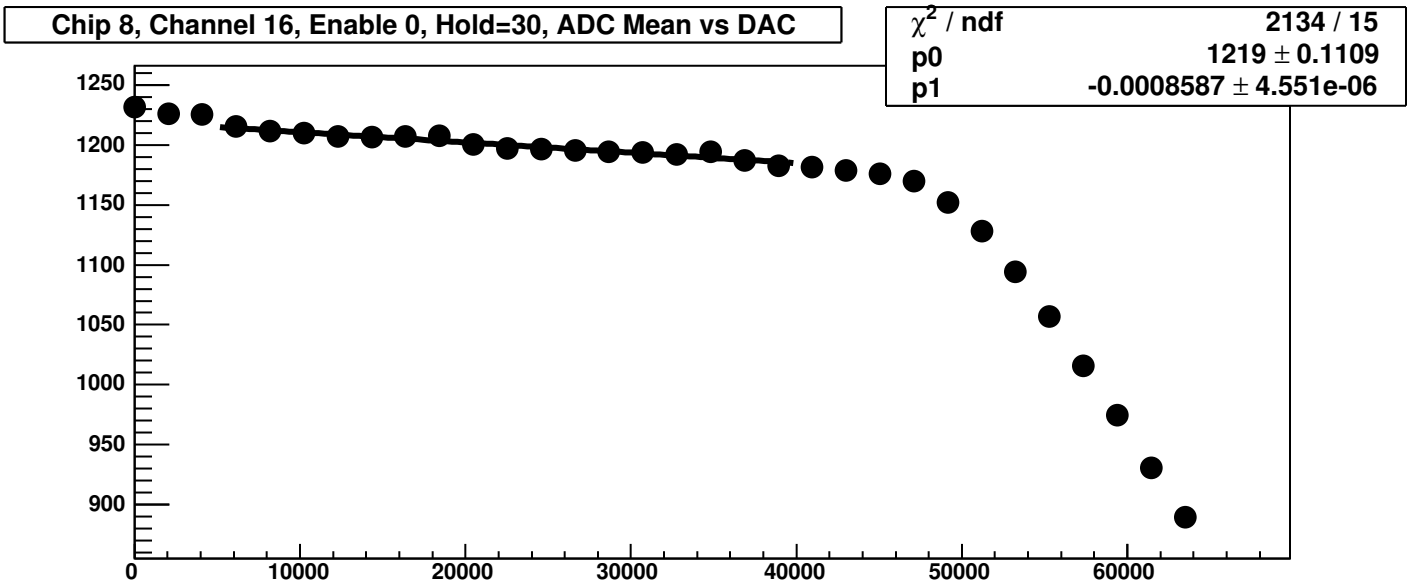

Chip 8, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

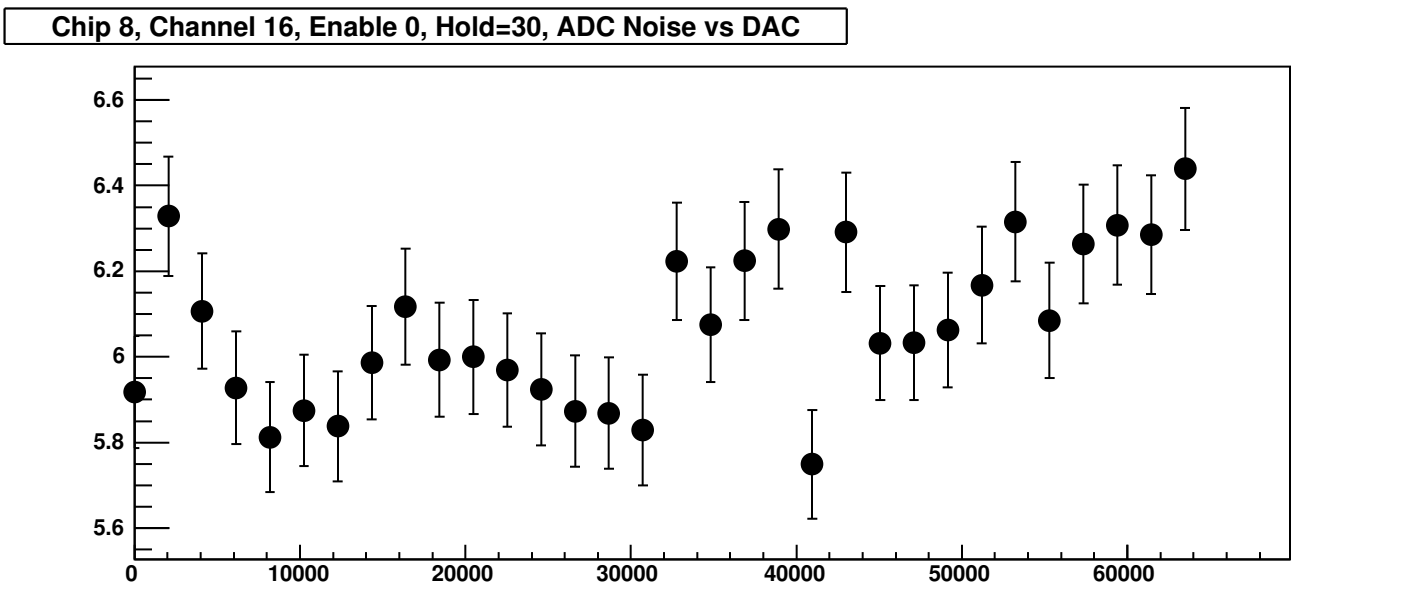

Chip 8, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

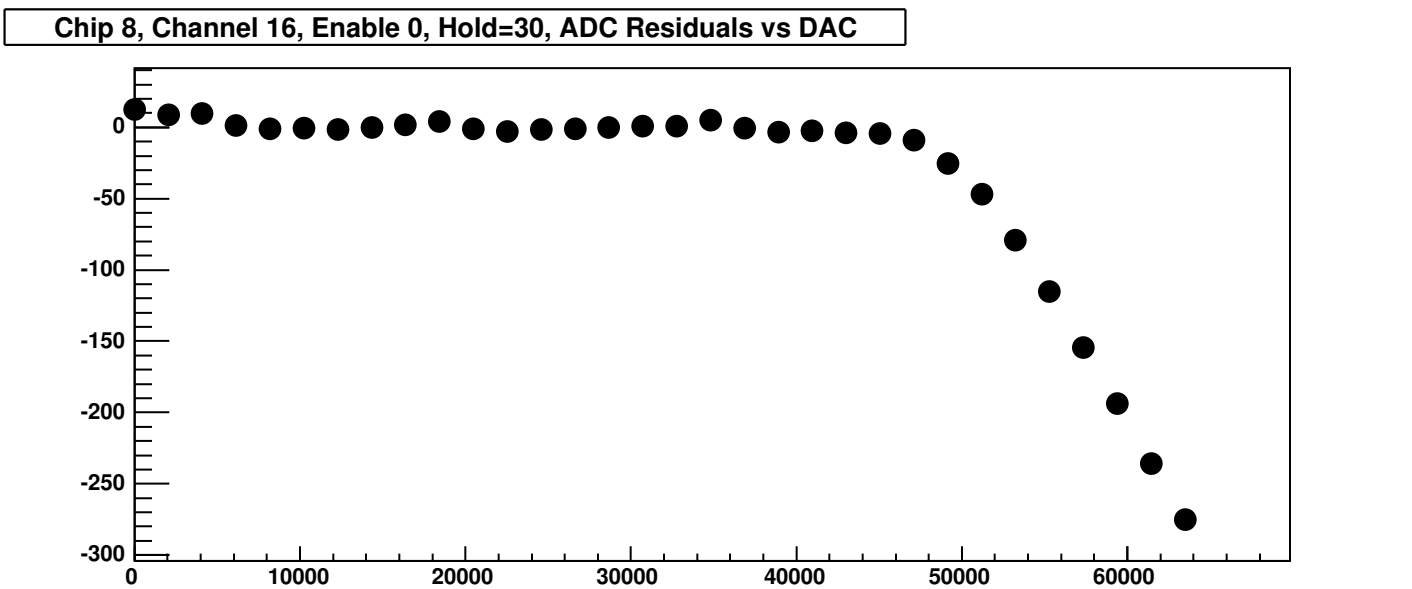

Chip 8, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 2999 / 15
p0 1288 ± 0.113
p1 0.0001286 ± 4.661e-06
1300
1280
1260
1240
1220
1200
1180
1160
0
10000
20000
30000
40000
50000
60000

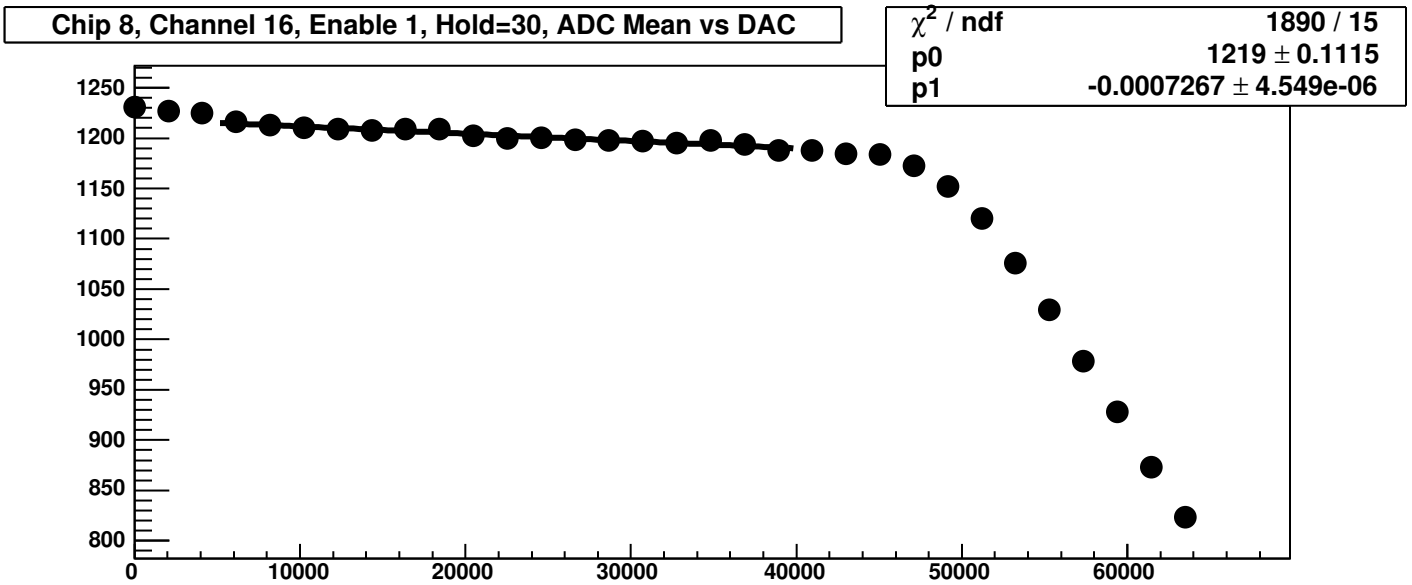

Chip 8, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

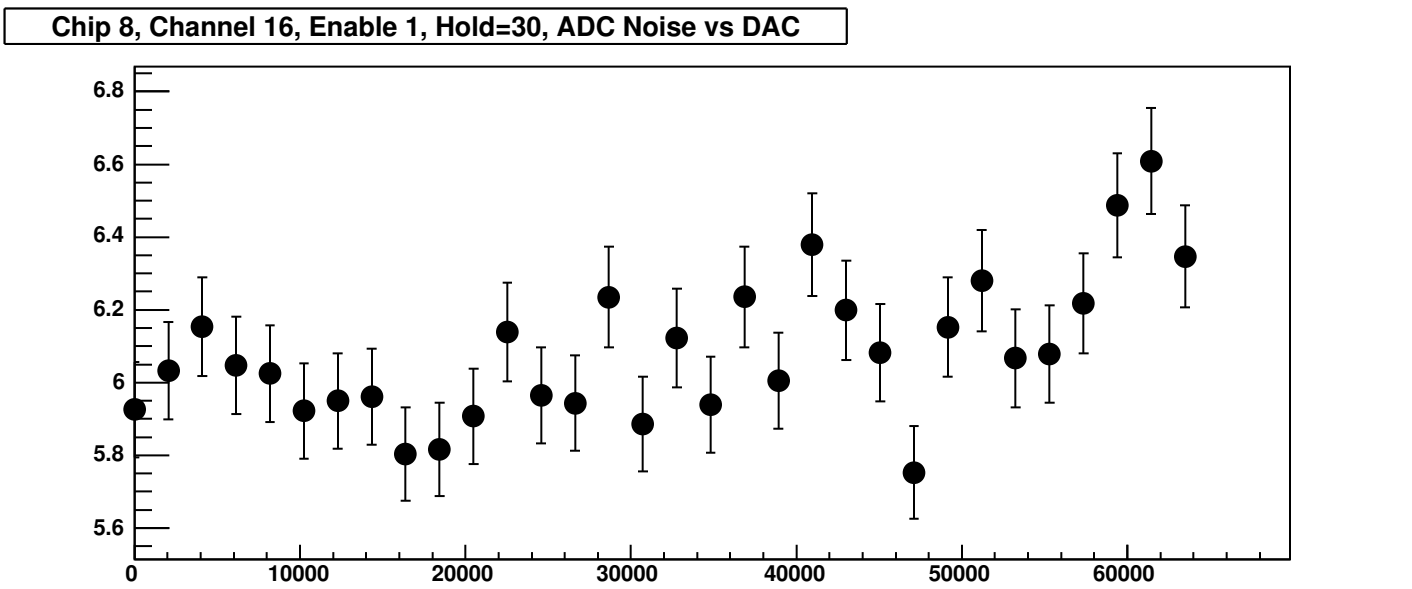

Chip 8, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

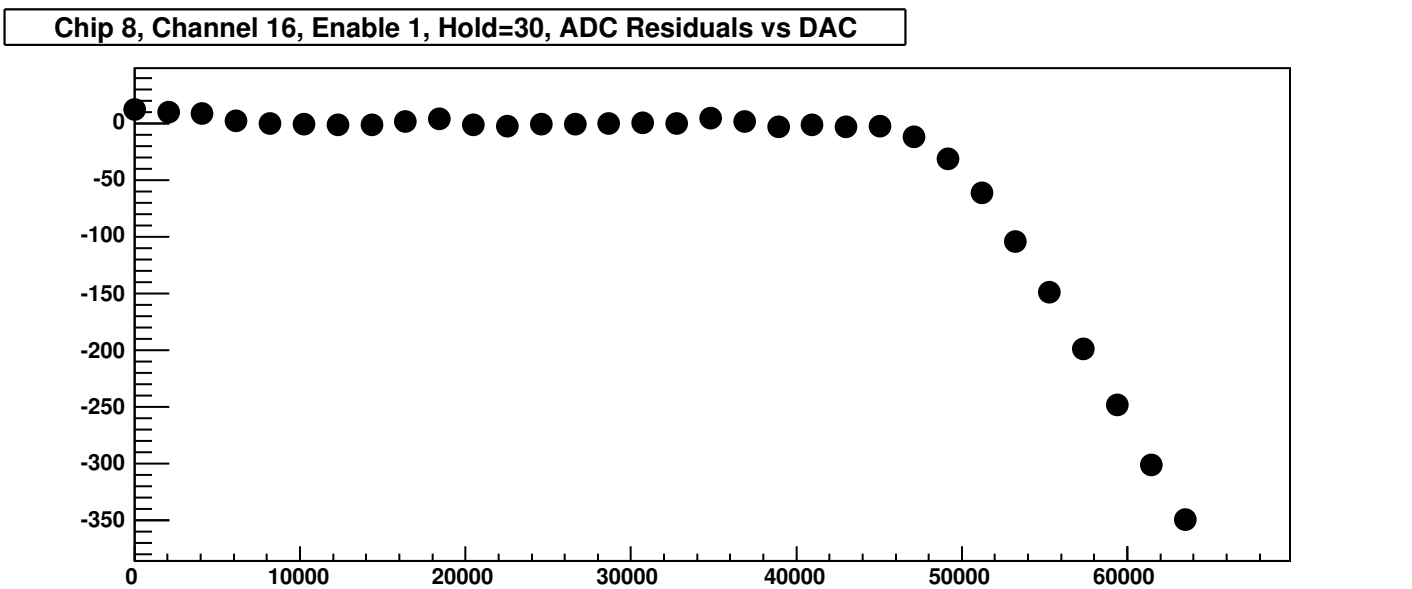

Chip 8, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2134 / 15
p0 1219 ± 0.1109
p1 -0.0008587 ± 4.551e-06
1250
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

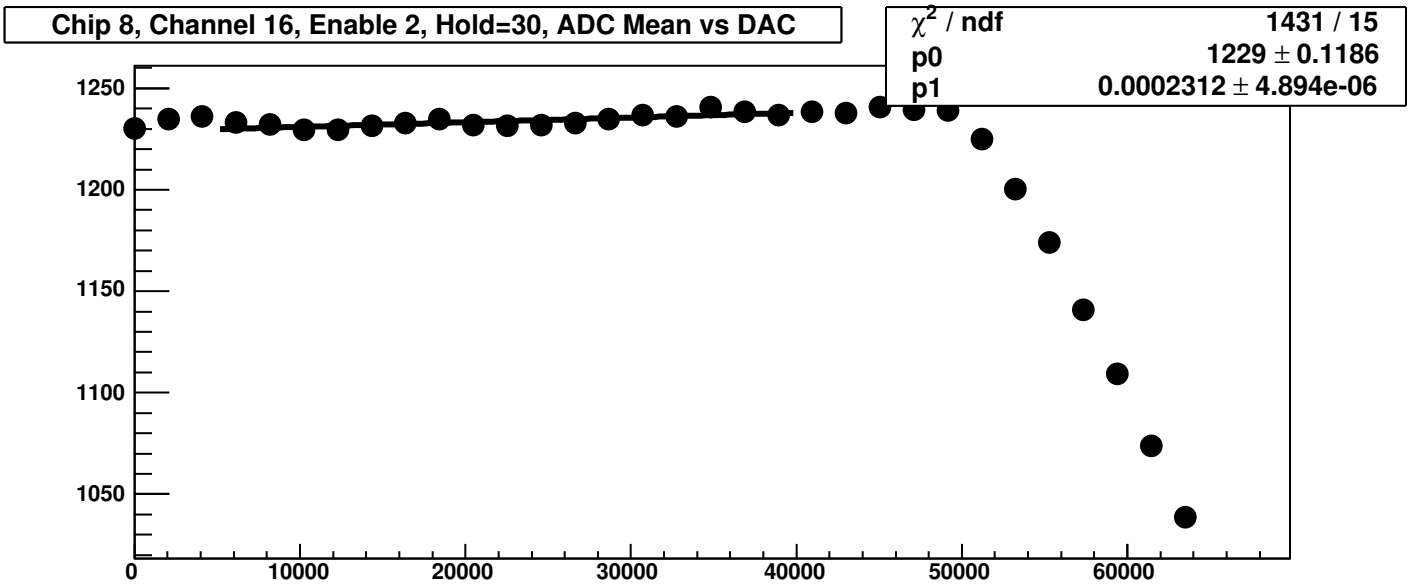

Chip 8, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

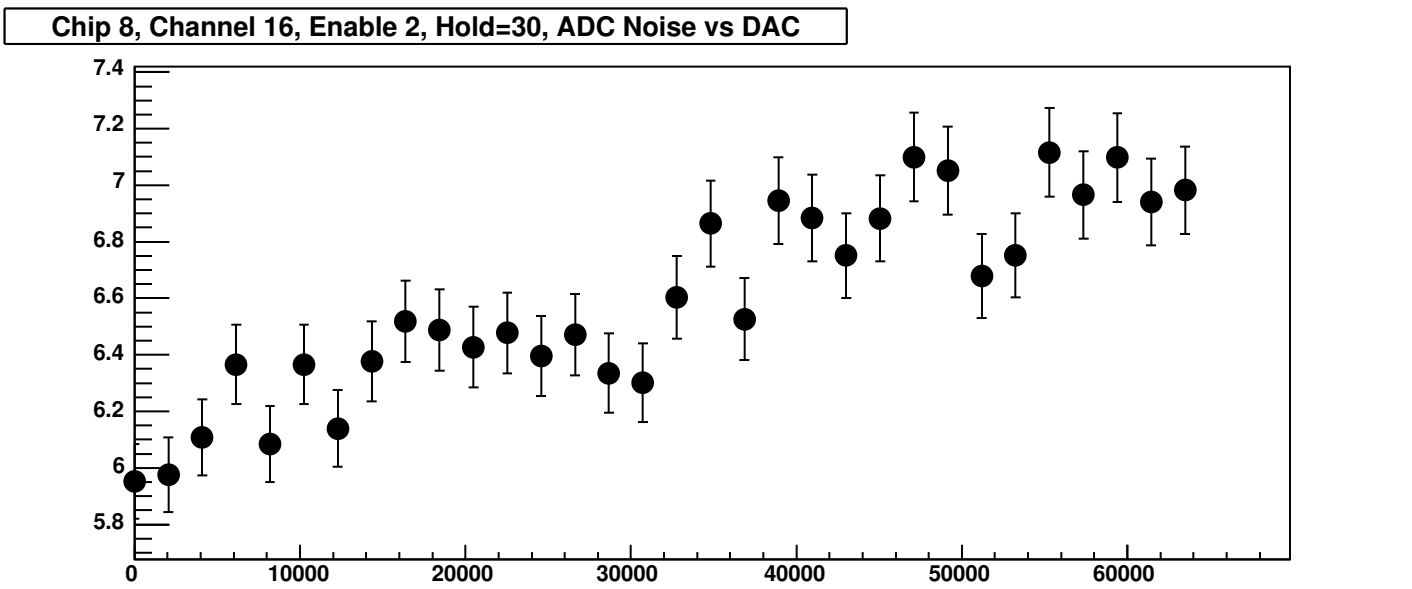

Chip 8, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

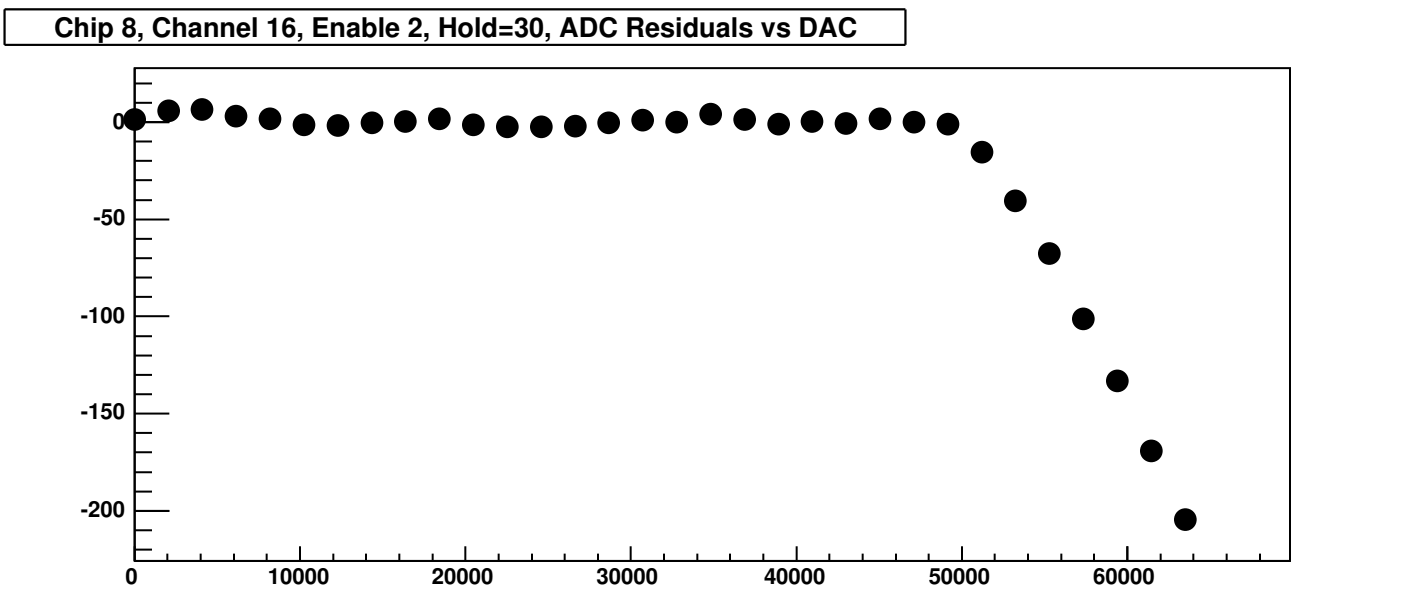

Chip 8, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1890 / 15
p0 1219 ± 0.1115
p1 -0.0007267 ± 4.549e-06
1250
1200
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

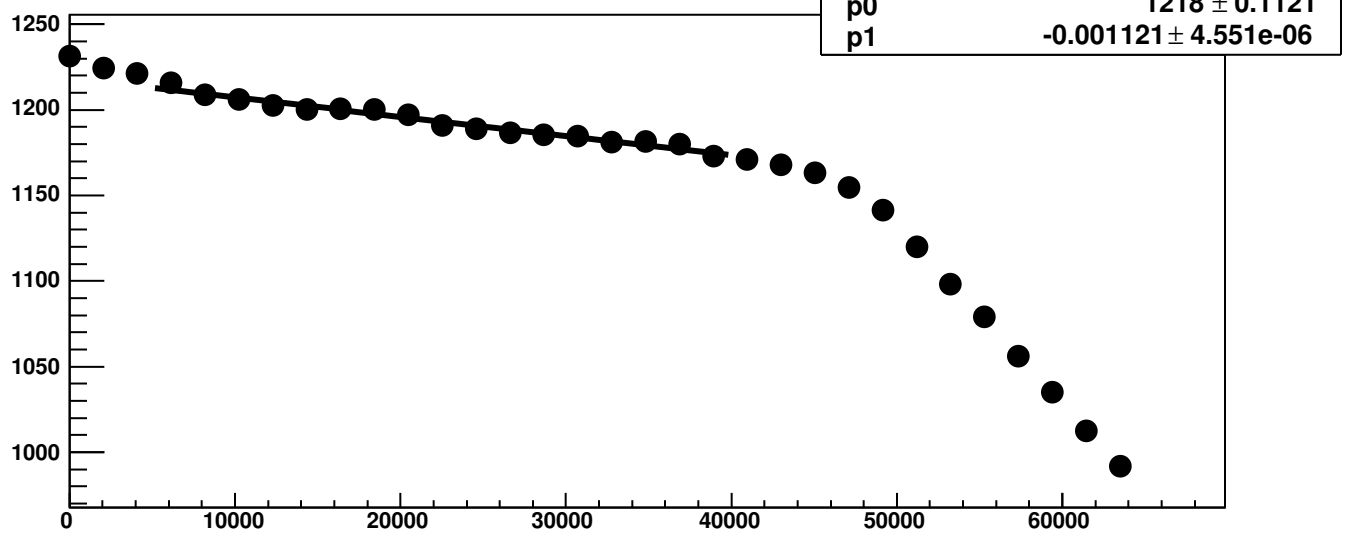

Chip 8, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1431 / 15
p0 1229 ± 0.1186
p1 0.0002312 ± 4.894e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

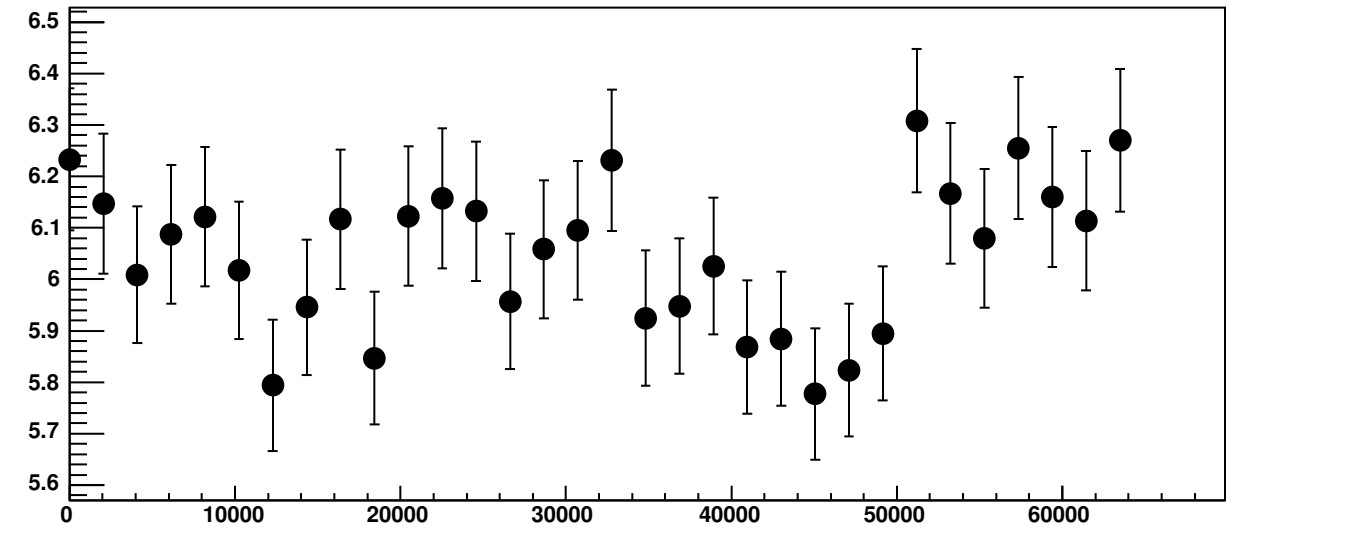

Chip 8, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 8, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

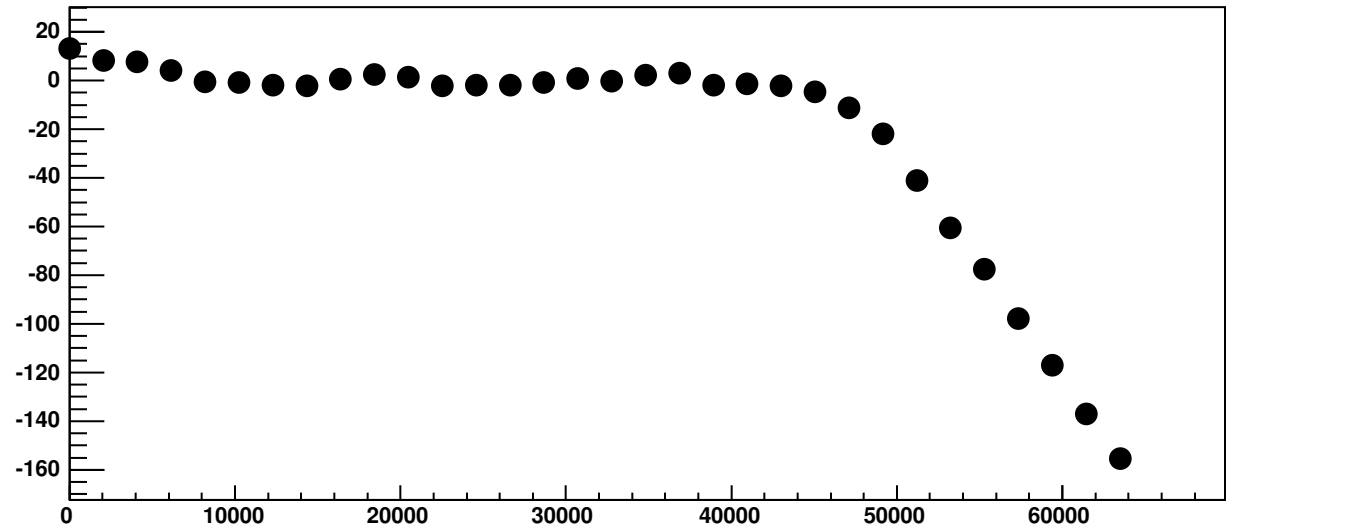
Chip 8, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

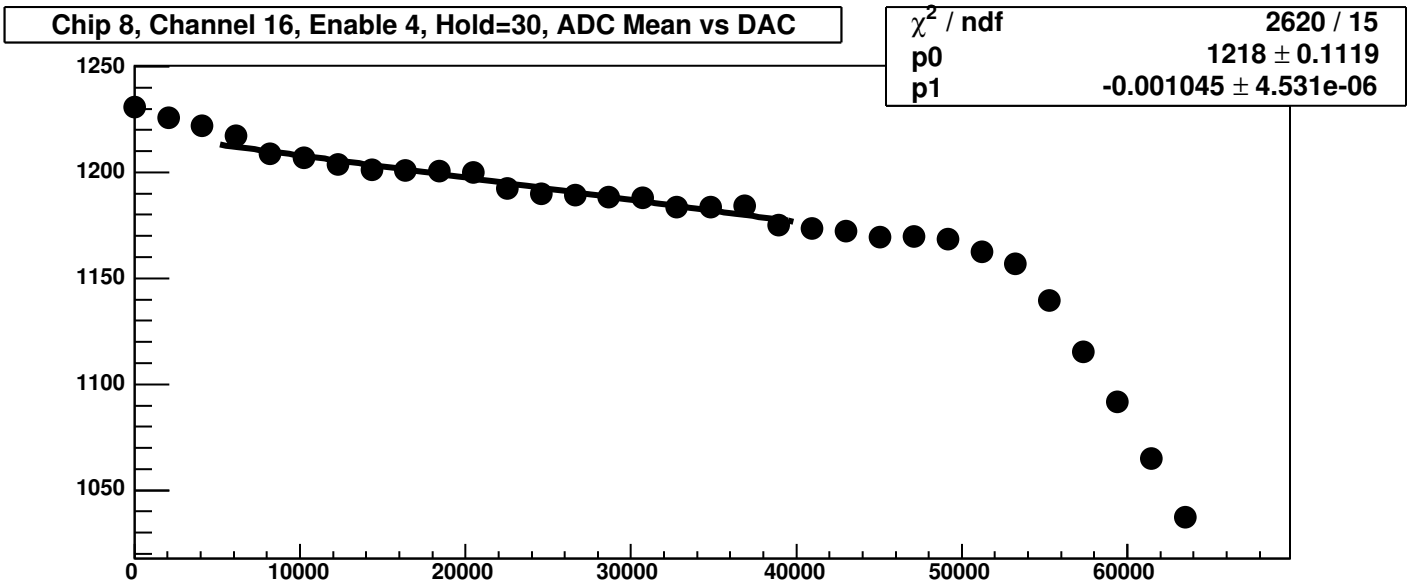


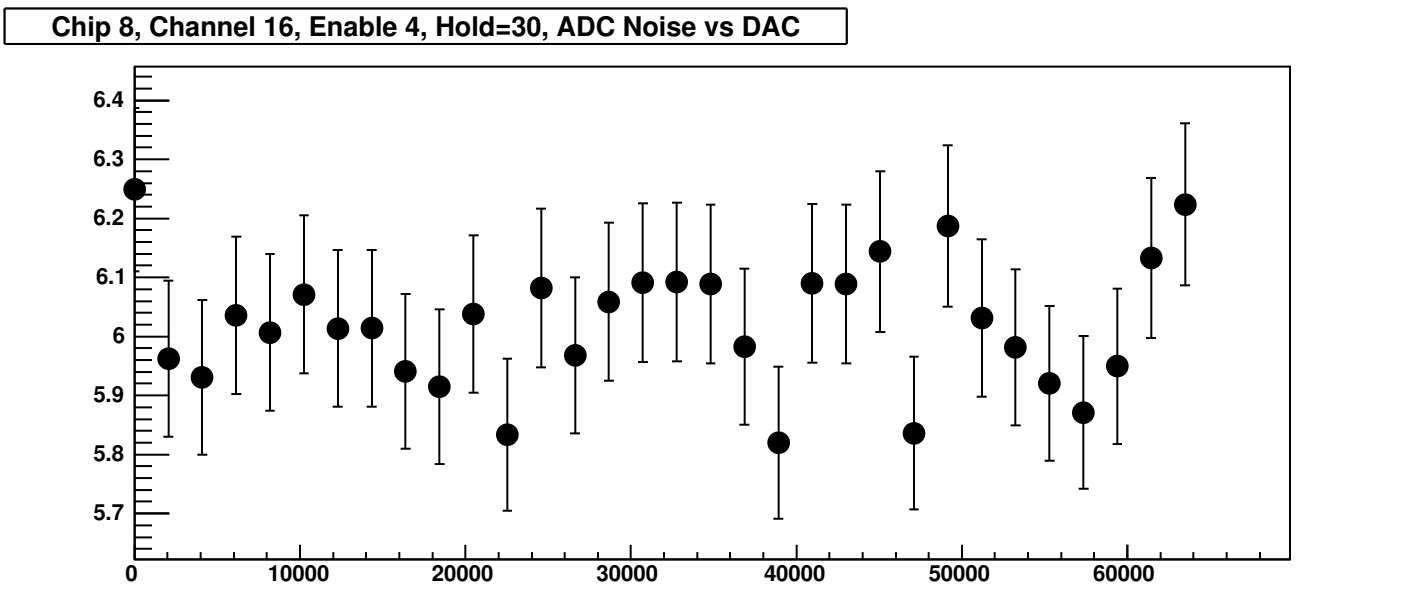
Chip 8, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC

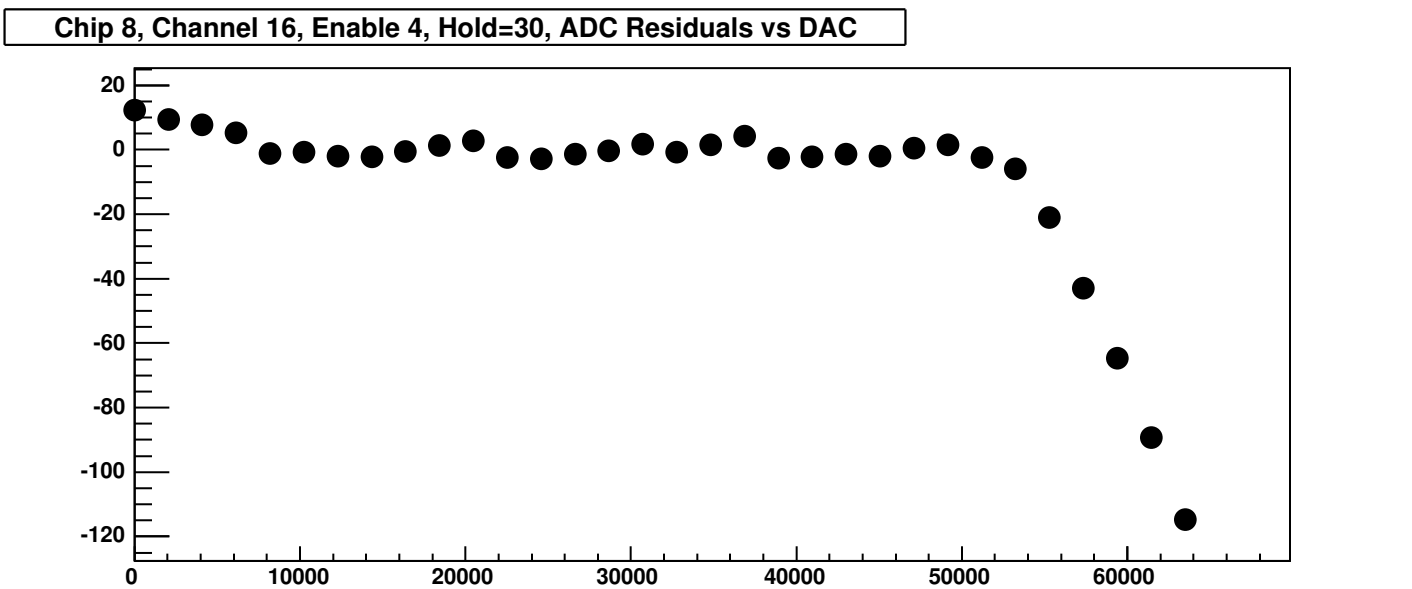


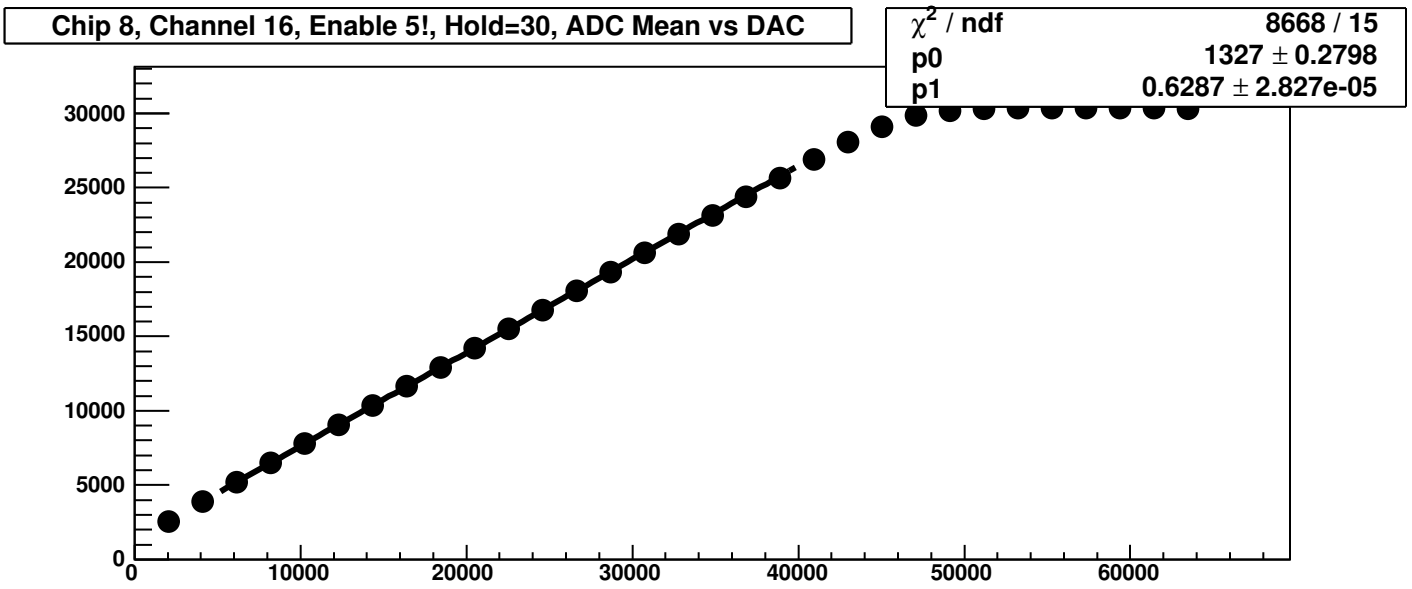
Chip 8, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

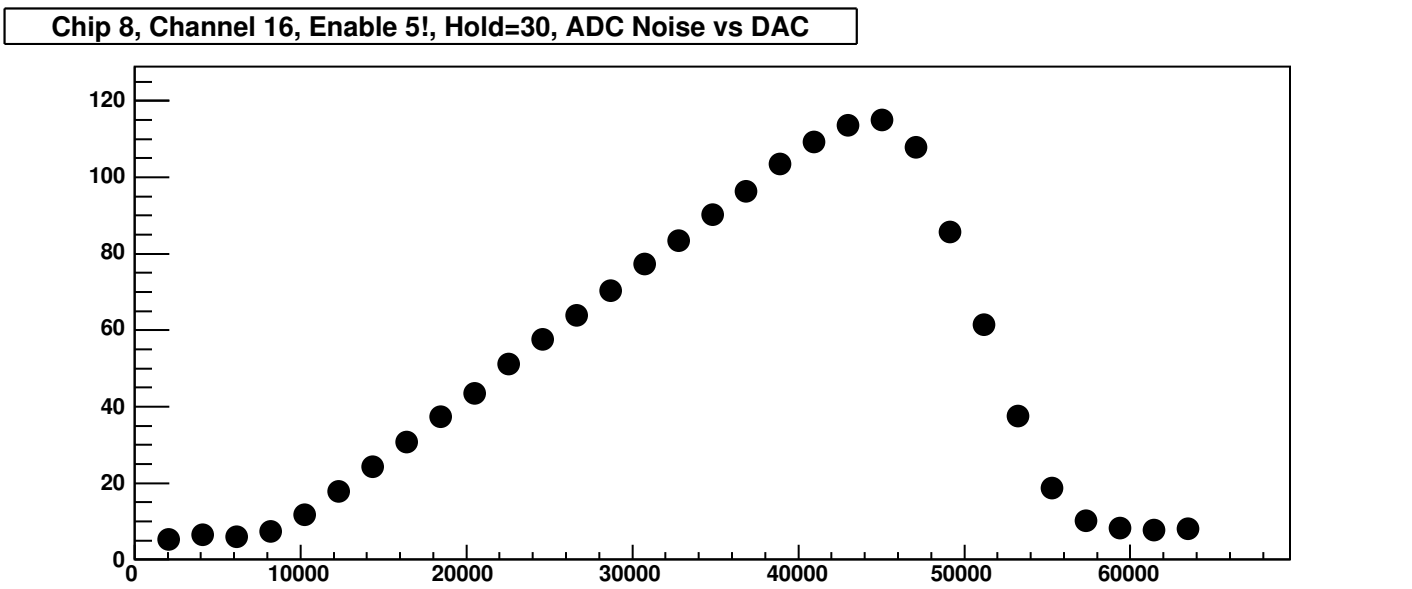


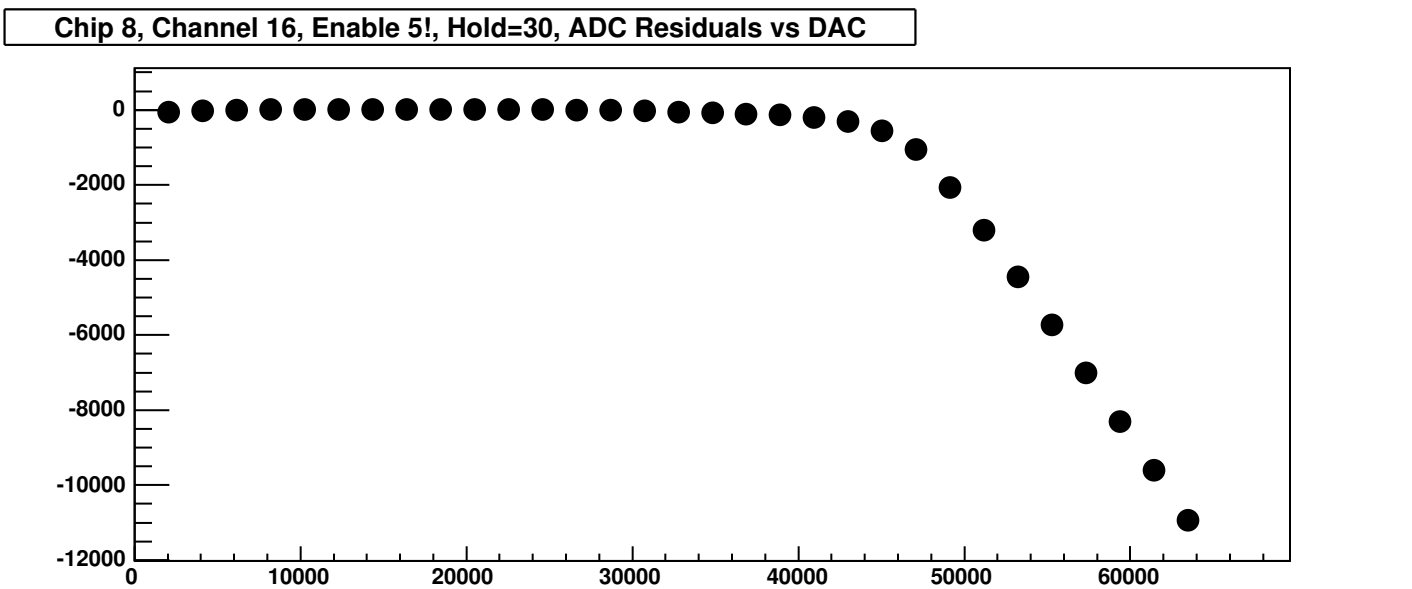

Chip 8, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 2620 / 15
p0 1218 ± 0.1119
p1 -0.001045 ± 4.531e-06
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

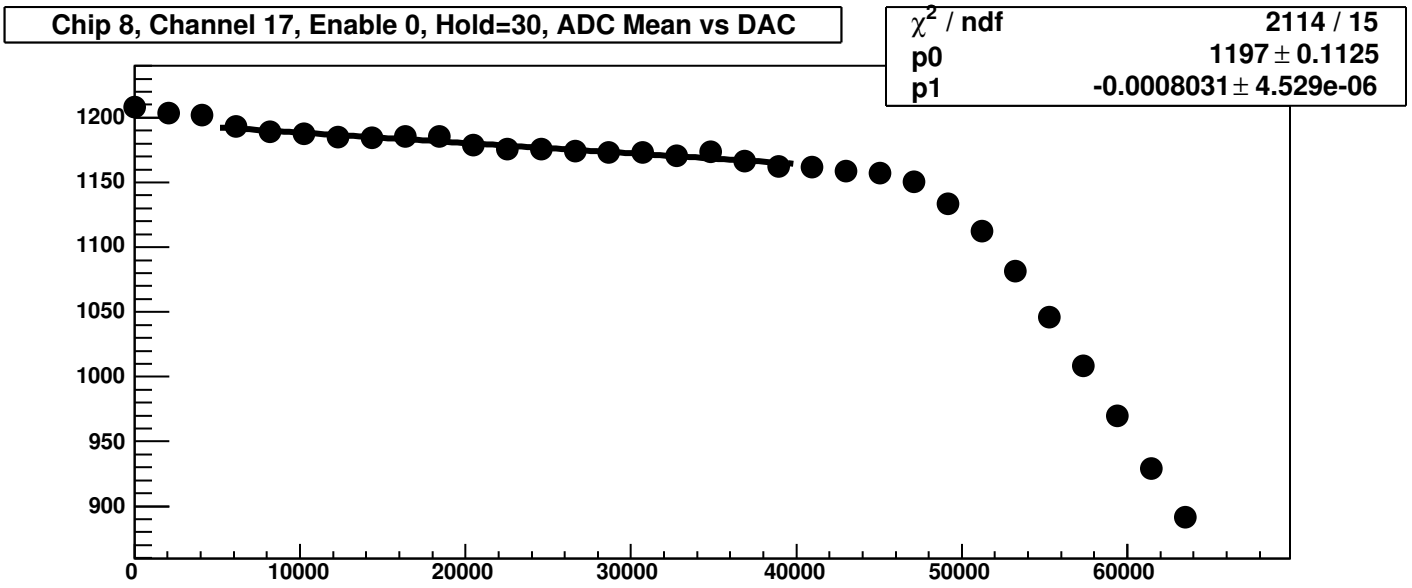

Chip 8, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
0
10000
20000
30000
40000
50000
60000

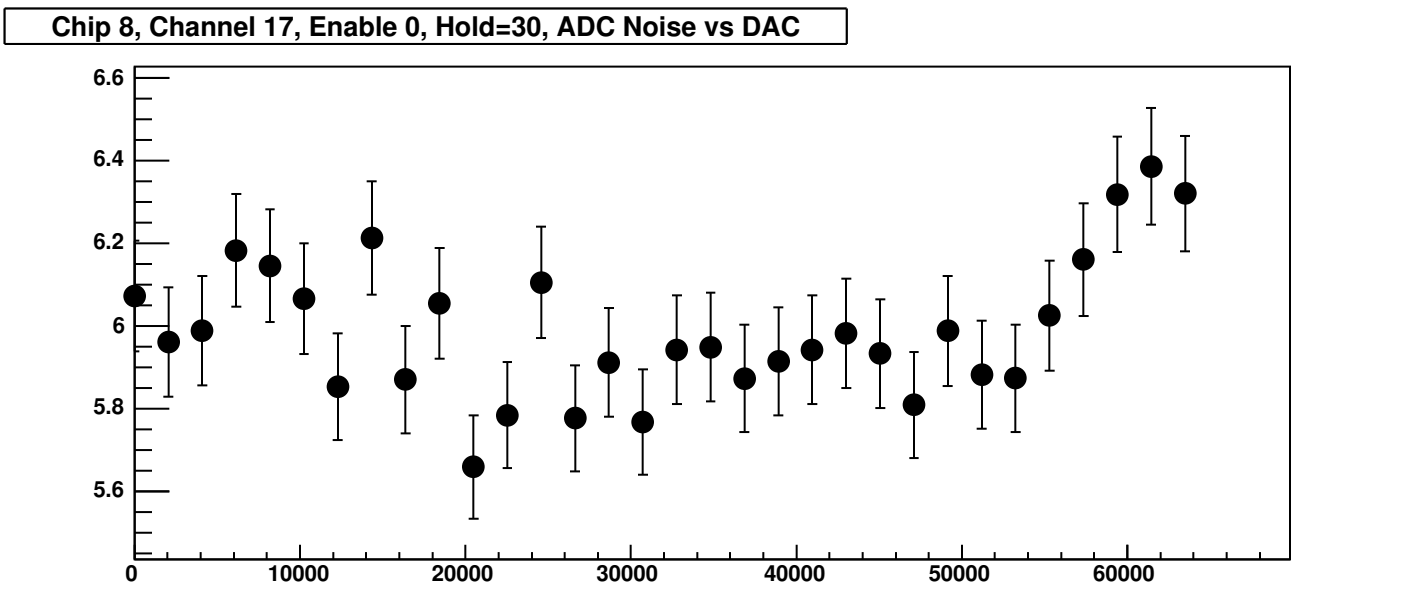

Chip 8, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

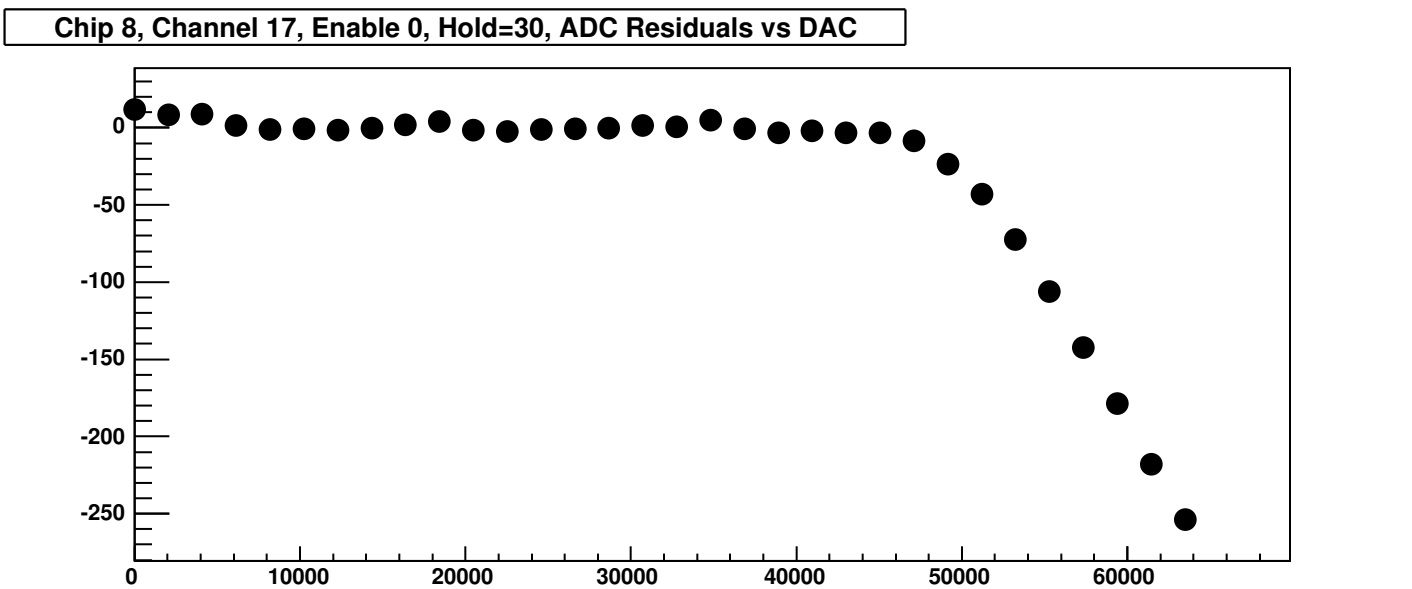

Chip 8, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 8668 / 15
p0 1327 ± 0.2798
p1 0.6287 ± 2.827e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

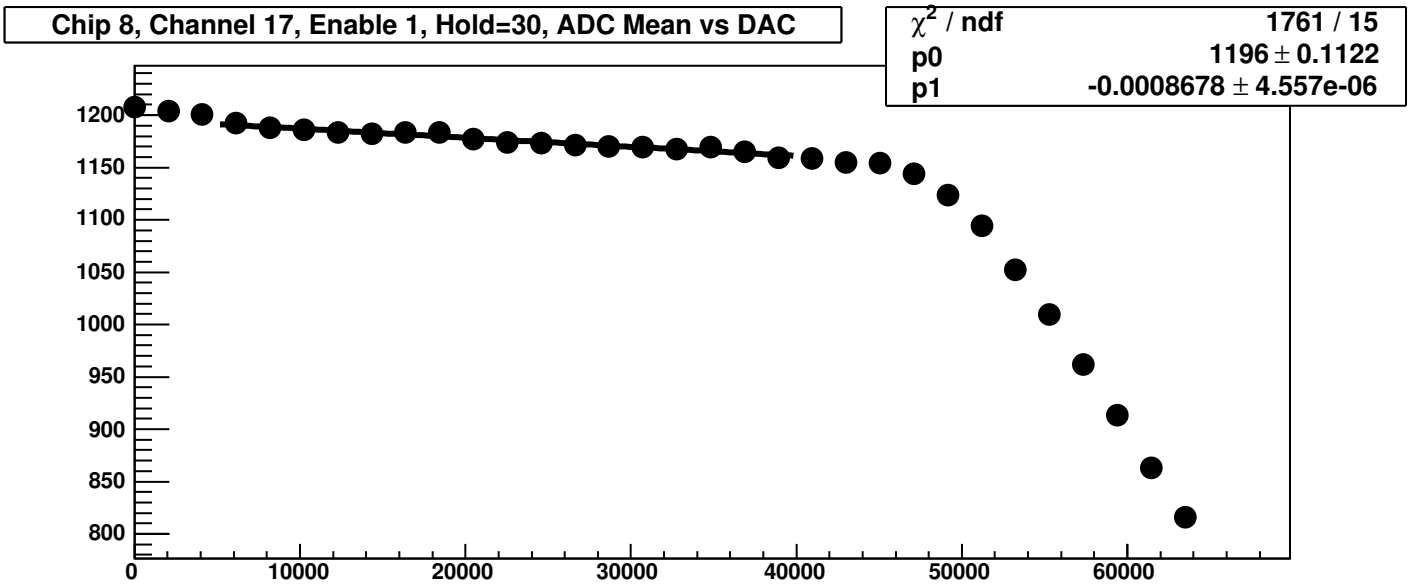

Chip 8, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

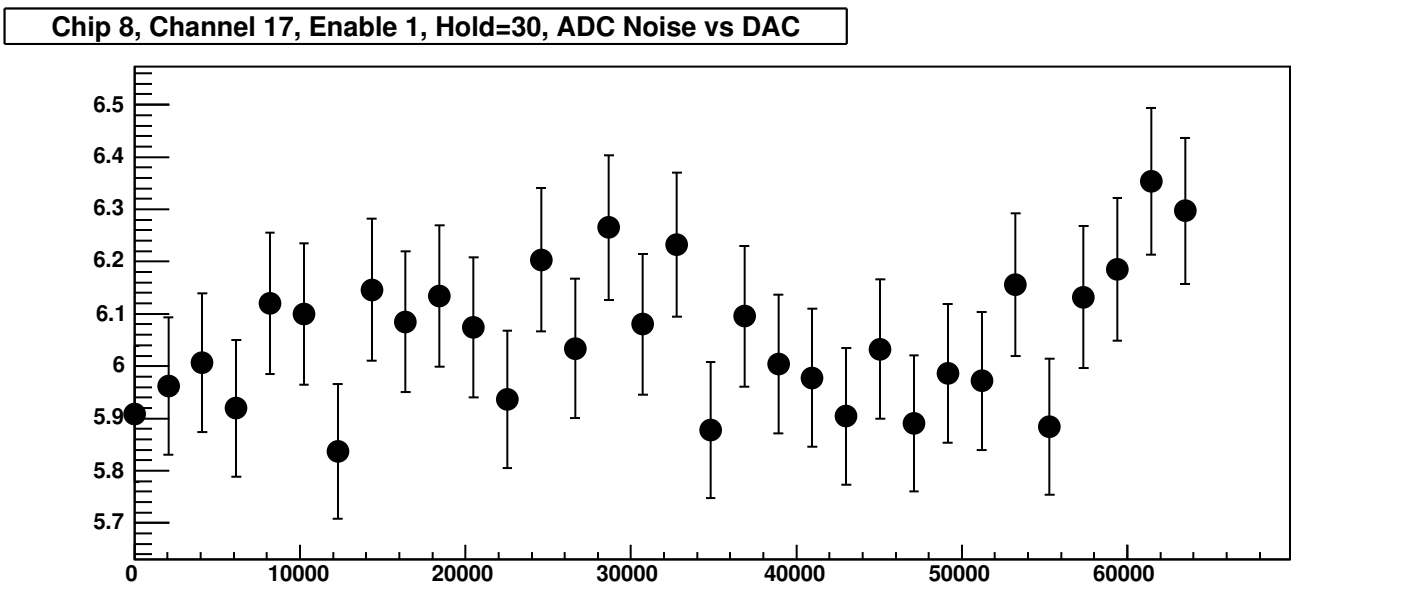

Chip 8, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

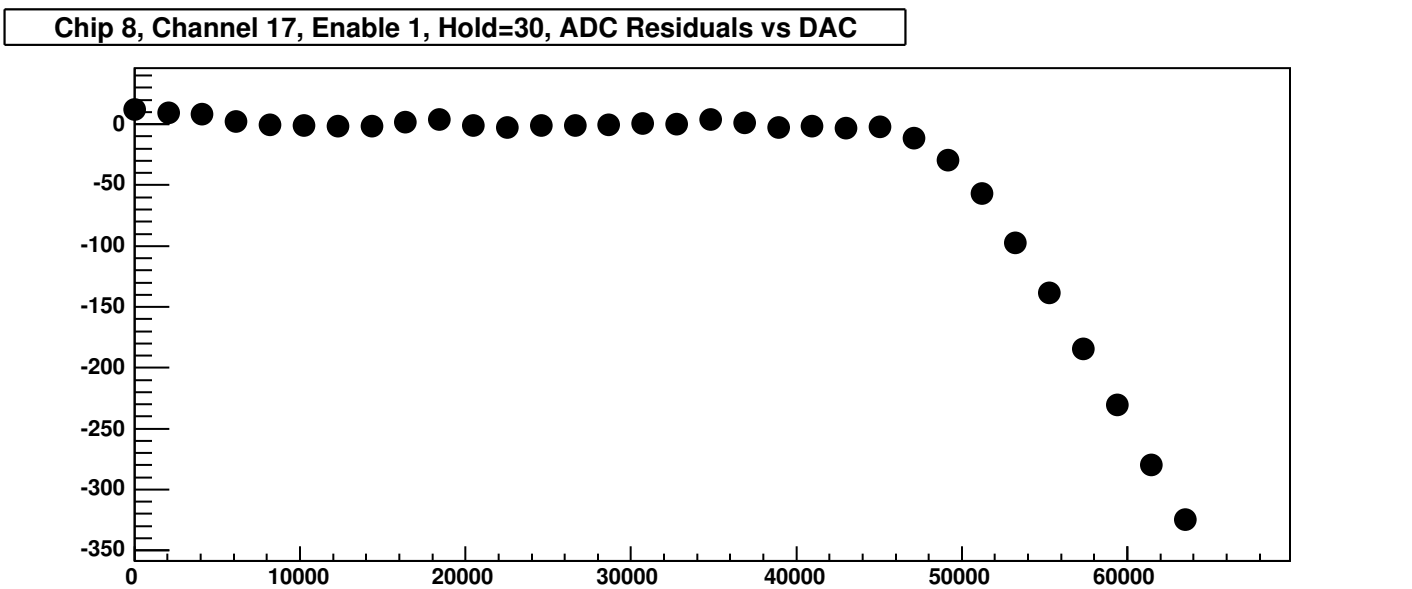

Chip 8, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2114 / 15
p0 1197 ± 0.1125
p1 -0.0008031 ± 4.529e-06
1200
1150
1100
1050
1000
950
900
0
10000
20000
30000
40000
50000
60000

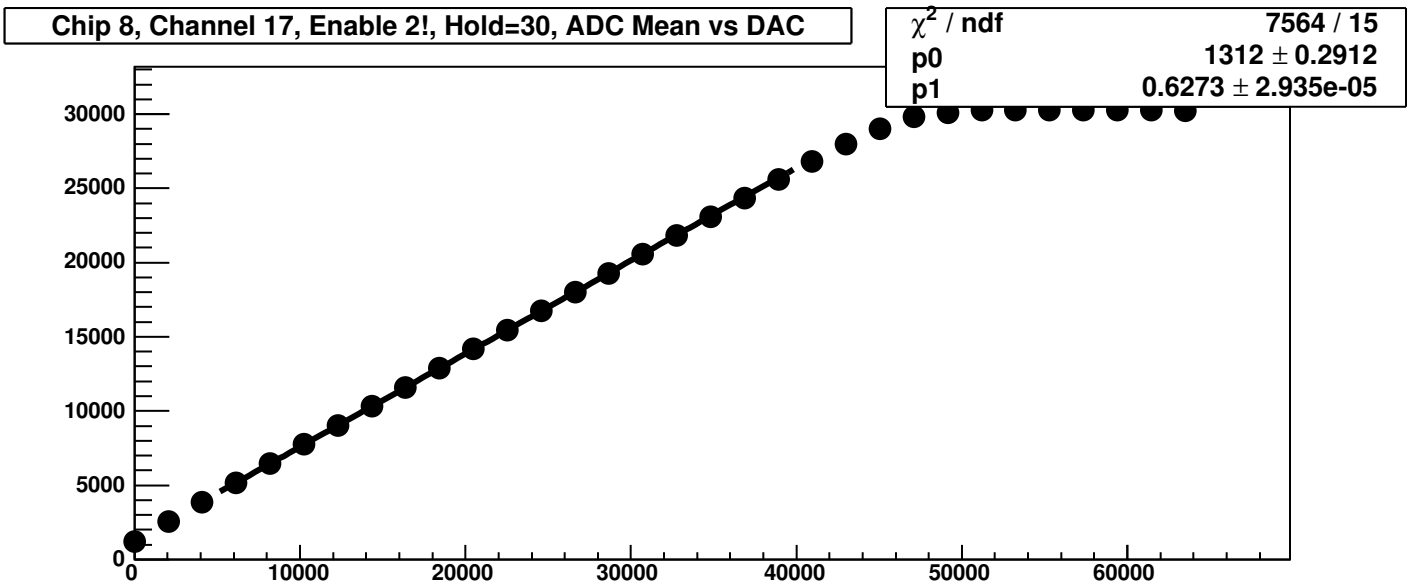

Chip 8, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

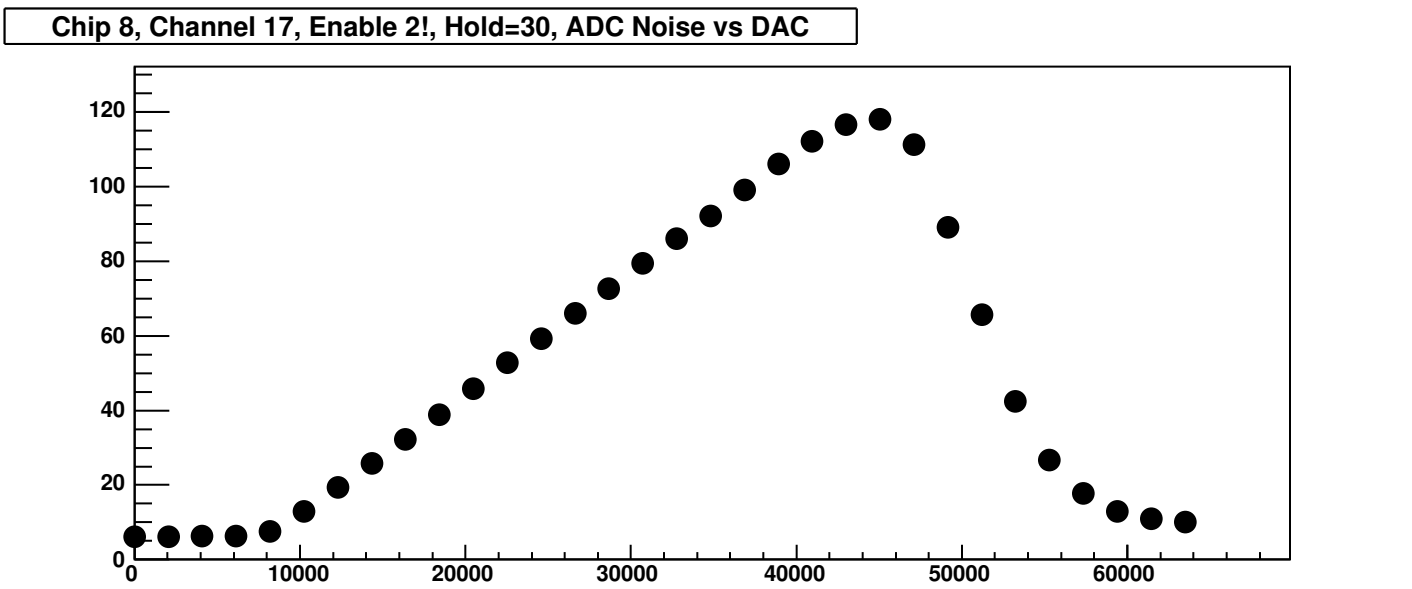

Chip 8, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

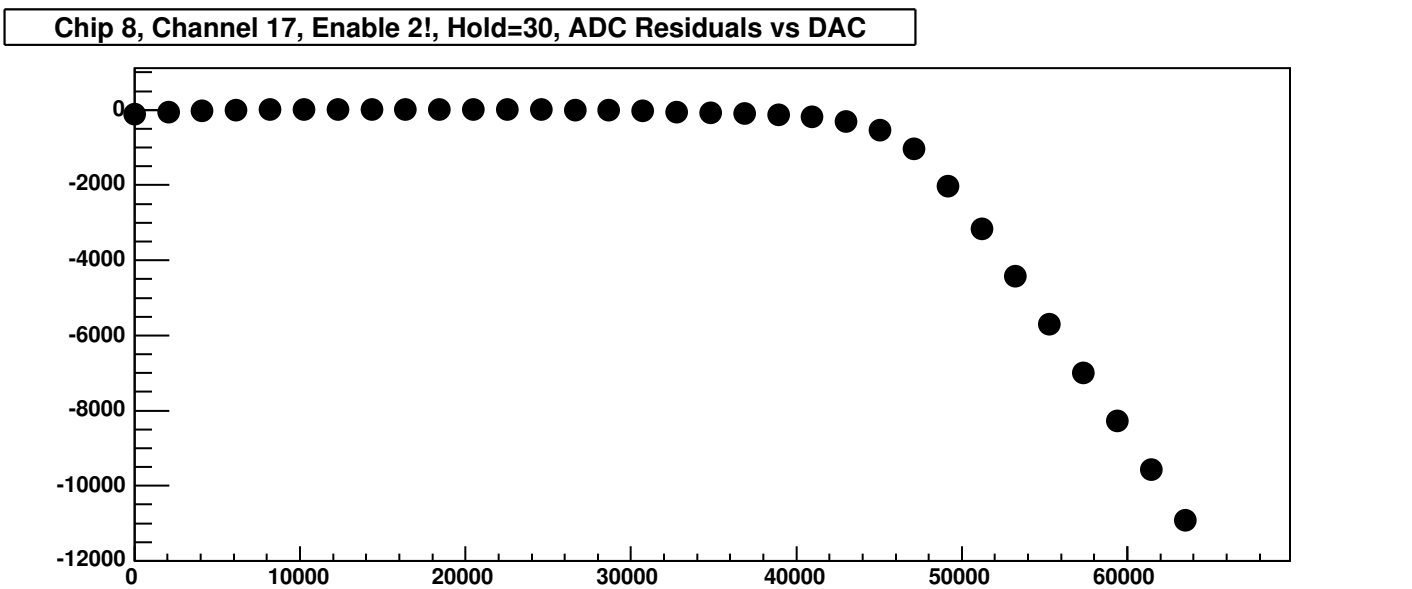

Chip 8, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1761 / 15
p0 1196 ± 0.1122
p1 -0.0008678 ± 4.557e-06
1200
1150
1100
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

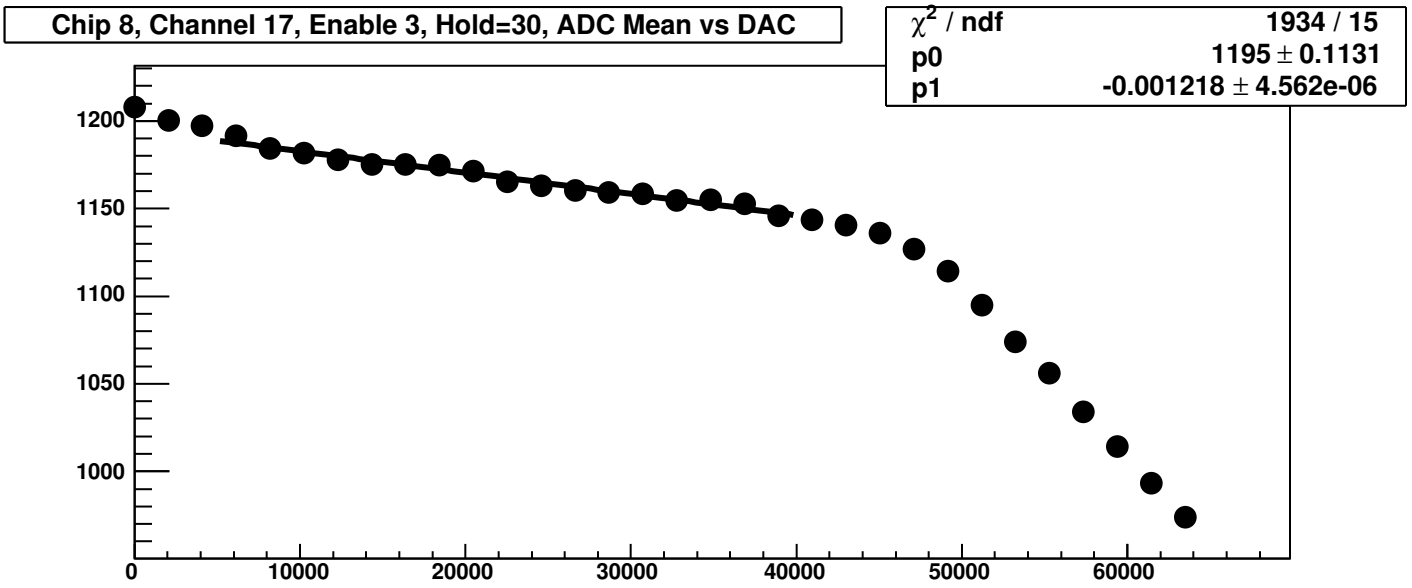

Chip 8, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
0
10000
20000
30000
40000
50000
60000

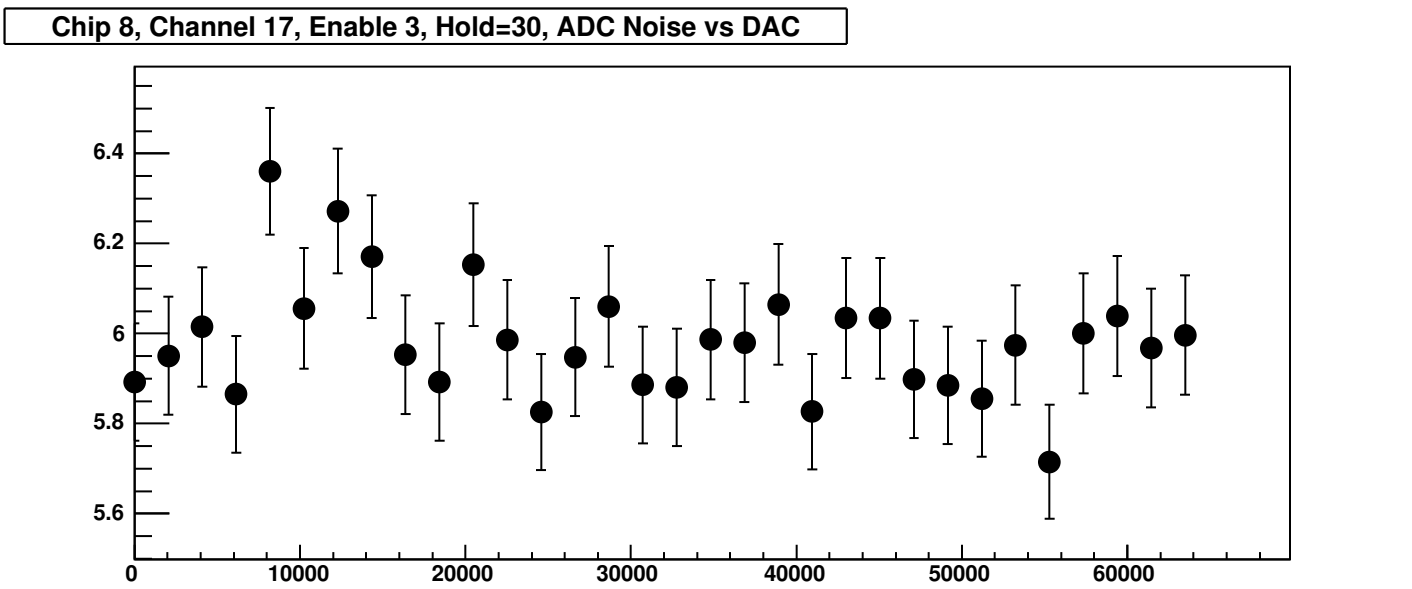

Chip 8, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

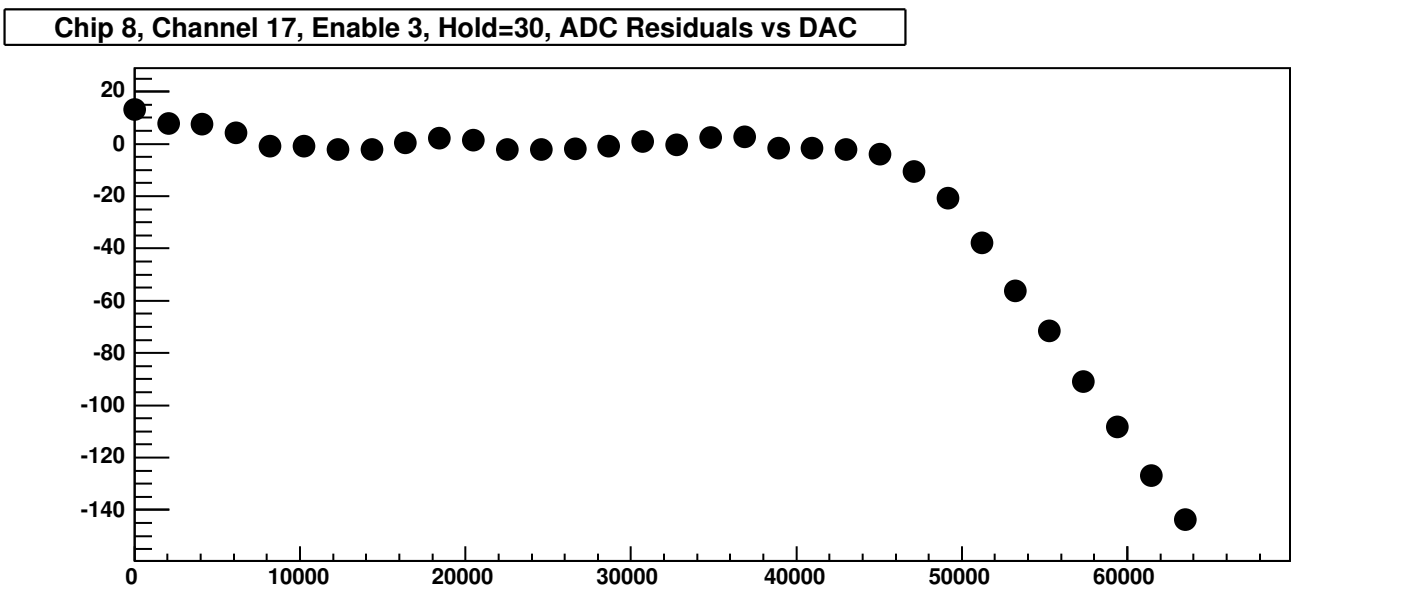

Chip 8, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 7564 / 15
p0 1312 ± 0.2912
p1 0.6273 ± 2.935e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

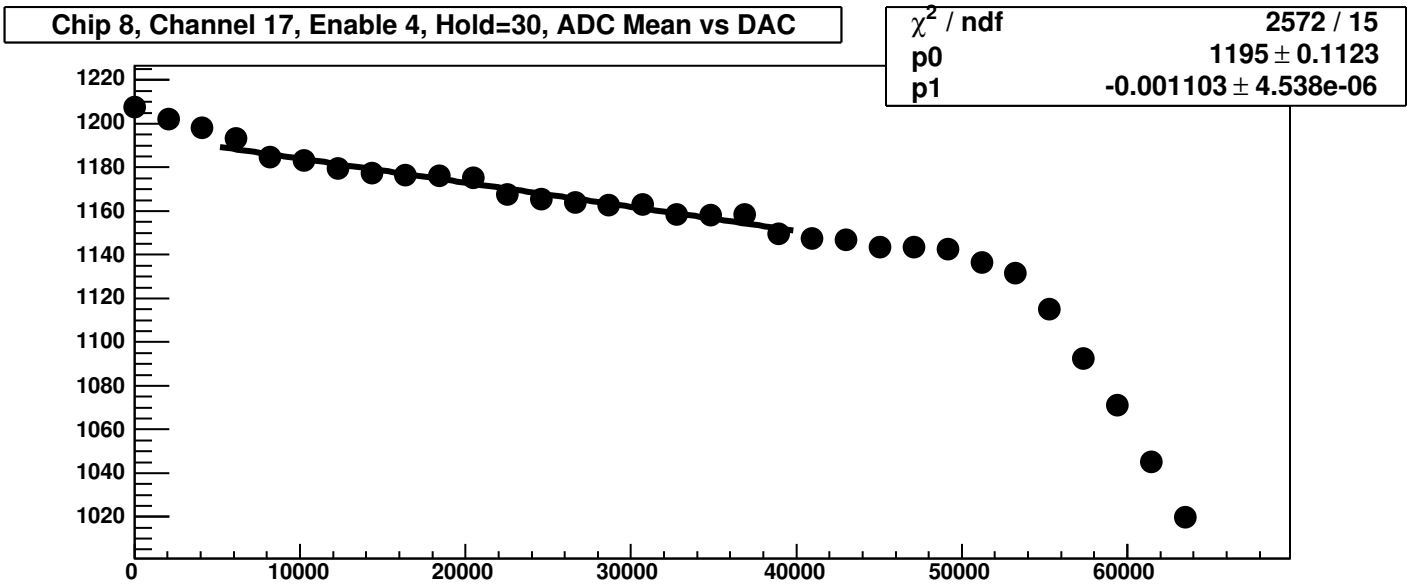

Chip 8, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

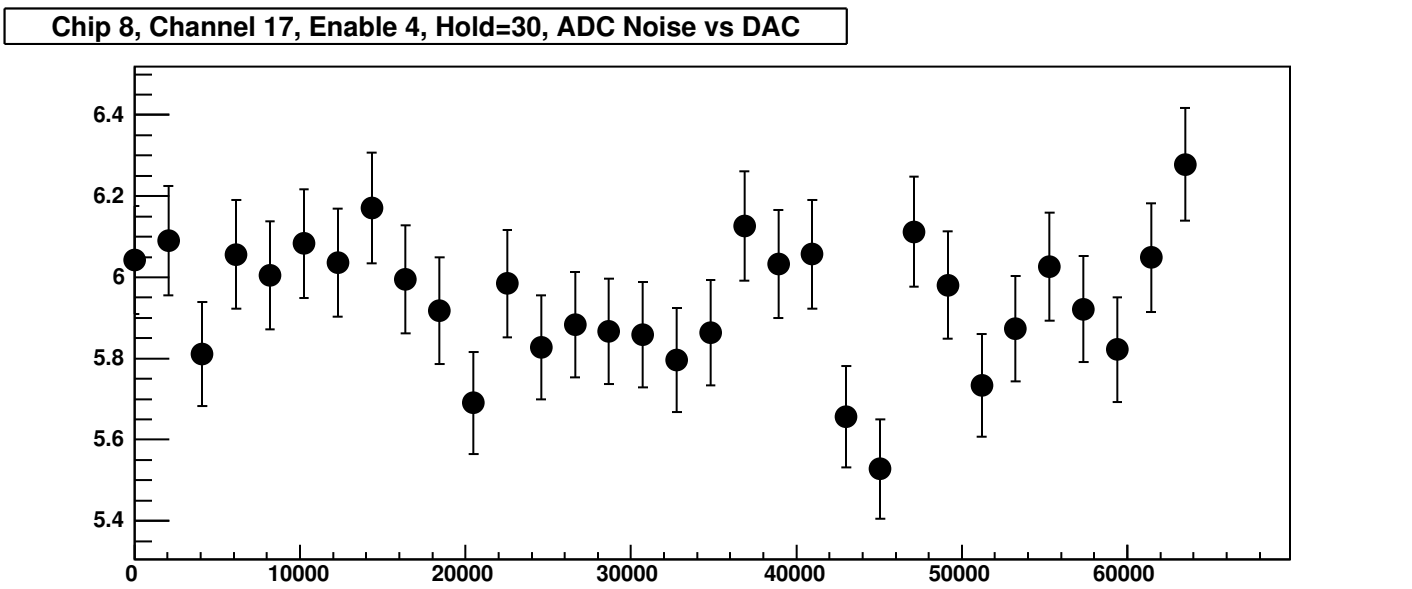

Chip 8, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

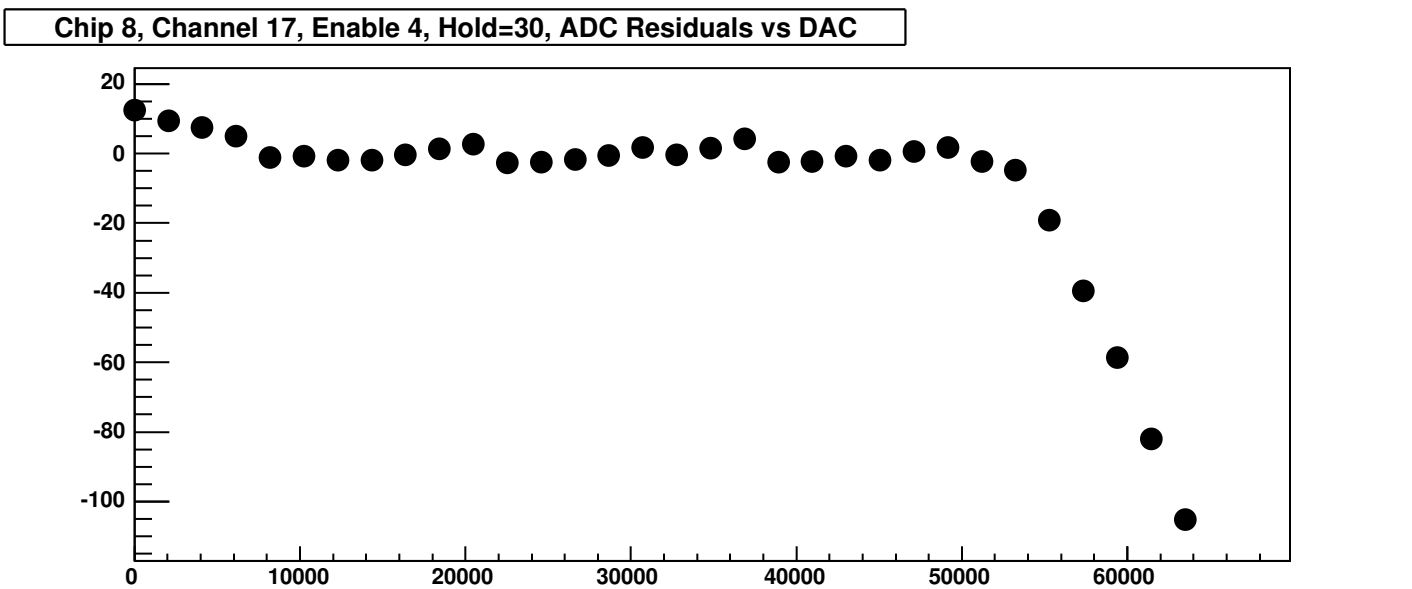

Chip 8, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 1934 / 15
p0 1195 ± 0.1131
p1 -0.001218 ± 4.562e-06
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

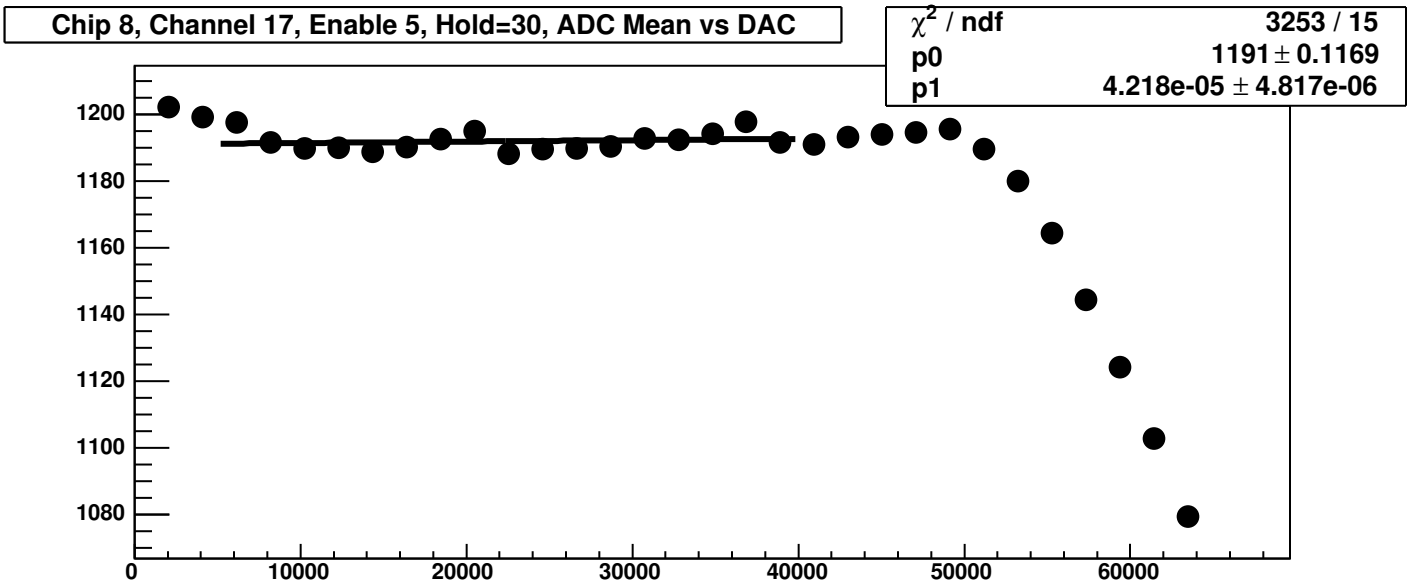

Chip 8, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

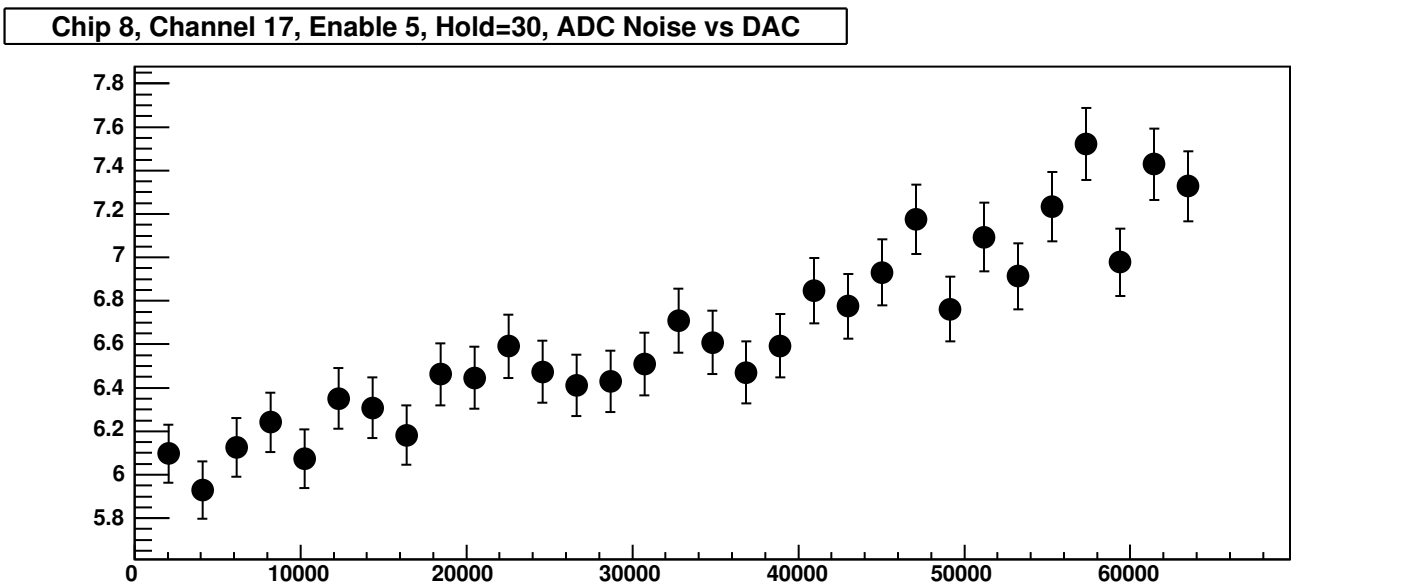

Chip 8, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

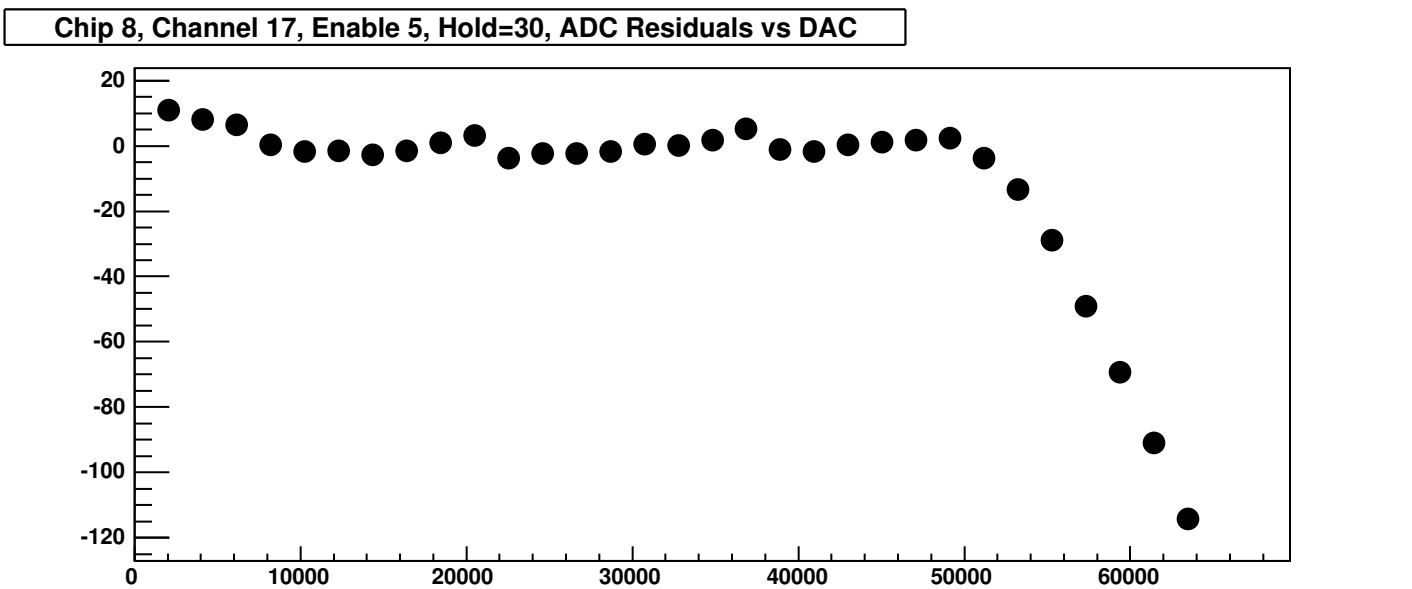

Chip 8, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 2572 / 15
p0 1195 ± 0.1123
p1 -0.001103 ± 4.538e-06
1220
1200
1180
1160
1140
1120
1100
1080
1060
1040
1020
0
10000
20000
30000
40000
50000
60000

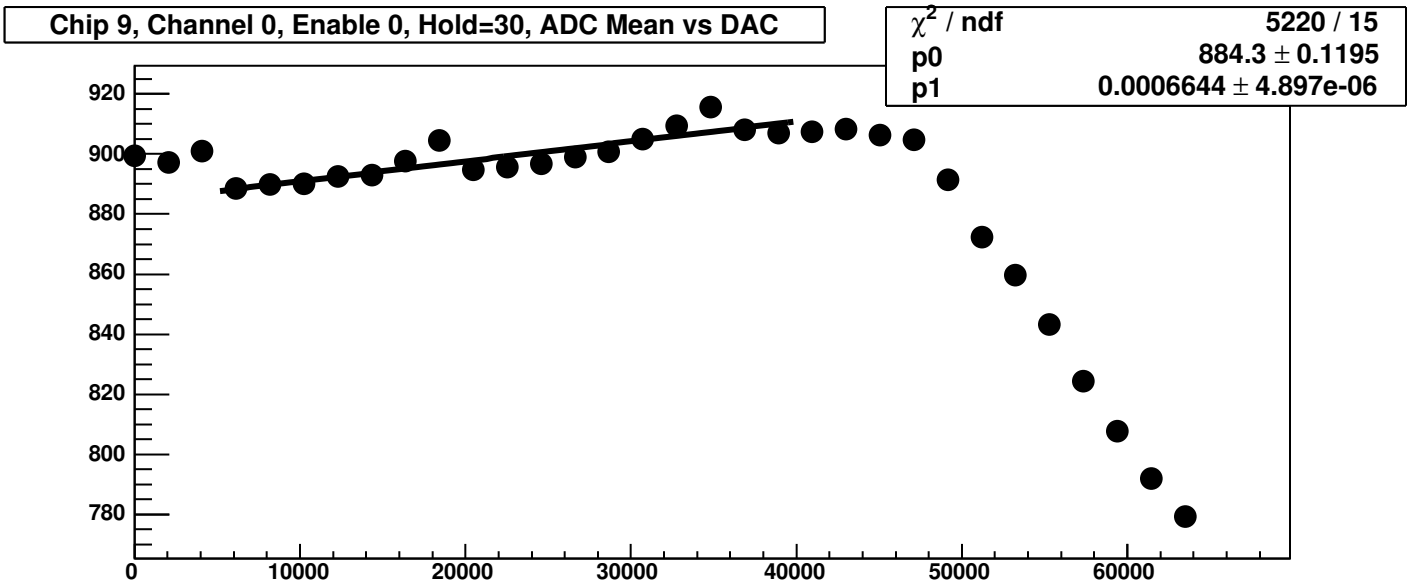

Chip 8, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

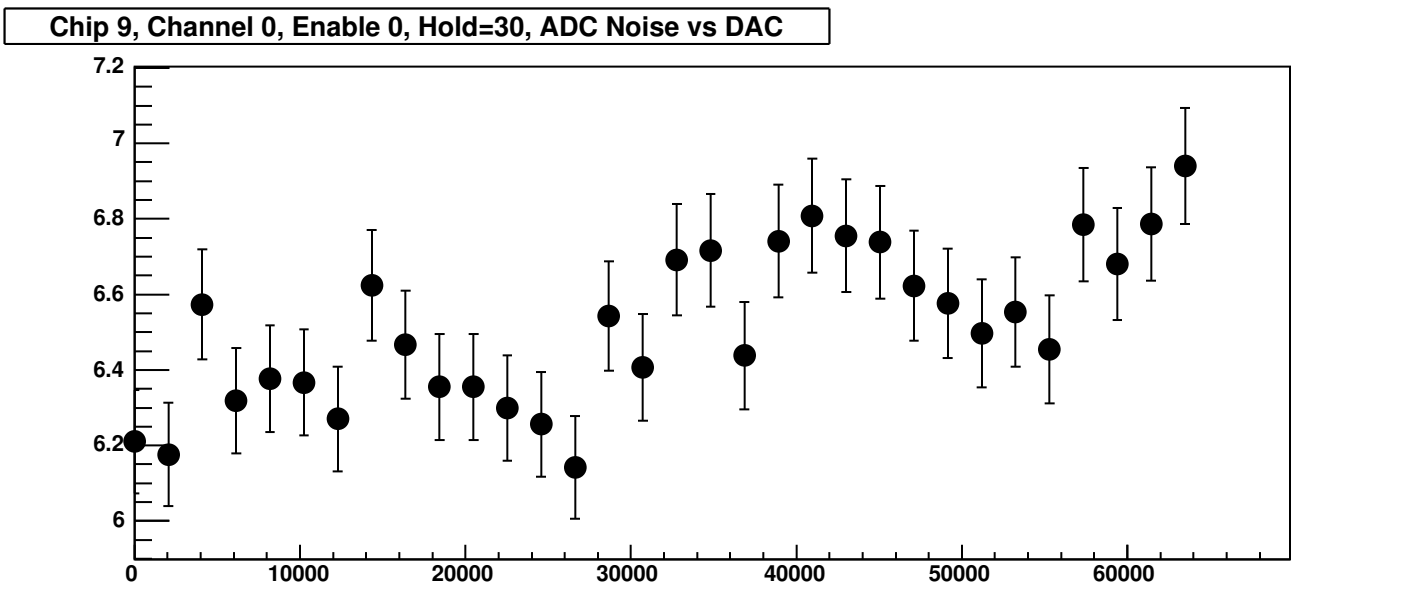

Chip 8, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

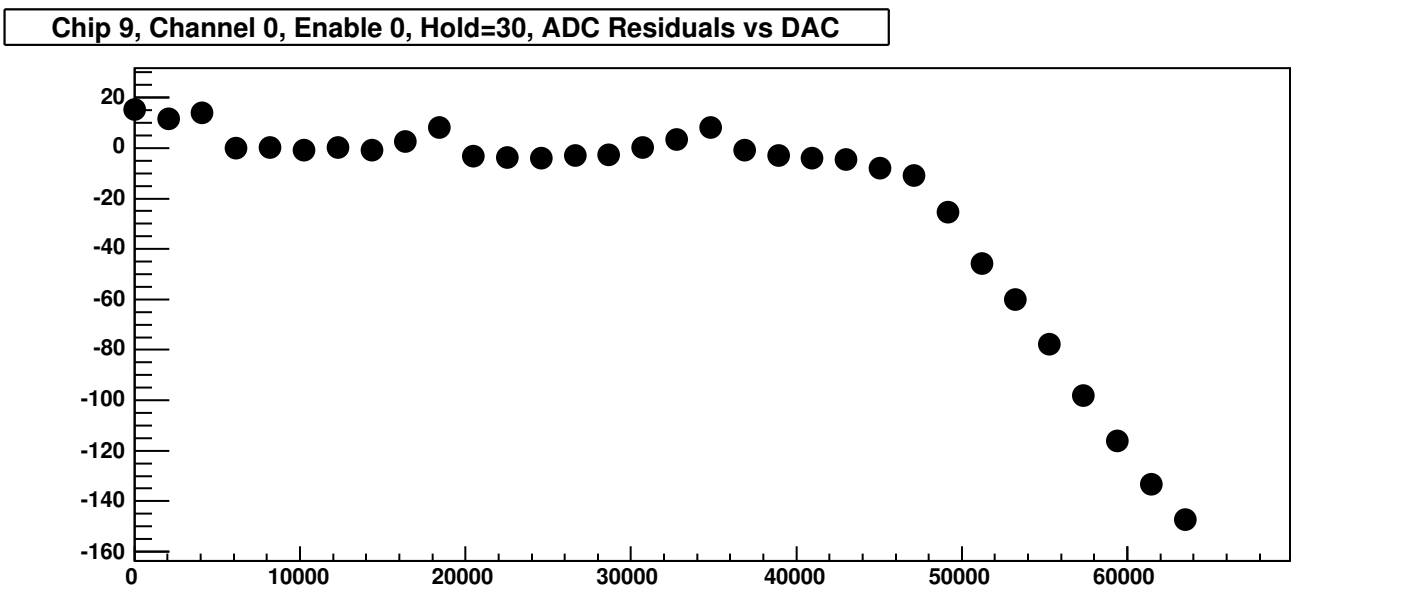

Chip 8, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 3253 / 15
p0 1191 ± 0.1169
p1 4.218e-05 ± 4.817e-06
1200
1180
1160
1140
1120
1100
1080
0
10000
20000
30000
40000
50000
60000

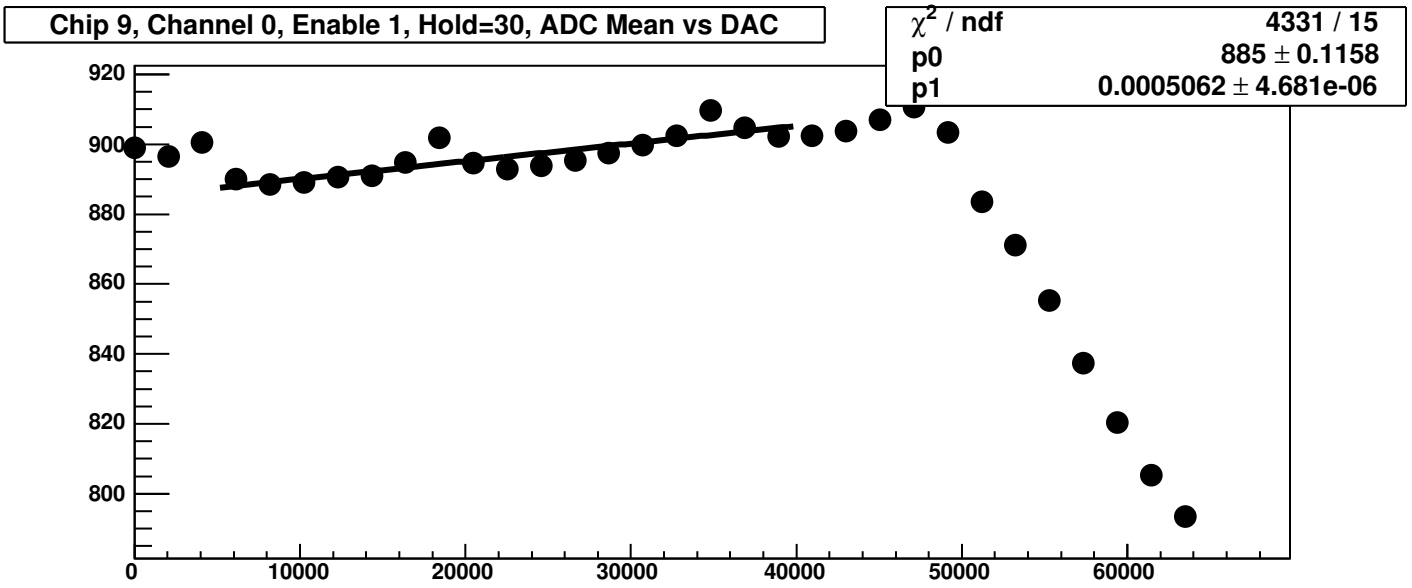

Chip 8, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

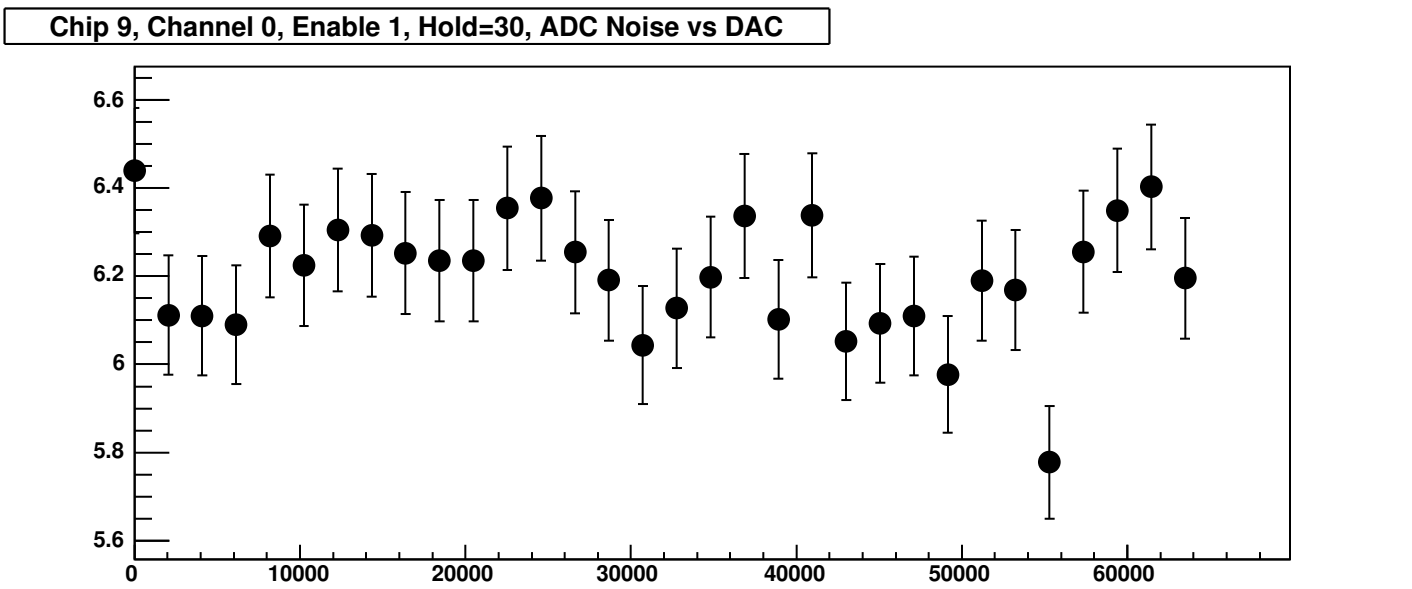

Chip 8, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

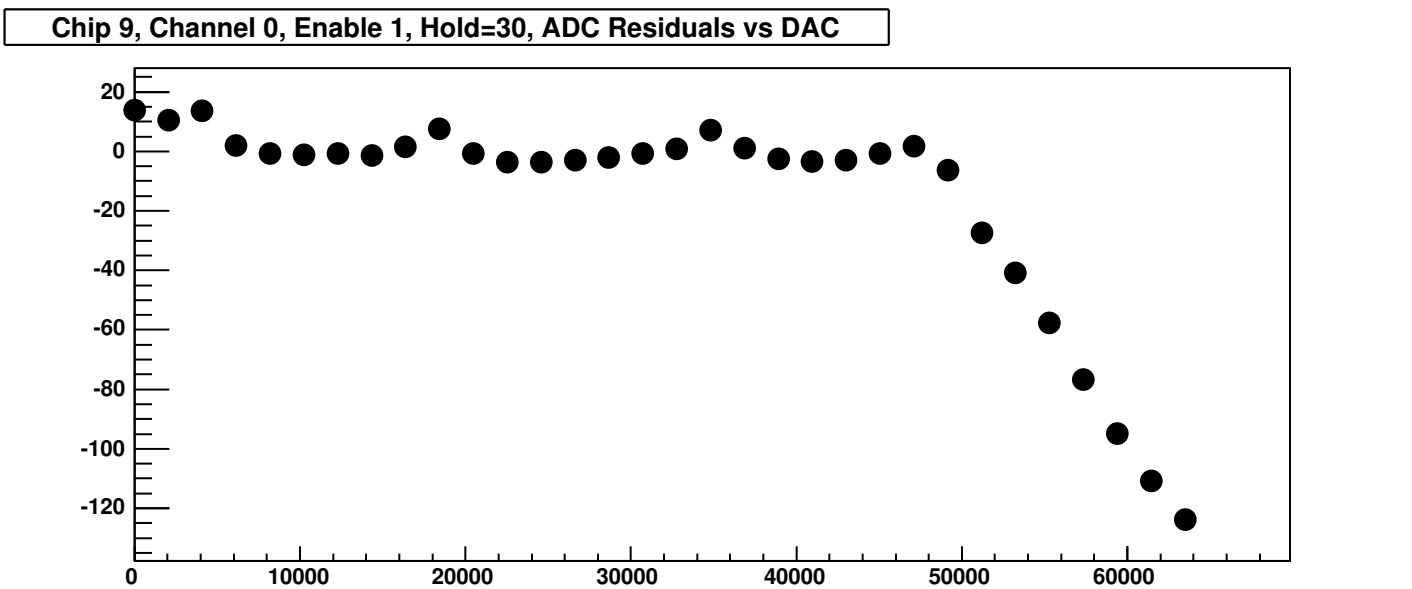

Chip 9, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5220 / 15
p0 884.3 ± 0.1195
p1 0.0006644 ± 4.897e-06
920
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

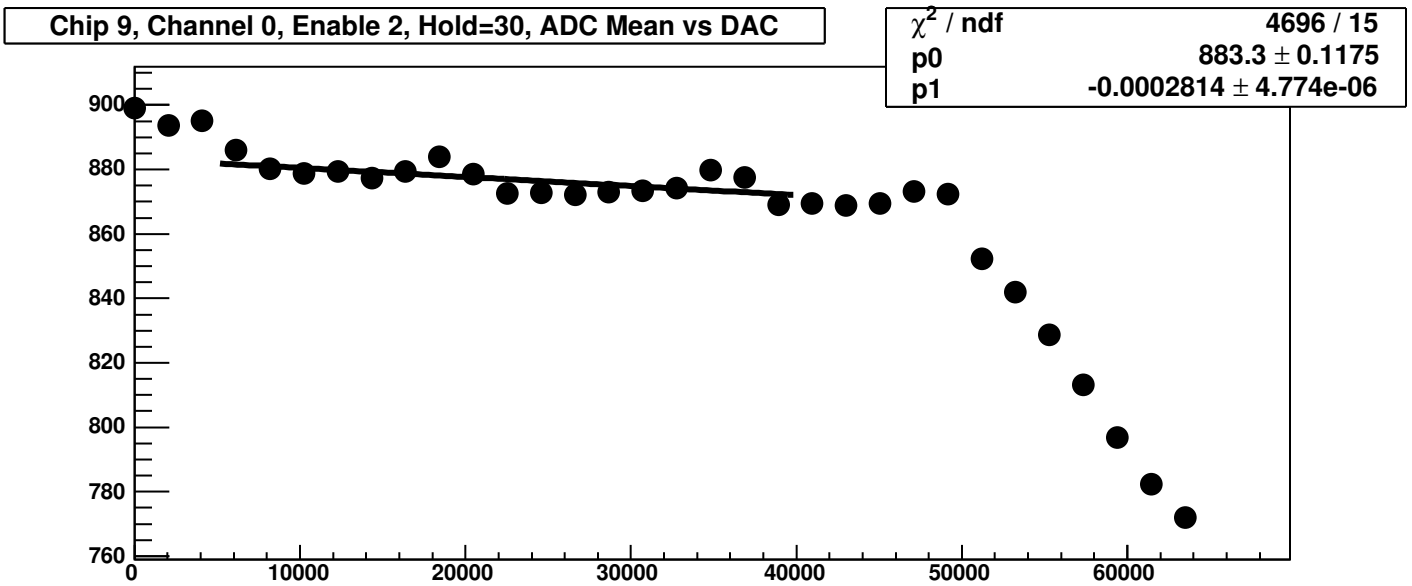

Chip 9, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

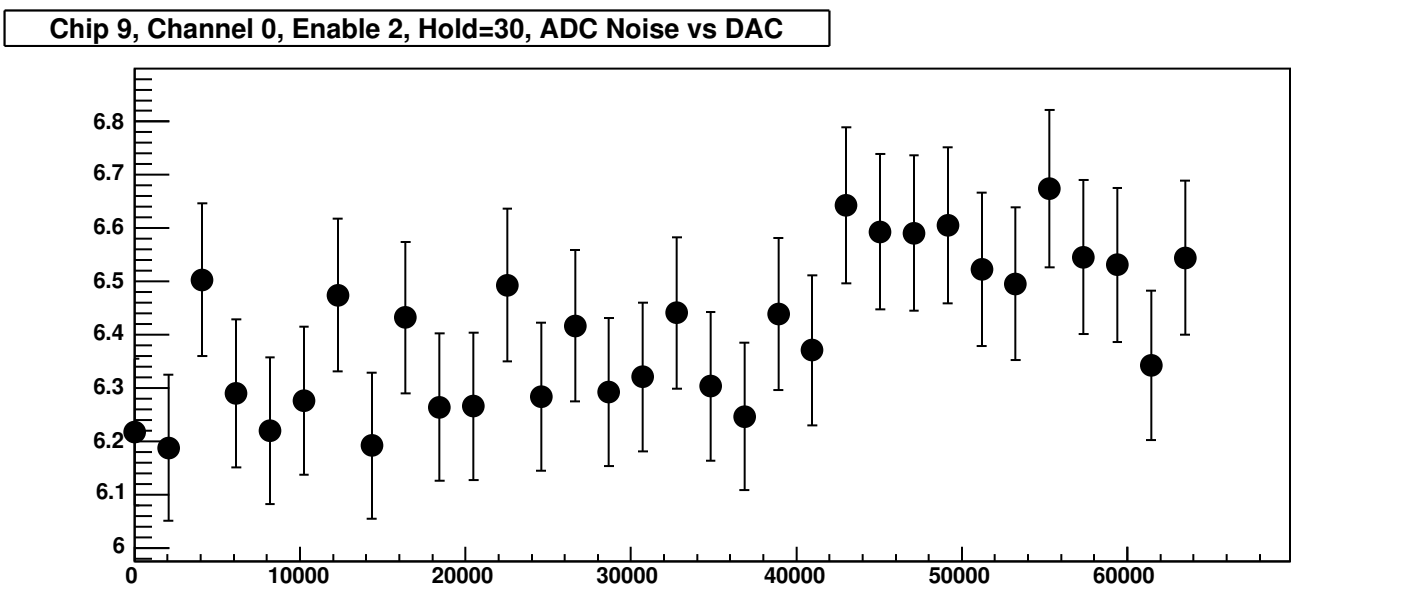

Chip 9, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

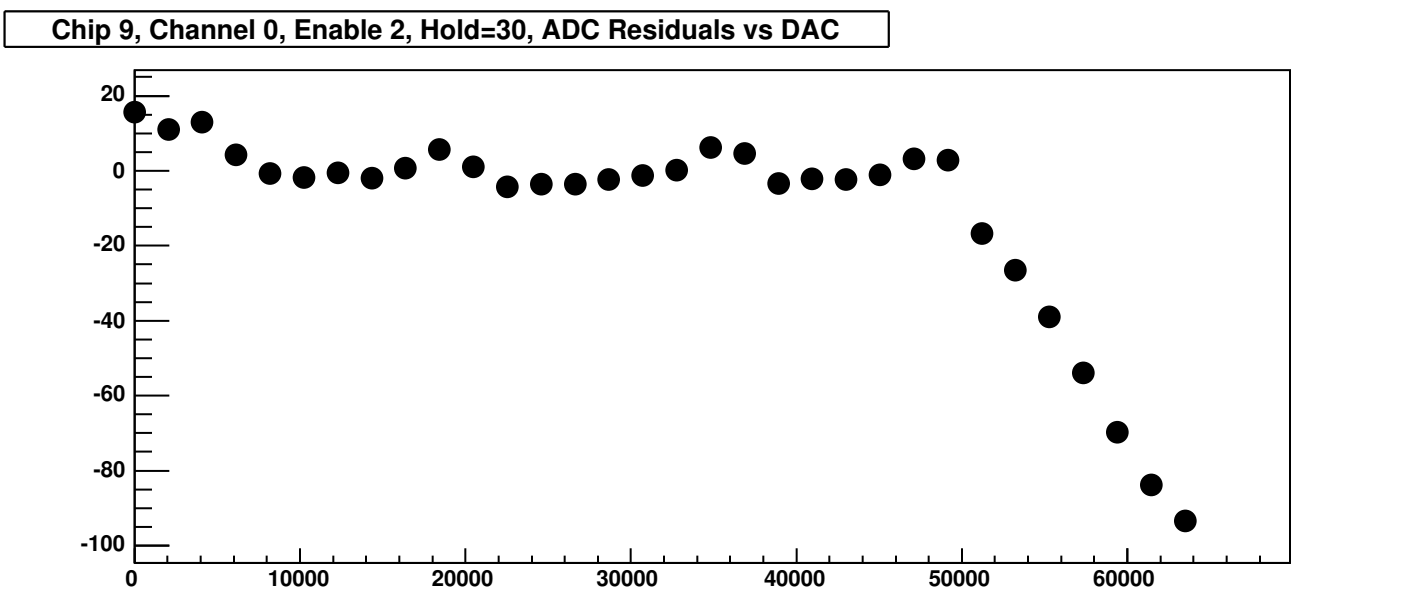

Chip 9, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4331 / 15
p0 885 ± 0.1158
p1 0.0005062 ± 4.681e-06
920
900
880
860
840
820
800
0
10000
20000
30000
40000
50000
60000

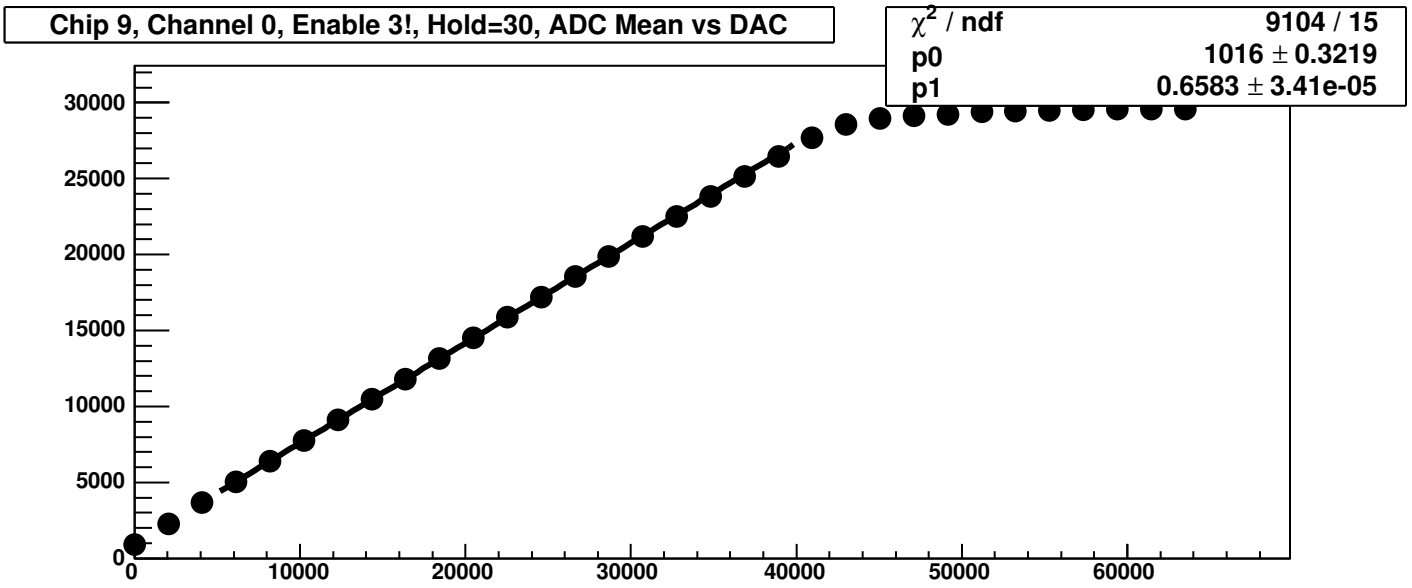

Chip 9, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

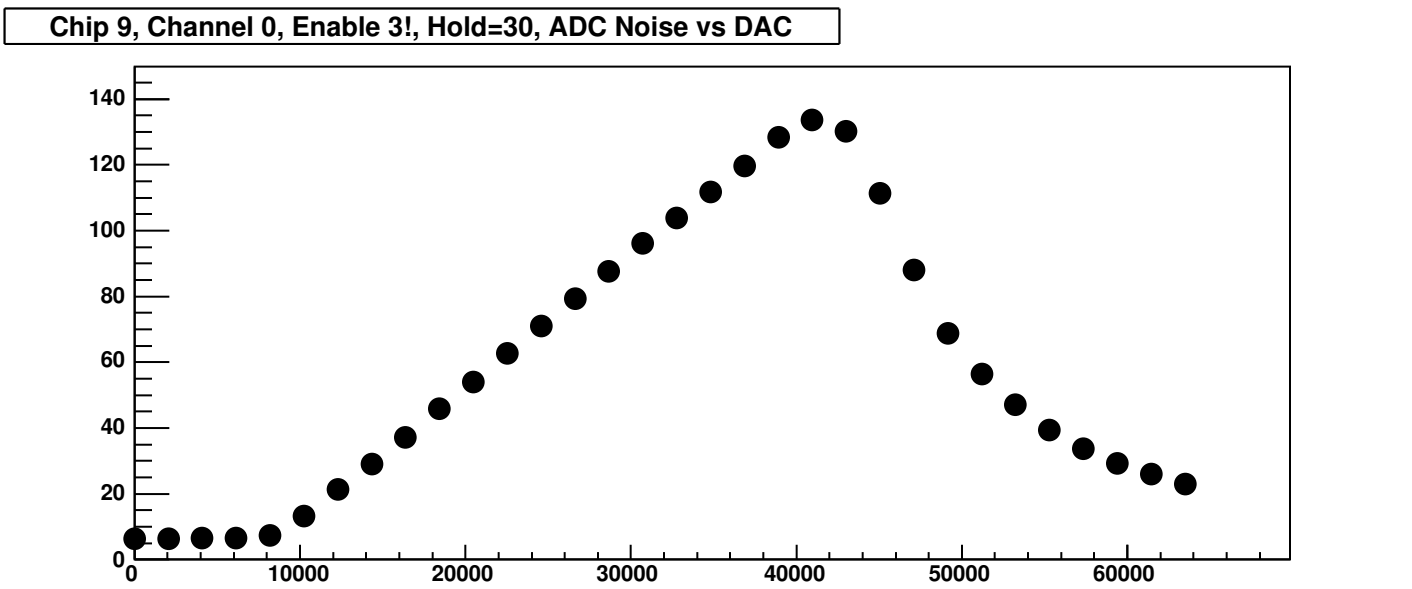

Chip 9, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

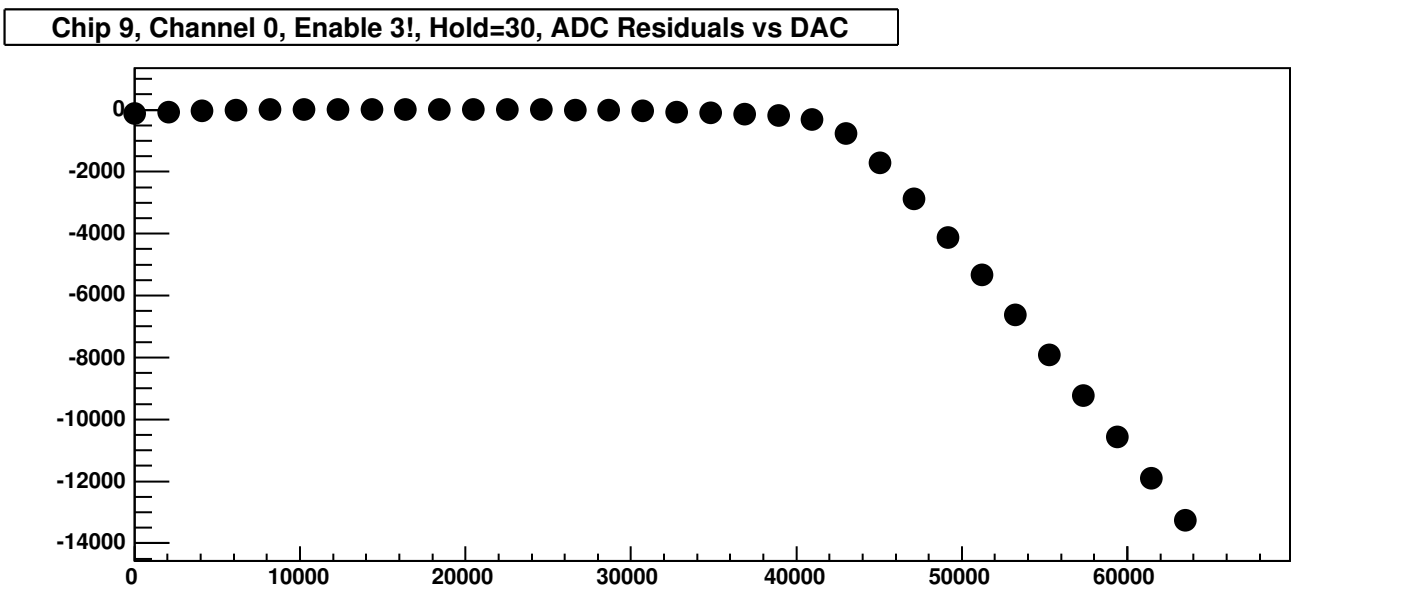

Chip 9, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4696 / 15
p0 883.3 ± 0.1175
p1 -0.0002814 ± 4.774e-06
900
880
860
840
820
800
780
760
0
10000
20000
30000
40000
50000
60000

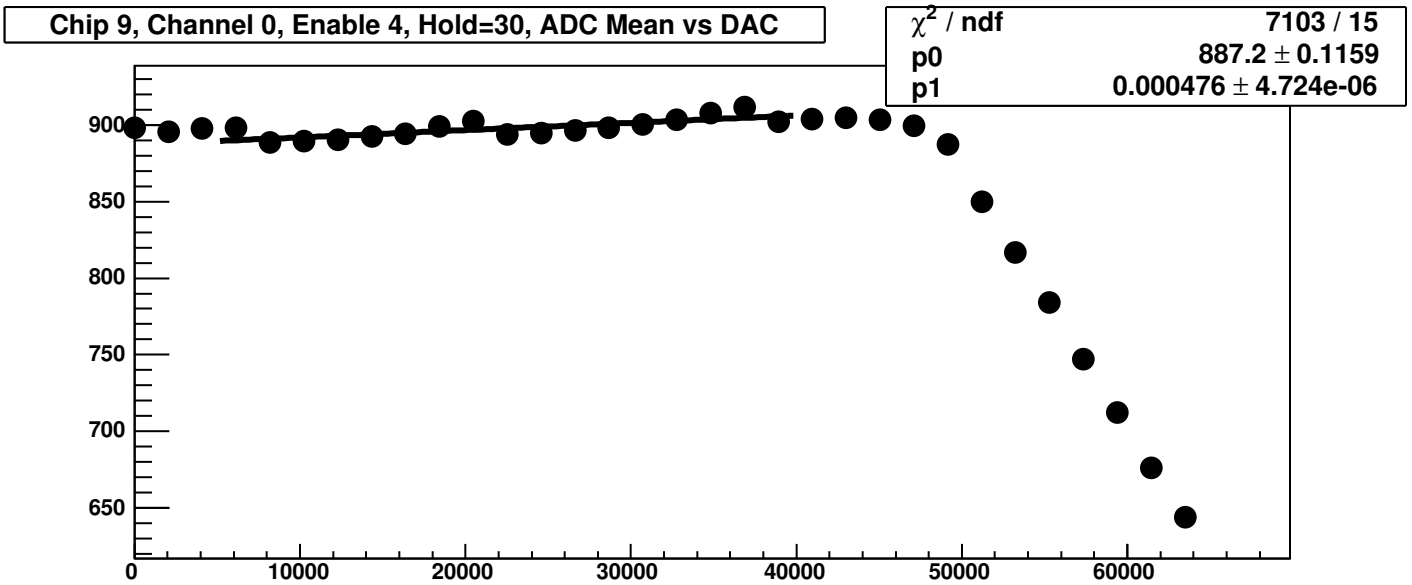

Chip 9, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.7
6.6
6.5
6.4
6.3
6.2
6.1
6
0
10000
20000
30000
40000
50000
60000

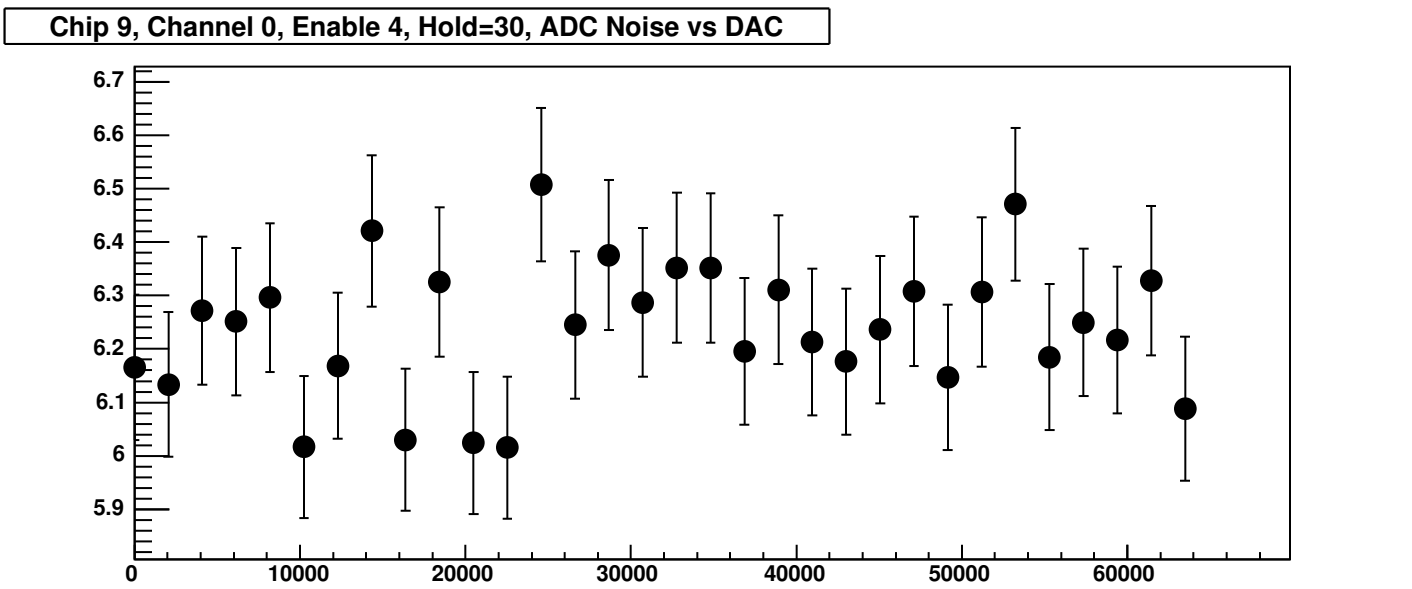

Chip 9, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

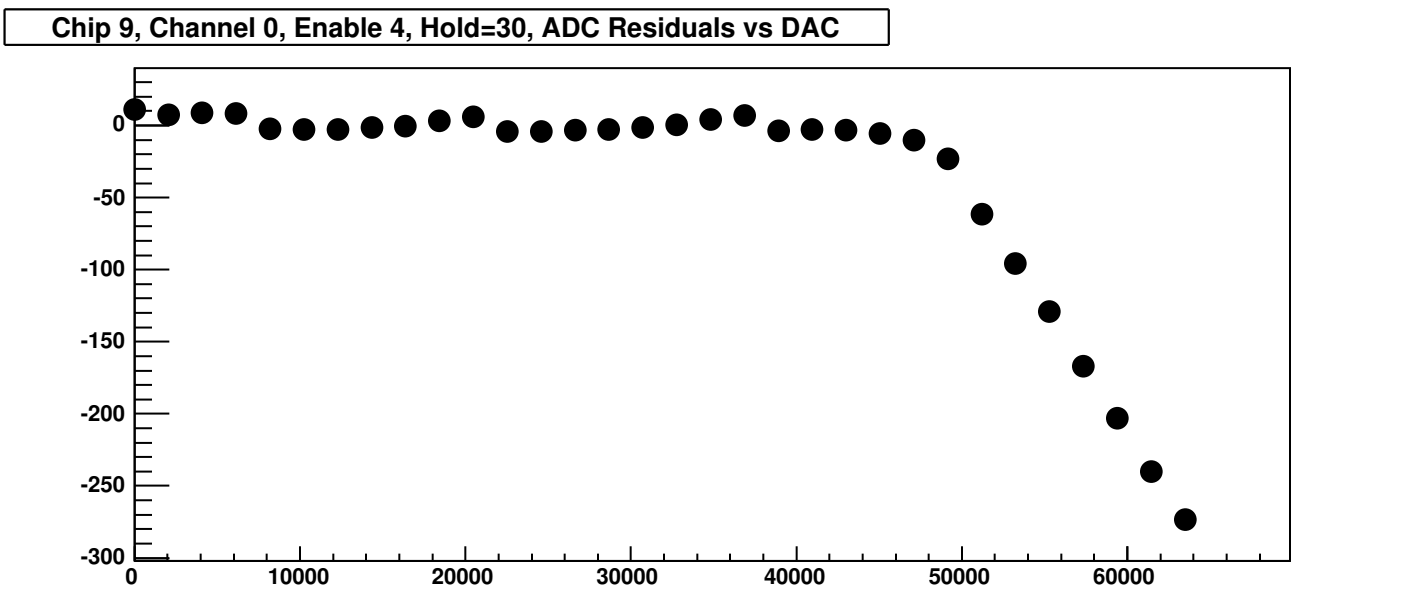

Chip 9, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 9104 / 15
p0 1016 ± 0.3219
p1 0.6583 ± 3.41e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

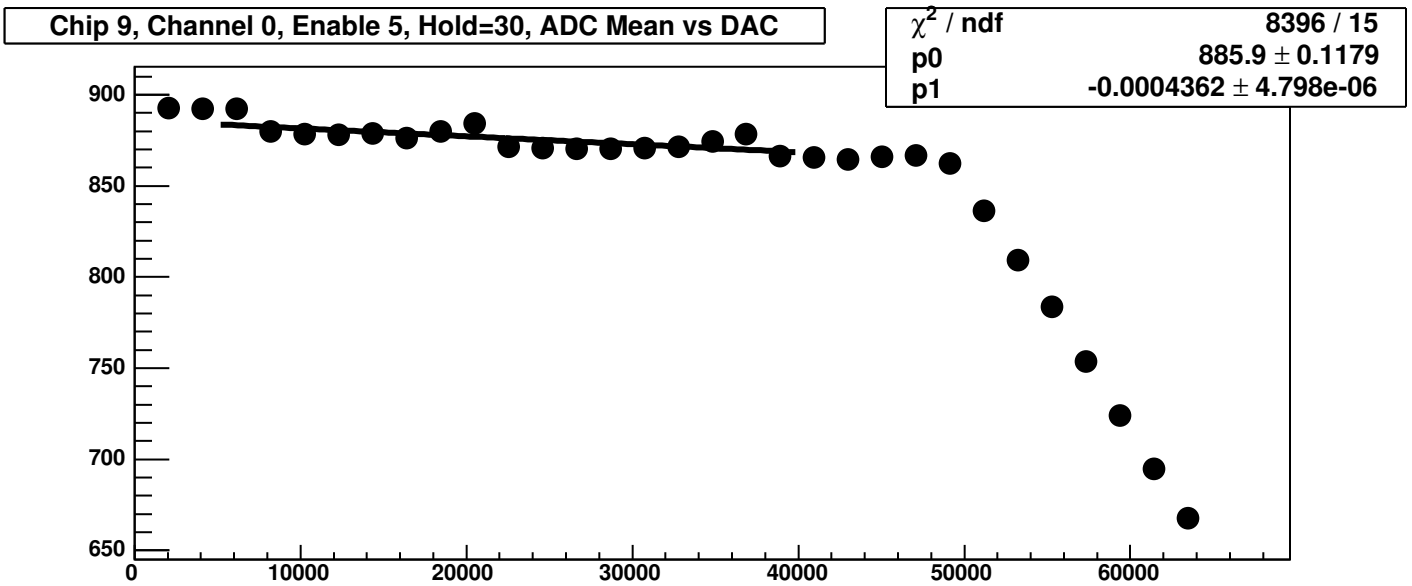

Chip 9, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

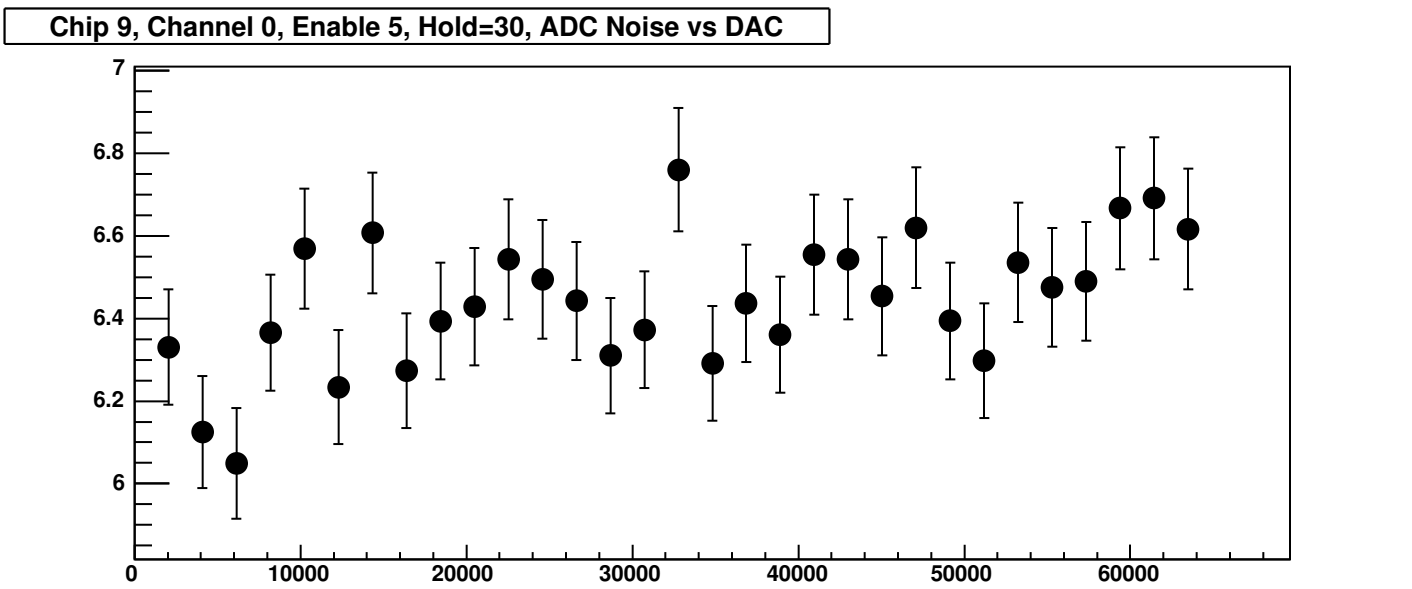

Chip 9, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

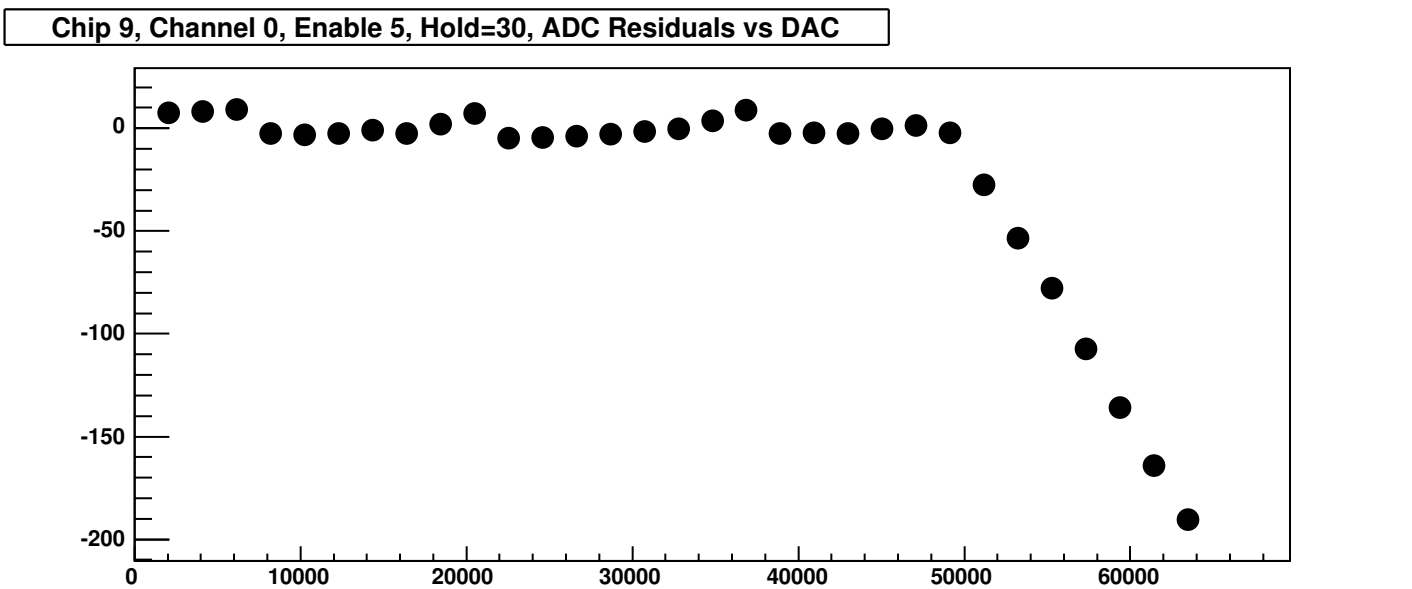

Chip 9, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7103 / 15
p0 887.2 ± 0.1159
p1 0.000476 ± 4.724e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

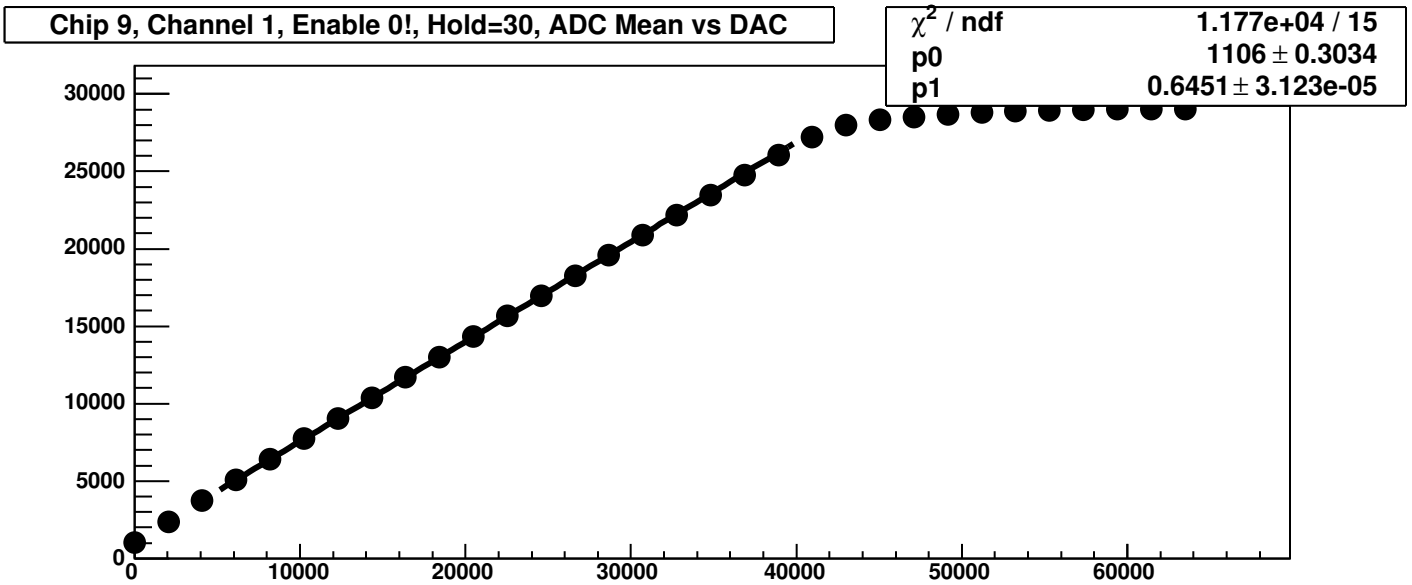

Chip 9, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
6.7
6.6
6.5
6.4
6.3
6.2
6.1
6
5.9
0
10000
20000
30000
40000
50000
60000

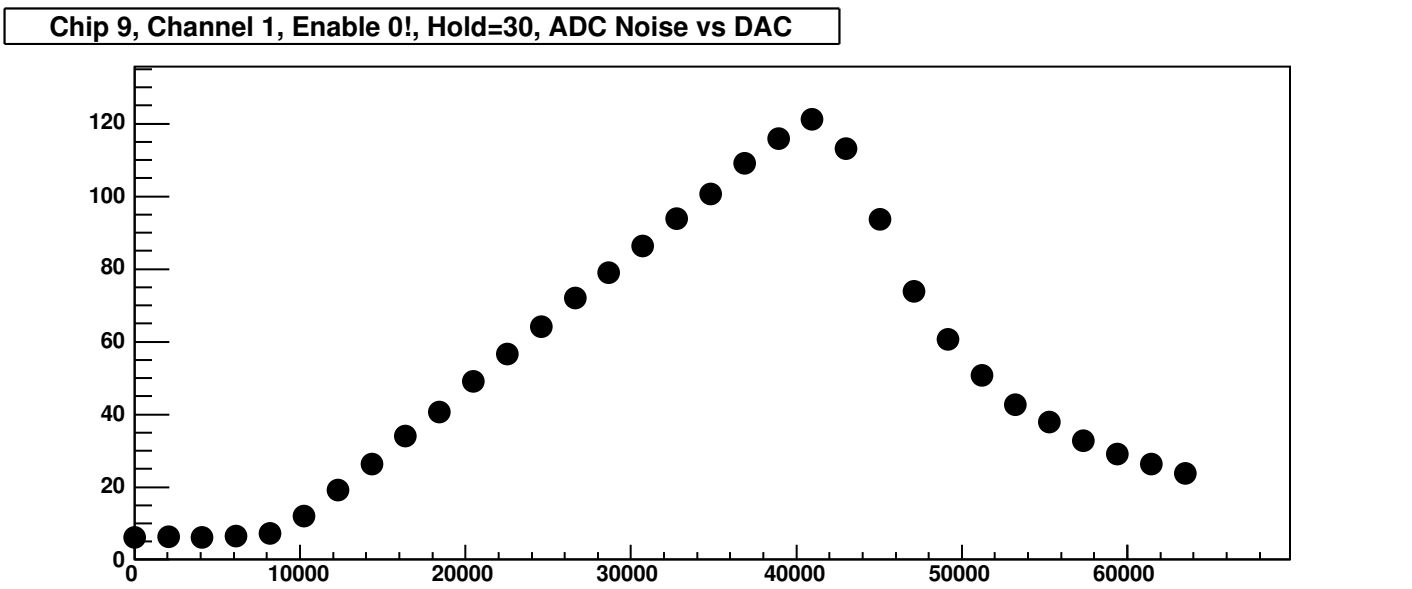

Chip 9, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

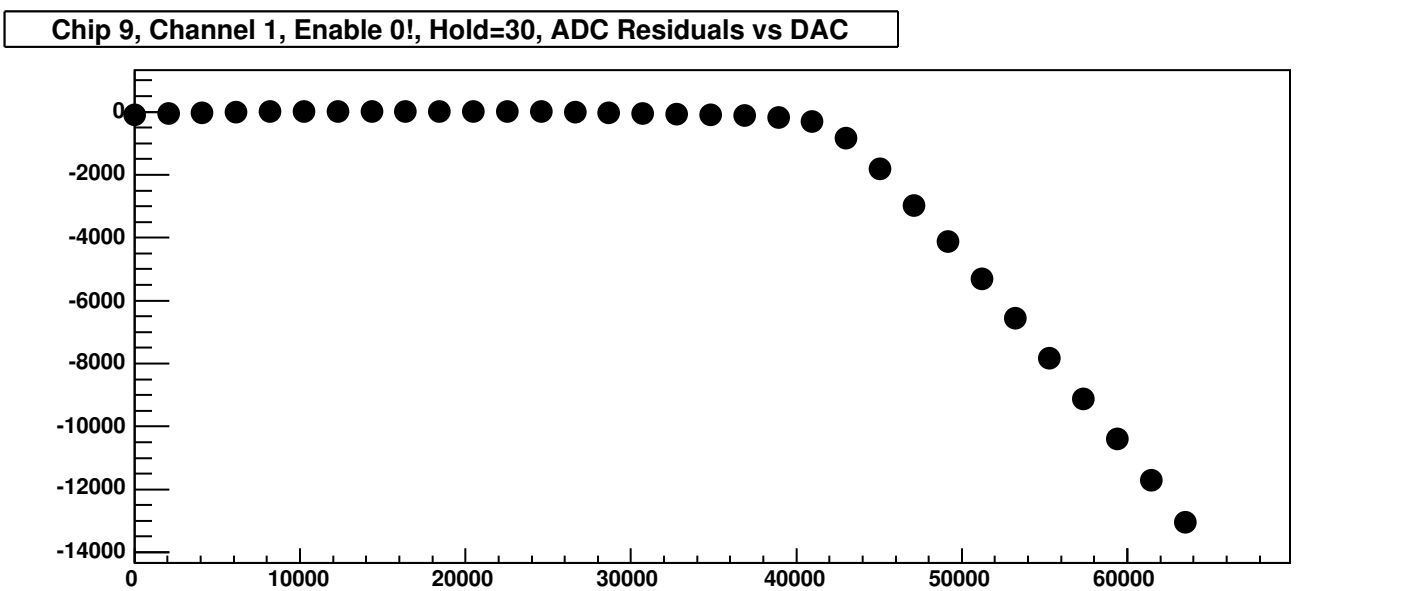

Chip 9, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8396 / 15
p0 885.9 ± 0.1179
p1 -0.0004362 ± 4.798e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

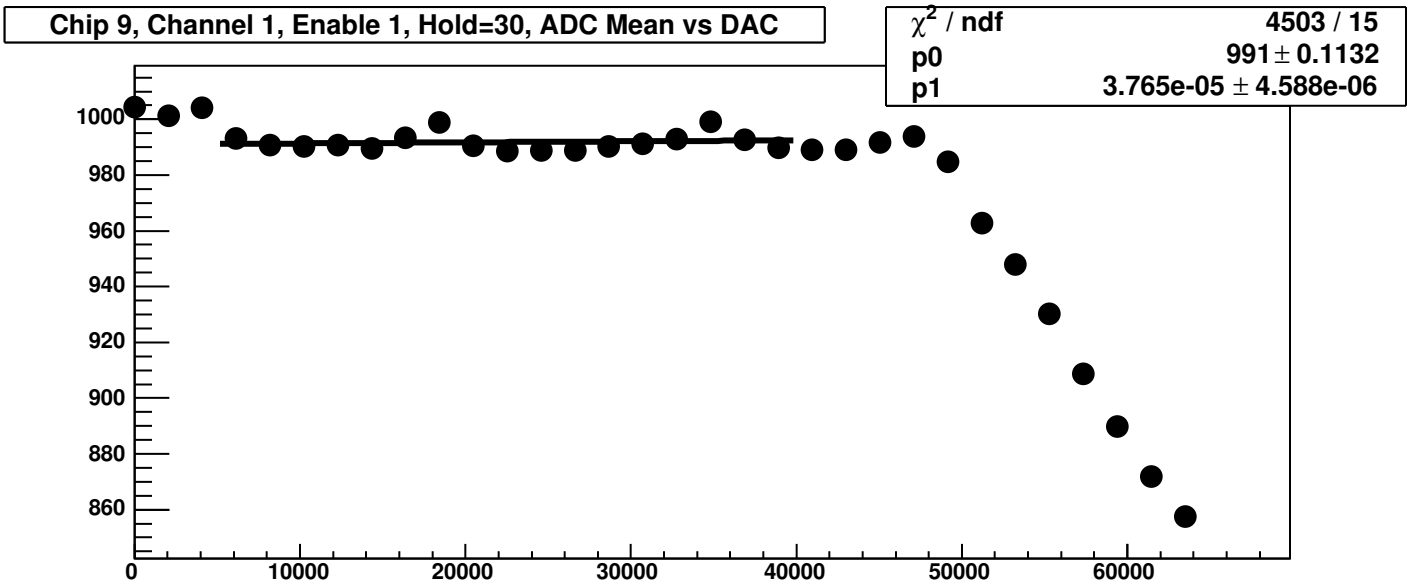

Chip 9, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.177e+04 / 15
p0 1106 ± 0.3034
p1 0.6451 ± 3.123e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

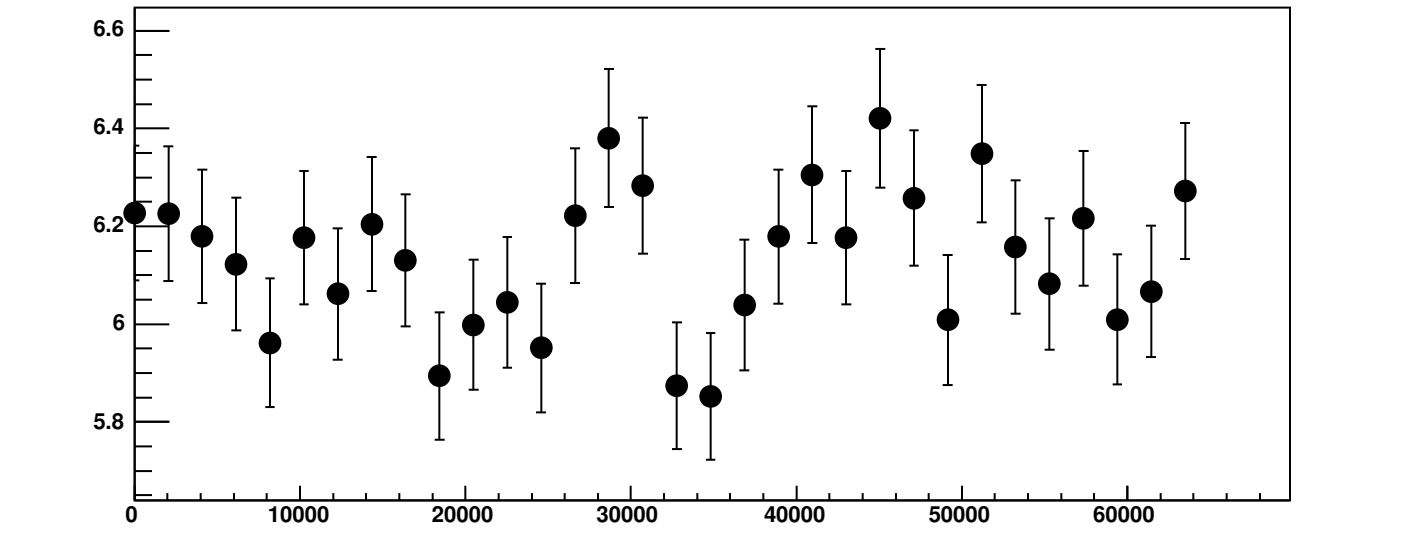

Chip 9, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

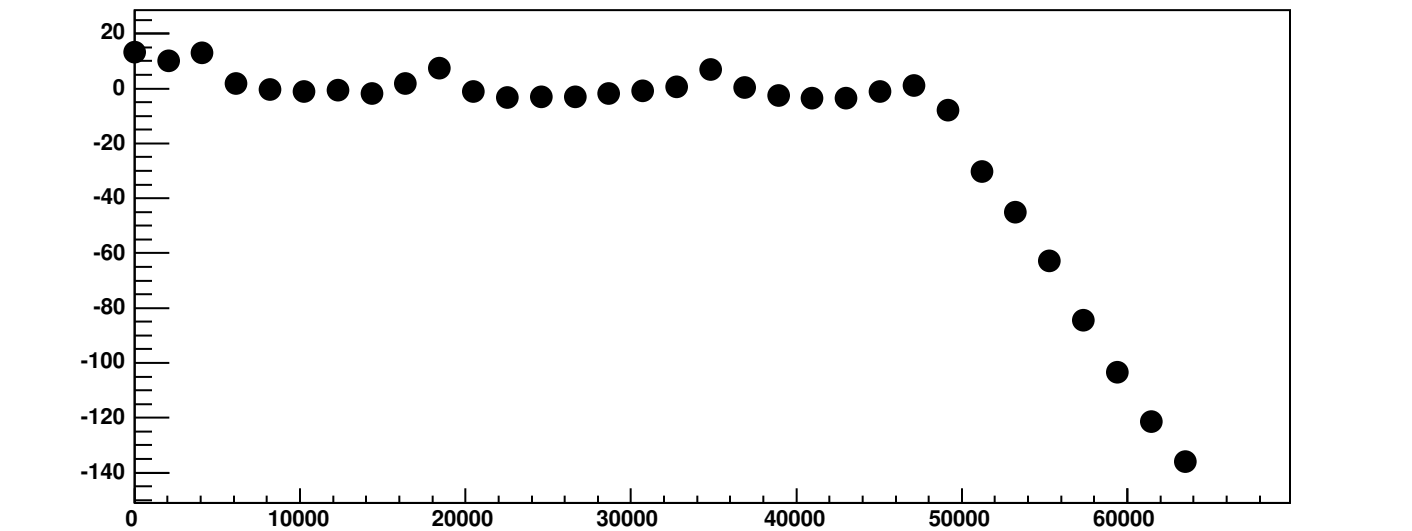
Chip 9, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

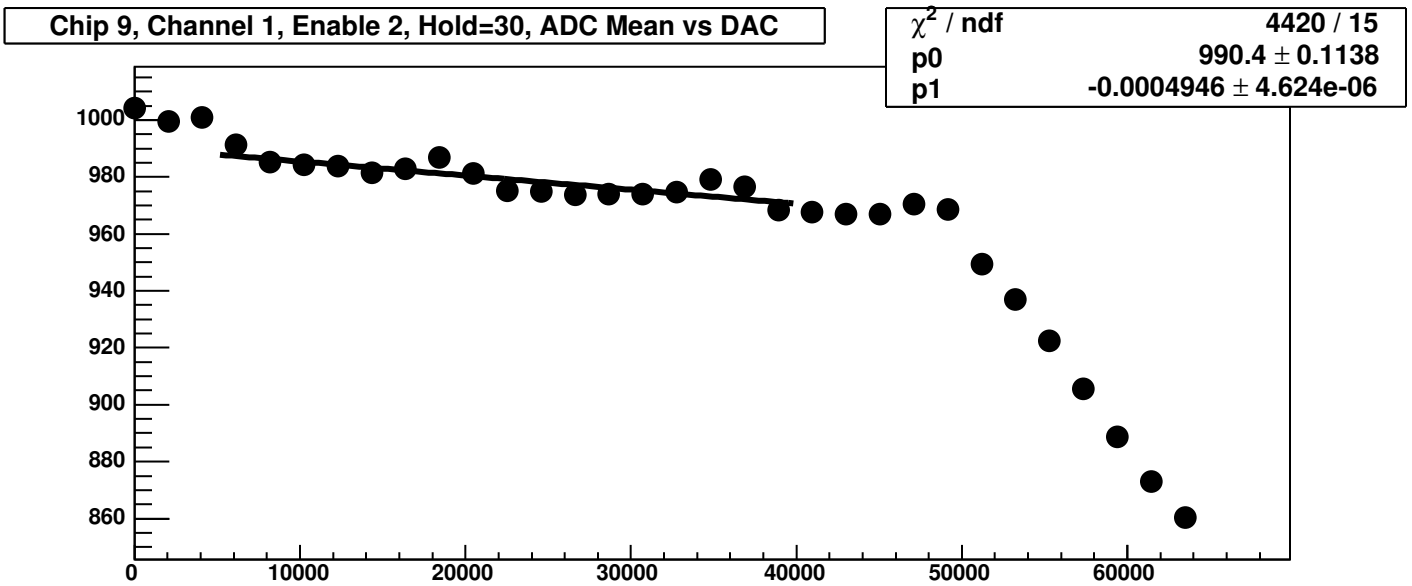


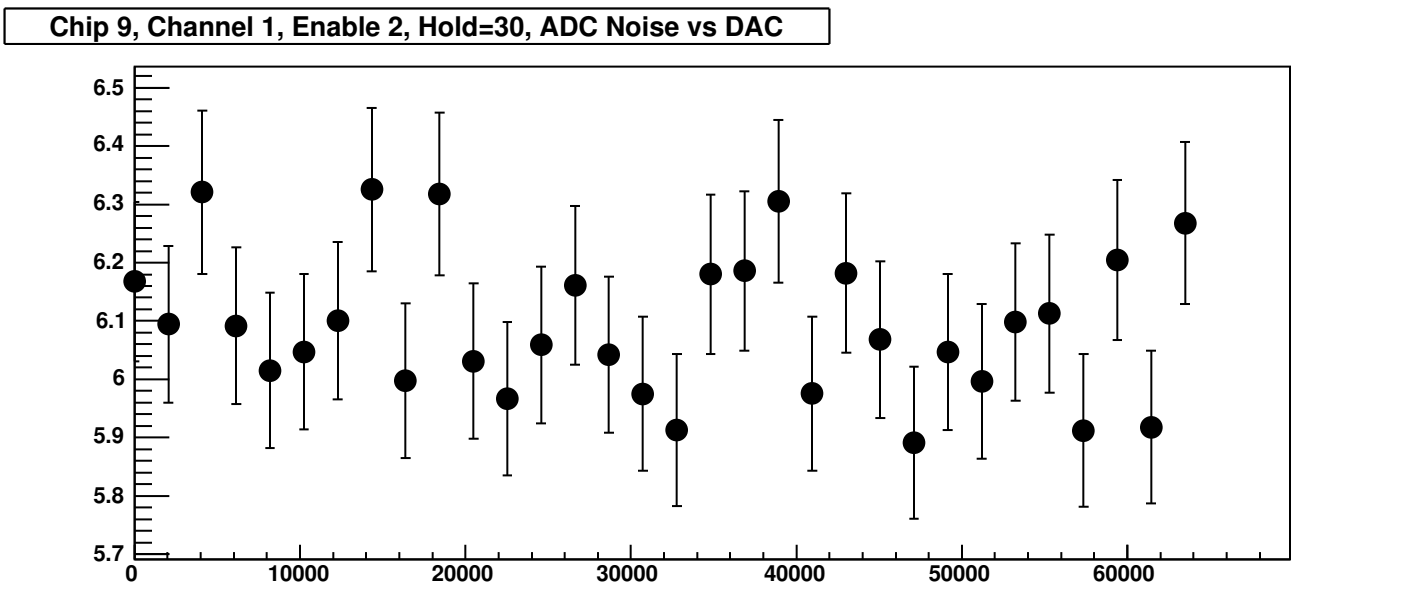
Chip 9, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC

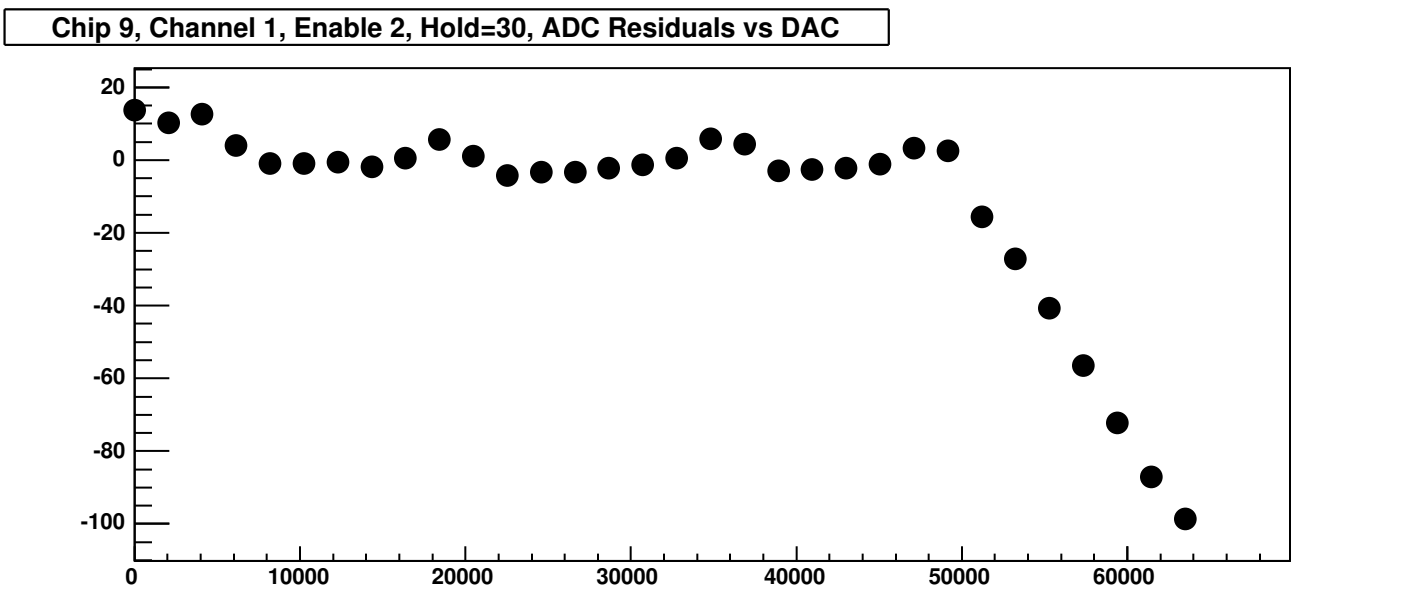


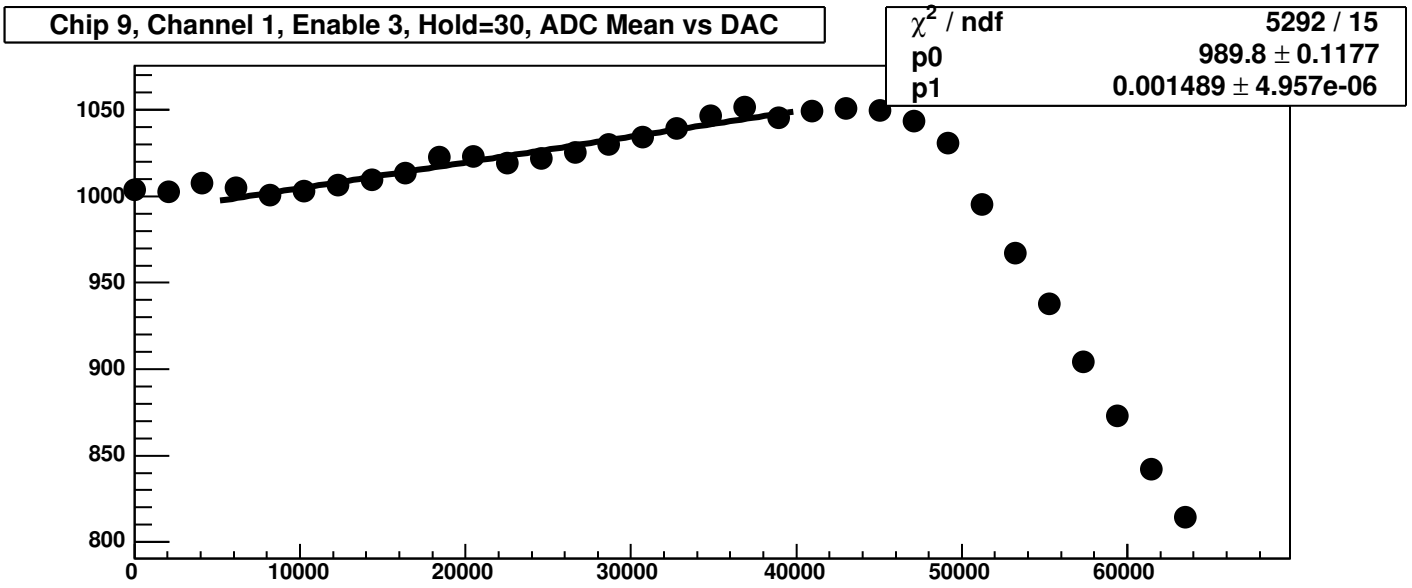
Chip 9, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

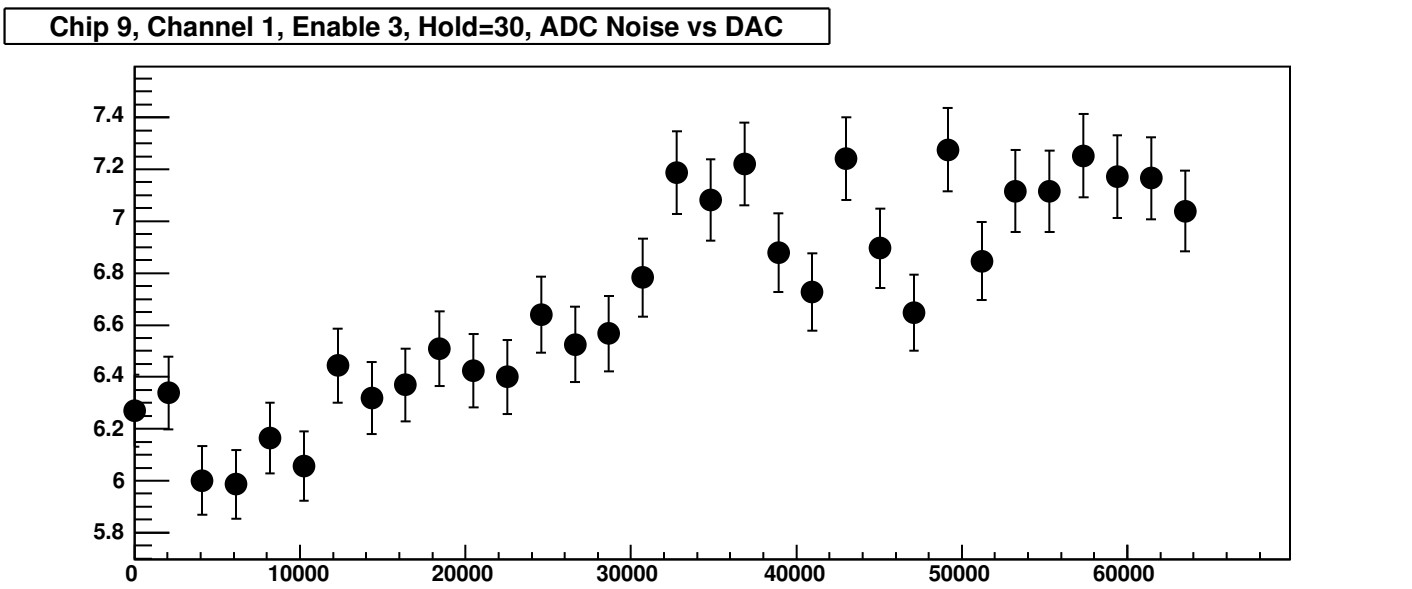


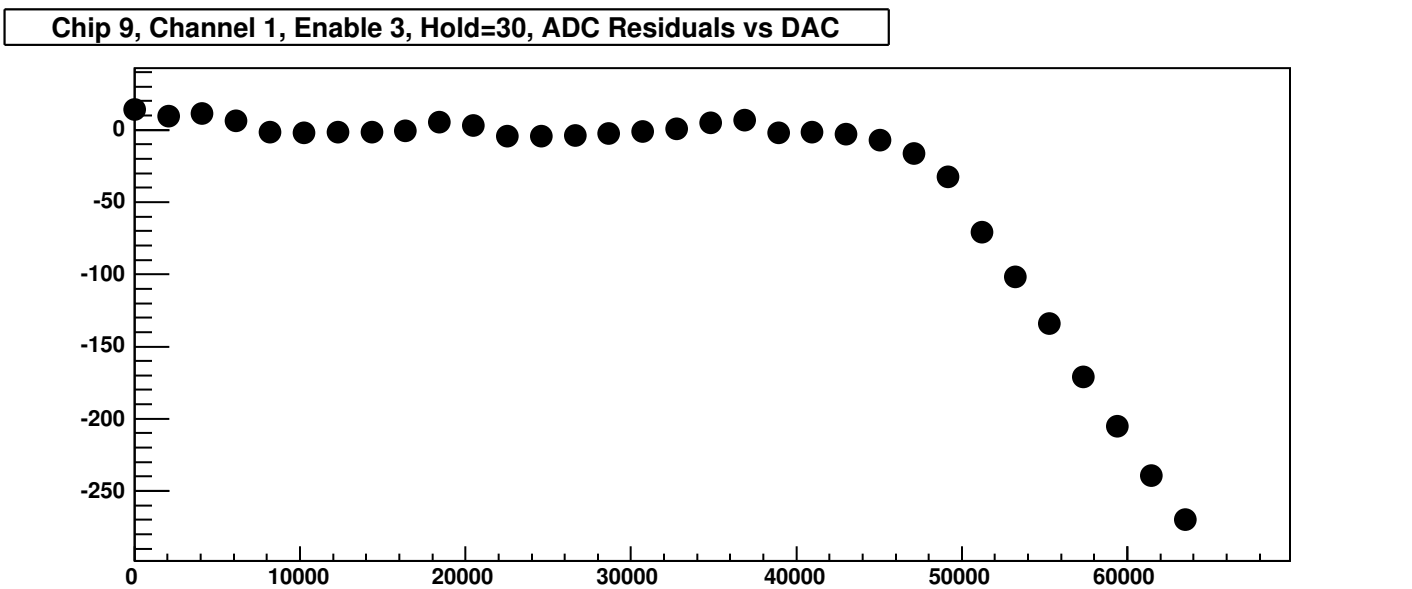

Chip 9, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4420 / 15
p0 990.4 ± 0.1138
p1 -0.0004946 ± 4.624e-06
1000
980
960
940
920
900
880
860
0
10000
20000
30000
40000
50000
60000

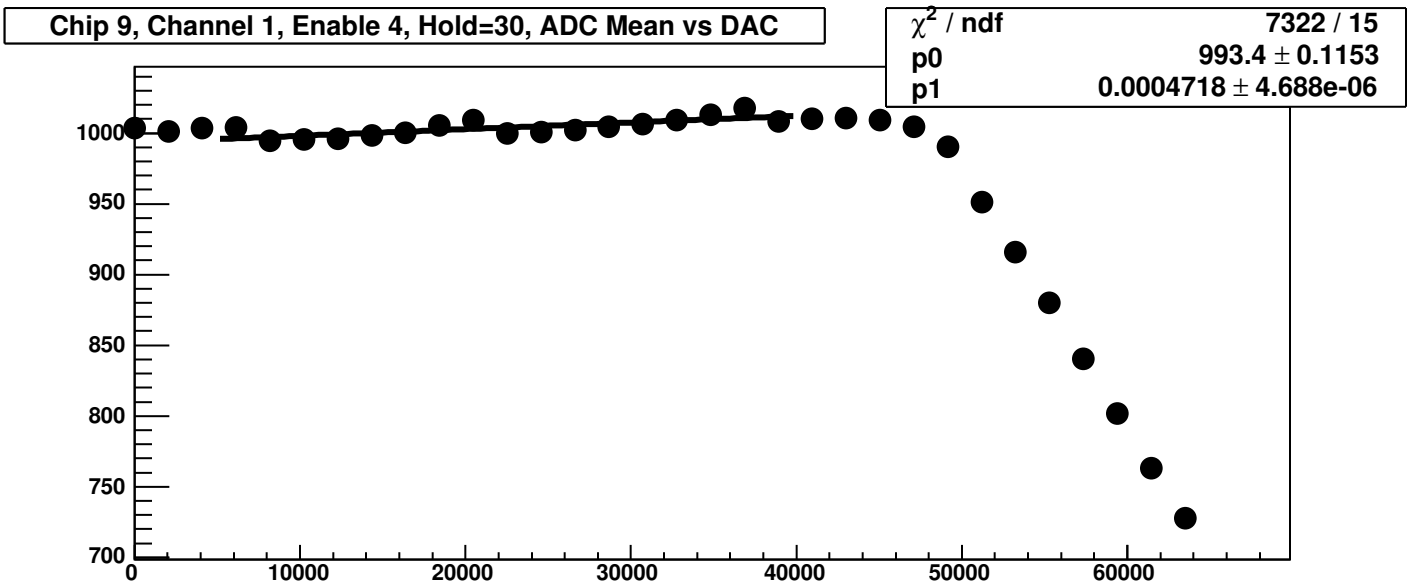

Chip 9, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
0
10000
20000
30000
40000
50000
60000

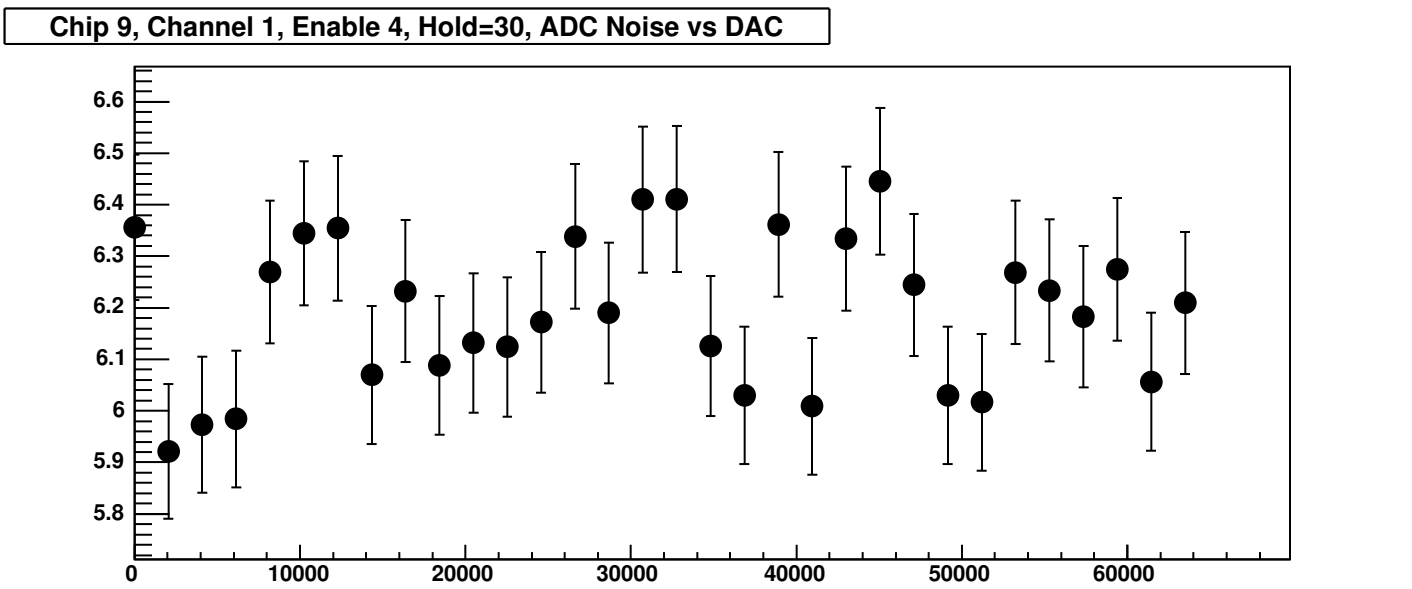

Chip 9, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

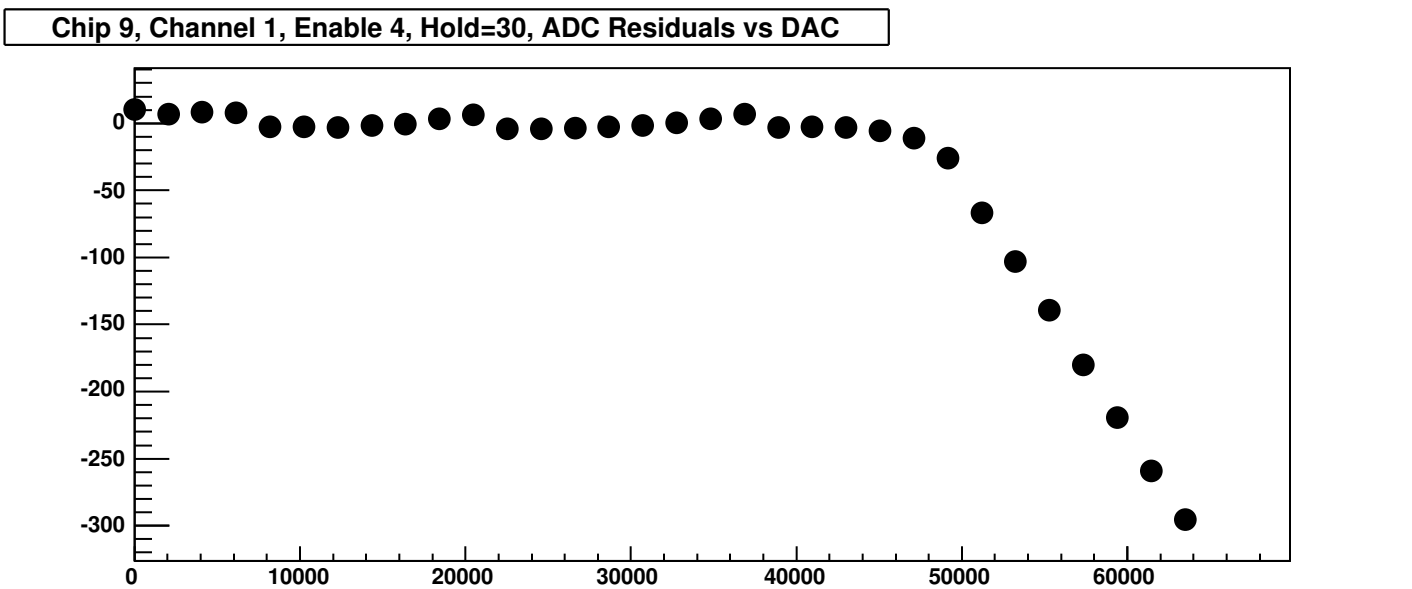

Chip 9, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5292 / 15
p0 989.8 ± 0.1177
p1 0.001489 ± 4.957e-06
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

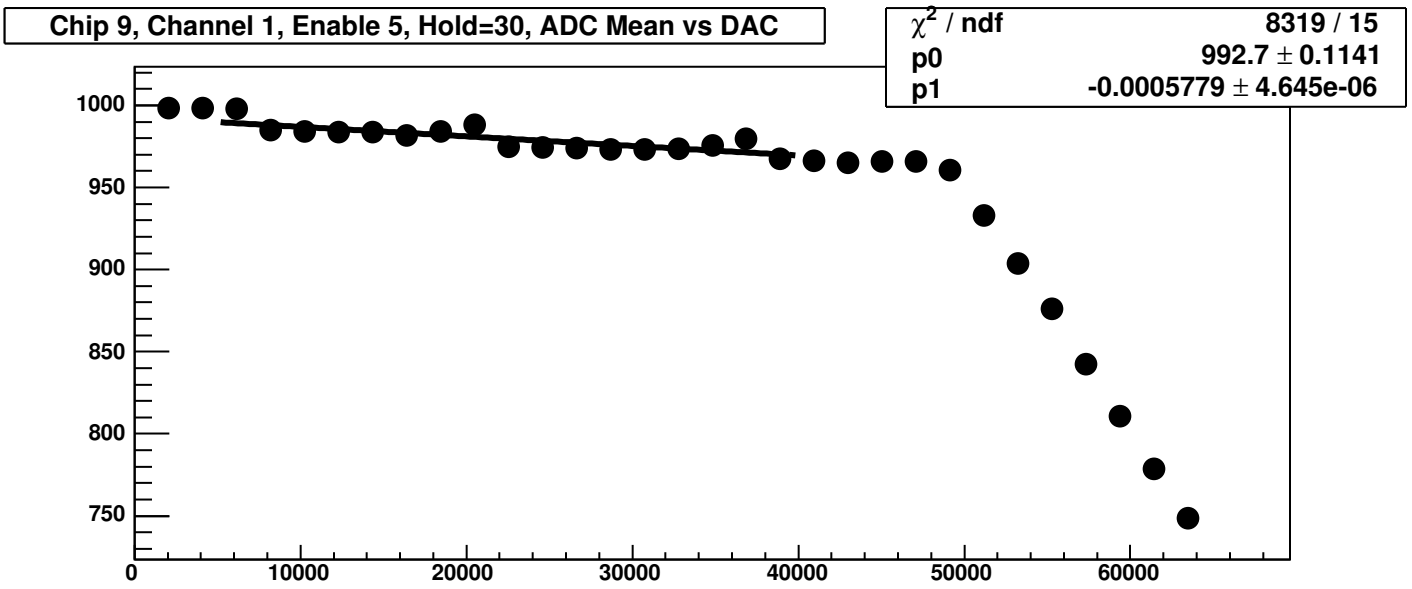

Chip 9, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

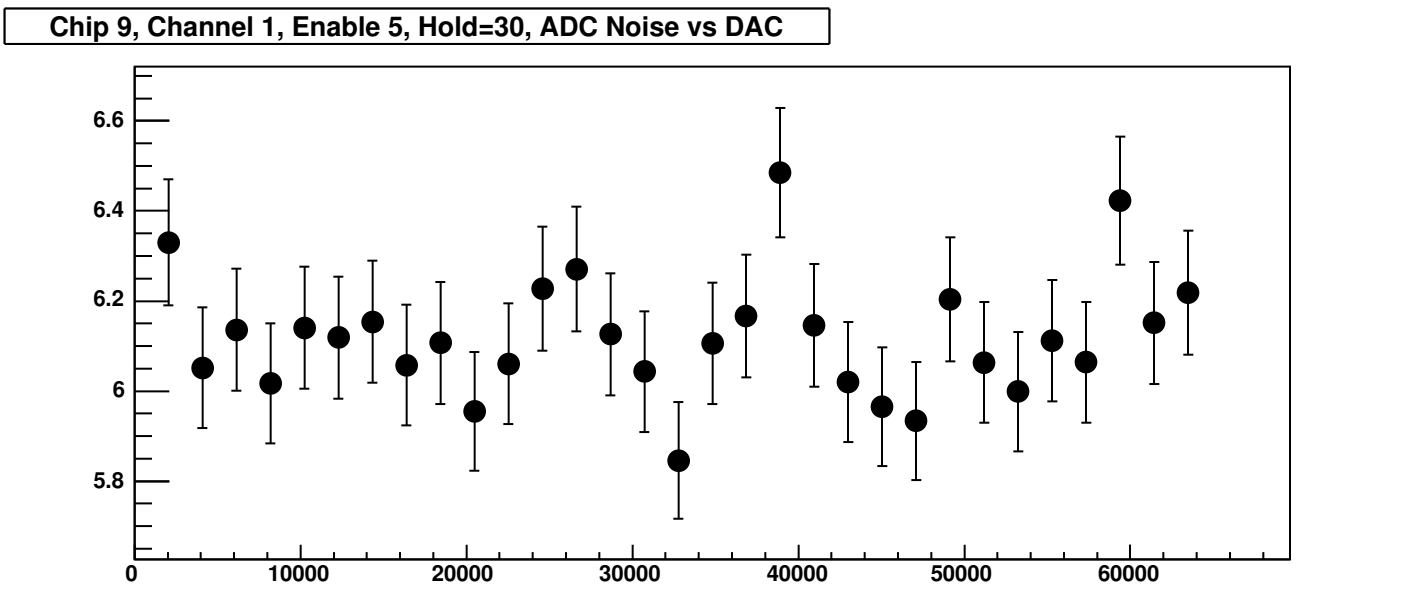

Chip 9, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

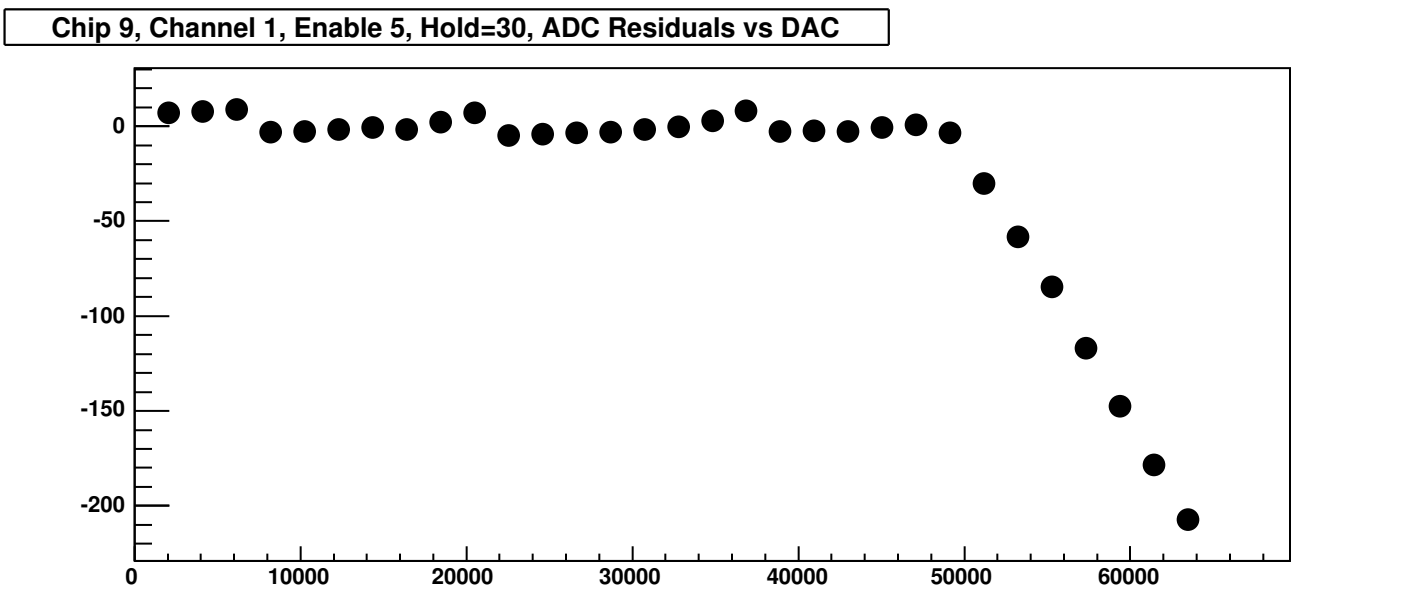

Chip 9, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7322 / 15
p0 993.4 ± 0.1153
p1 0.0004718 ± 4.688e-06
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

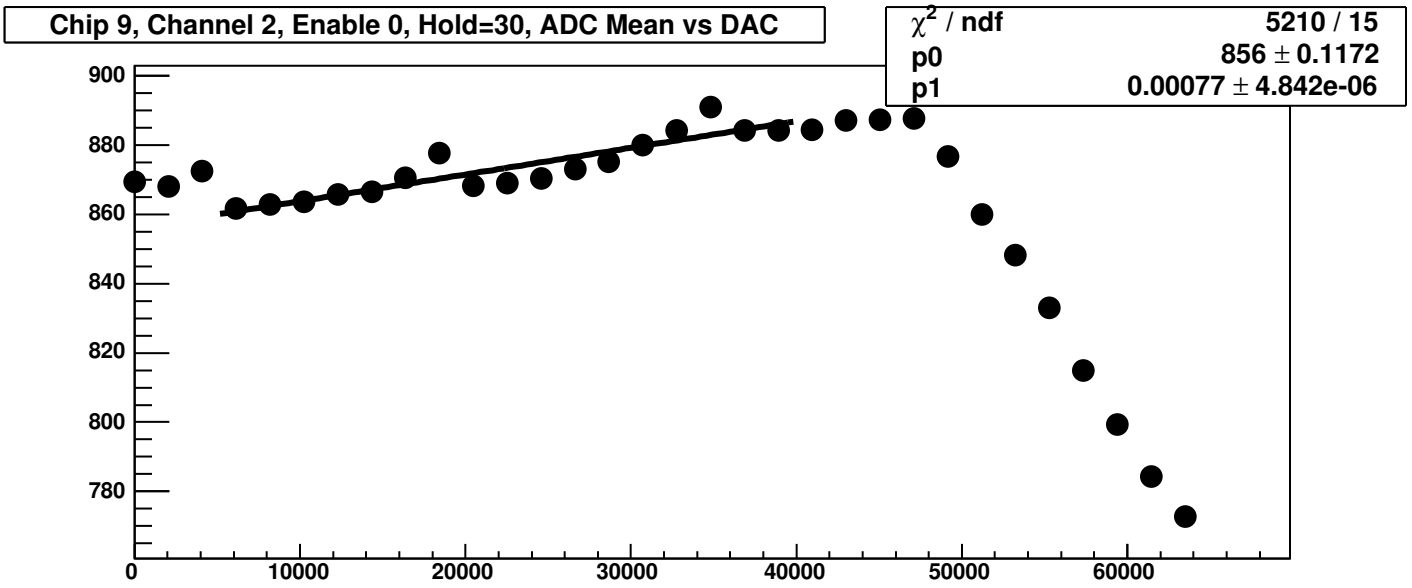

Chip 9, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
0
10000
20000
30000
40000
50000
60000

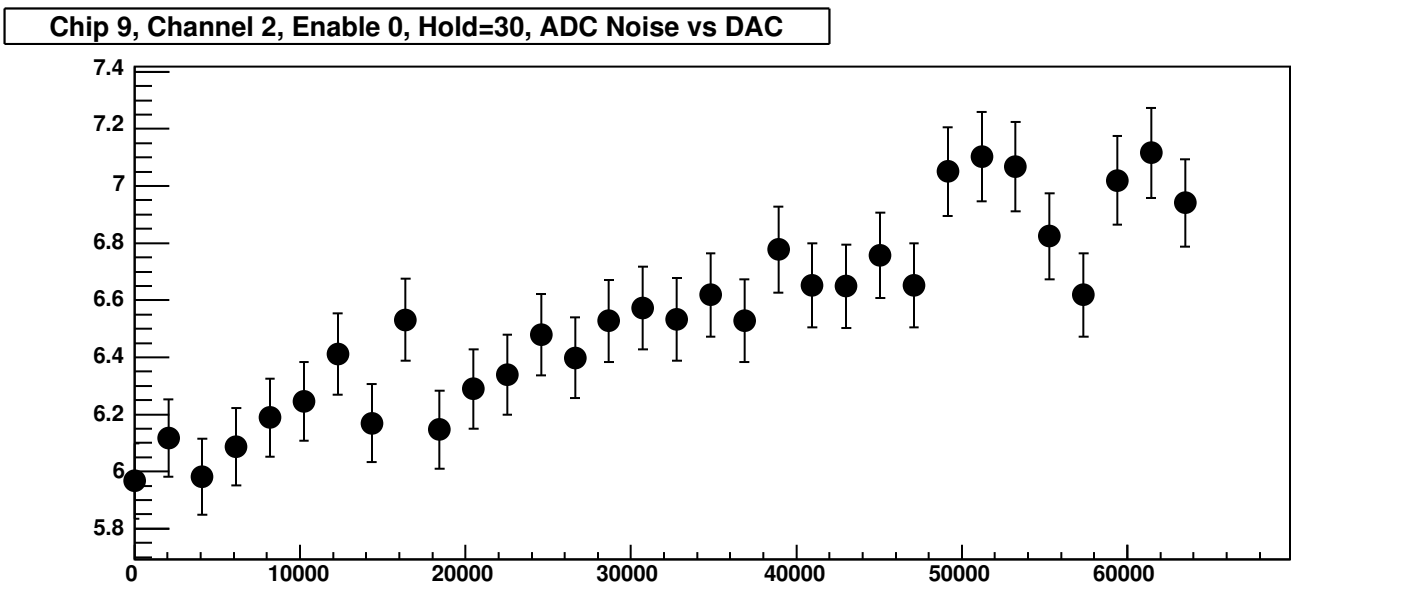

Chip 9, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

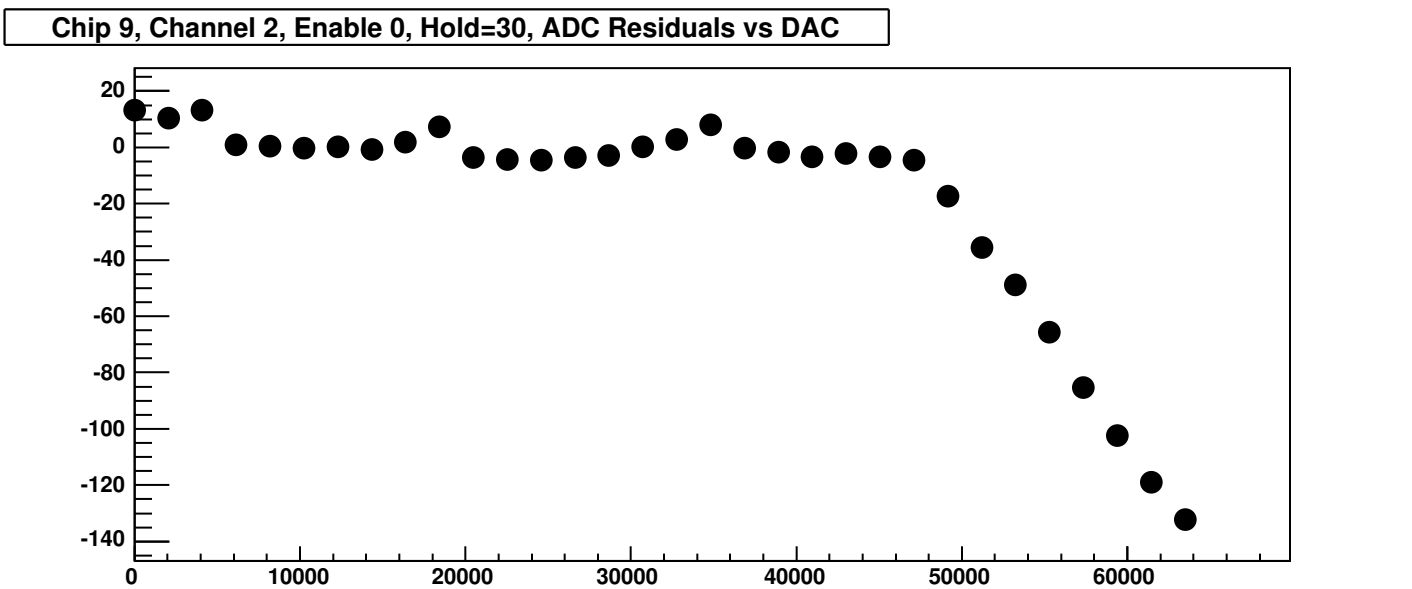

Chip 9, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8319 / 15
p0 992.7 ± 0.1141
p1 -0.0005779 ± 4.645e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

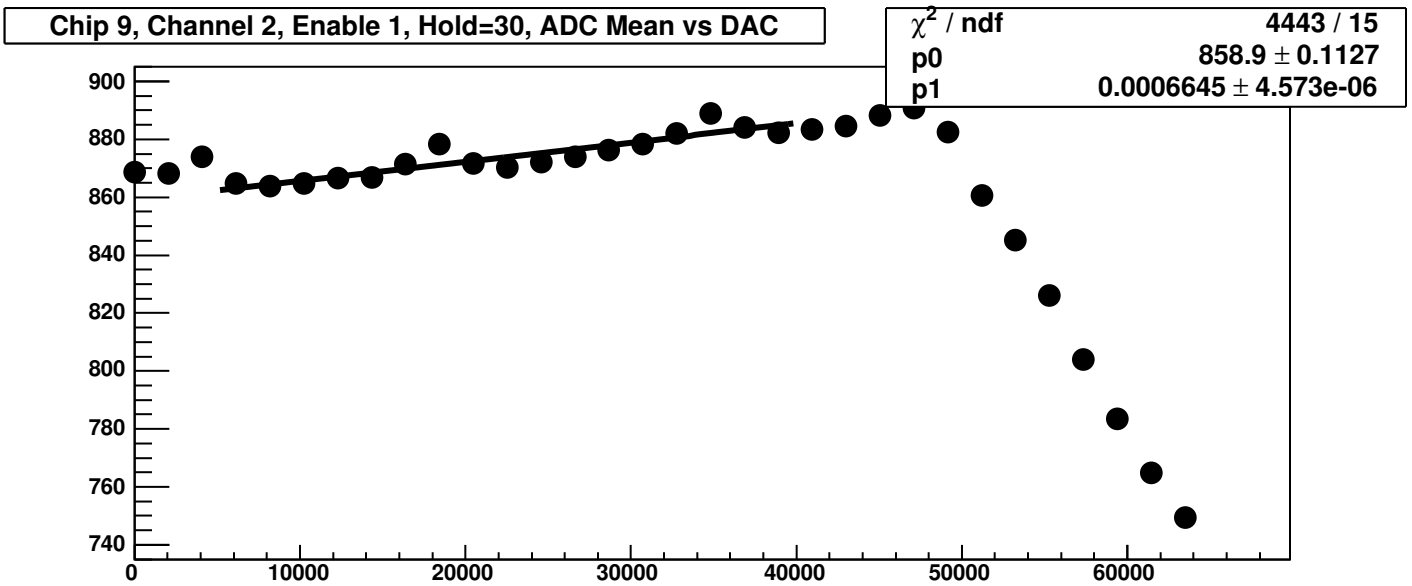

Chip 9, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

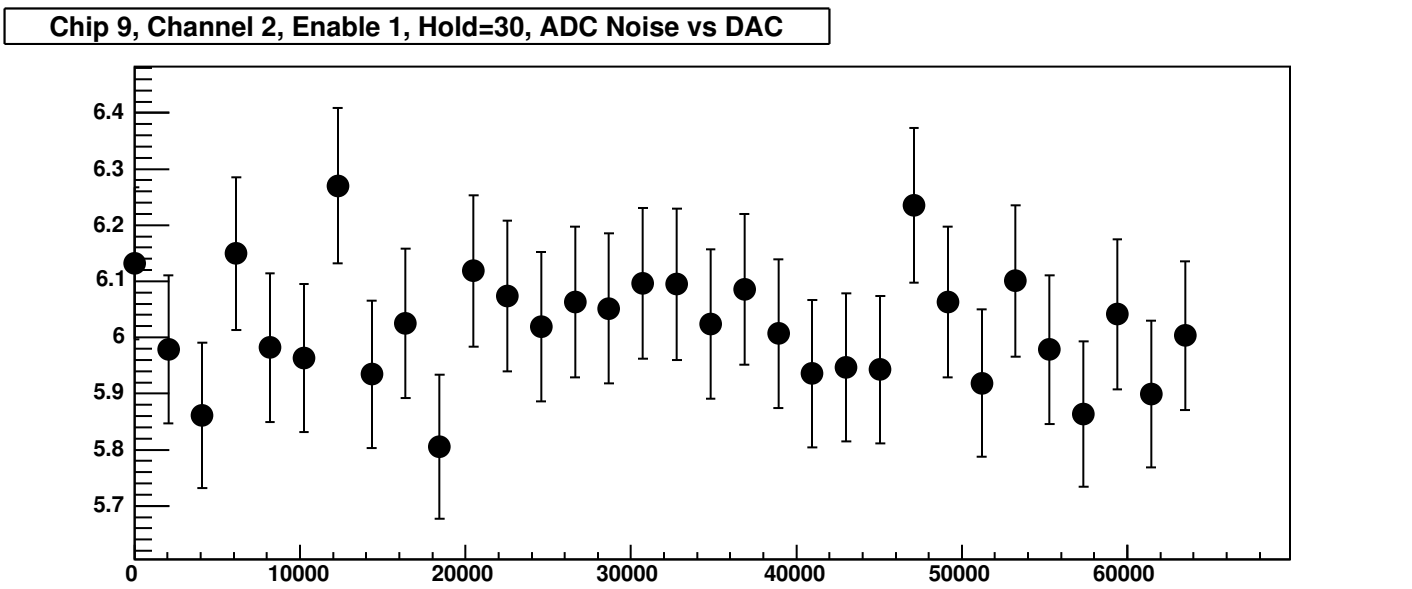

Chip 9, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

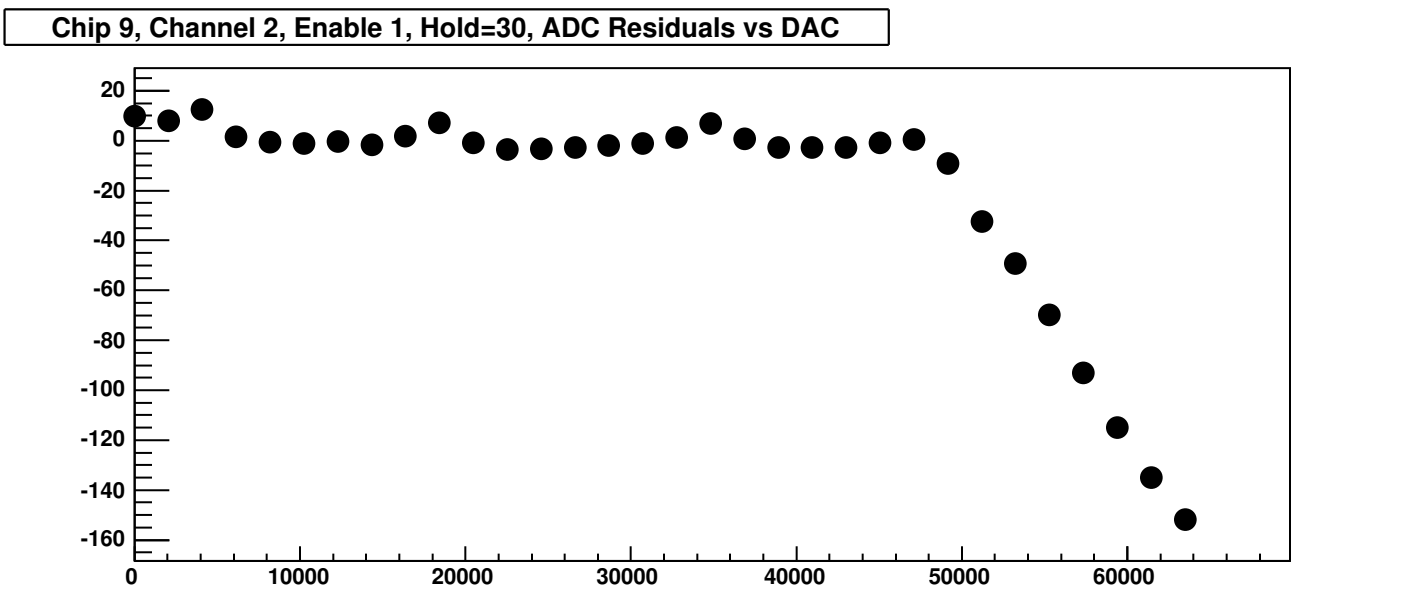

Chip 9, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5210 / 15
p0 856 ± 0.1172
p1 0.00077 ± 4.842e-06
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

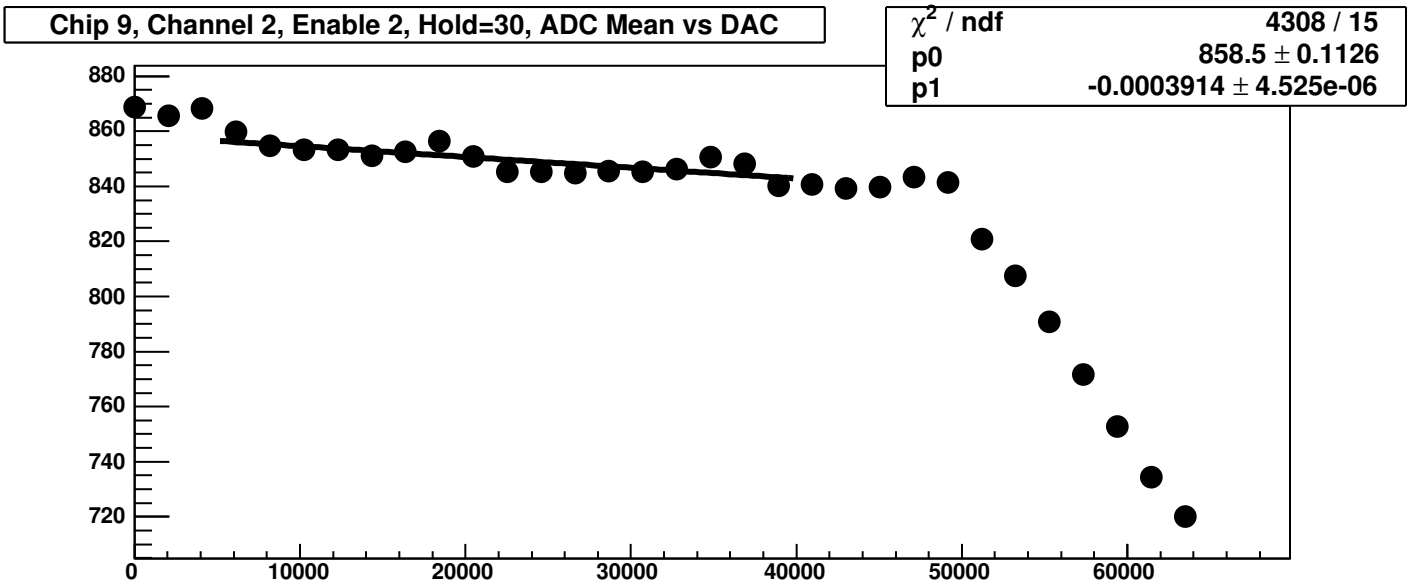

Chip 9, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

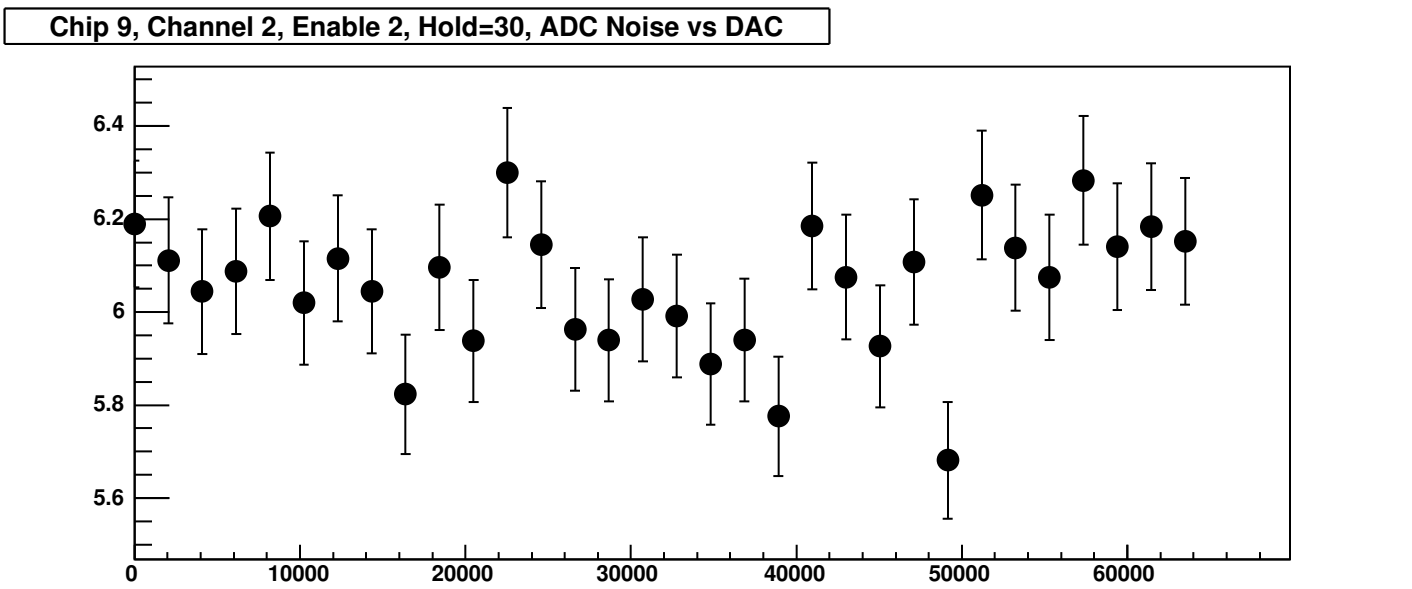

Chip 9, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

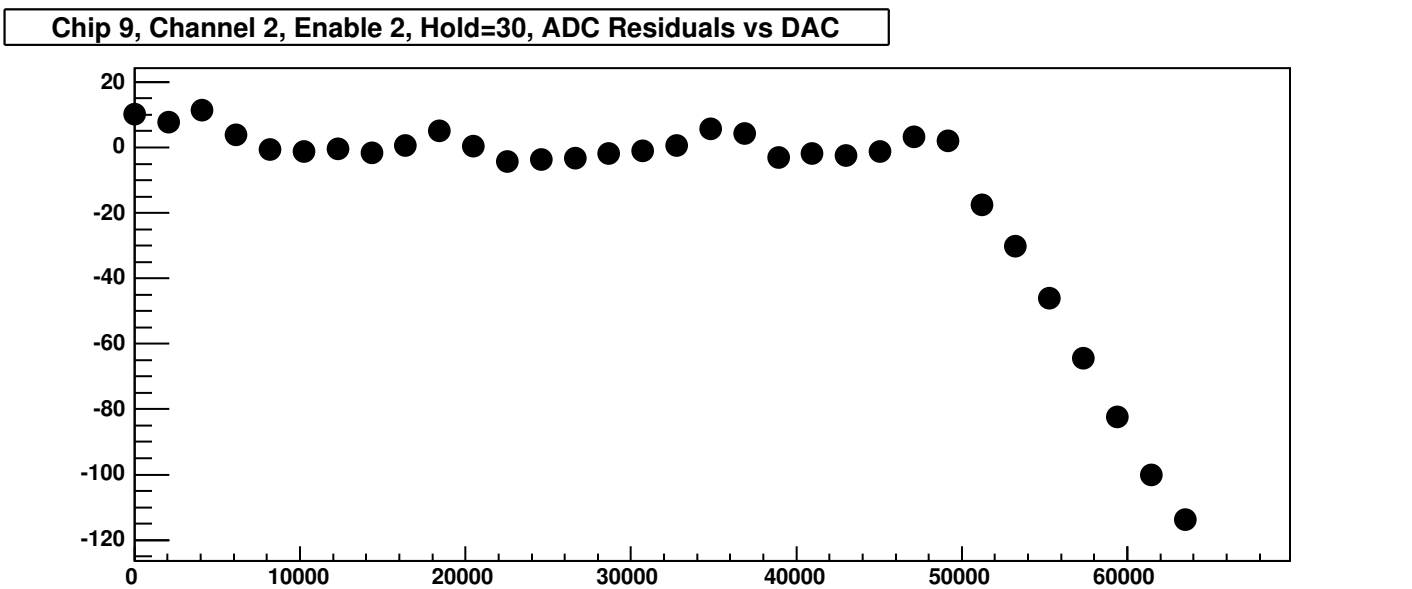

Chip 9, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4443 / 15
p0 858.9 ± 0.1127
p1 0.0006645 ± 4.573e-06
900
880
860
840
820
800
780
760
740
0
10000
20000
30000
40000
50000
60000

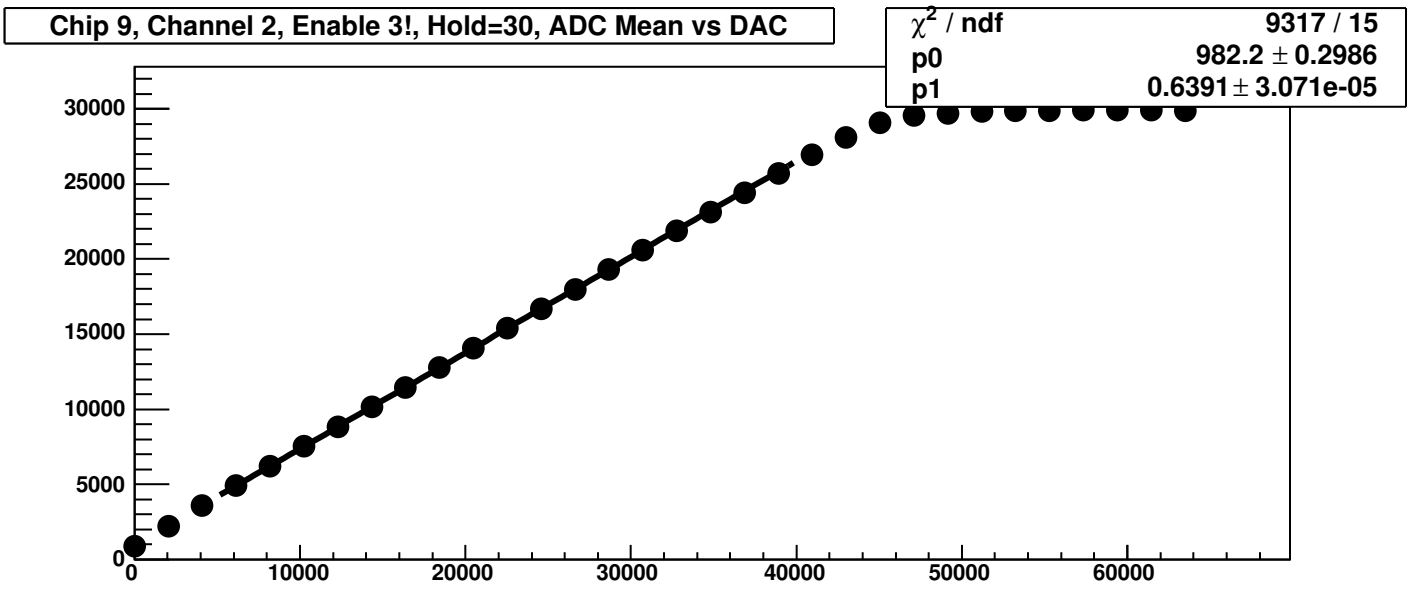

Chip 9, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
0
10000
20000
30000
40000
50000
60000

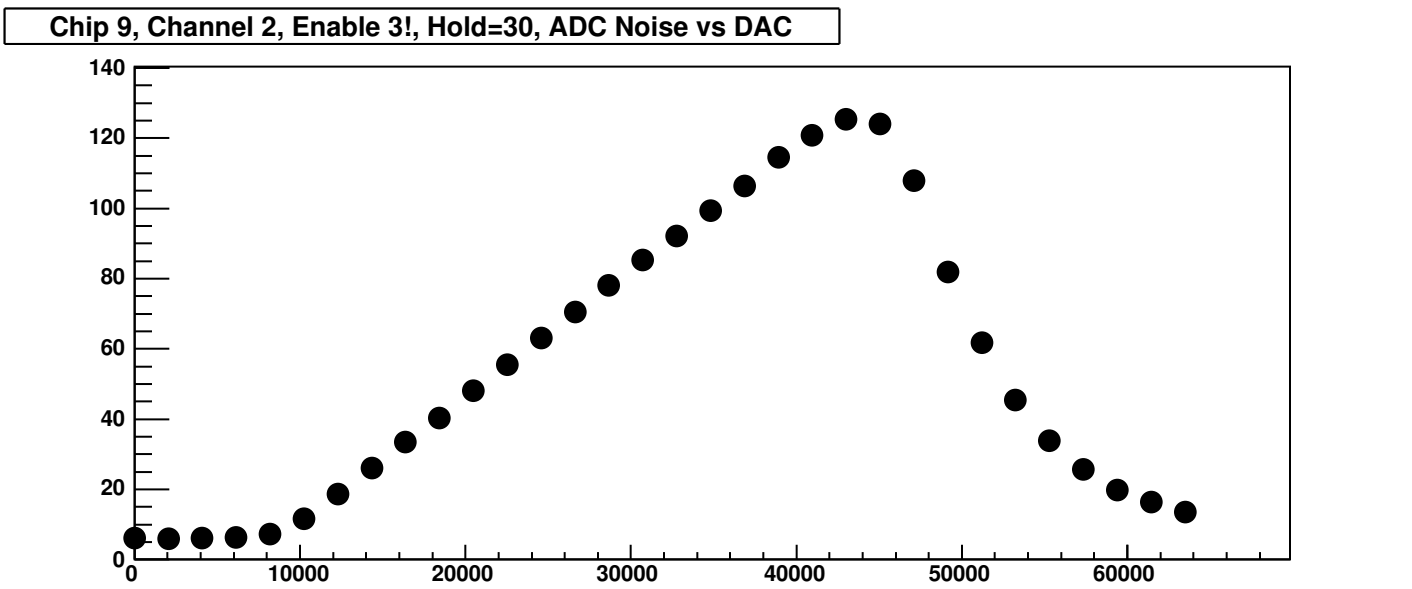

Chip 9, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

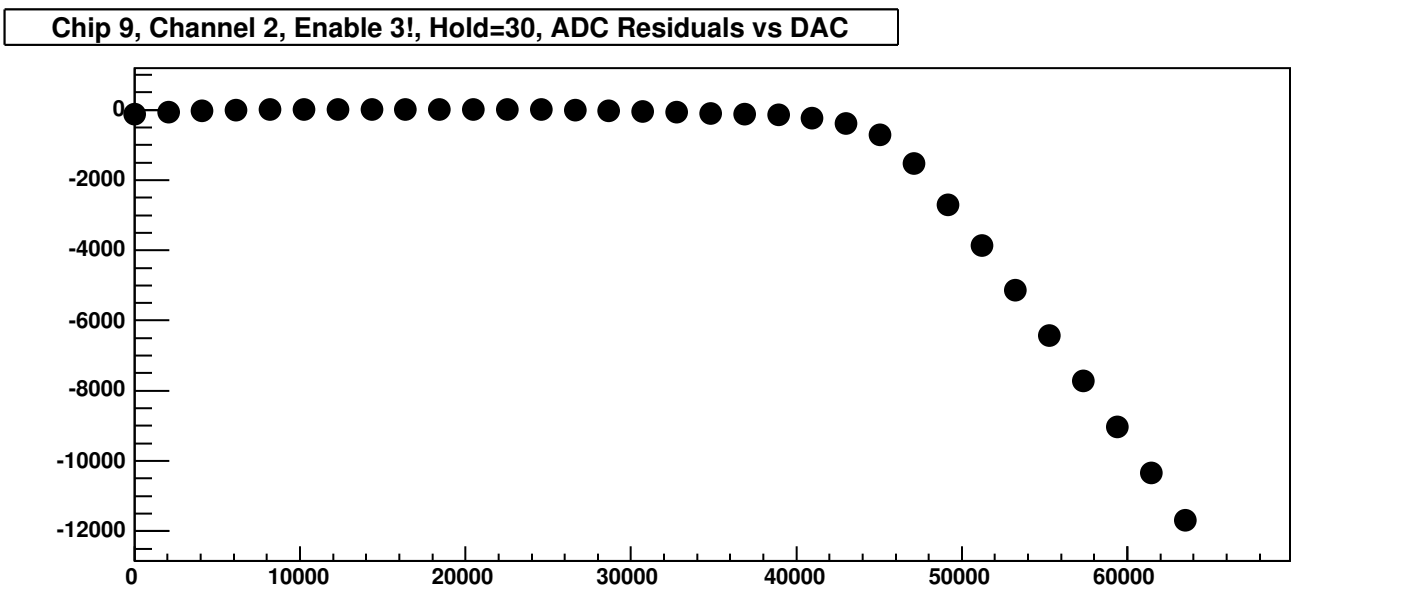

Chip 9, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4308 / 15
p0 858.5 ± 0.1126
p1 -0.0003914 ± 4.525e-06
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

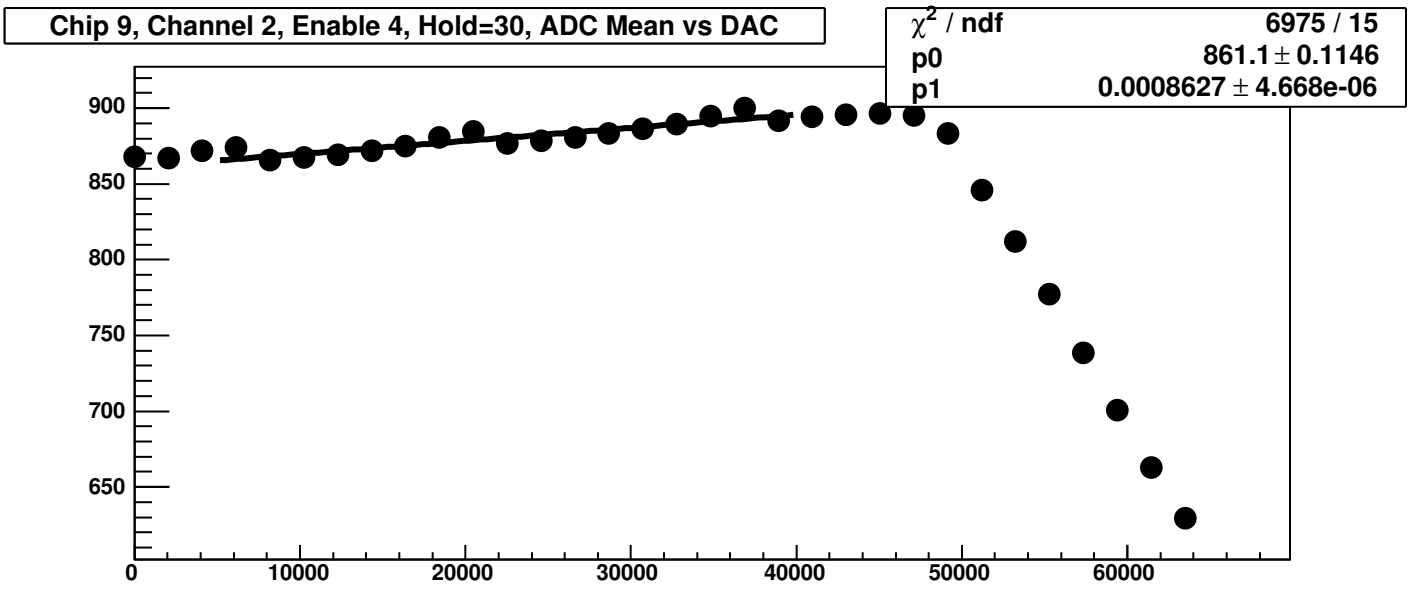

Chip 9, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

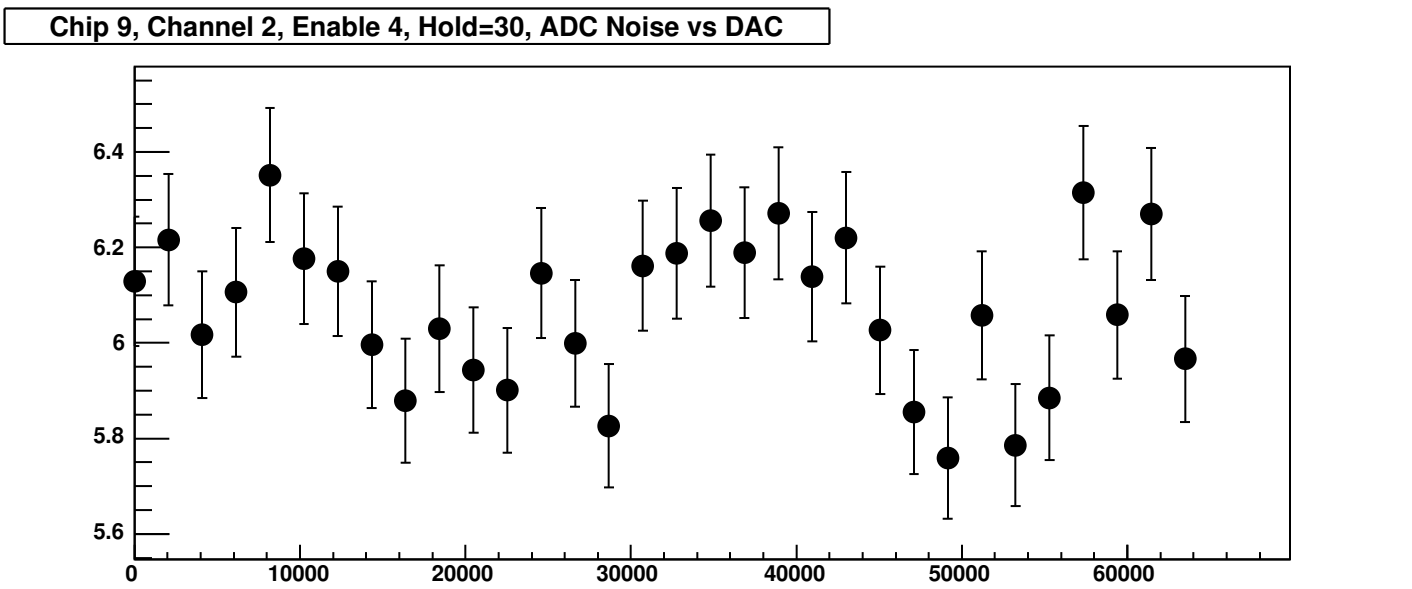

Chip 9, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

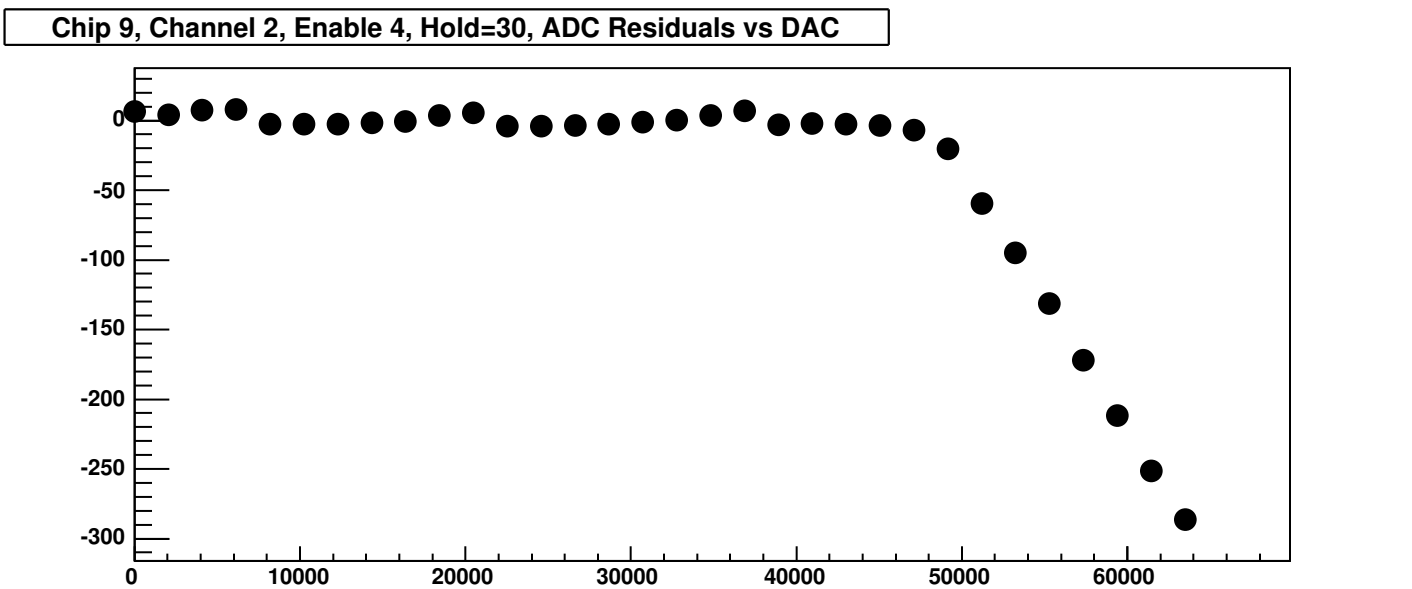

Chip 9, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 9317 / 15
p0 982.2 ± 0.2986
p1 0.6391 ± 3.071e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

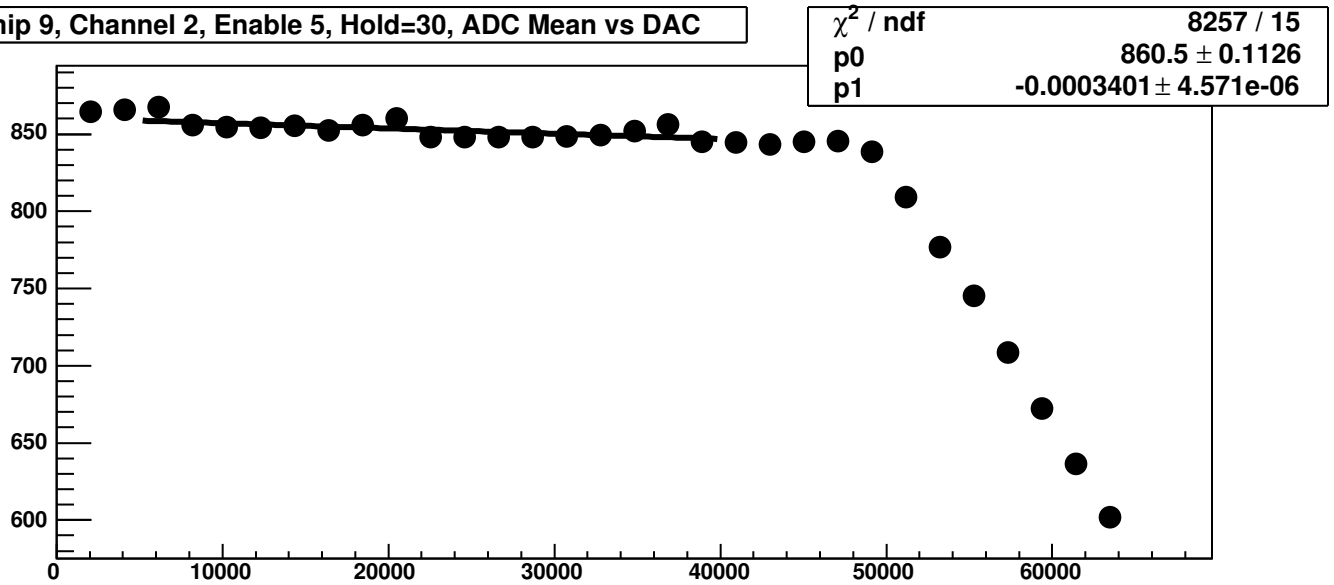

Chip 9, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6975 / 15
p0 861.1 ± 0.1146
p1 0.0008627 ± 4.668e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

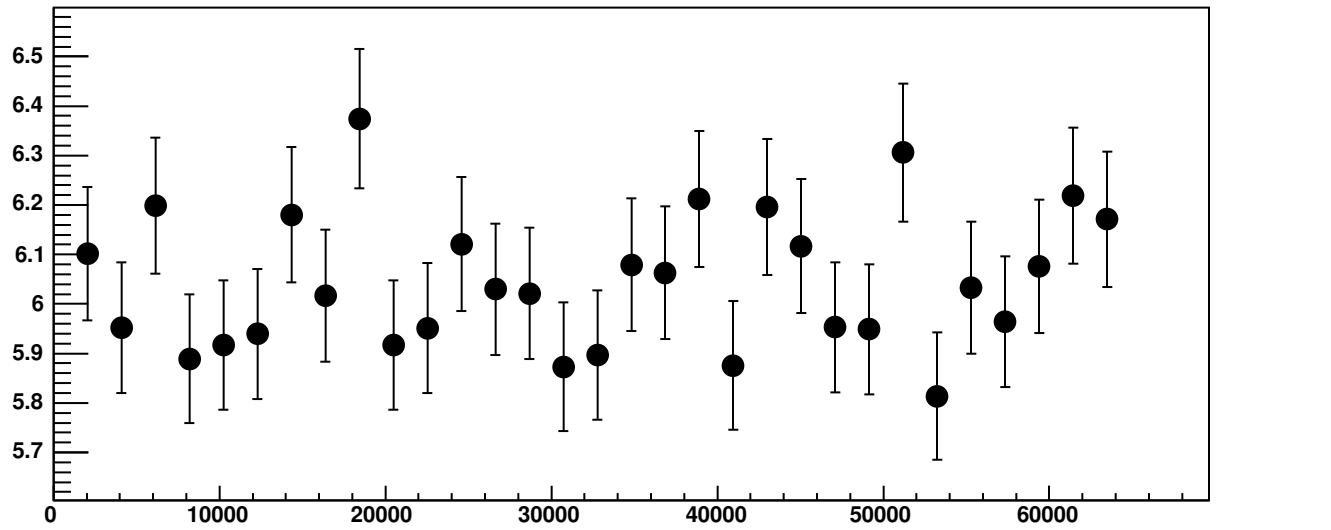

Chip 9, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

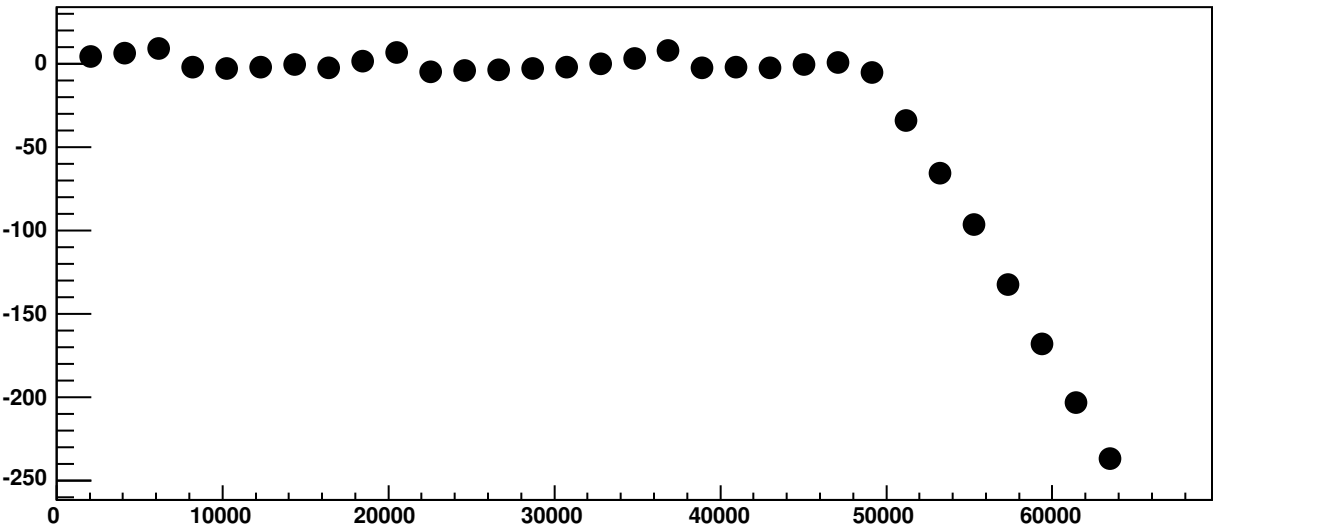
Chip 9, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC

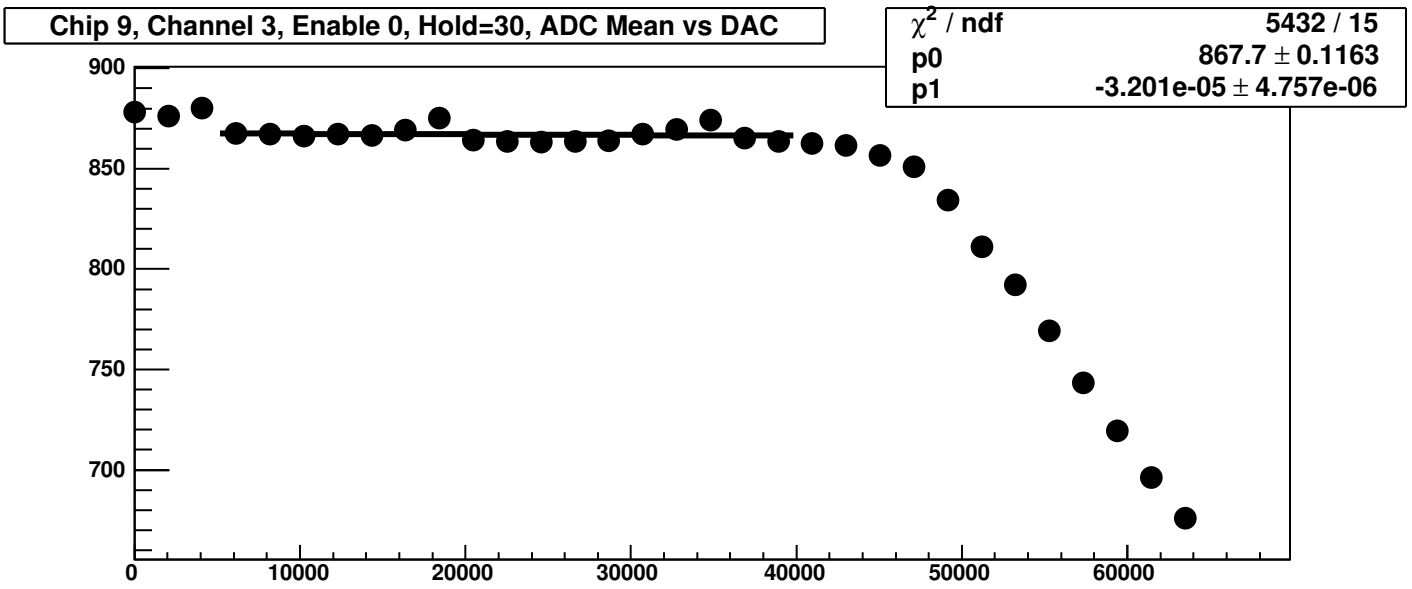


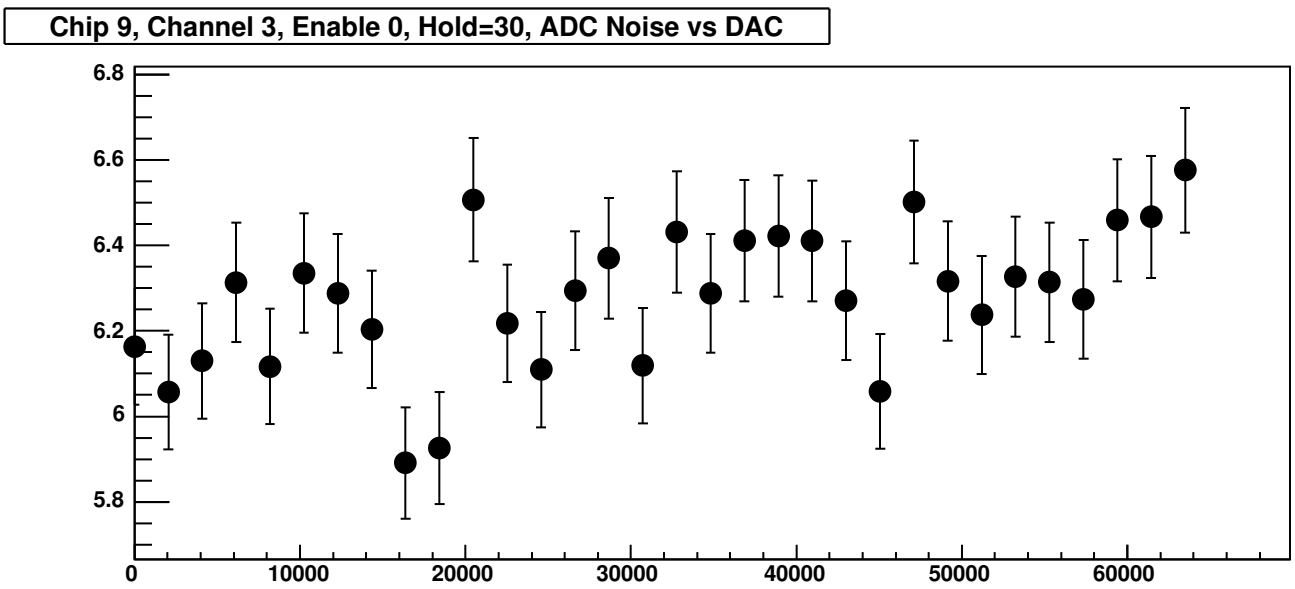
Chip 9, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

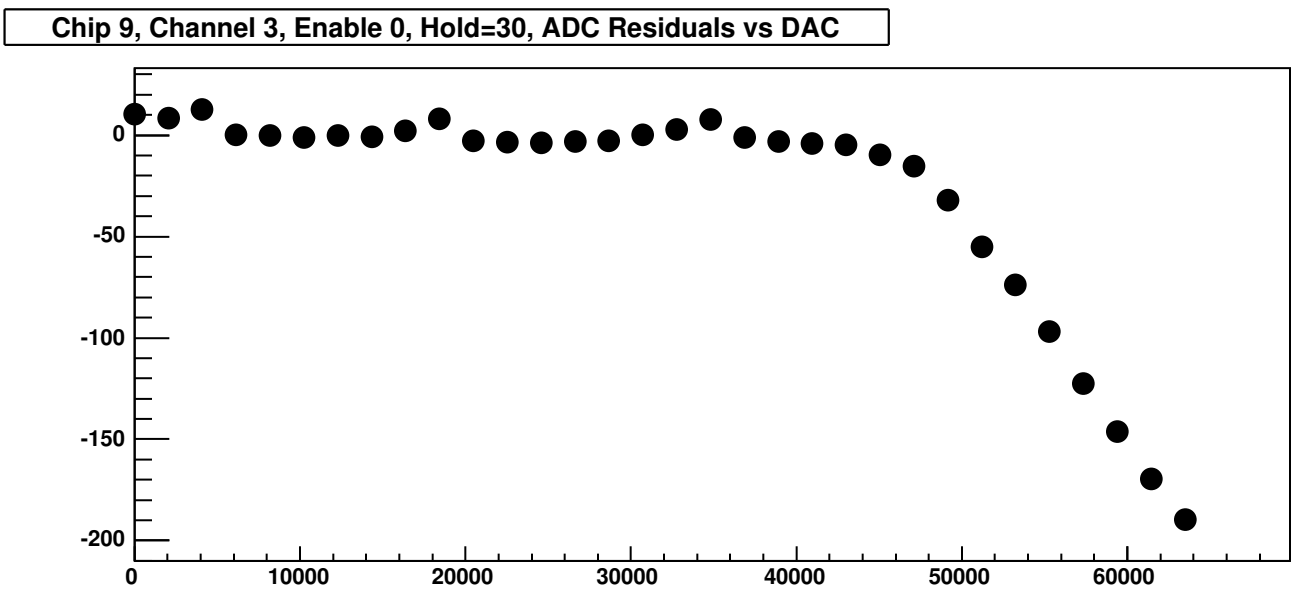


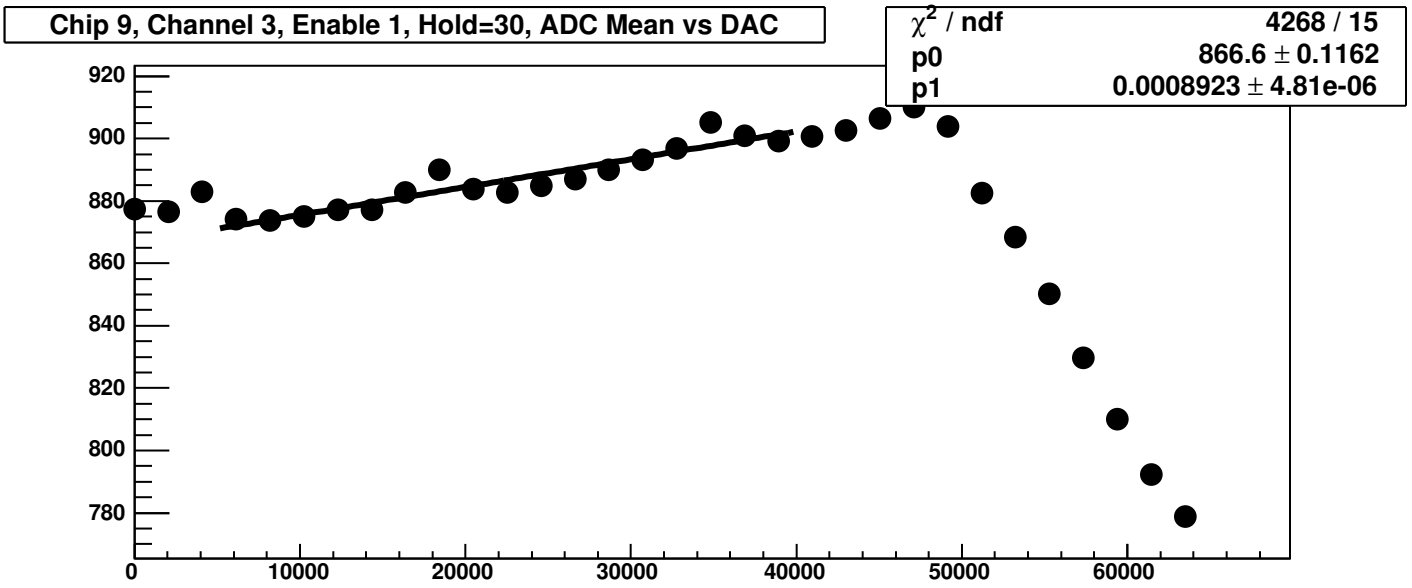
Chip 9, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC

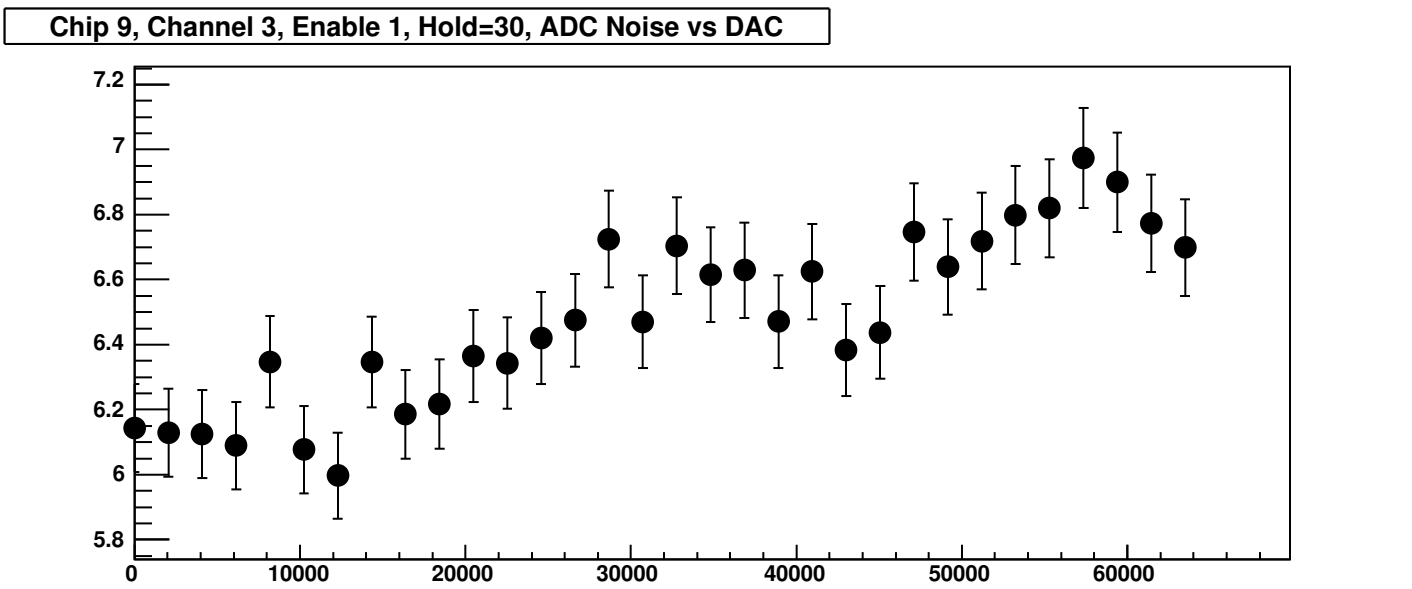


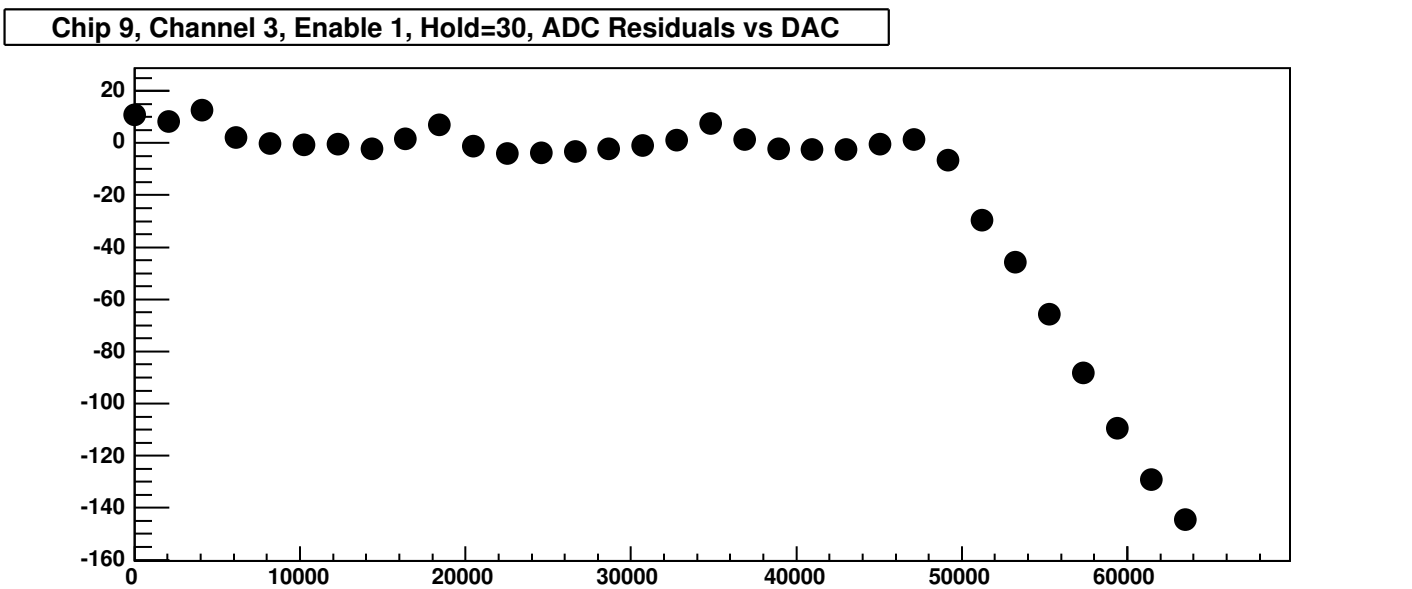

Chip 9, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5432 / 15
p0 867.7 ± 0.1163
p1 -3.201e-05 ± 4.757e-06
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

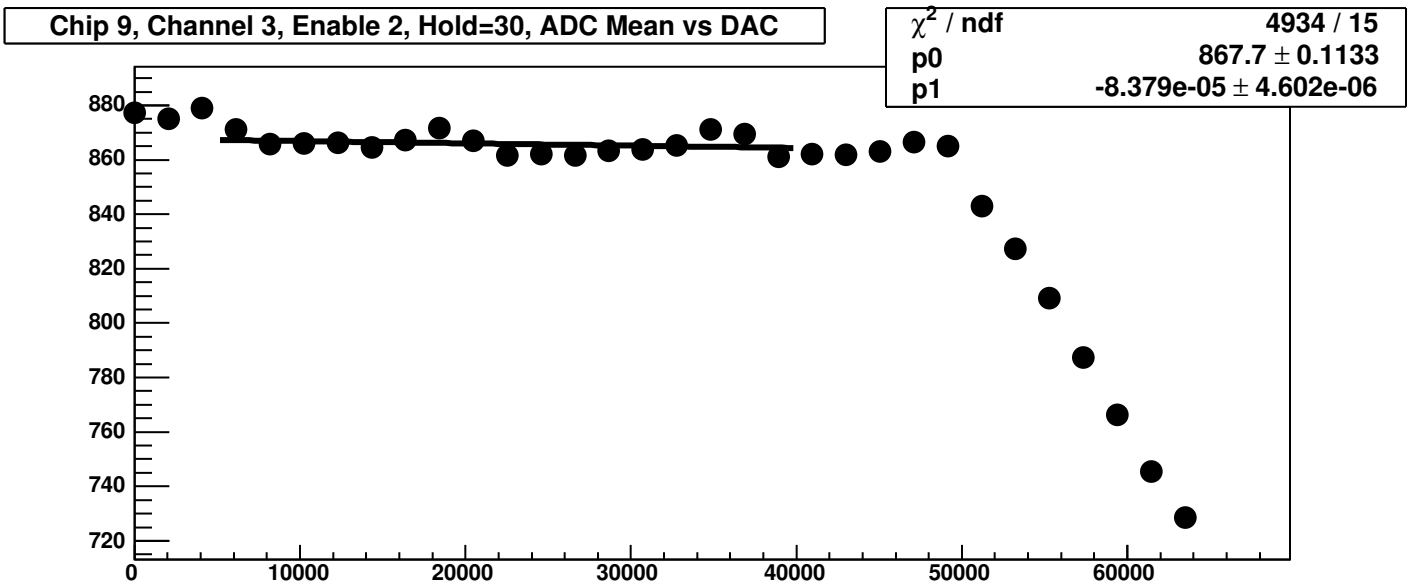

Chip 9, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

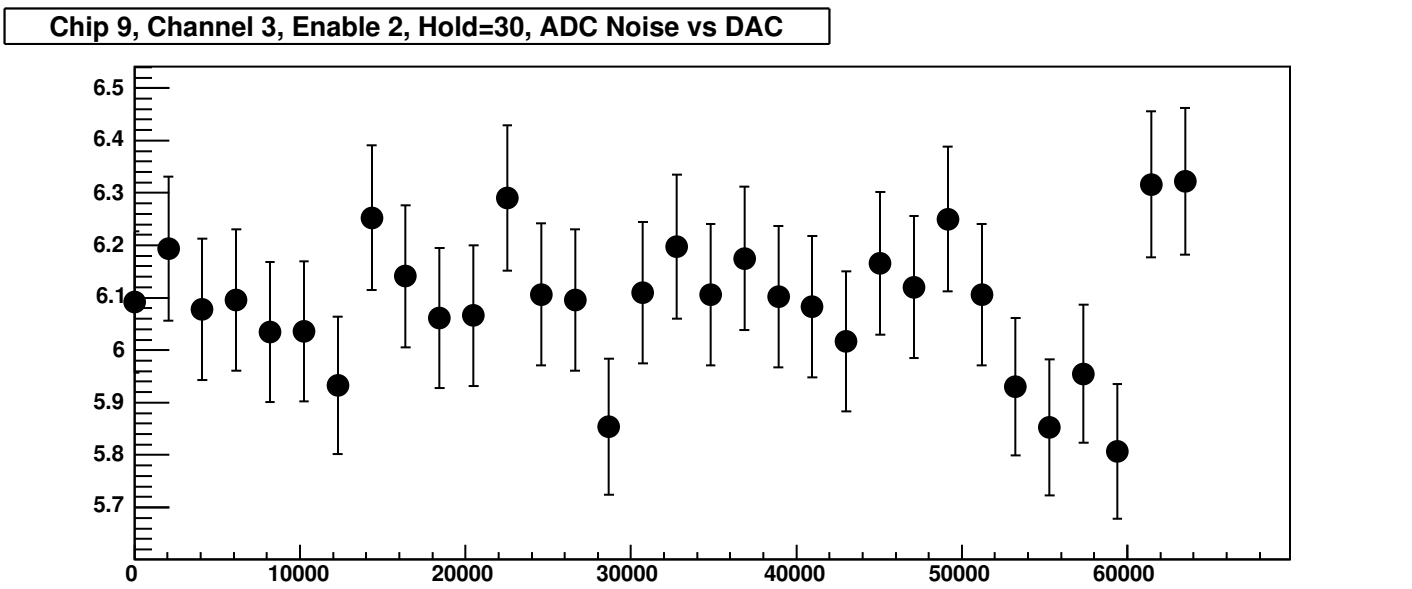

Chip 9, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

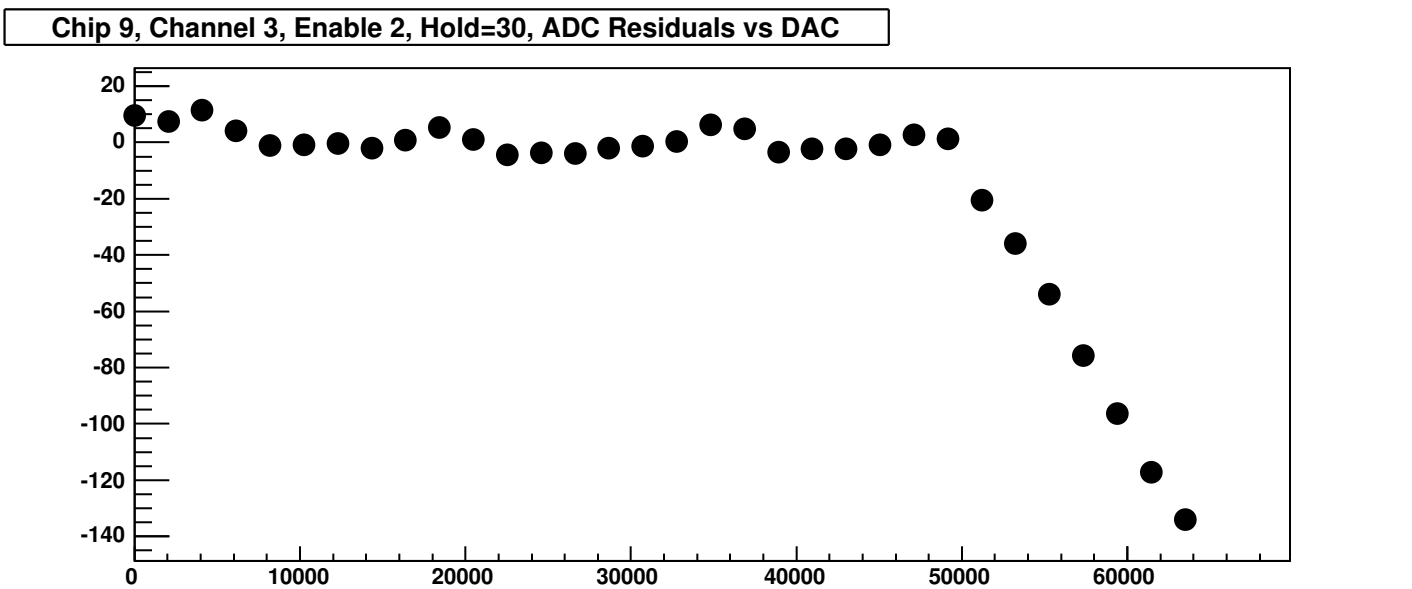

Chip 9, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4268 / 15
p0 866.6 ± 0.1162
p1 0.0008923 ± 4.81e-06
920
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

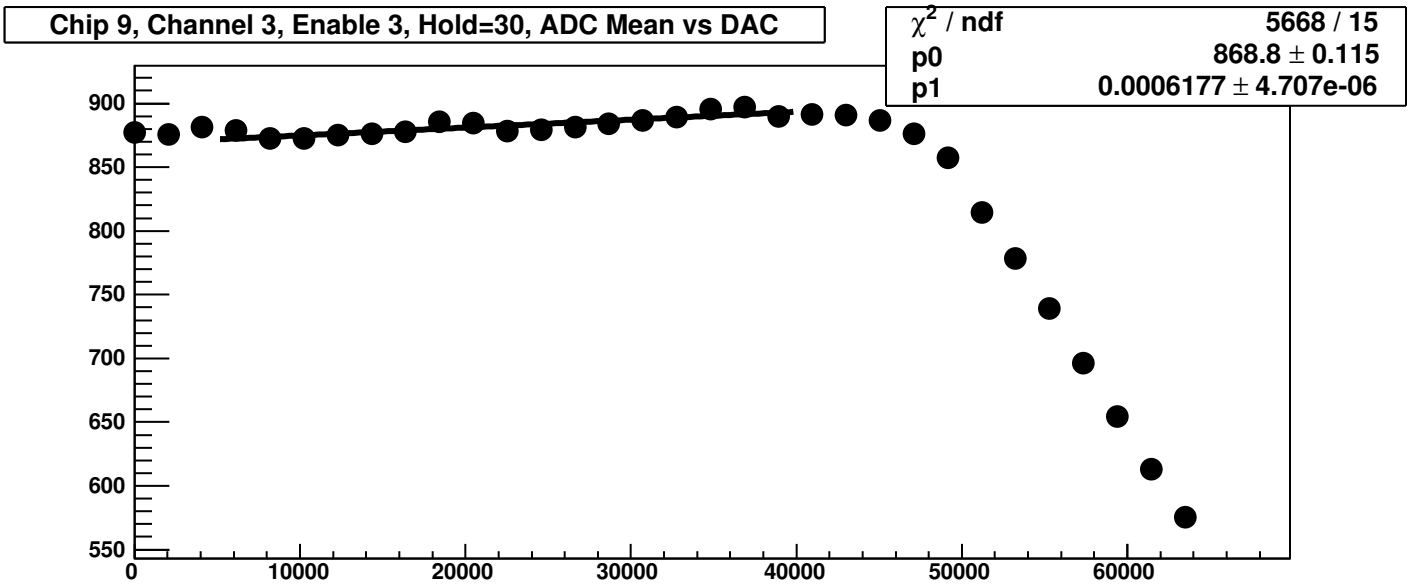

Chip 9, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

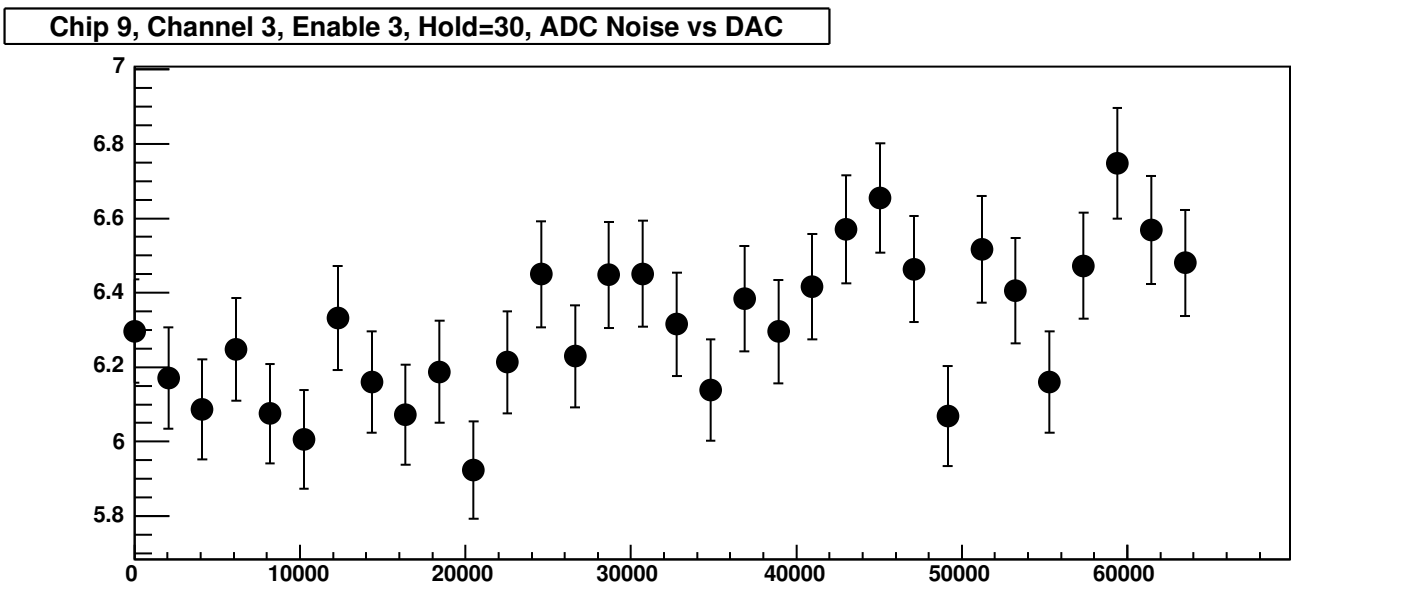

Chip 9, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

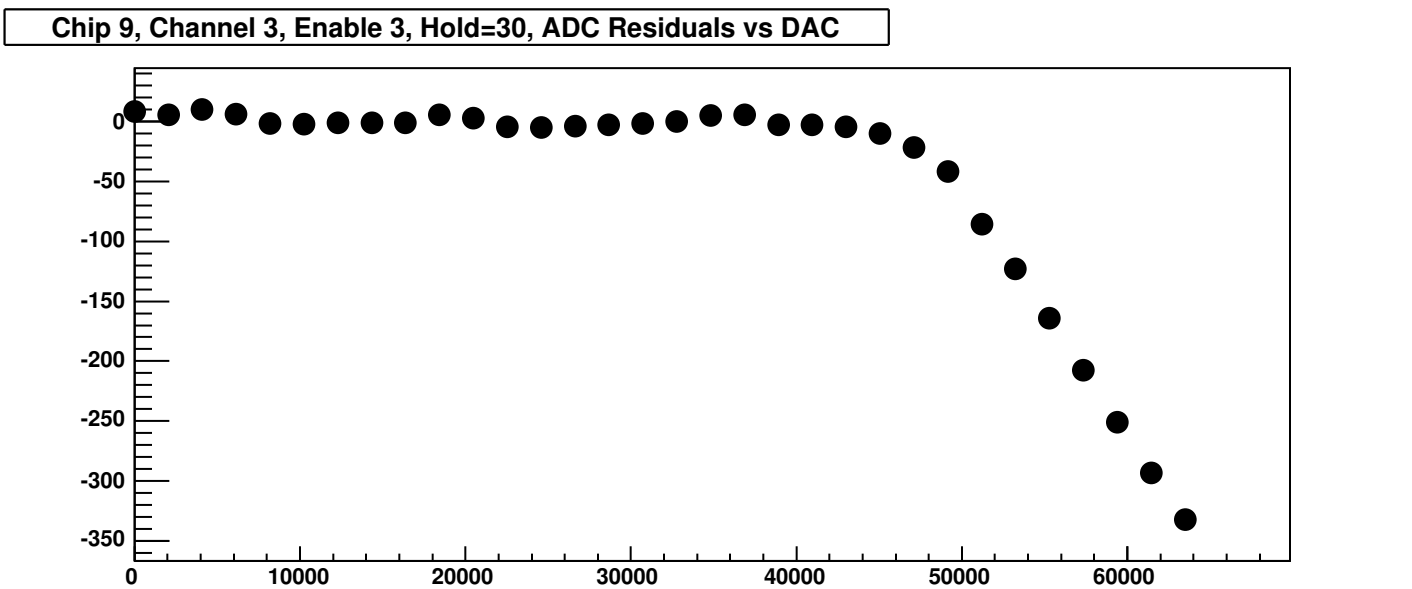

Chip 9, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4934 / 15
p0 867.7 ± 0.1133
p1 -8.379e-05 ± 4.602e-06
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

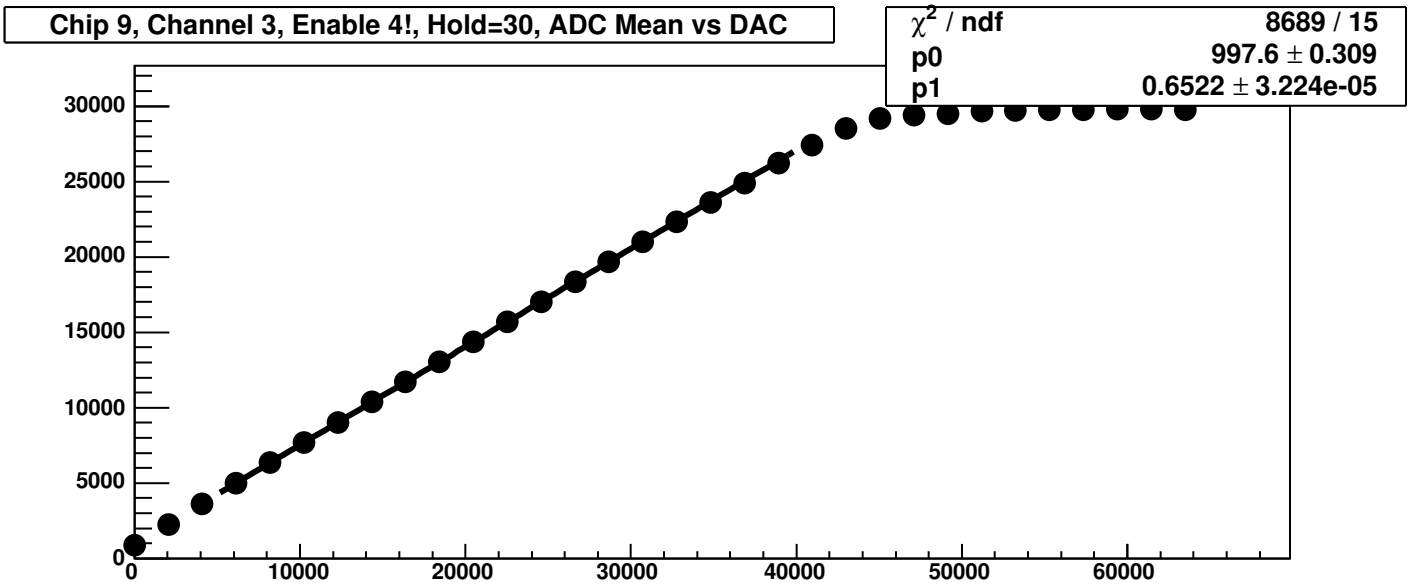

Chip 9, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
0
10000
20000
30000
40000
50000
60000

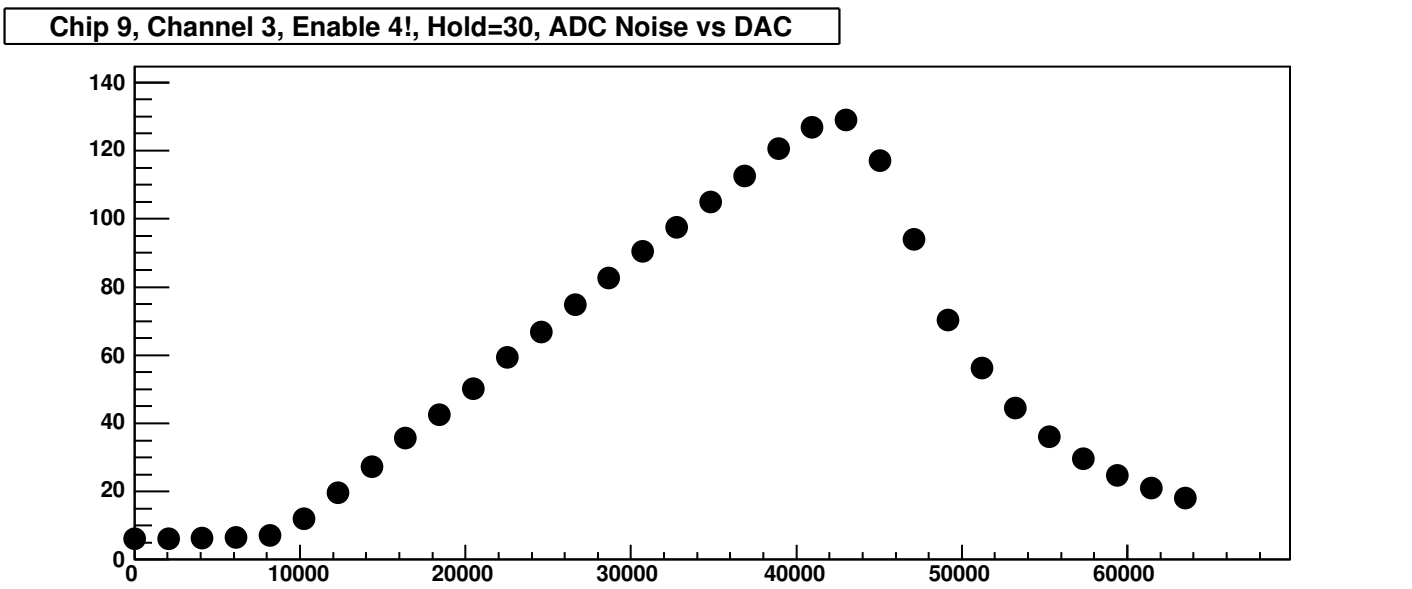

Chip 9, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

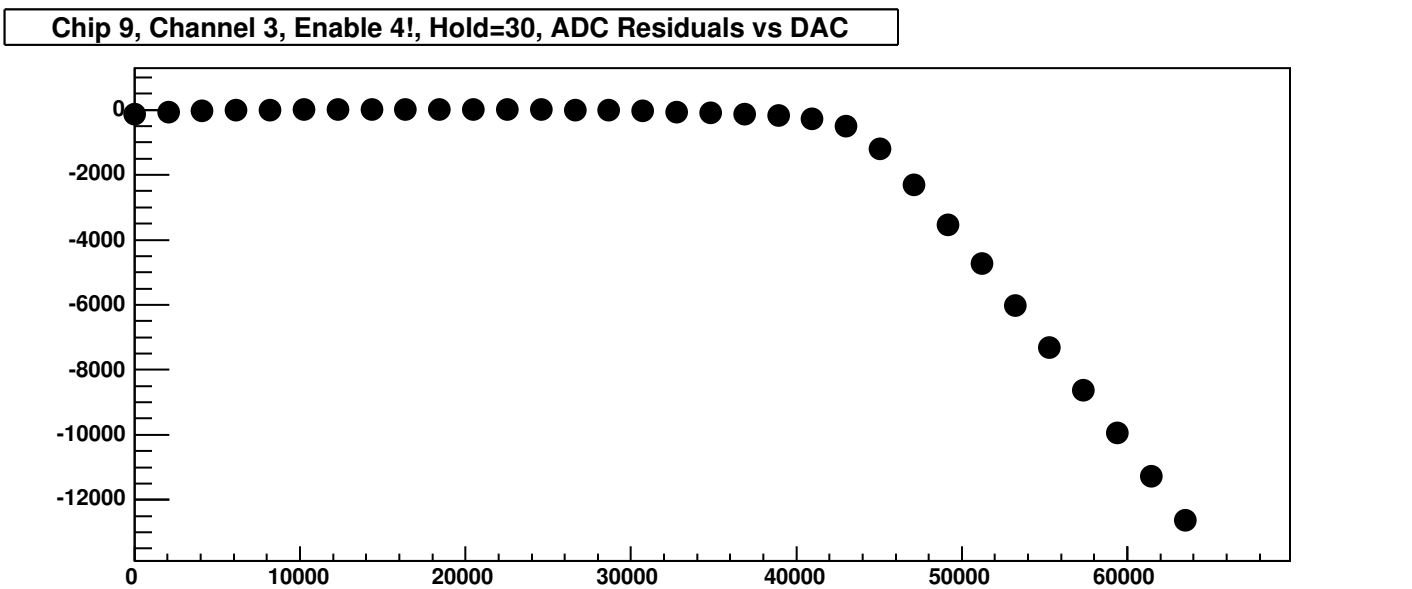

Chip 9, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5668 / 15
p0 868.8 ± 0.115
p1 0.0006177 ± 4.707e-06
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

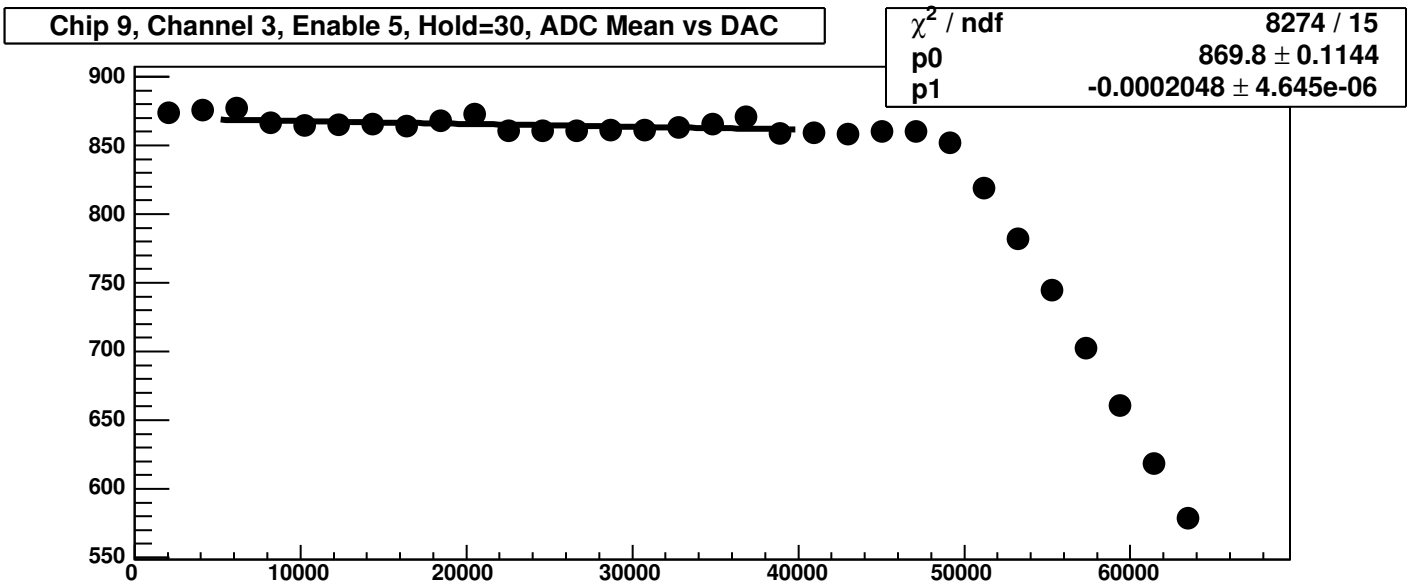

Chip 9, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

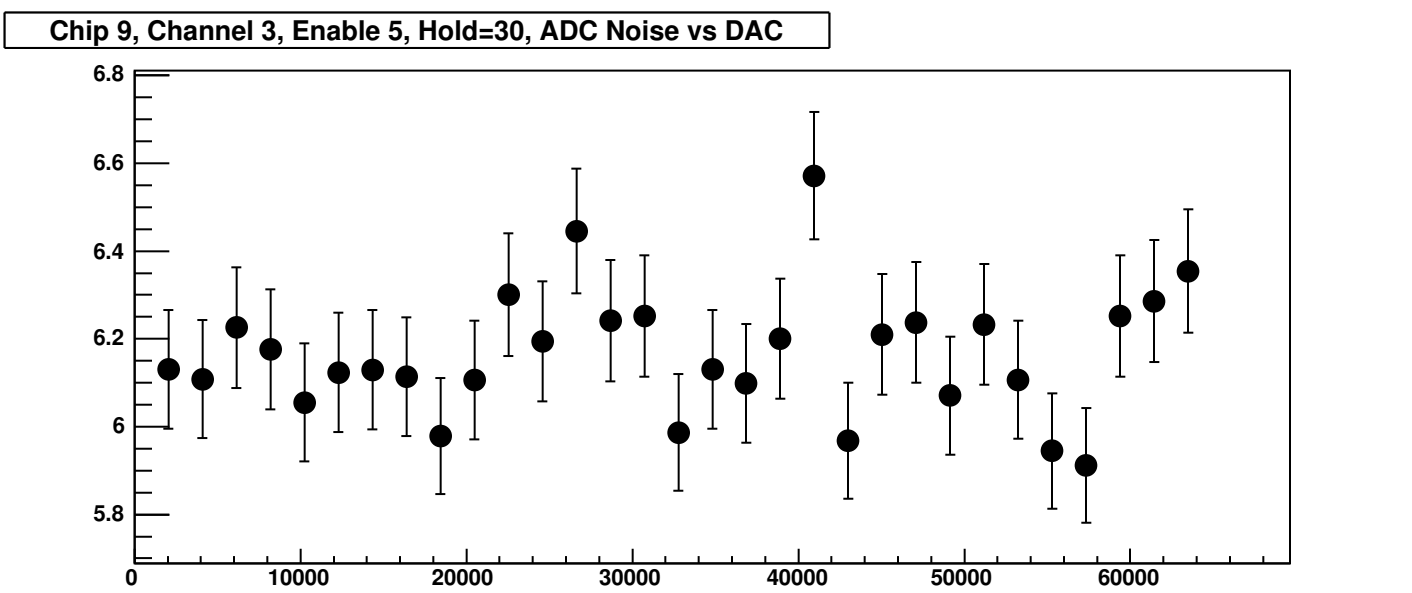

Chip 9, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

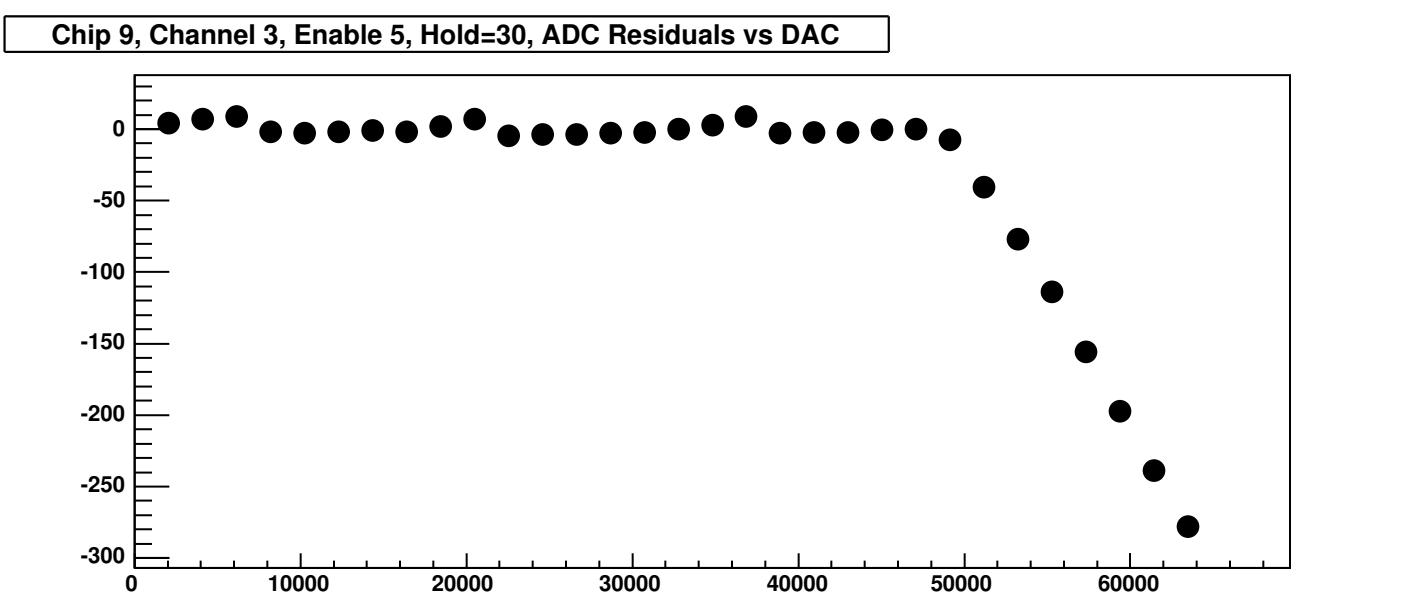

Chip 9, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 8689 / 15
p0 997.6 ± 0.309
p1 0.6522 ± 3.224e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

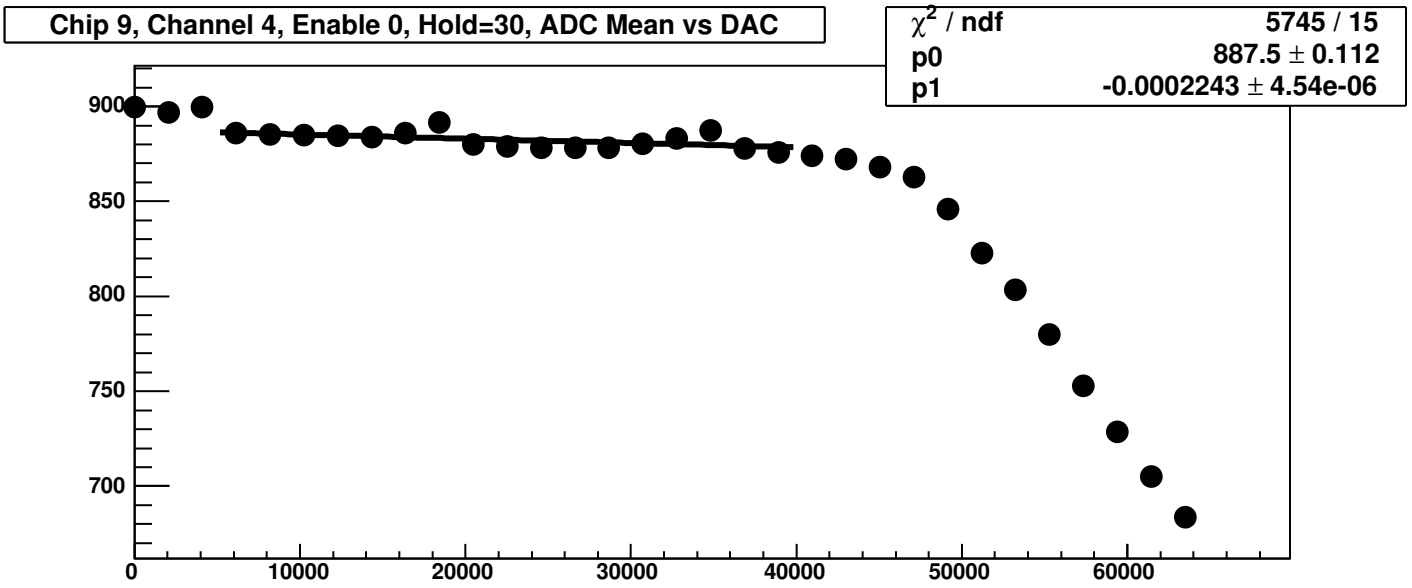

Chip 9, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

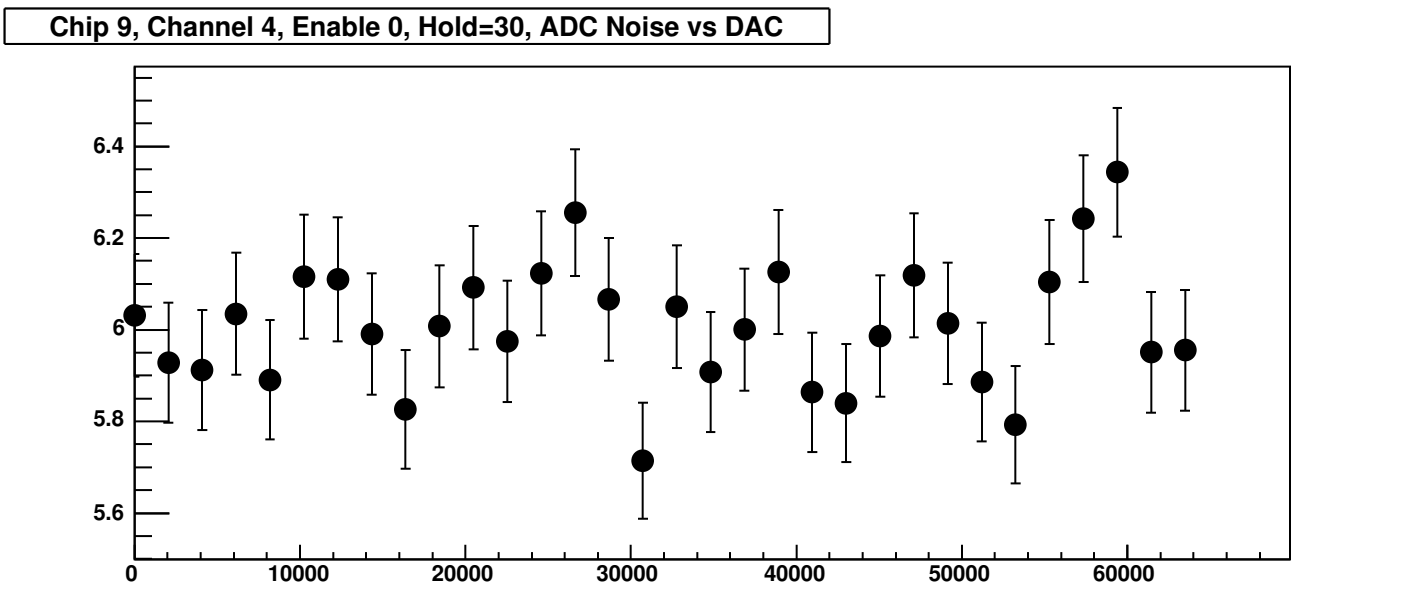

Chip 9, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

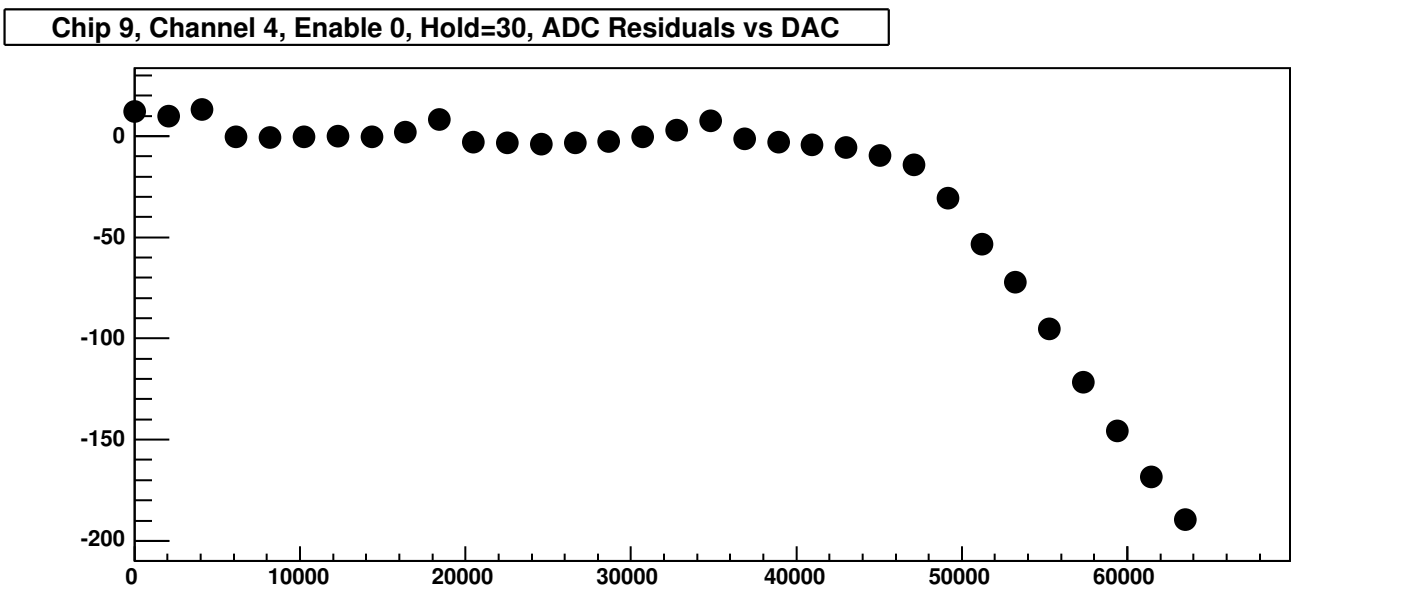

Chip 9, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8274 / 15
p0 869.8 ± 0.1144
p1 -0.0002048 ± 4.645e-06
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

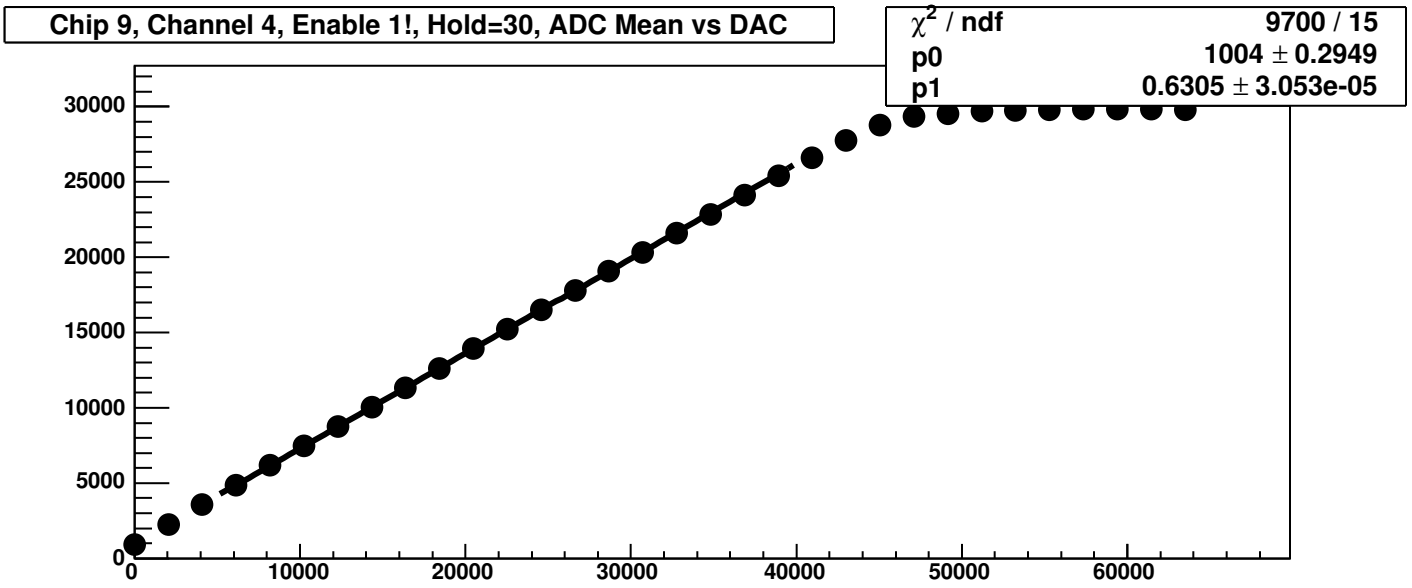

Chip 9, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

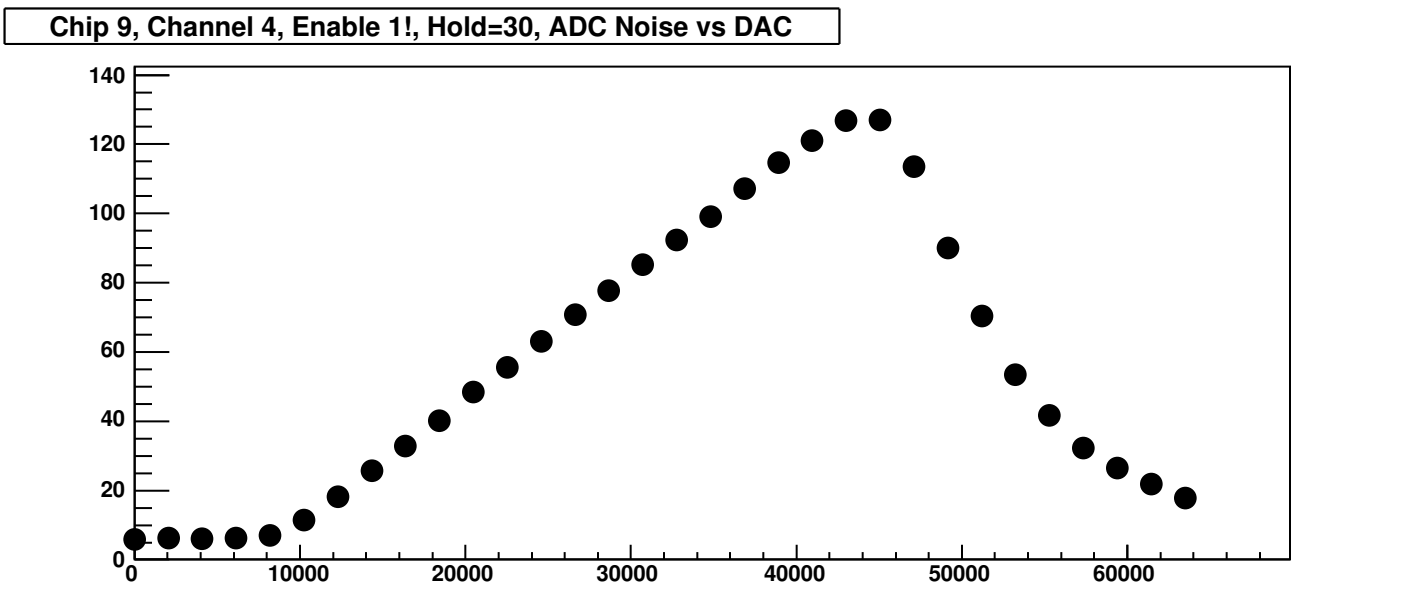

Chip 9, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

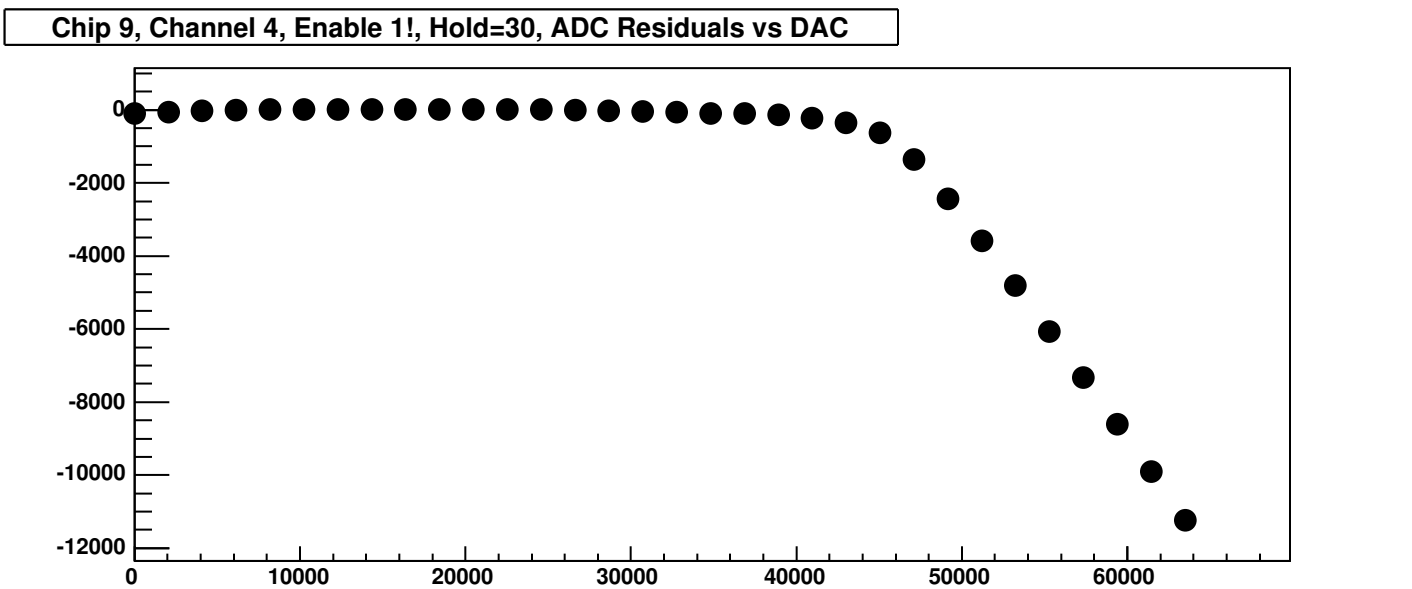

Chip 9, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5745 / 15
p0 887.5 ± 0.112
p1 -0.0002243 ± 4.54e-06
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

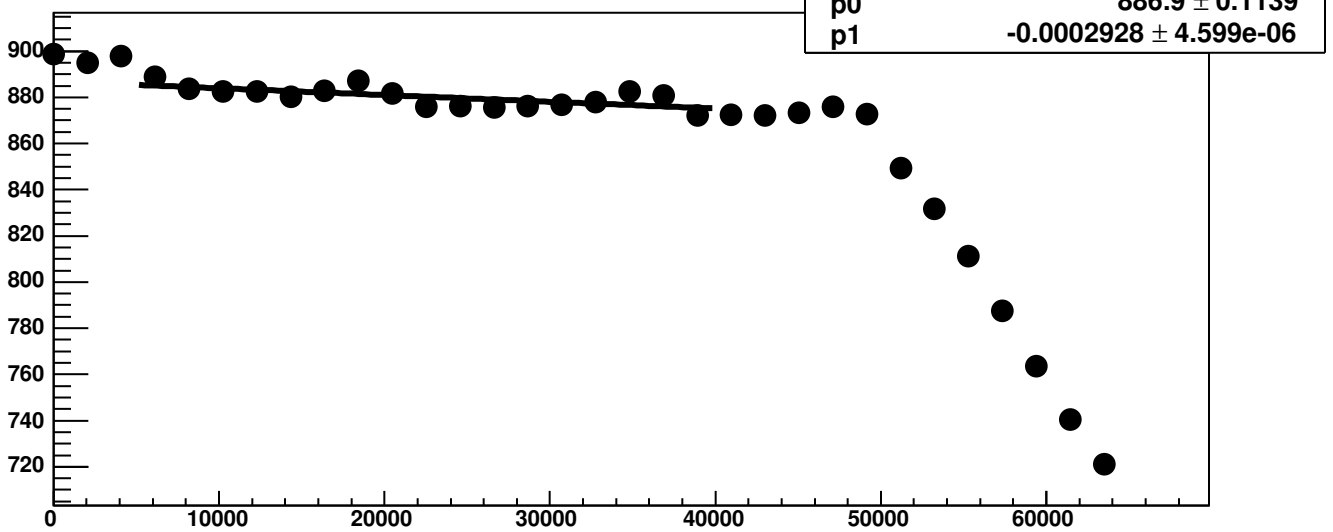

Chip 9, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 9700 / 15
p0 1004 ± 0.2949
p1 0.6305 ± 3.053e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

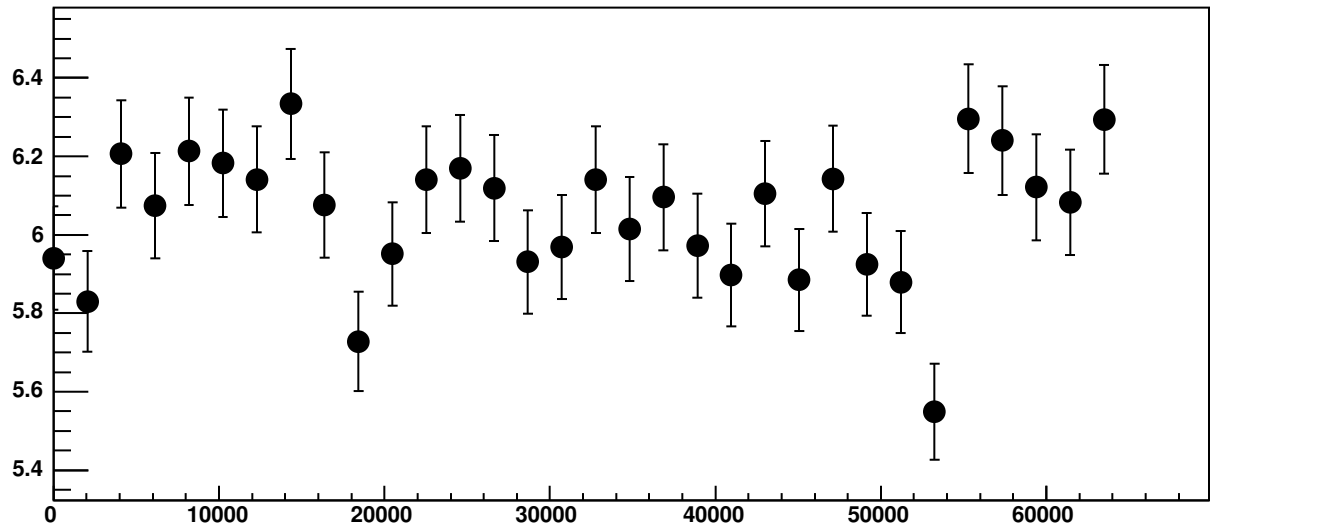

Chip 9, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

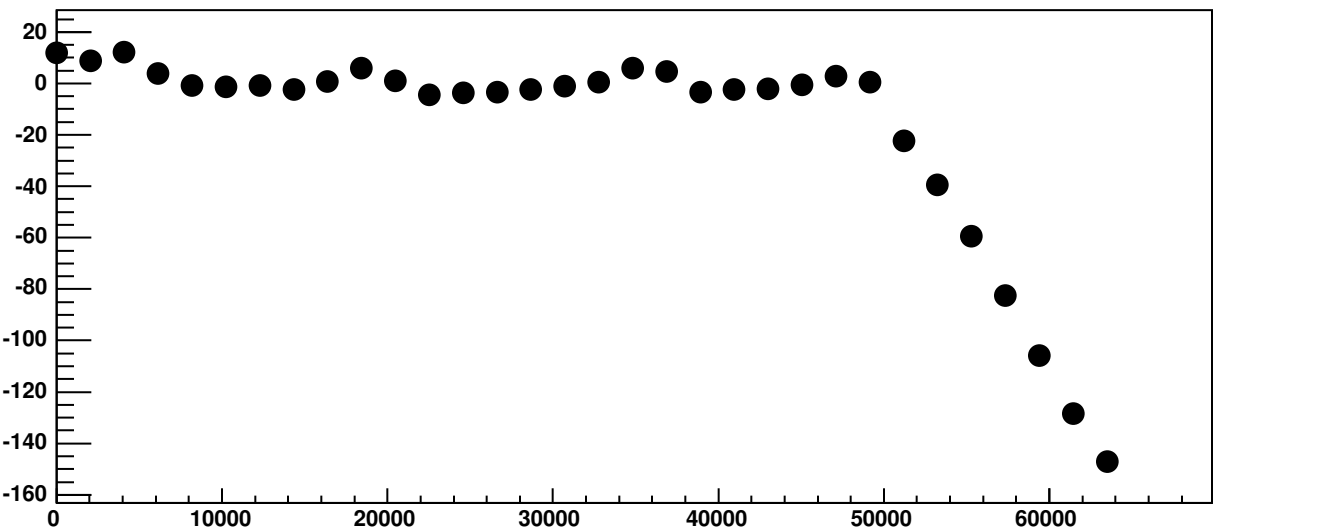
Chip 9, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

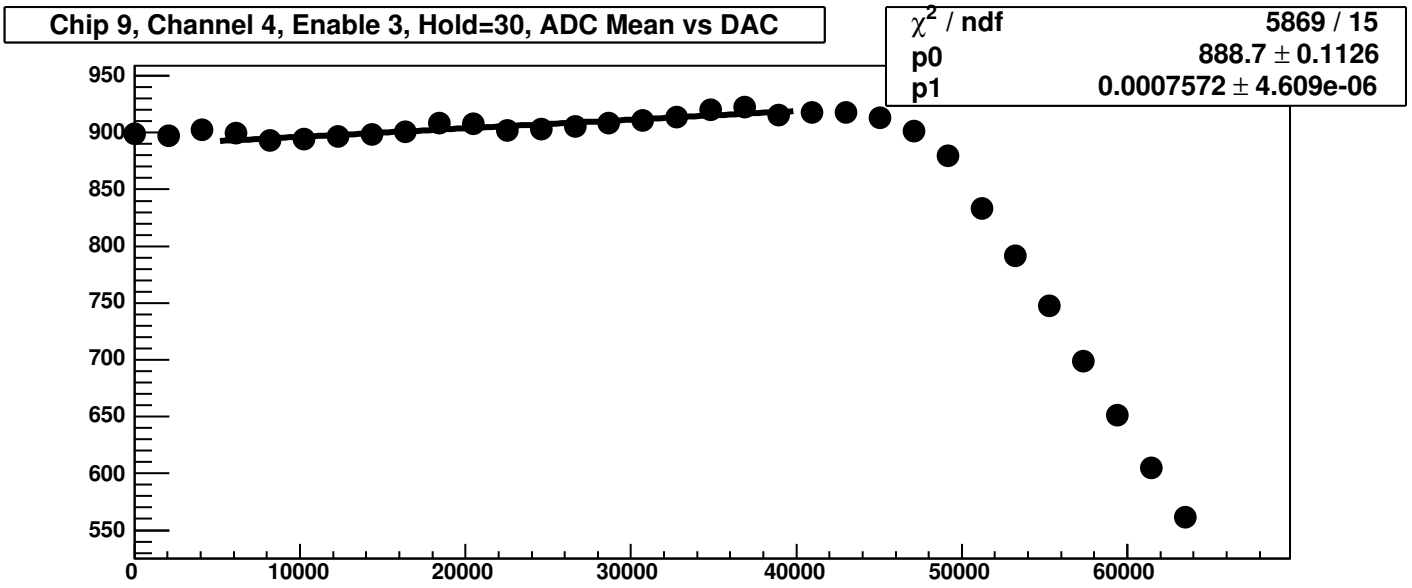


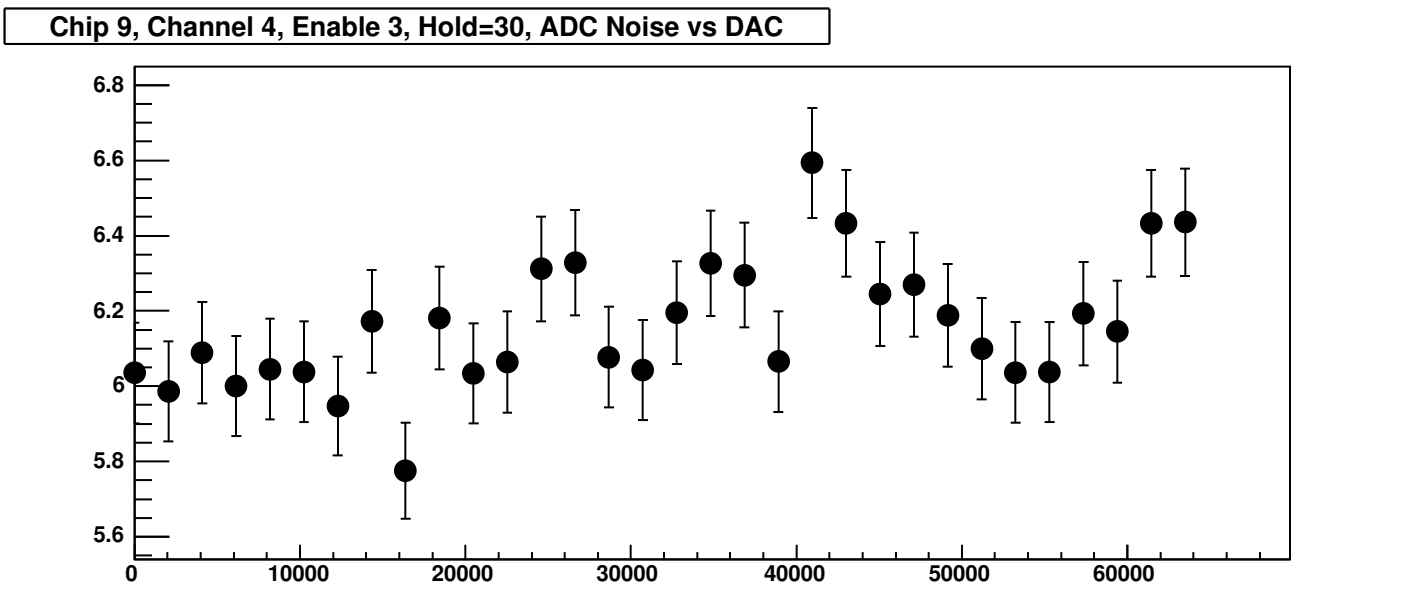
Chip 9, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC

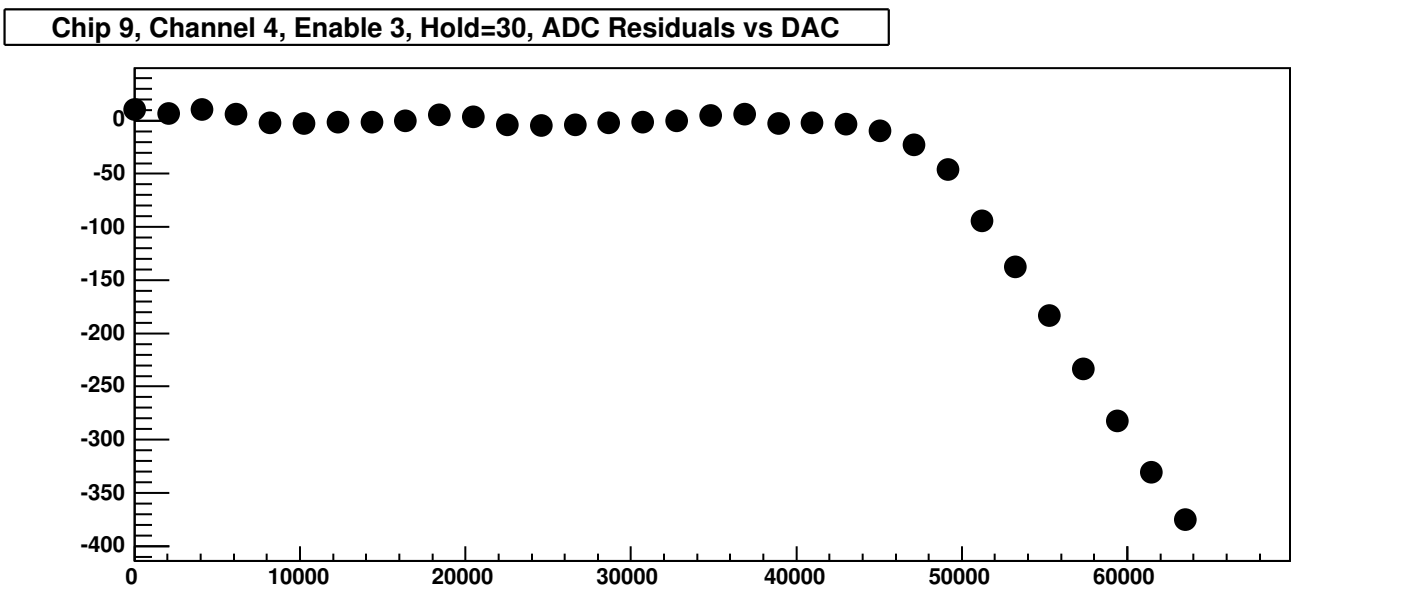


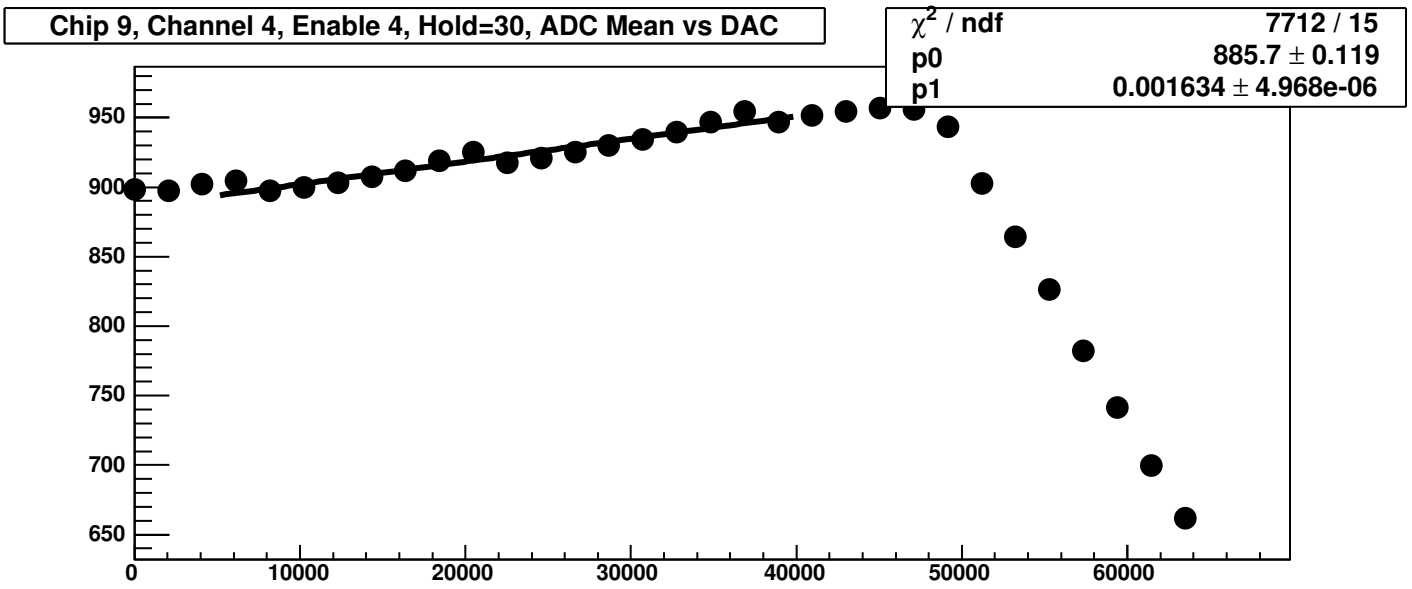
Chip 9, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

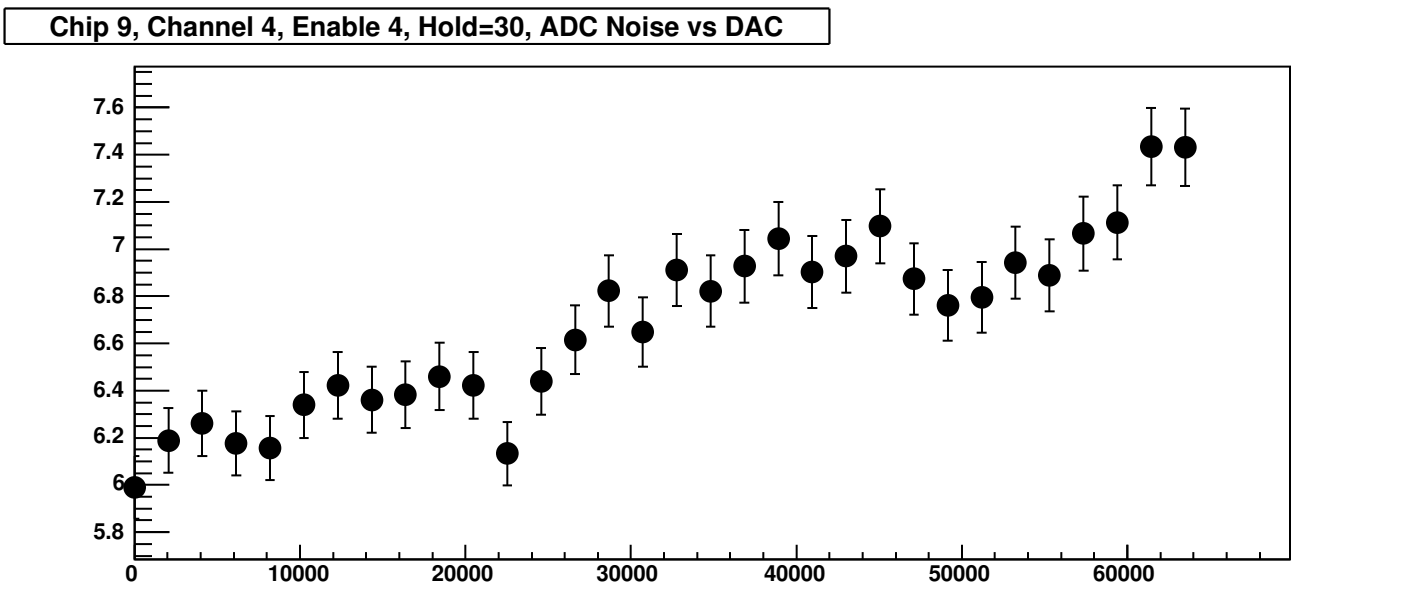


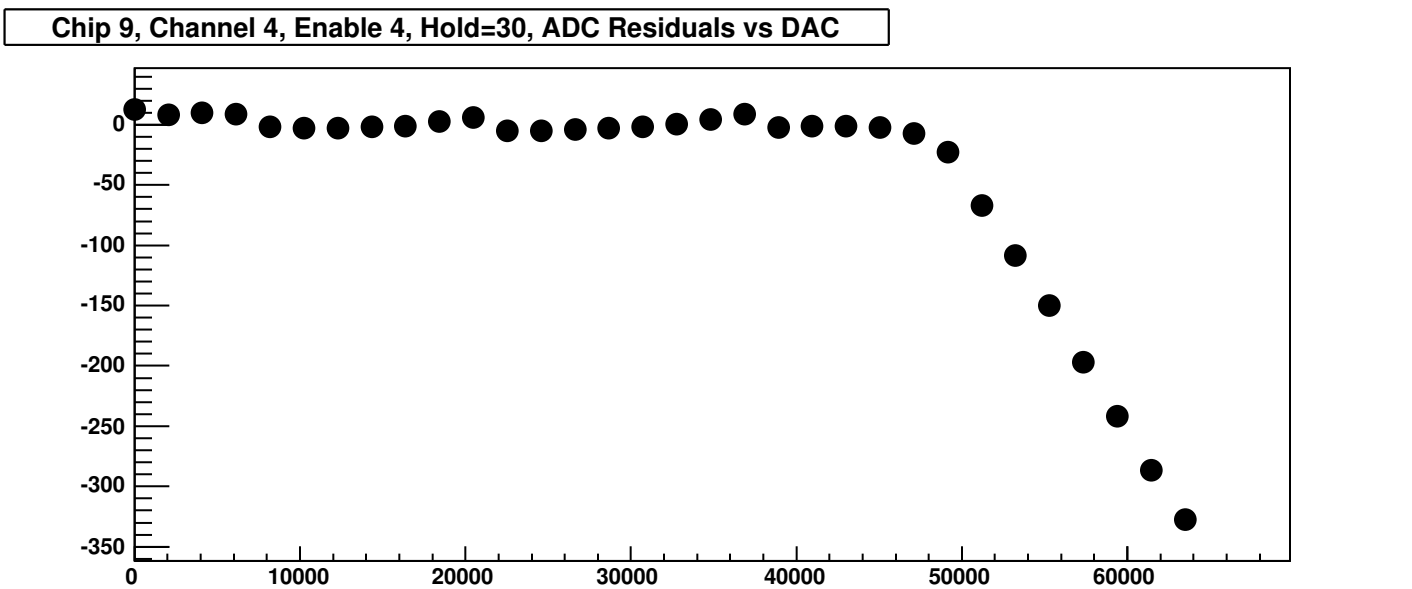

Chip 9, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5869 / 15
p0 888.7 ± 0.1126
p1 0.0007572 ± 4.609e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

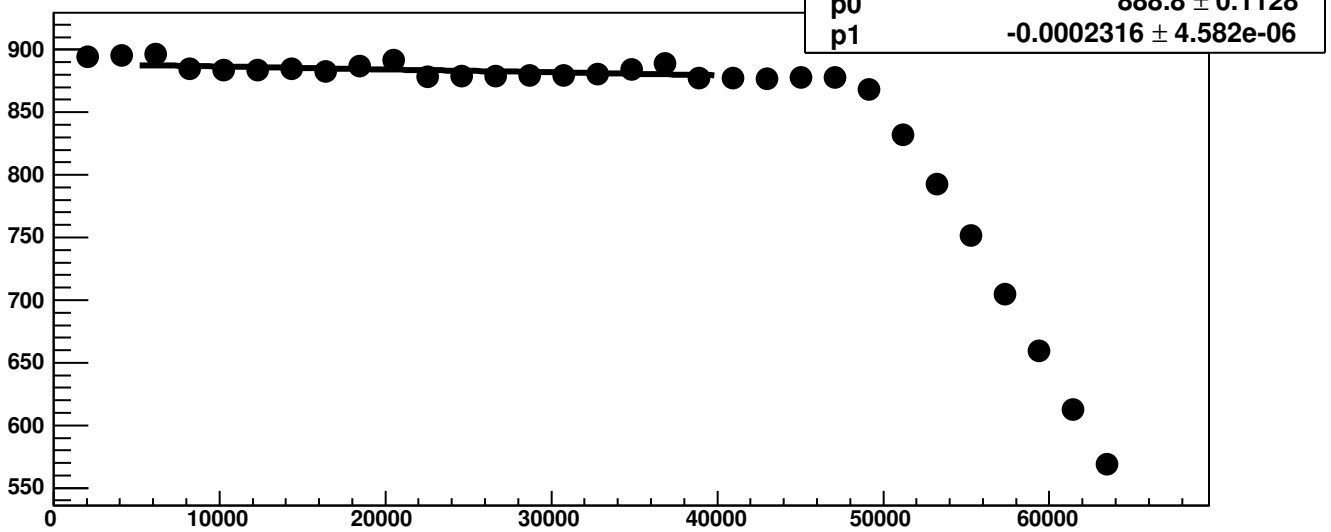

Chip 9, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7712 / 15
p0 885.7 ± 0.119
p1 0.001634 ± 4.968e-06
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

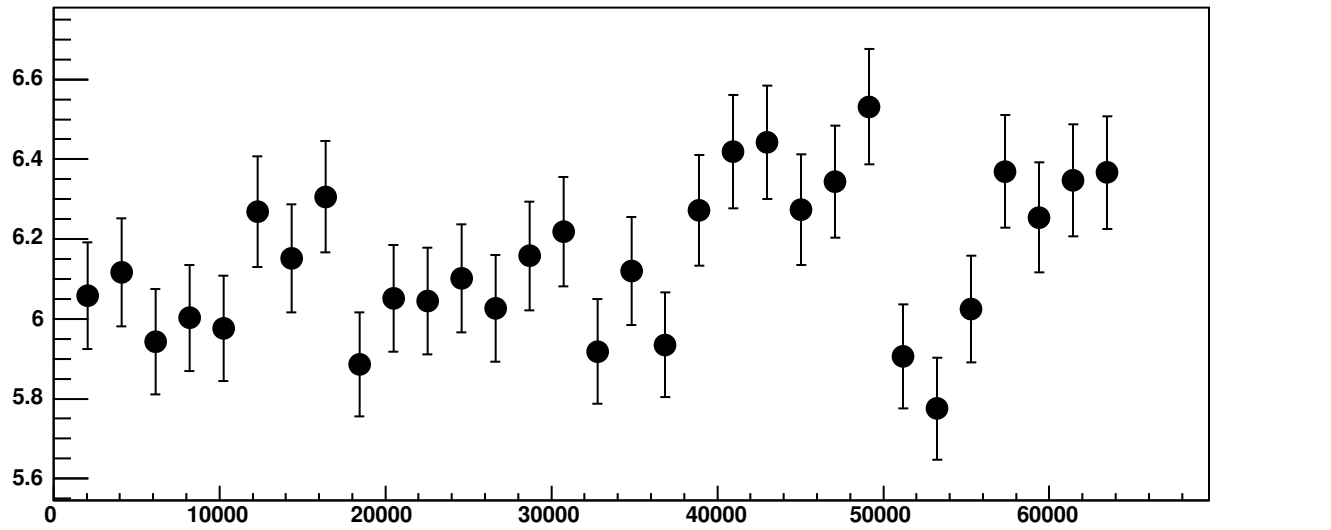

Chip 9, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

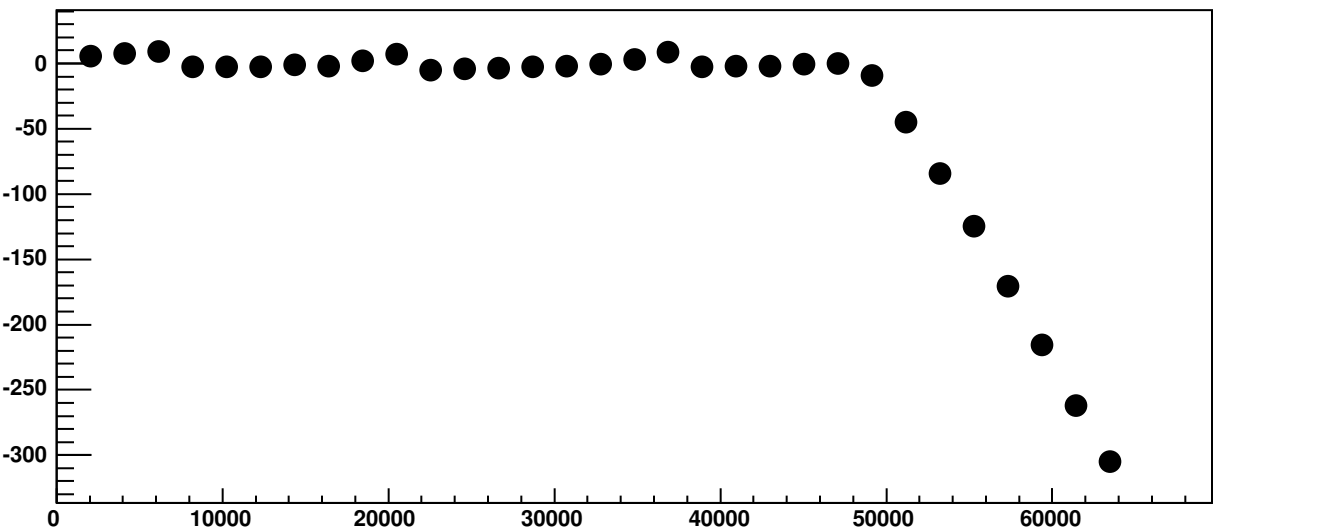
Chip 9, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC

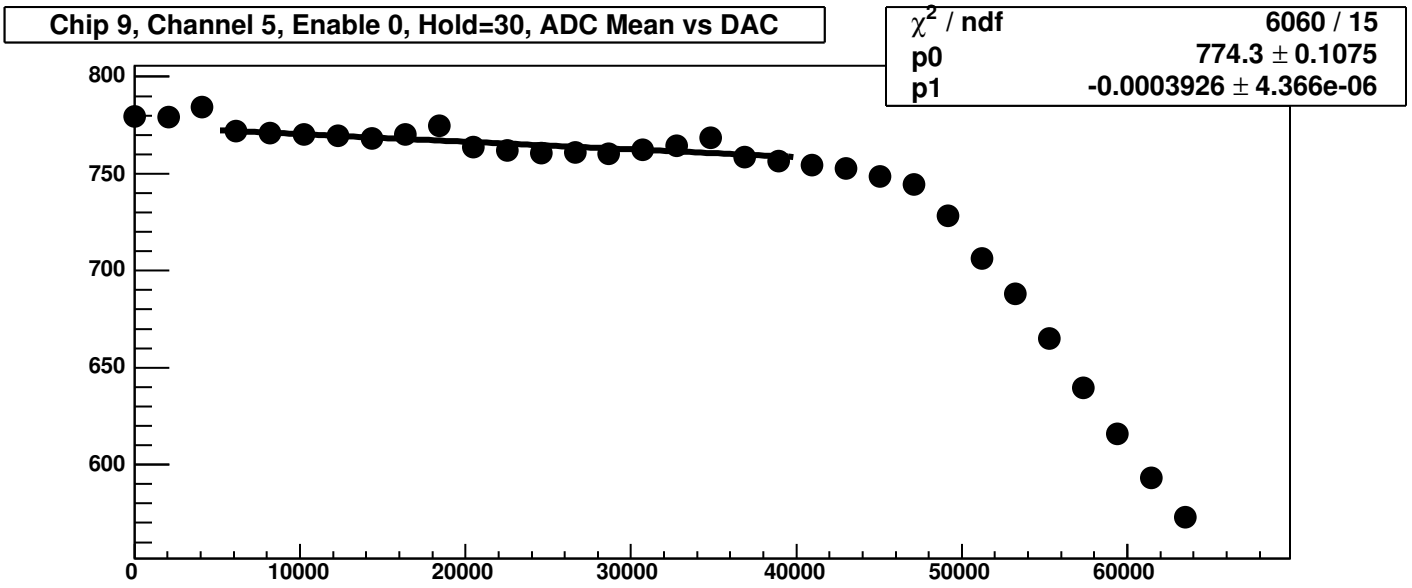


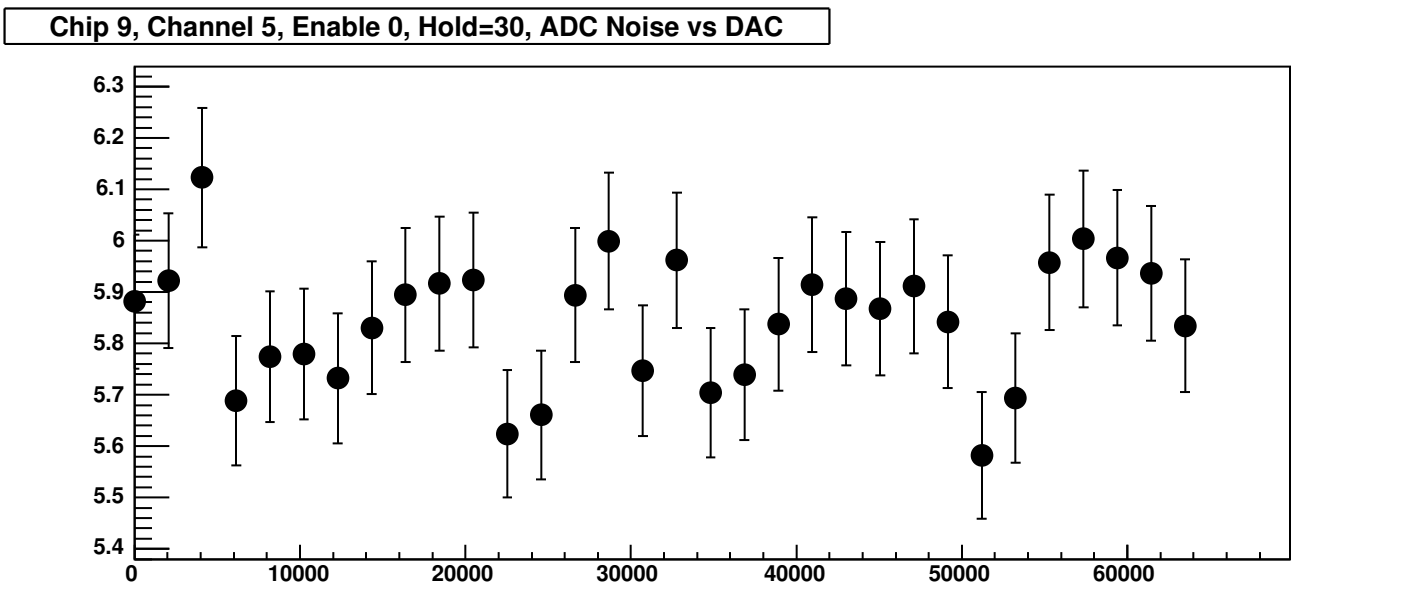
Chip 9, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

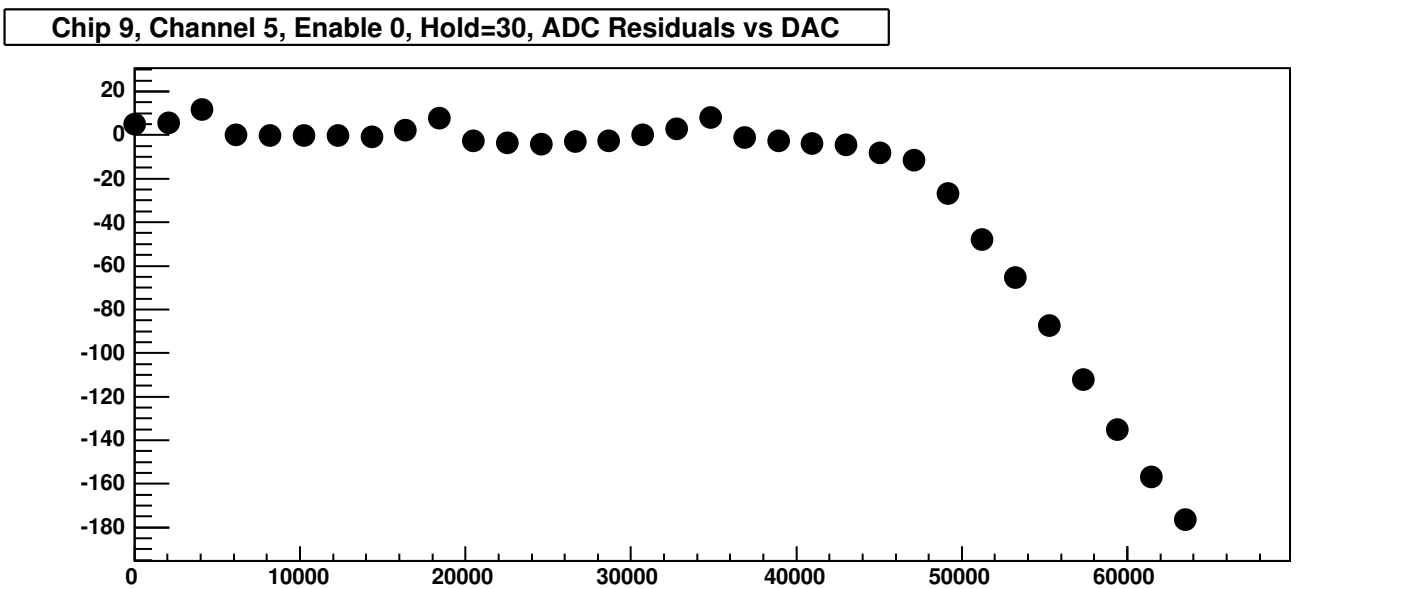


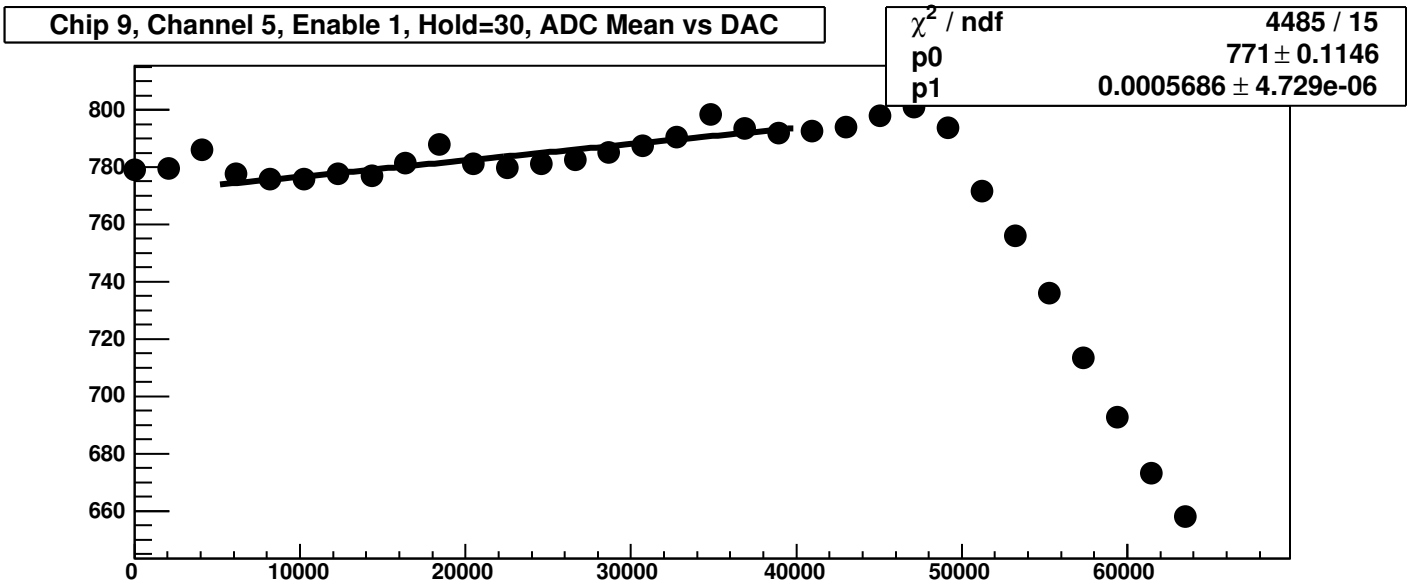
Chip 9, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC

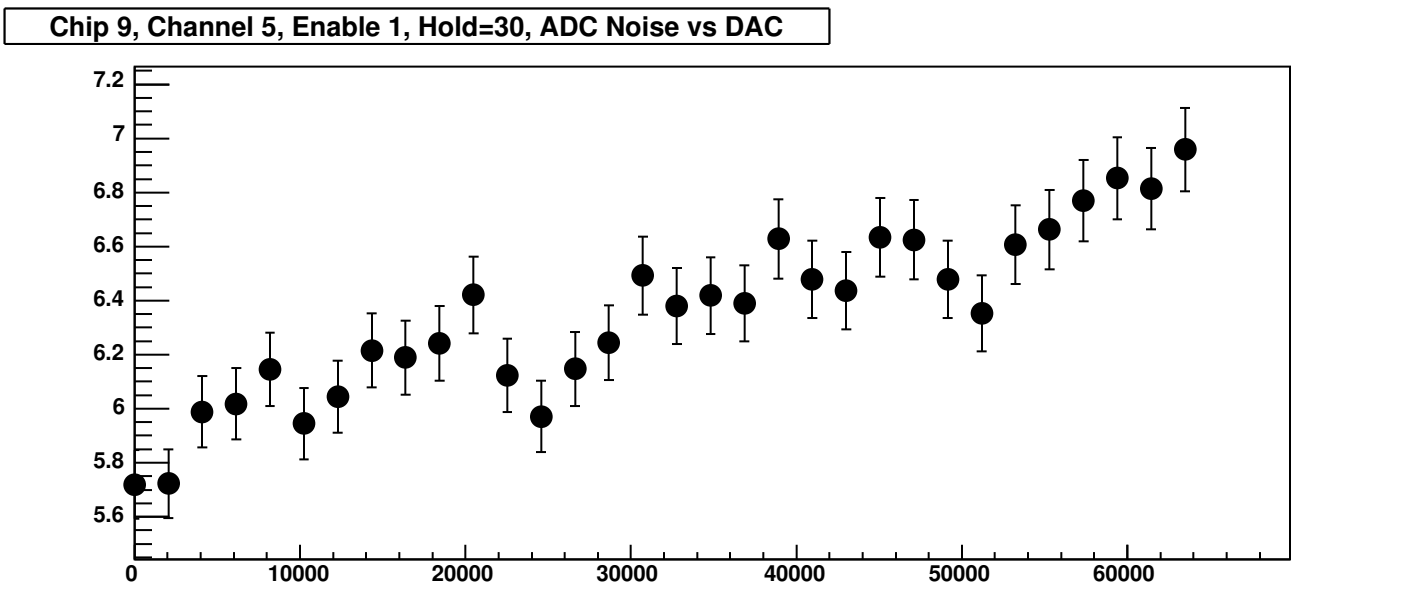


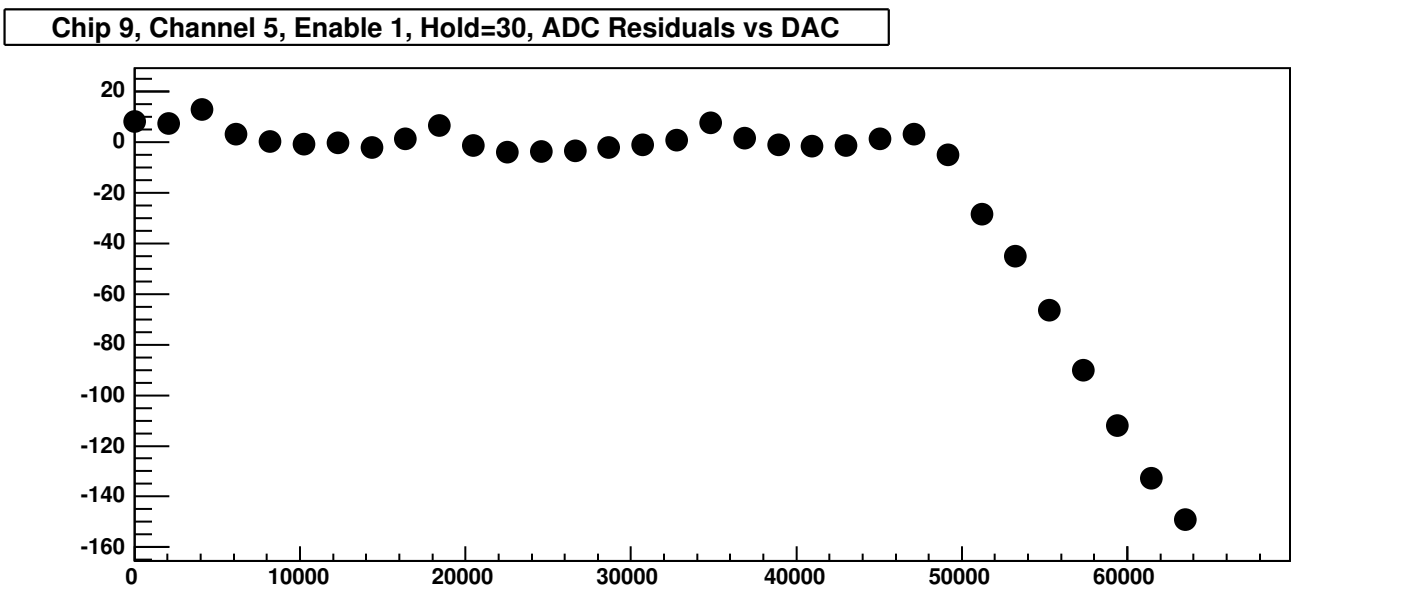

Chip 9, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6060 / 15
p0 774.3 ± 0.1075
p1 -0.0003926 ± 4.366e-06
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

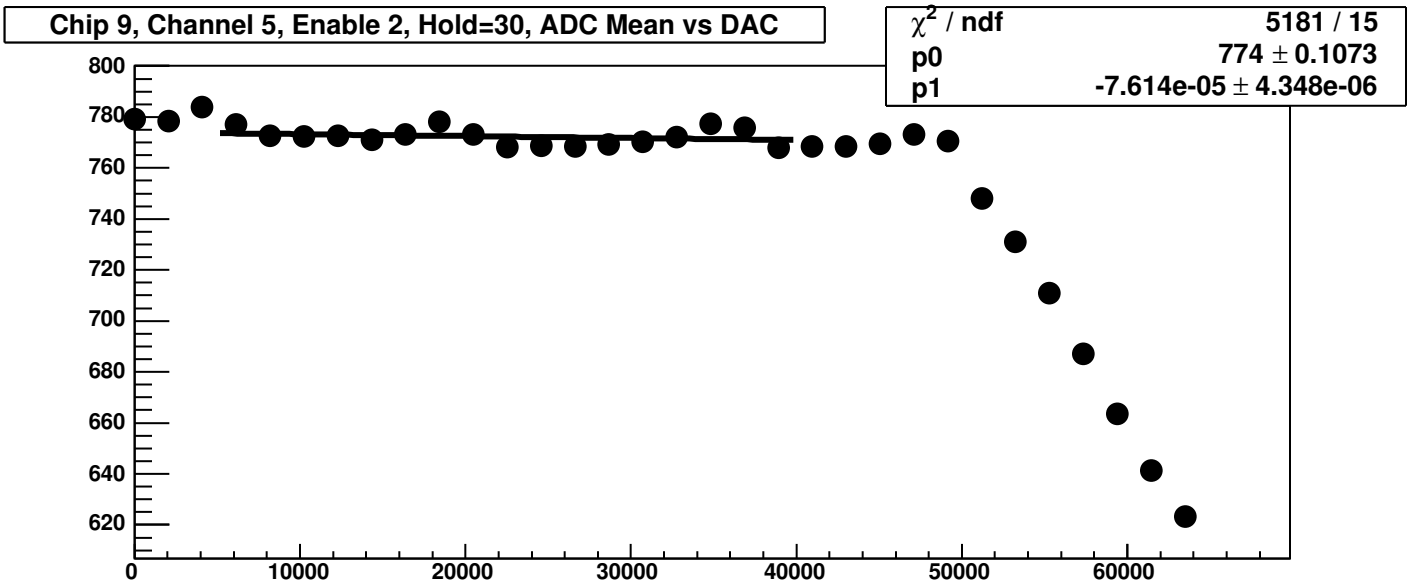

Chip 9, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

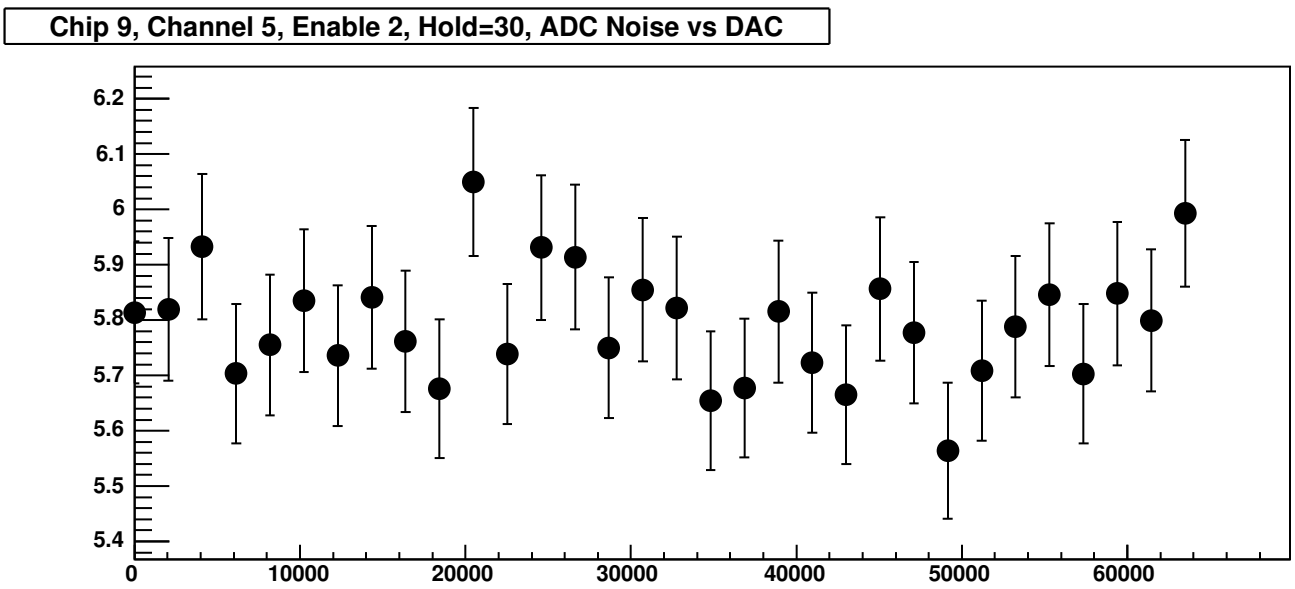

Chip 9, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

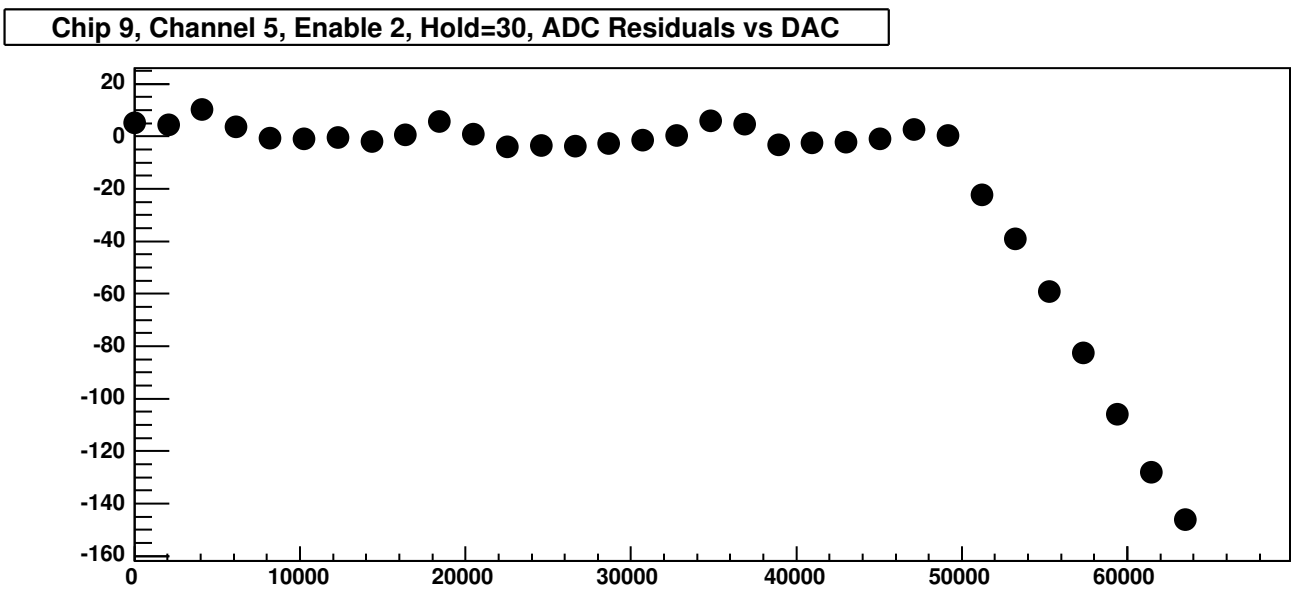

Chip 9, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4485 / 15
p0 771 ± 0.1146
p1 0.0005686 ± 4.729e-06
800
780
760
740
720
700
680
660
0
10000
20000
30000
40000
50000
60000

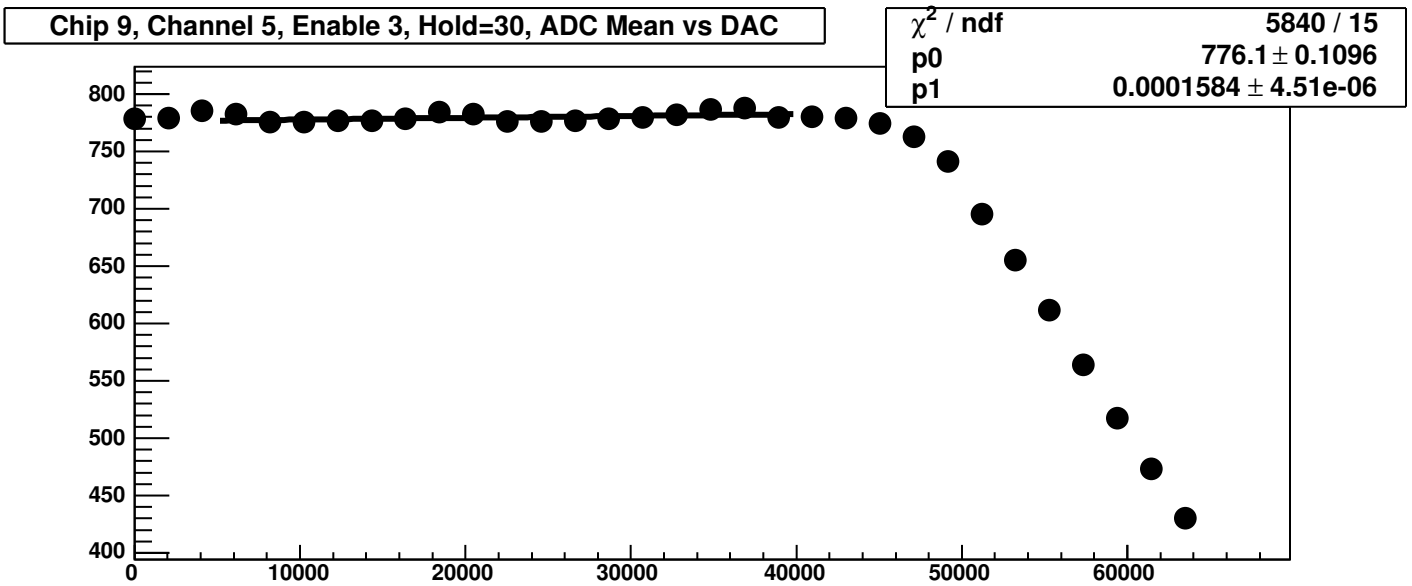

Chip 9, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

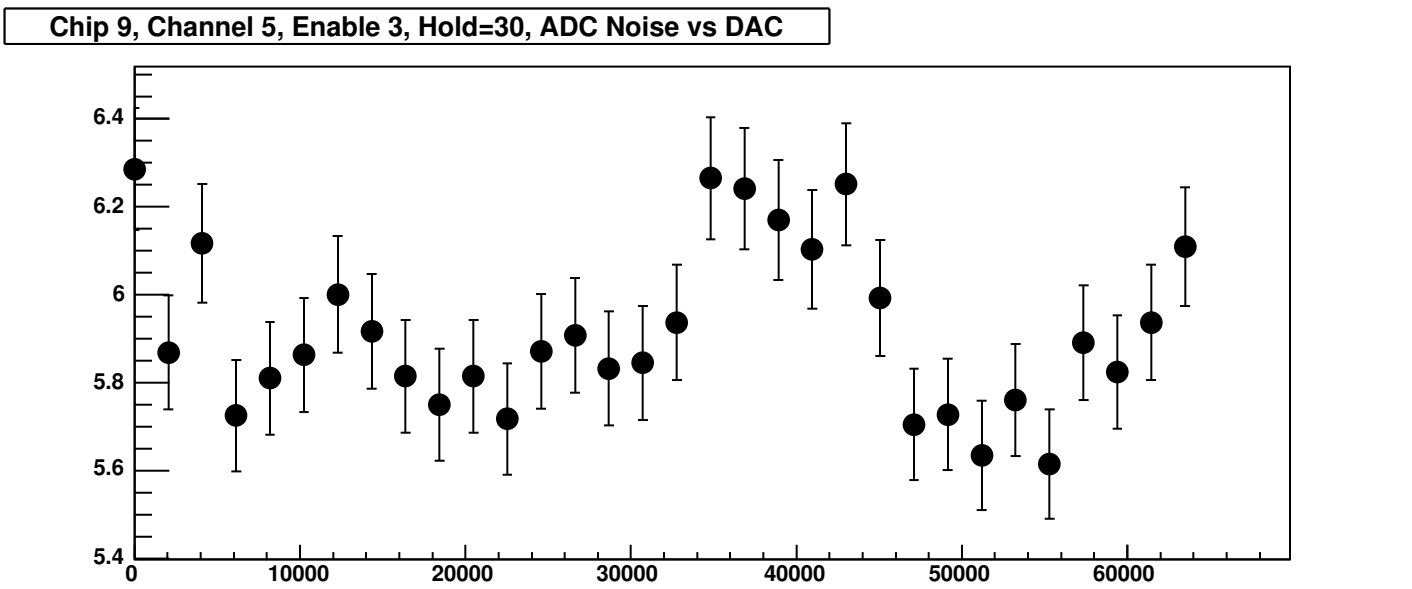

Chip 9, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

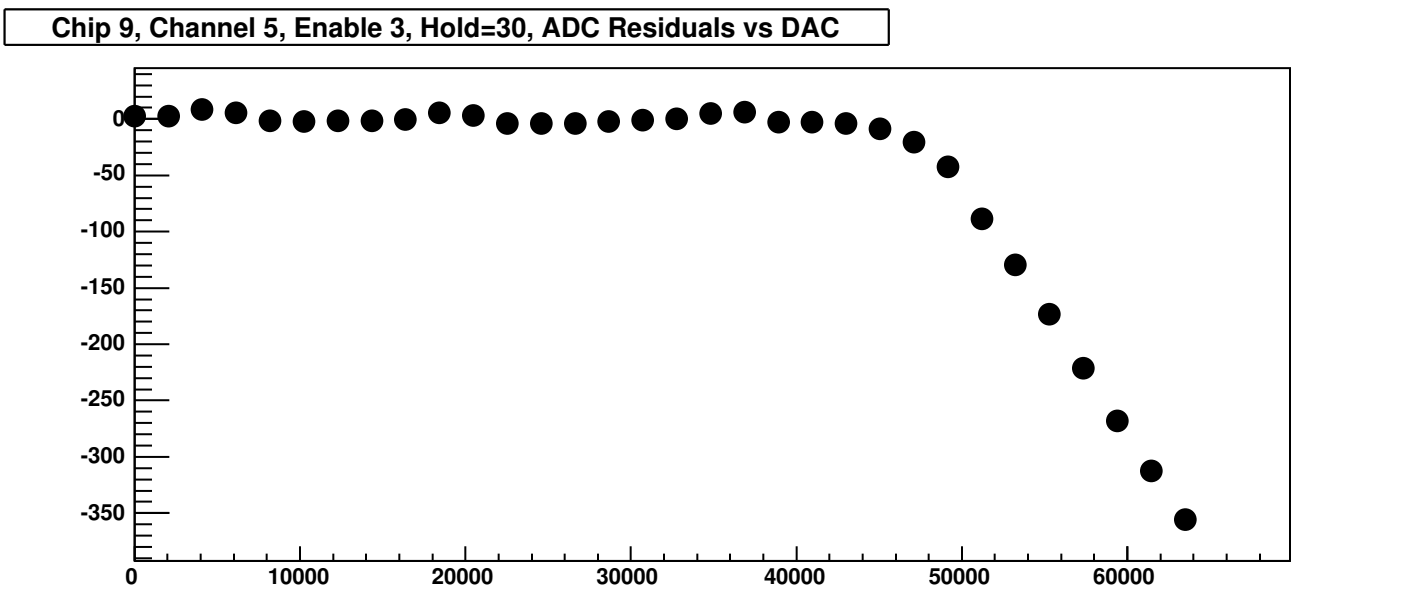

Chip 9, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5181 / 15
p0 774 ± 0.1073
p1 -7.614e-05 ± 4.348e-06
800
780
760
740
720
700
680
660
640
620
0
10000
20000
30000
40000
50000
60000

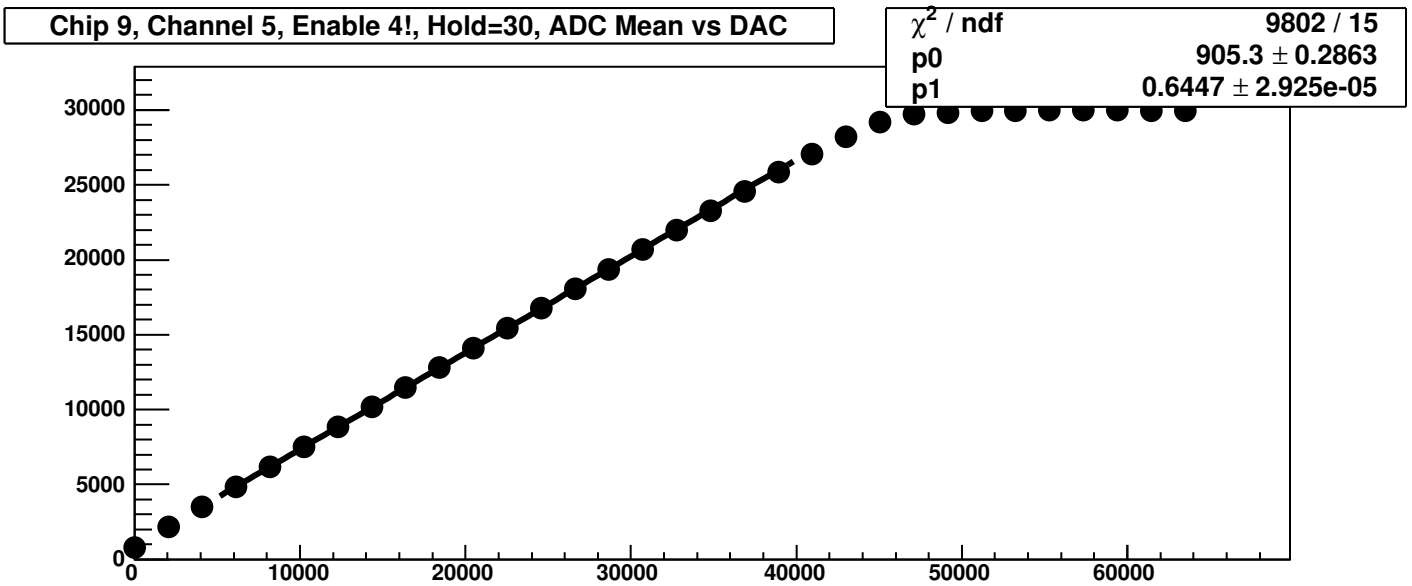

Chip 9, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

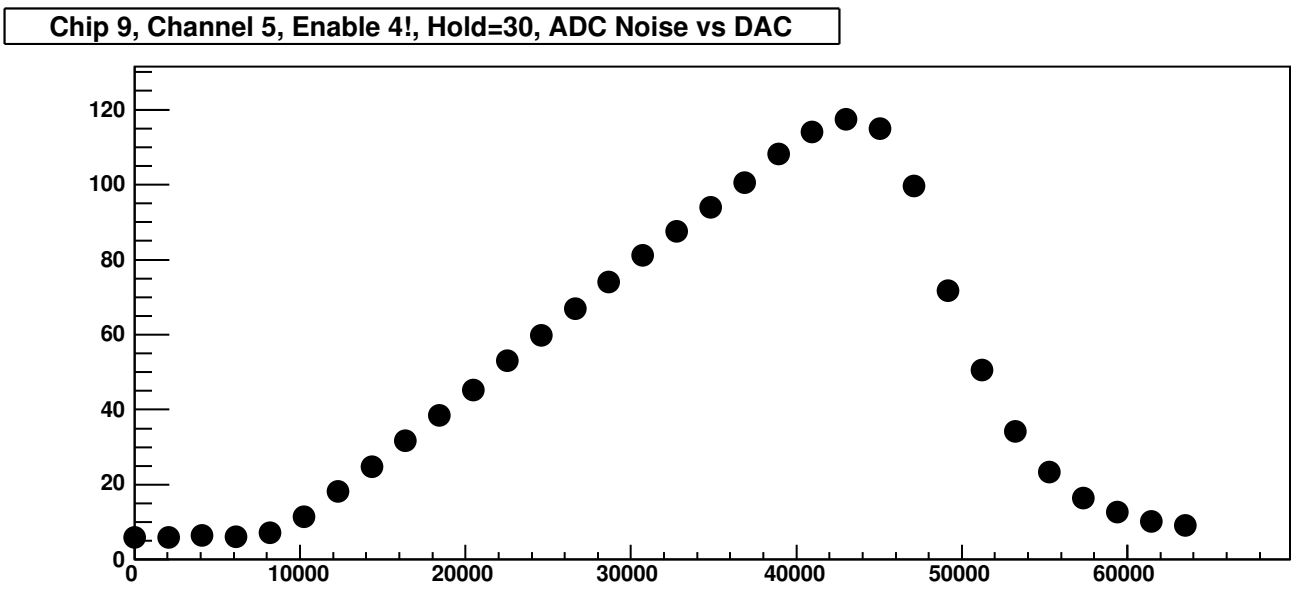

Chip 9, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

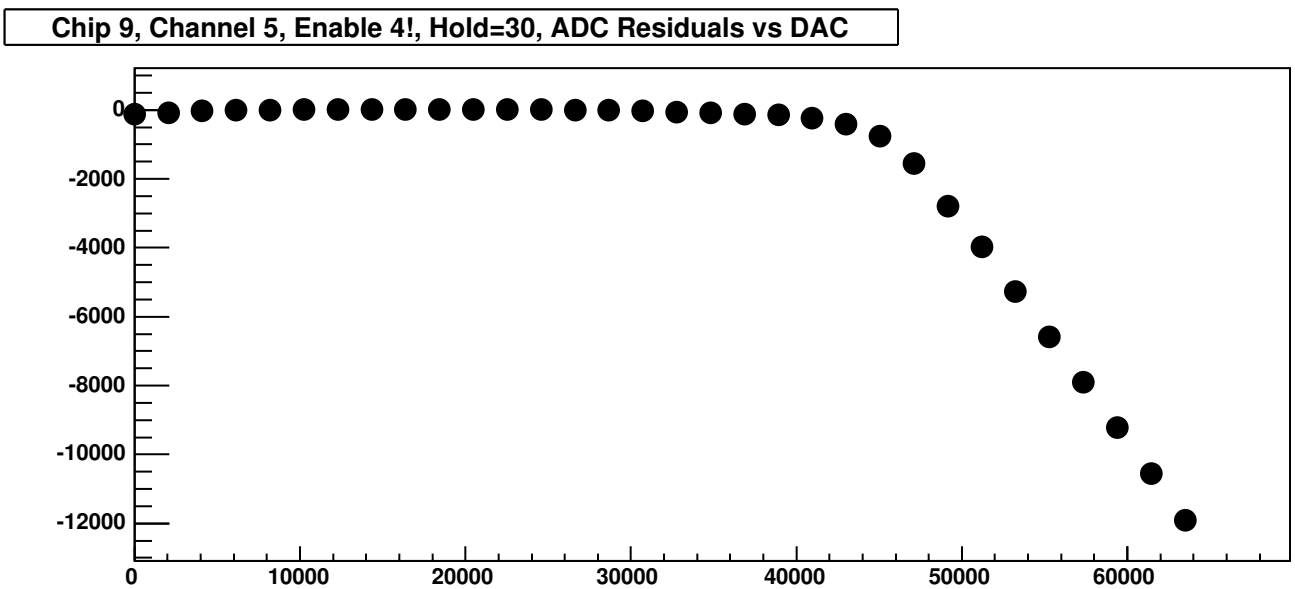

Chip 9, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5840 / 15
p0 776.1 ± 0.1096
p1 0.0001584 ± 4.51e-06
800
750
700
650
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000

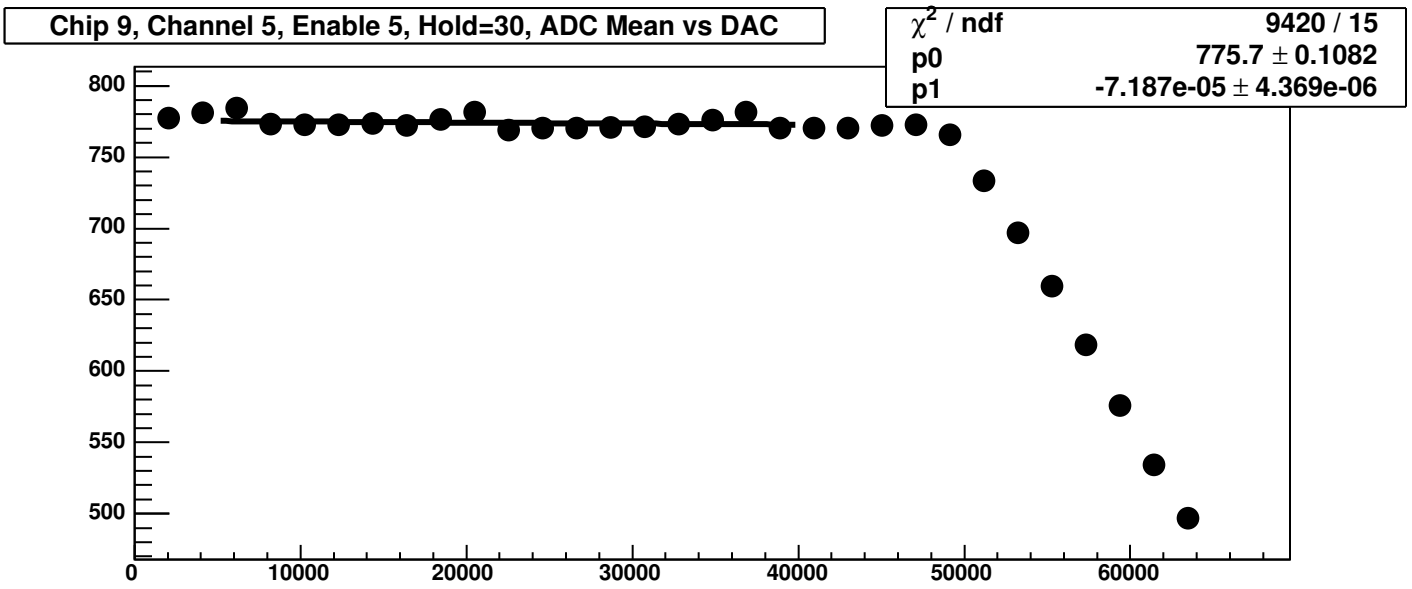

Chip 9, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

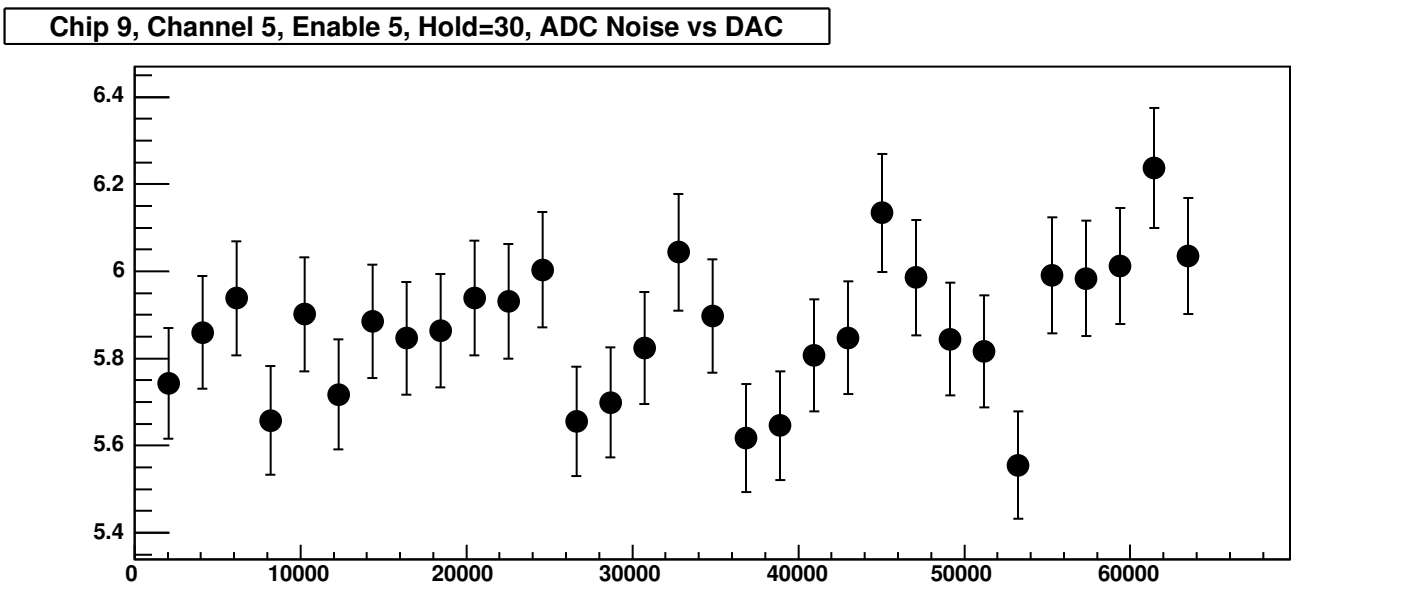

Chip 9, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

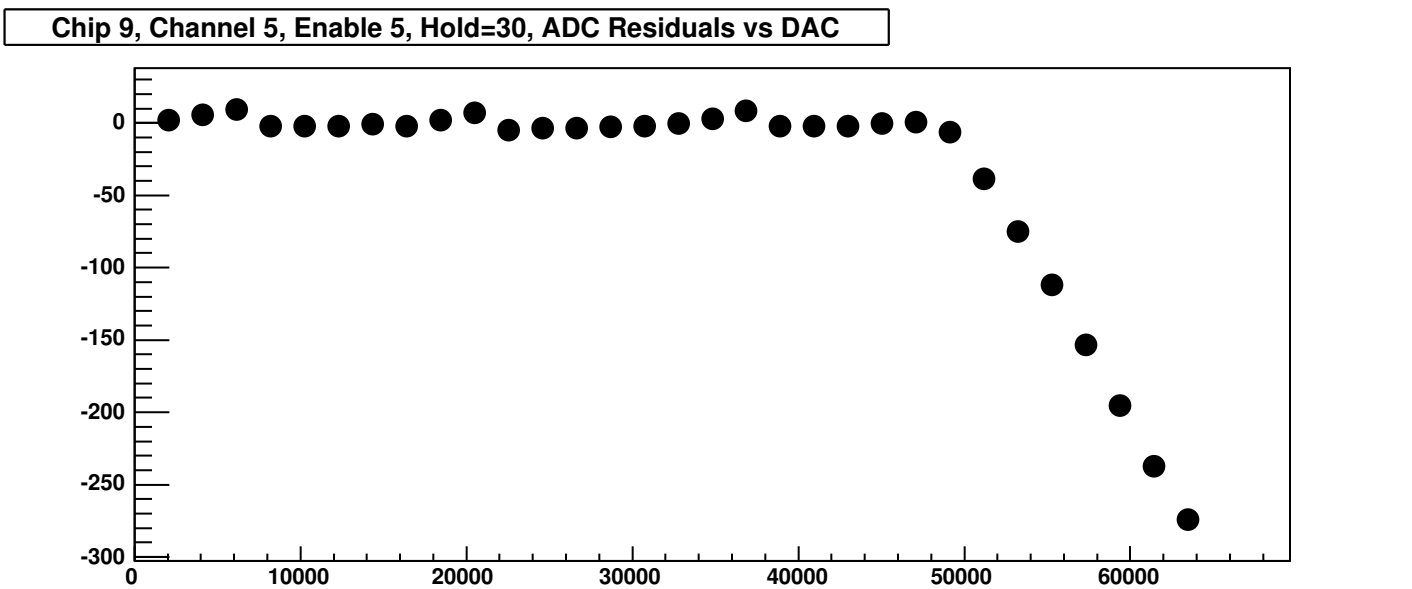

Chip 9, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 9802 / 15
p0 905.3 ± 0.2863
p1 0.6447 ± 2.925e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

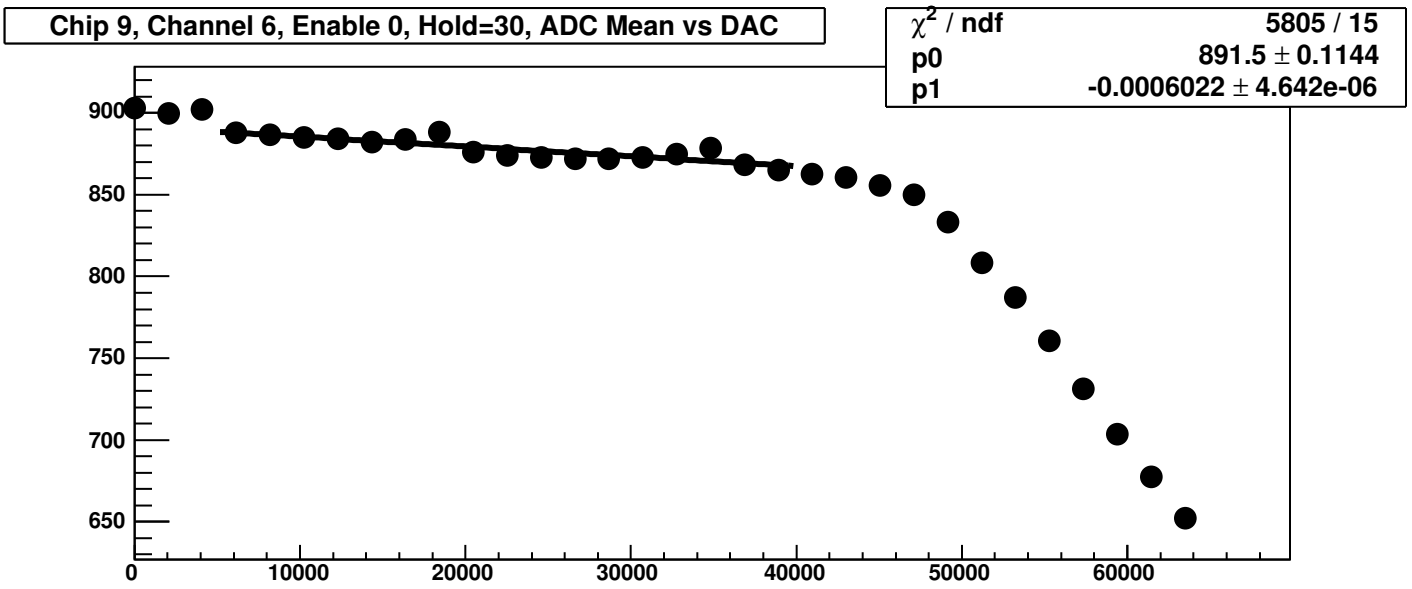

Chip 9, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

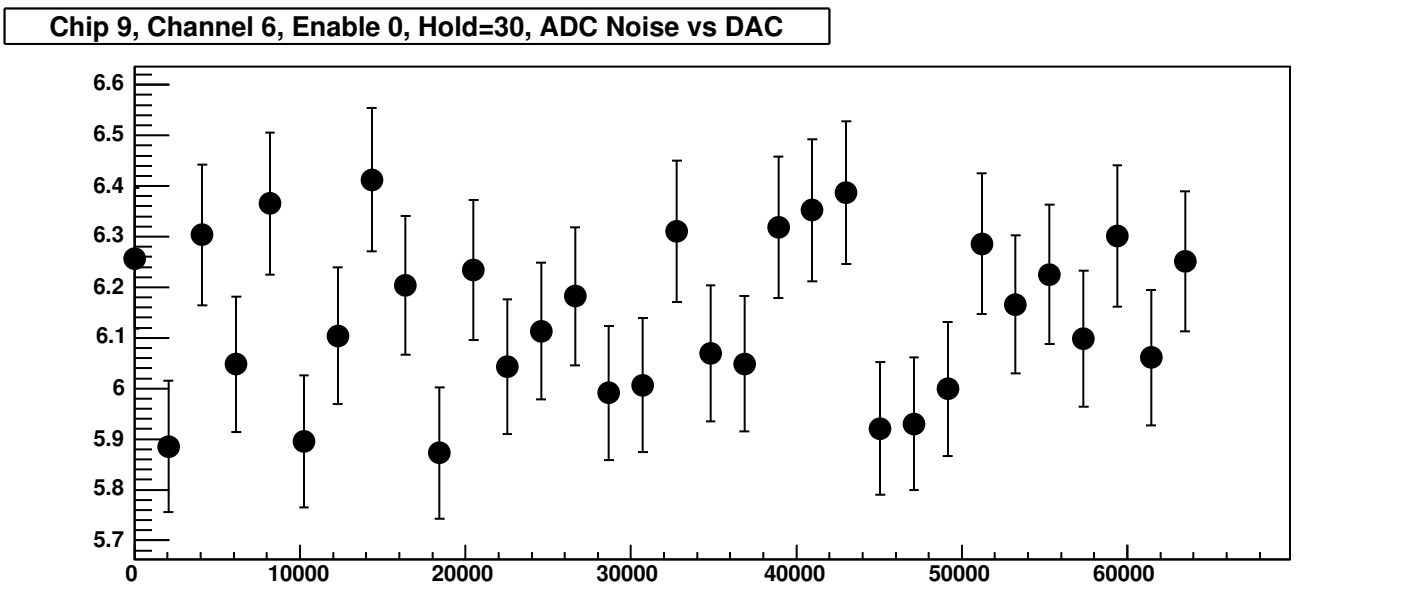

Chip 9, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

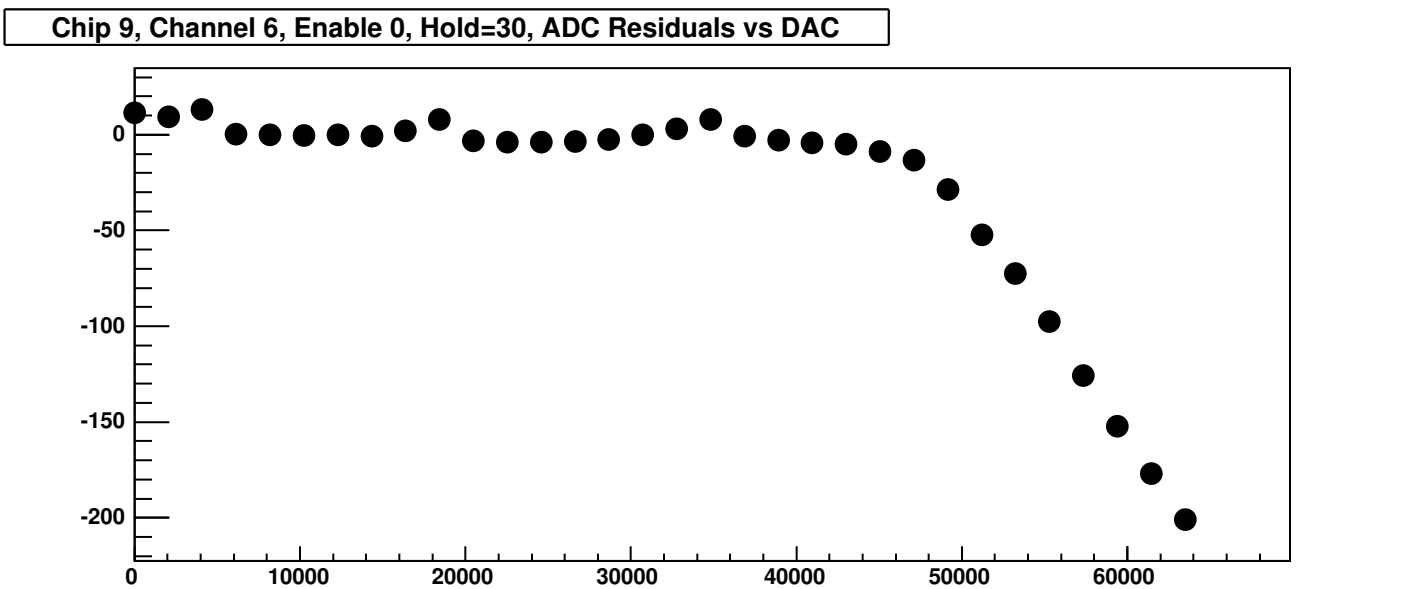

Chip 9, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9420 / 15
p0 775.7 ± 0.1082
p1 -7.187e-05 ± 4.369e-06
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

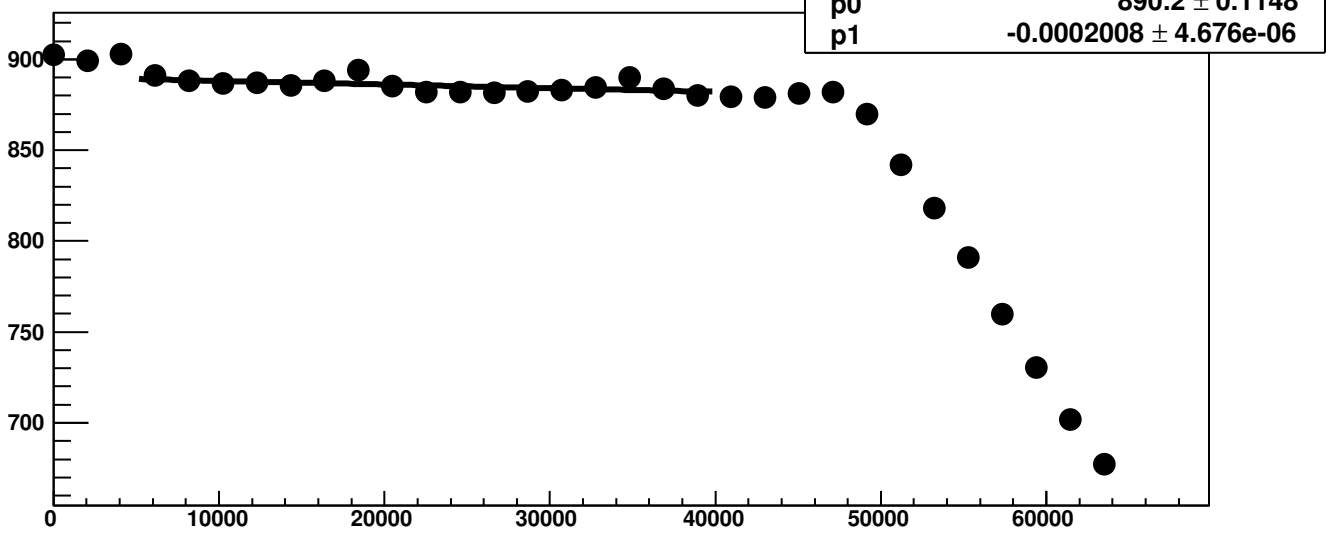

Chip 9, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5805 / 15
p0 891.5 ± 0.1144
p1 -0.0006022 ± 4.642e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

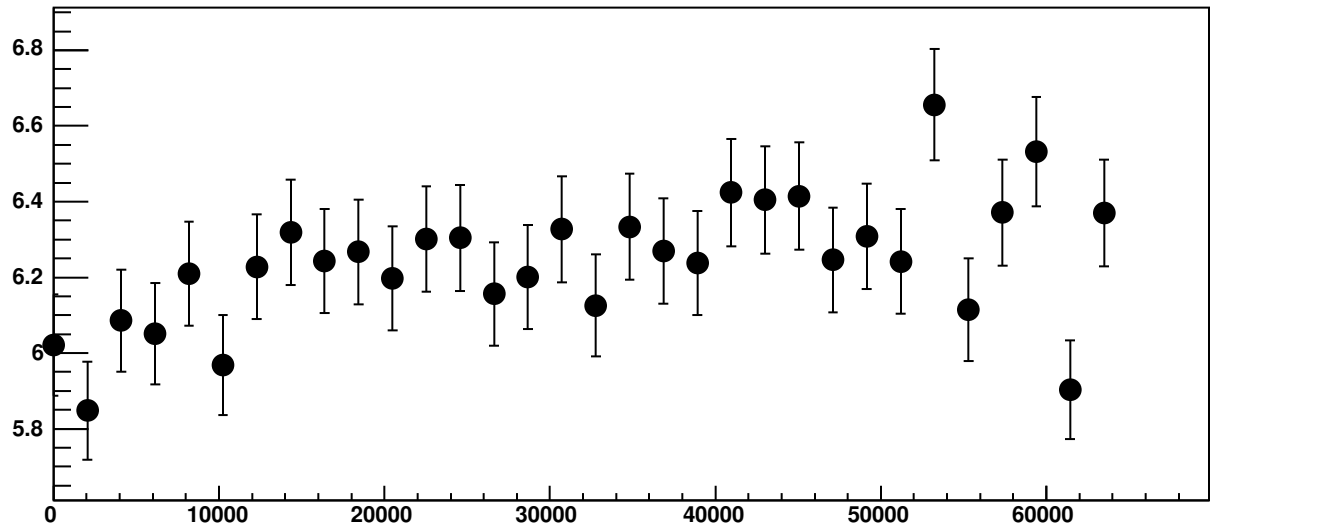

Chip 9, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

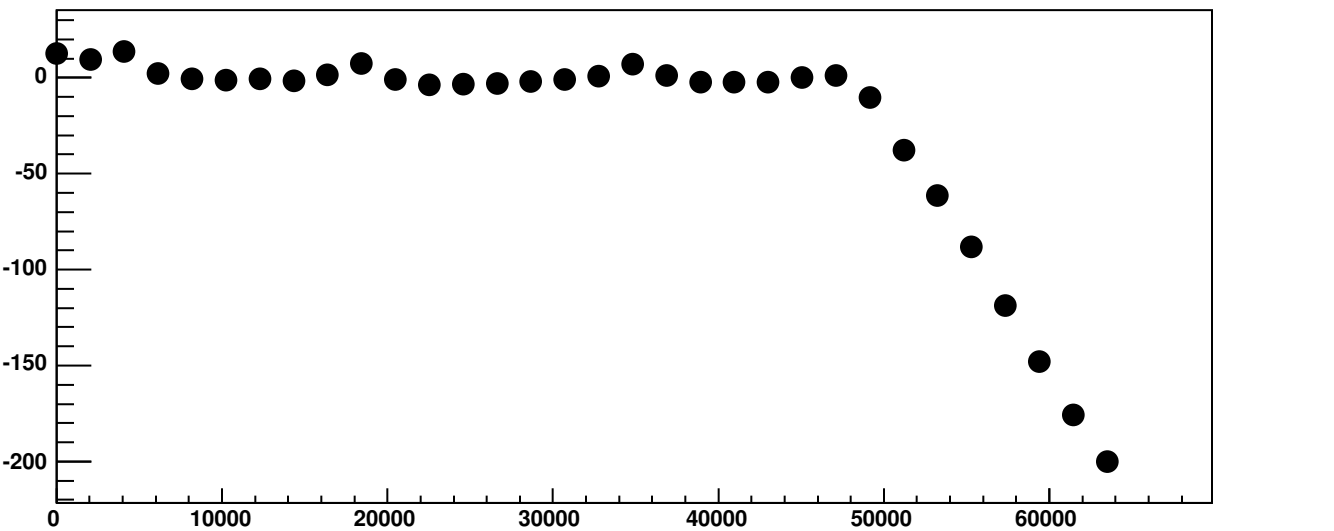
Chip 9, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

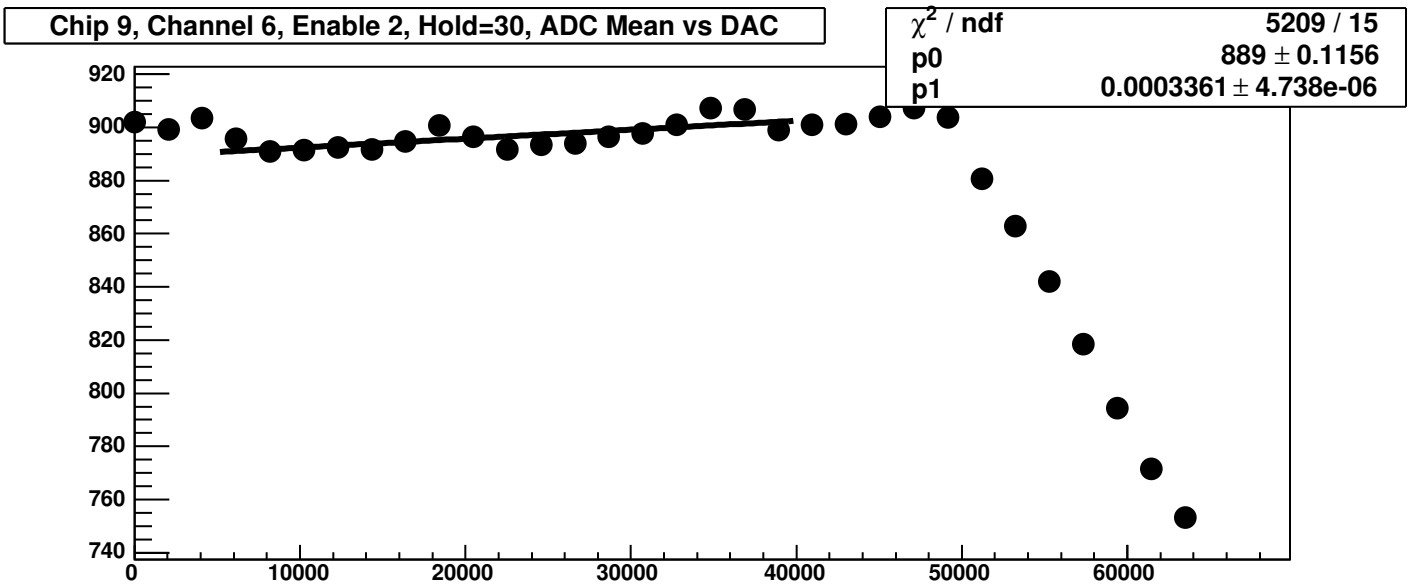


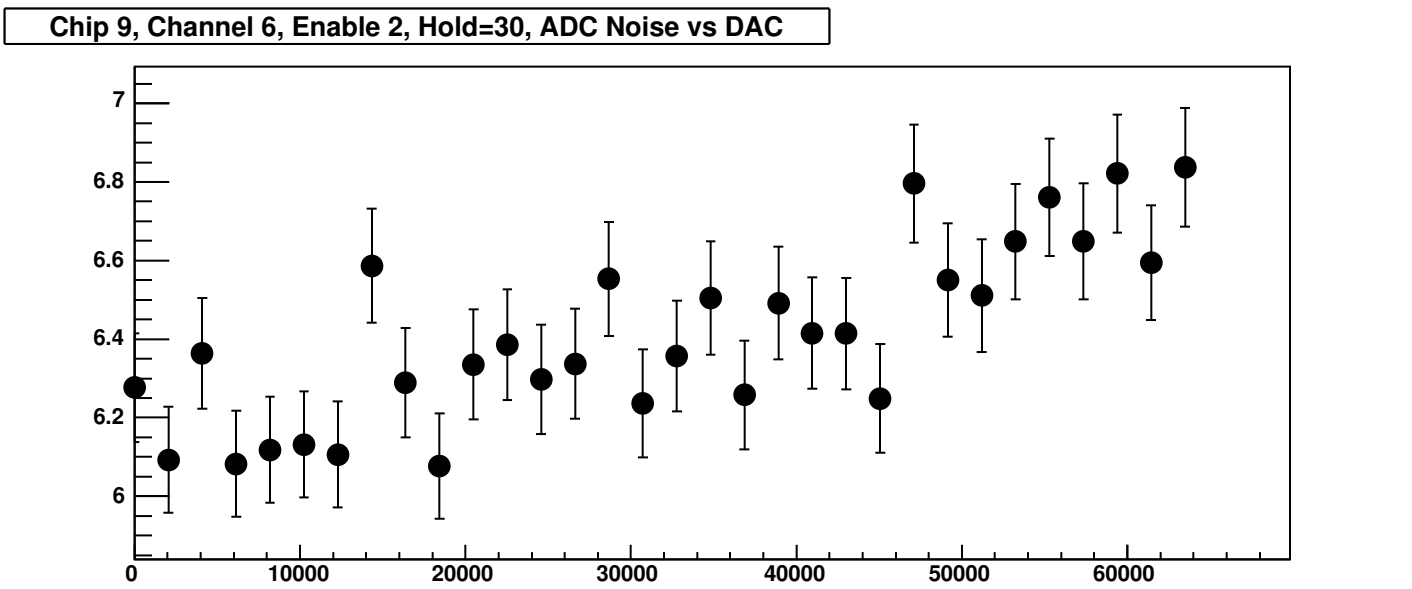
Chip 9, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

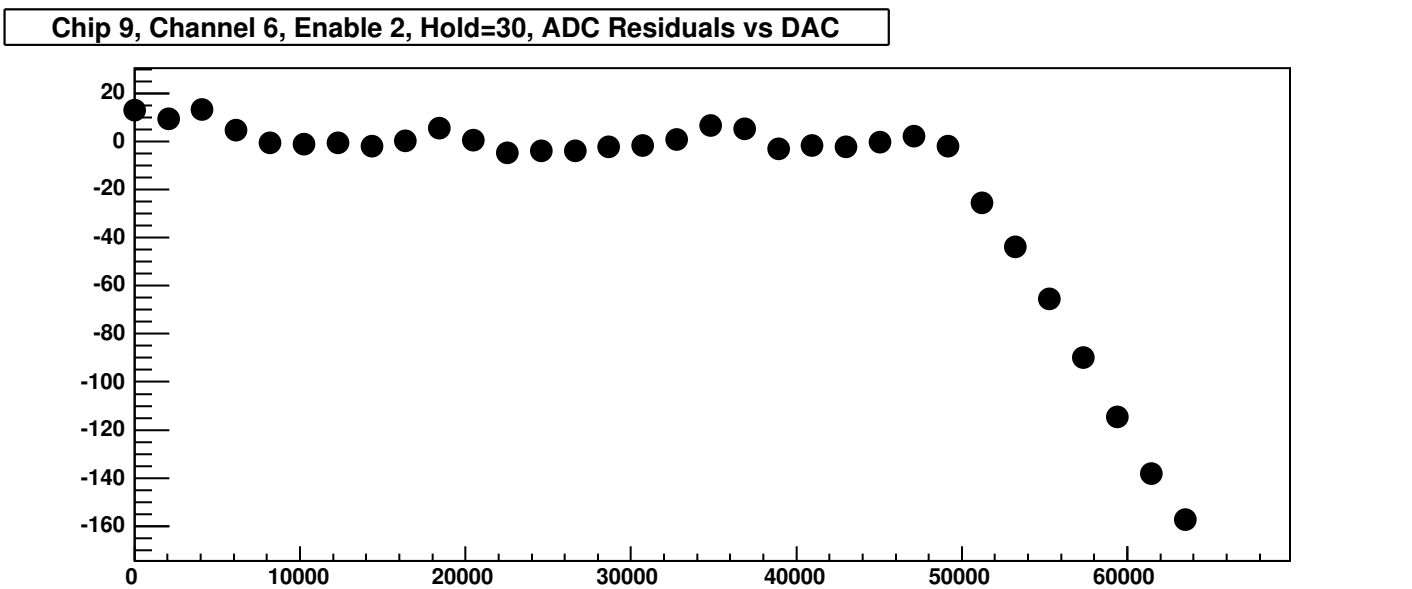


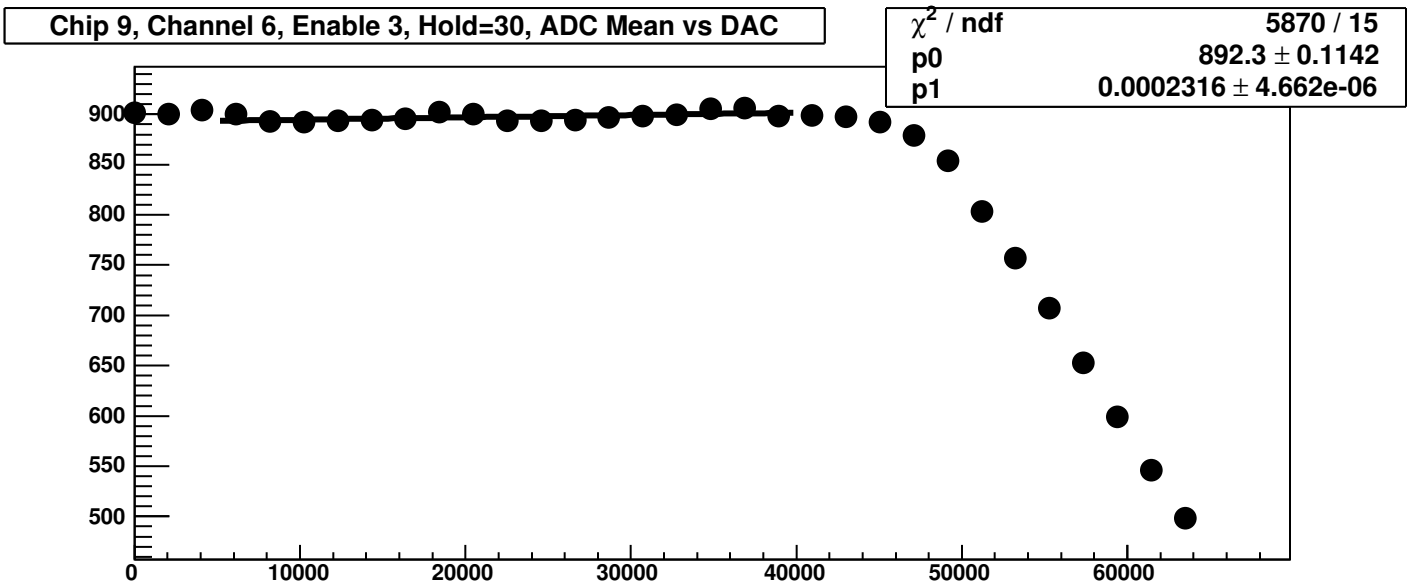
Chip 9, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

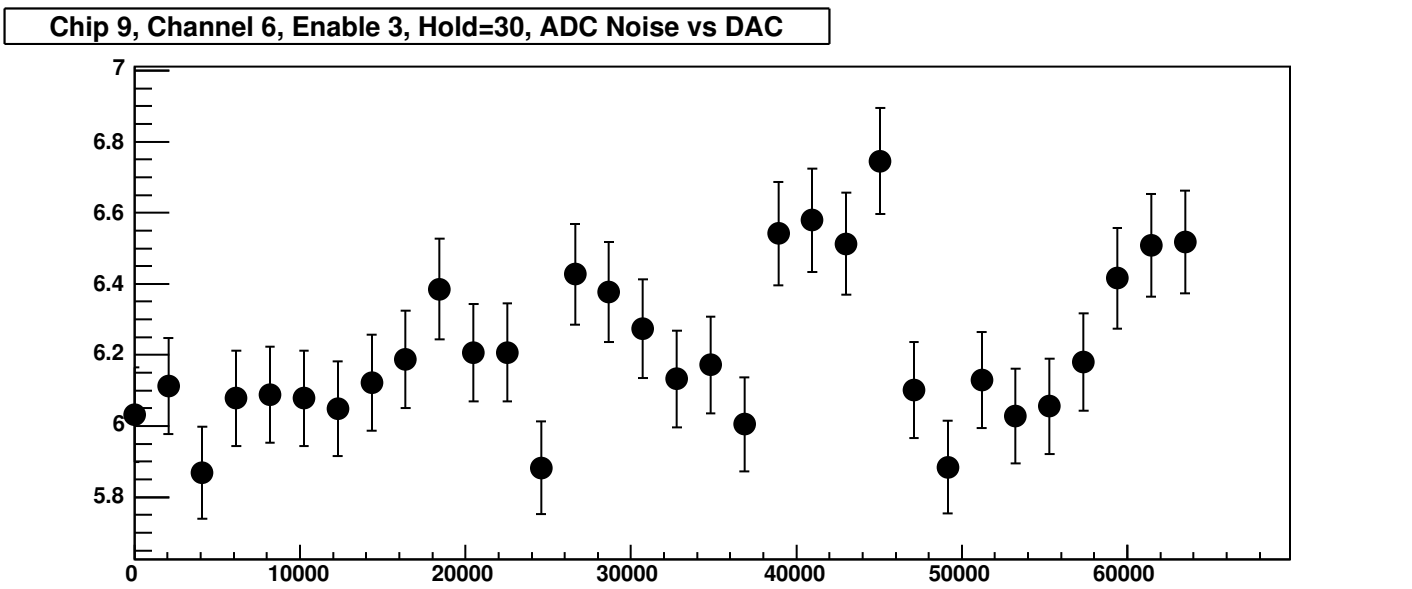


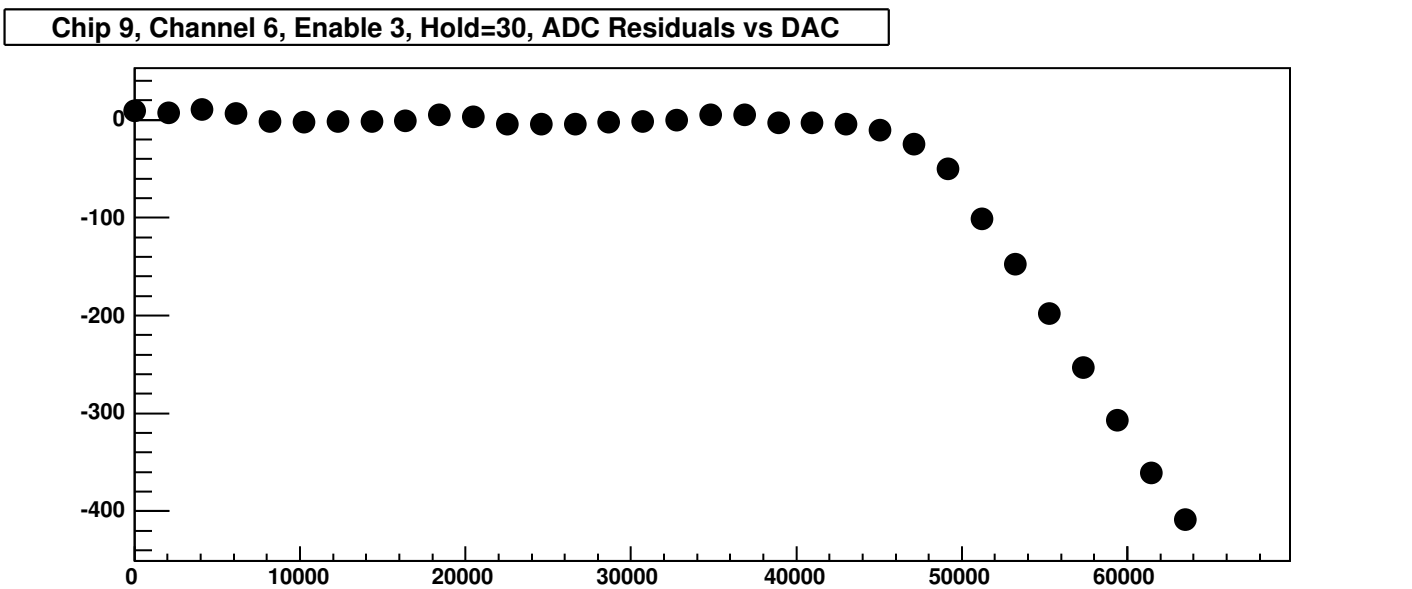

Chip 9, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5209 / 15
p0 889 ± 0.1156
p1 0.0003361 ± 4.738e-06
920
900
880
860
840
820
800
780
760
740
0
10000
20000
30000
40000
50000
60000

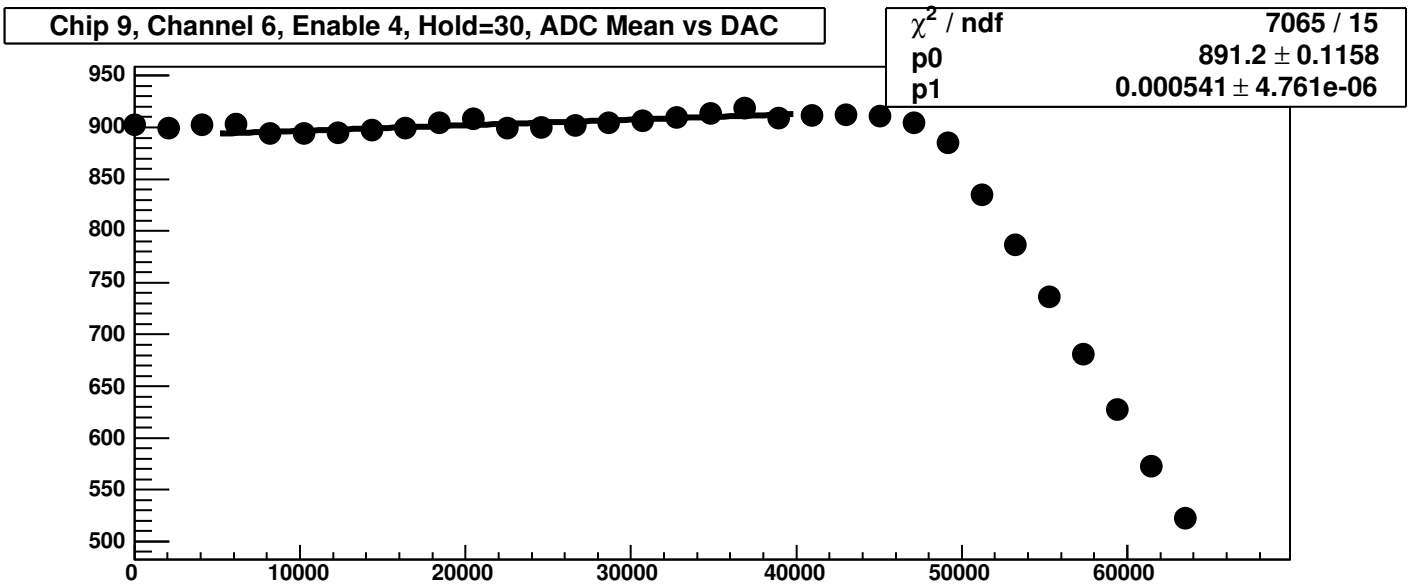

Chip 9, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

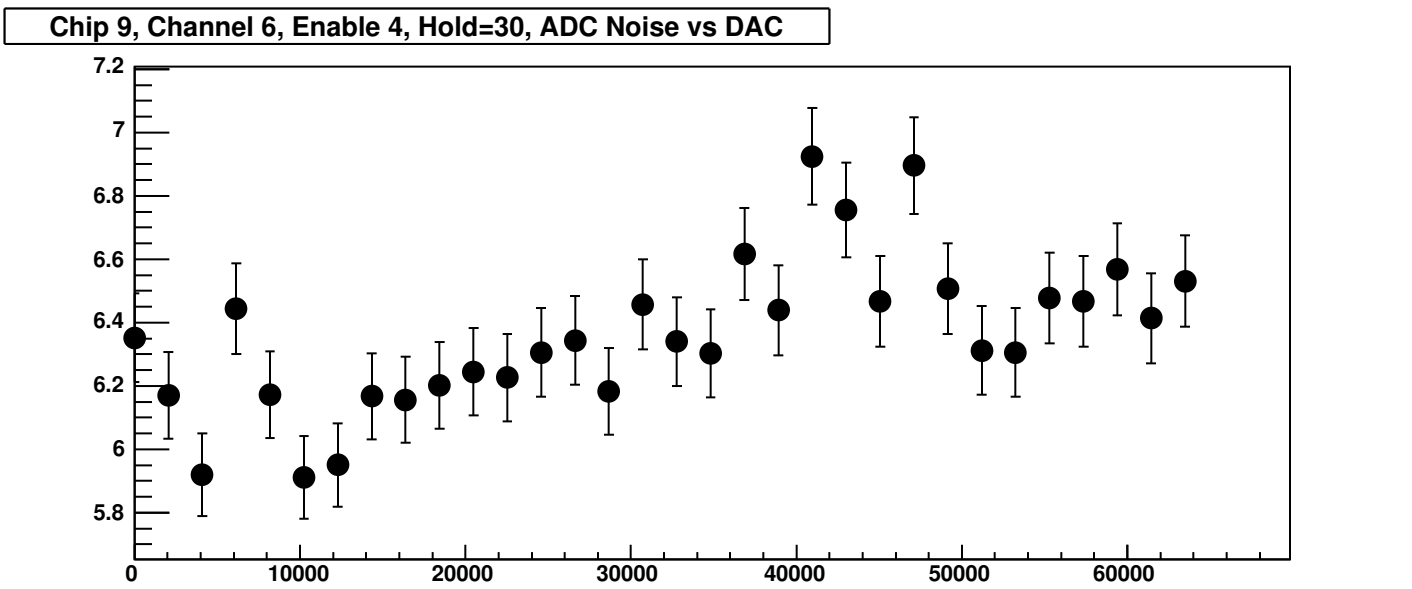

Chip 9, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

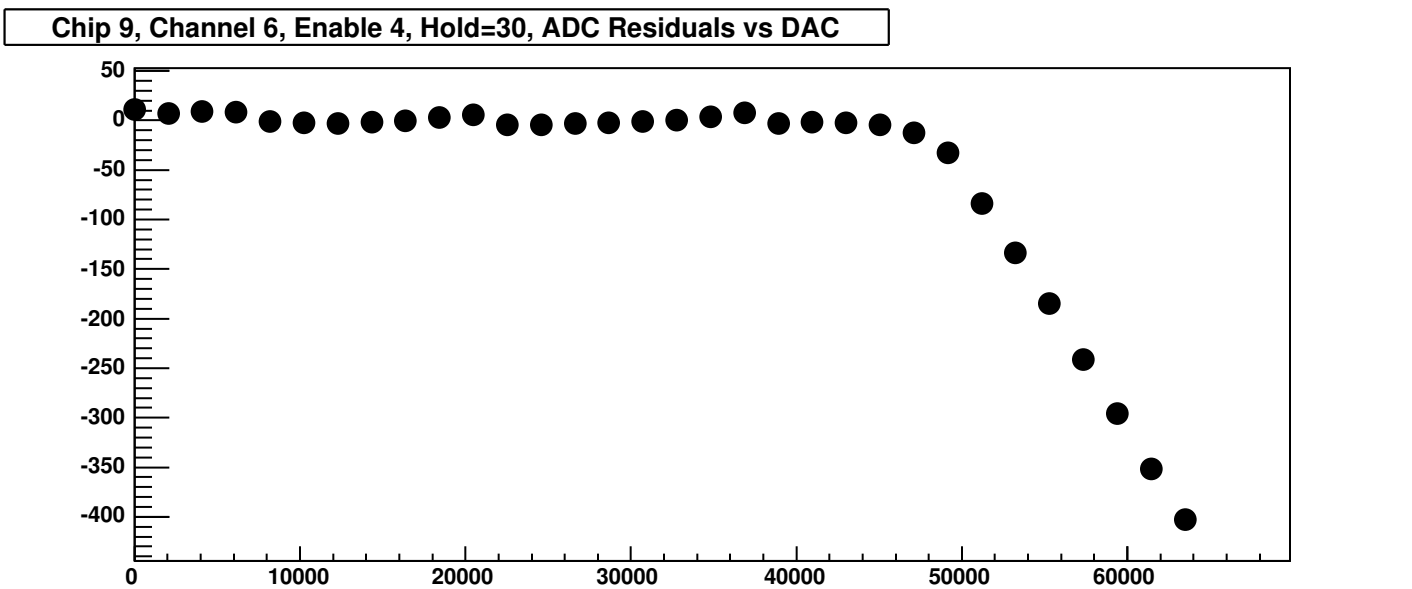

Chip 9, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5870 / 15
p0 892.3 ± 0.1142
p1 0.0002316 ± 4.662e-06
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

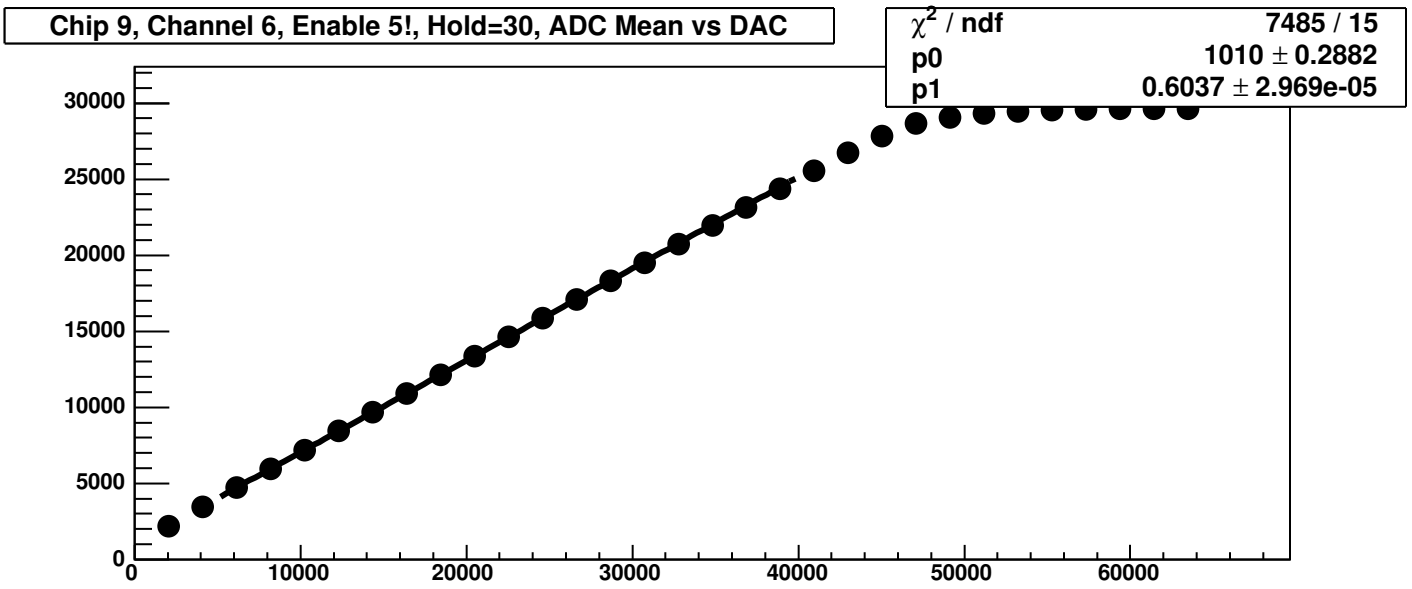

Chip 9, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

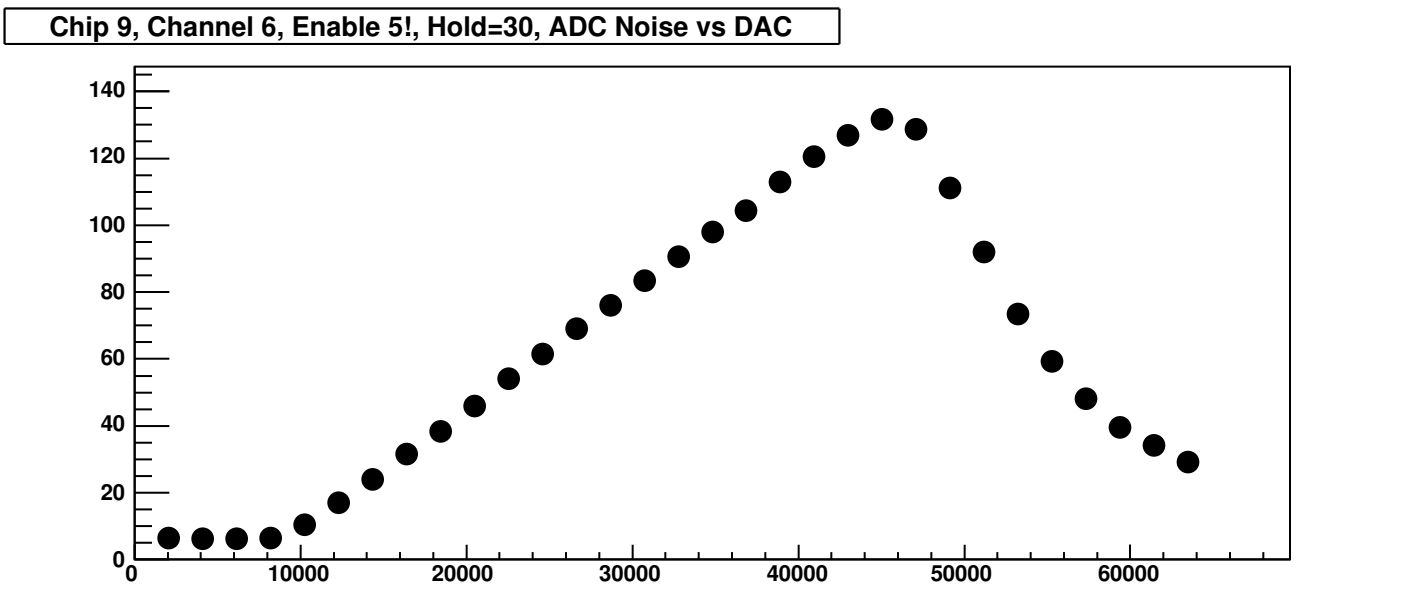

Chip 9, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

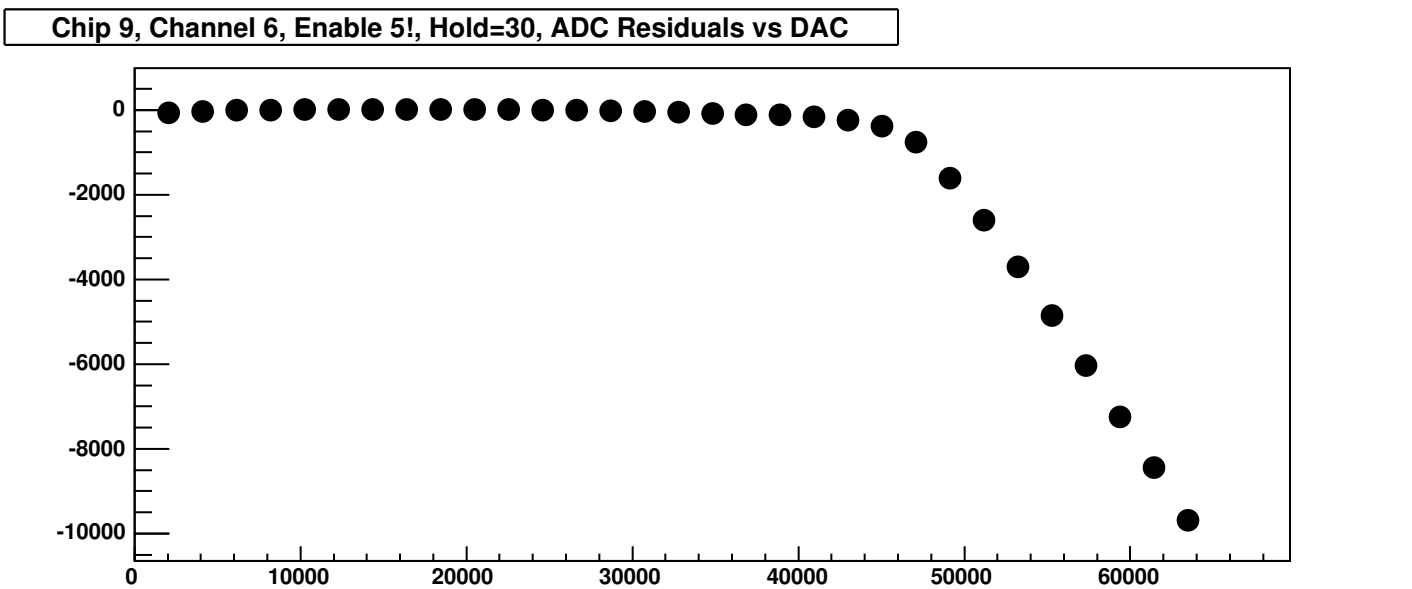

Chip 9, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7065 / 15
p0 891.2 ± 0.1158
p1 0.000541 ± 4.761e-06
950
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

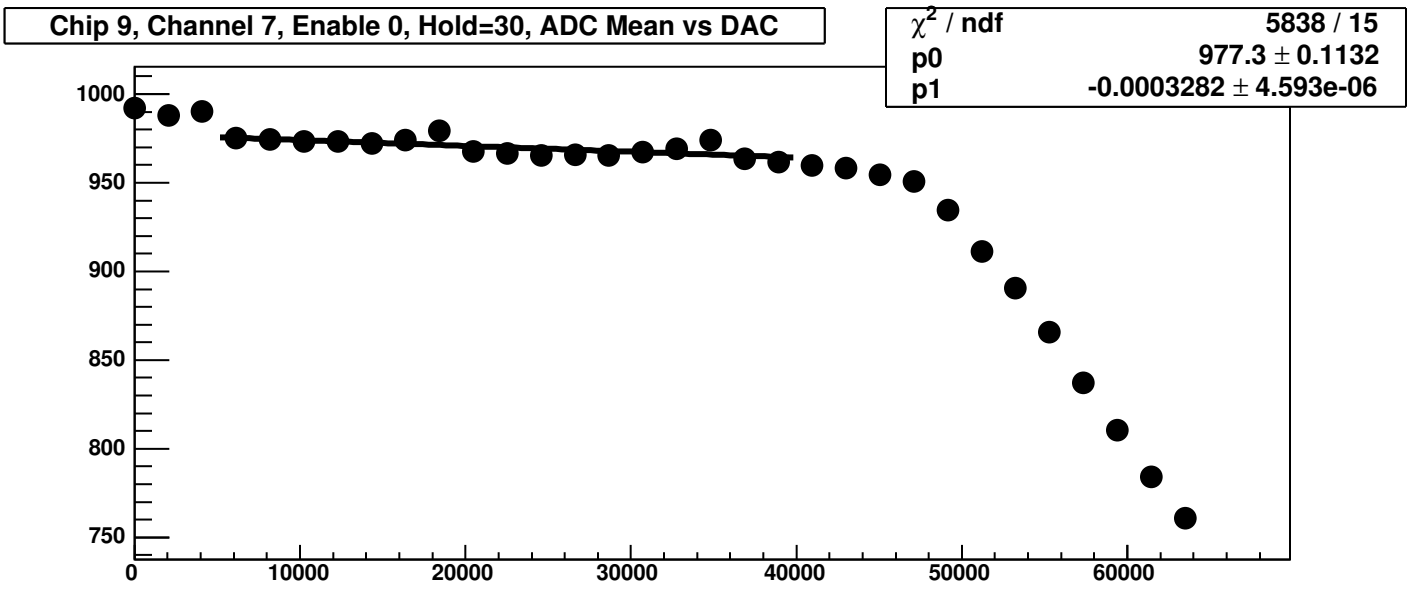

Chip 9, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

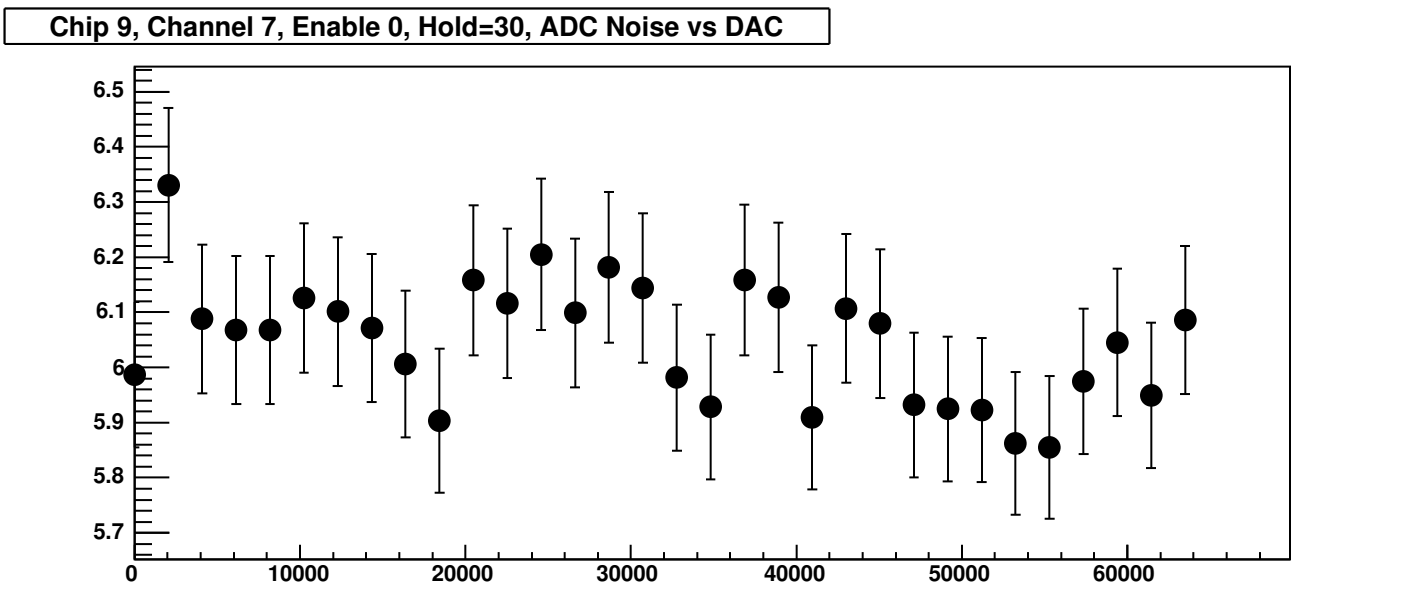

Chip 9, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

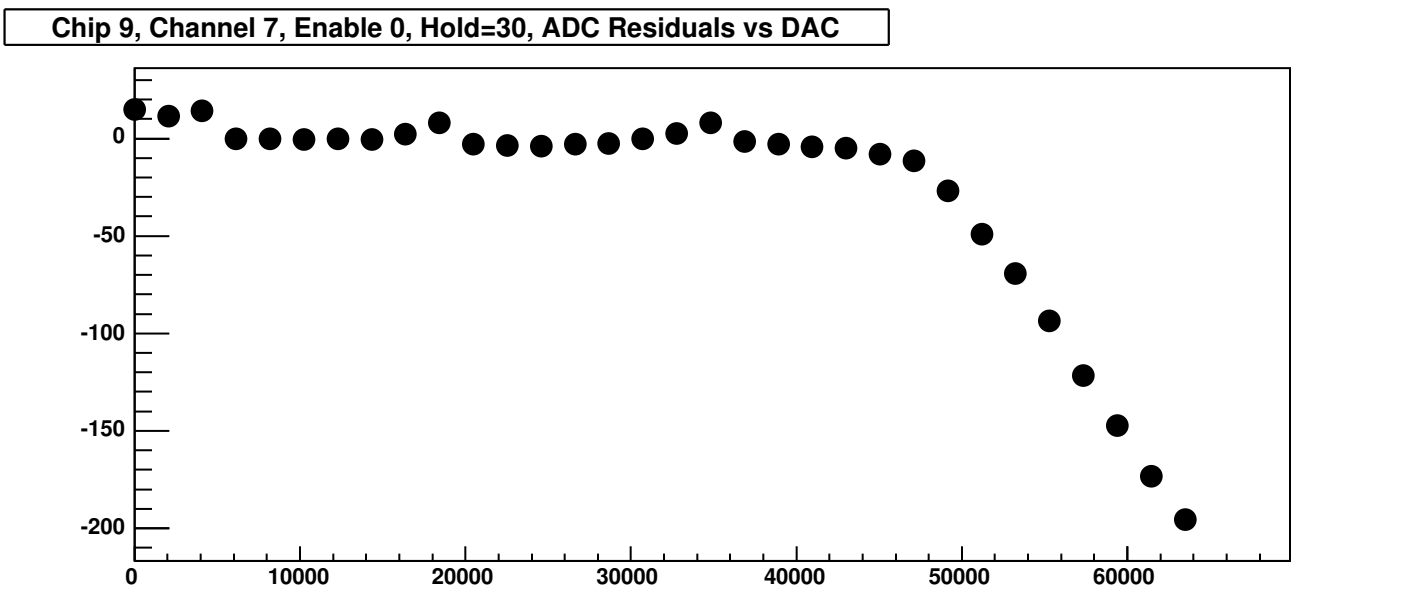

Chip 9, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7485 / 15
p0 1010 ± 0.2882
p1 0.6037 ± 2.969e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

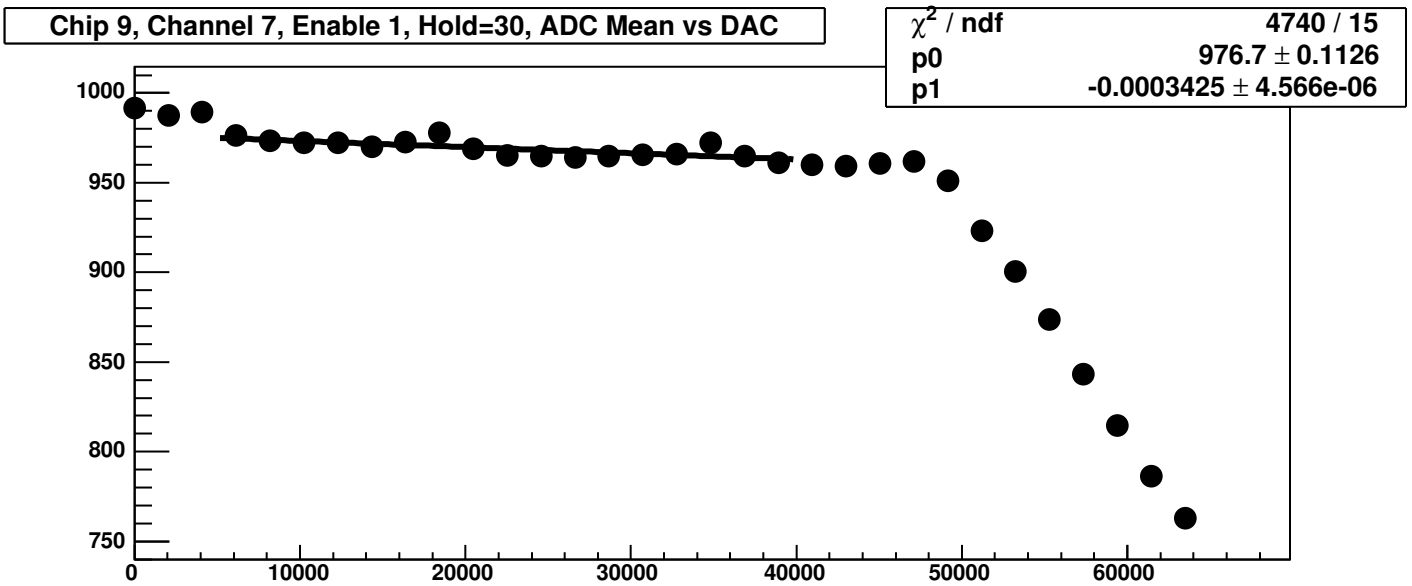

Chip 9, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

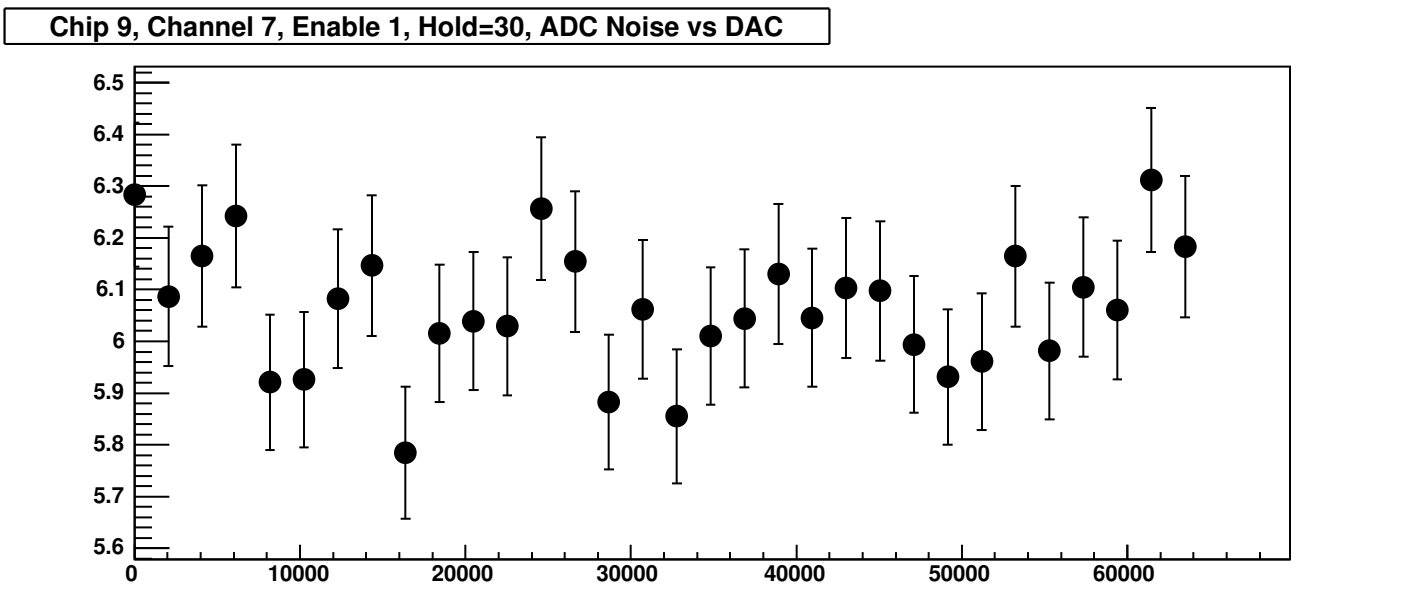

Chip 9, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

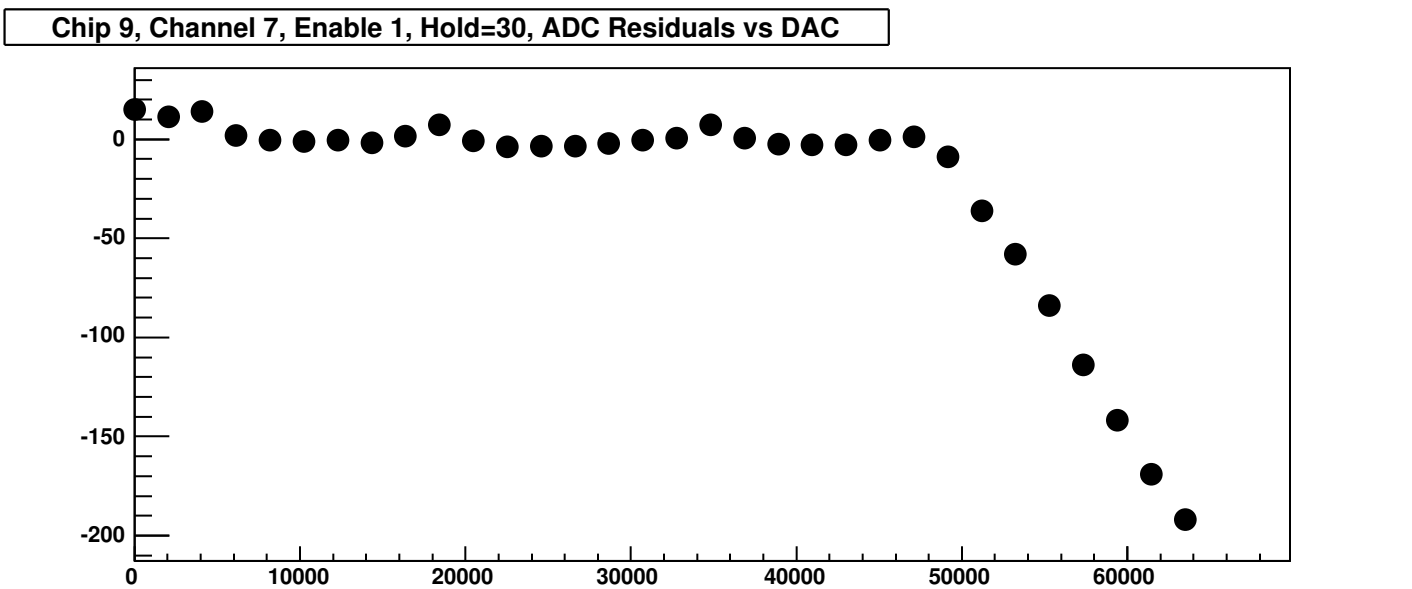

Chip 9, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5838 / 15
p0 977.3 ± 0.1132
p1 -0.0003282 ± 4.593e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

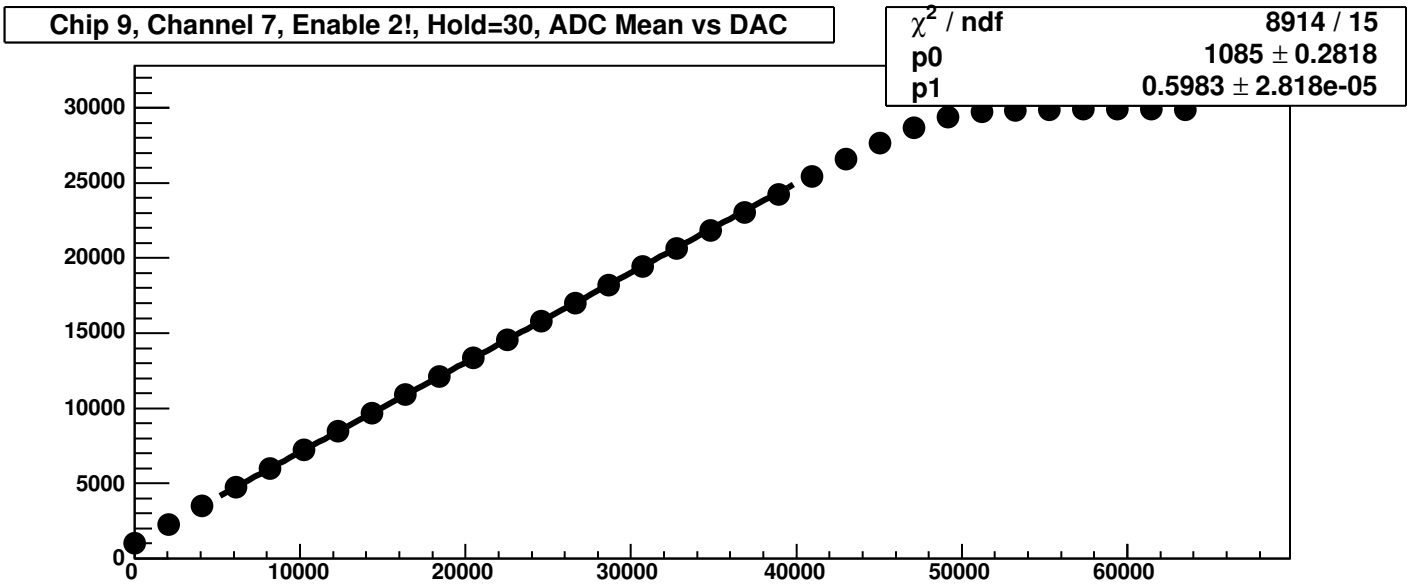

Chip 9, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
0
10000
20000
30000
40000
50000
60000

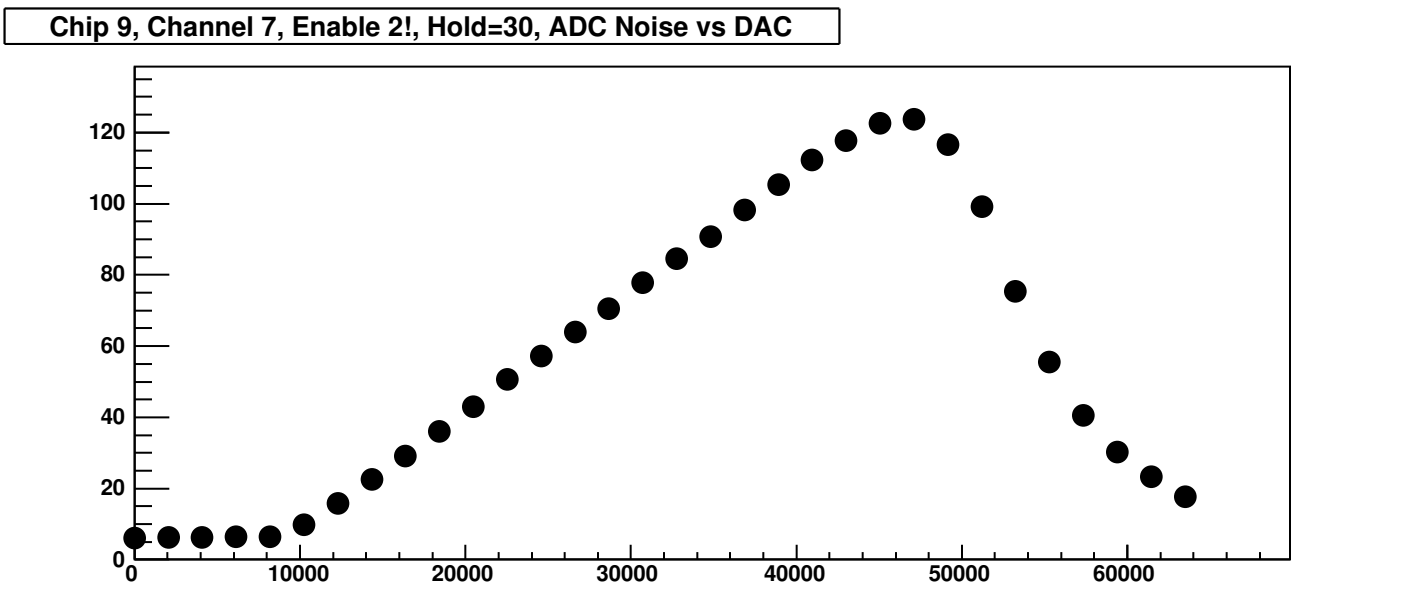

Chip 9, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

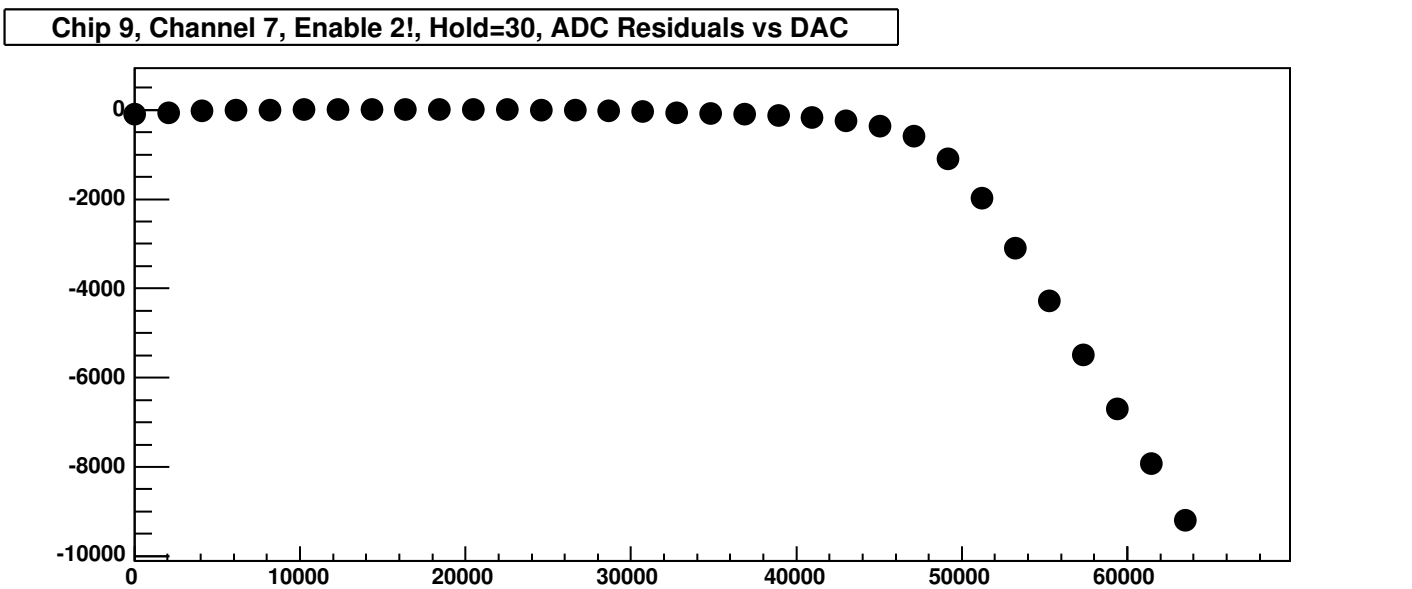

Chip 9, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4740 / 15
p0 976.7 ± 0.1126
p1 -0.0003425 ± 4.566e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

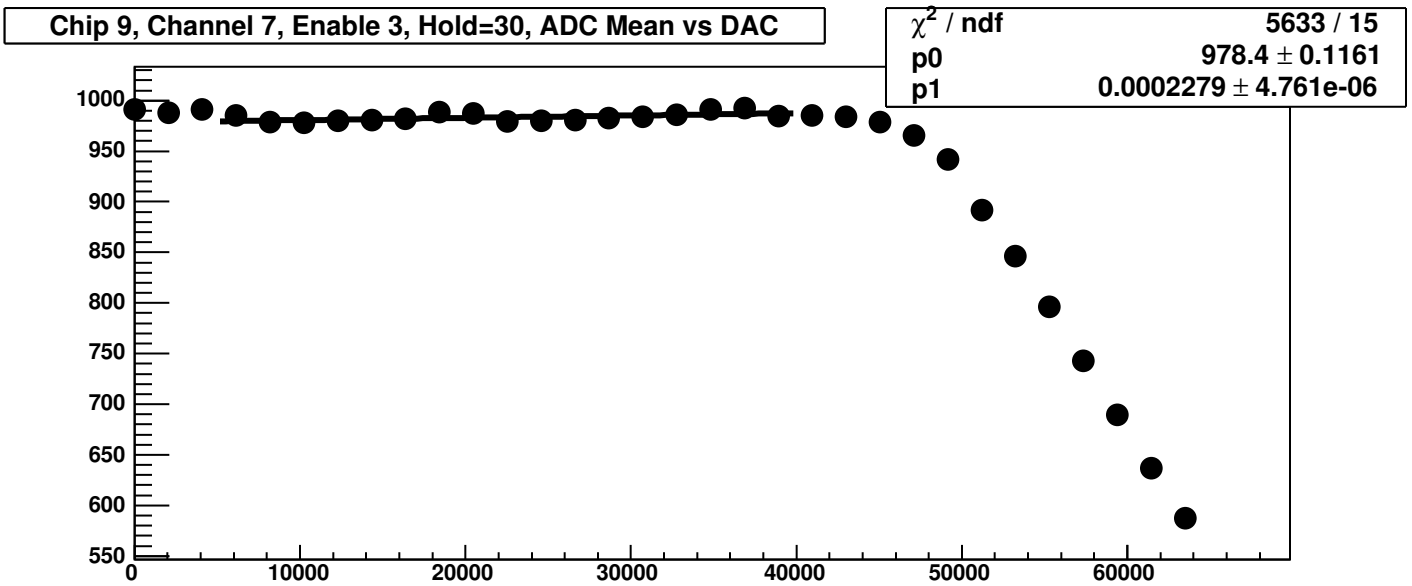

Chip 9, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
0
10000
20000
30000
40000
50000
60000

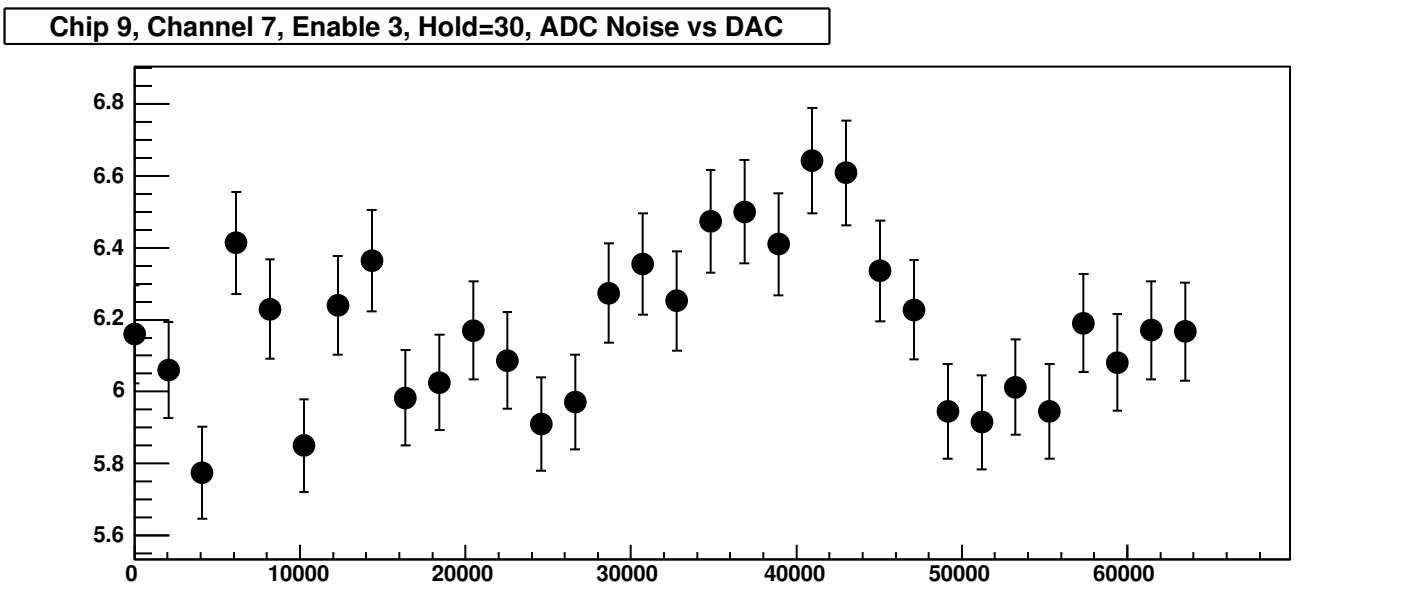

Chip 9, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

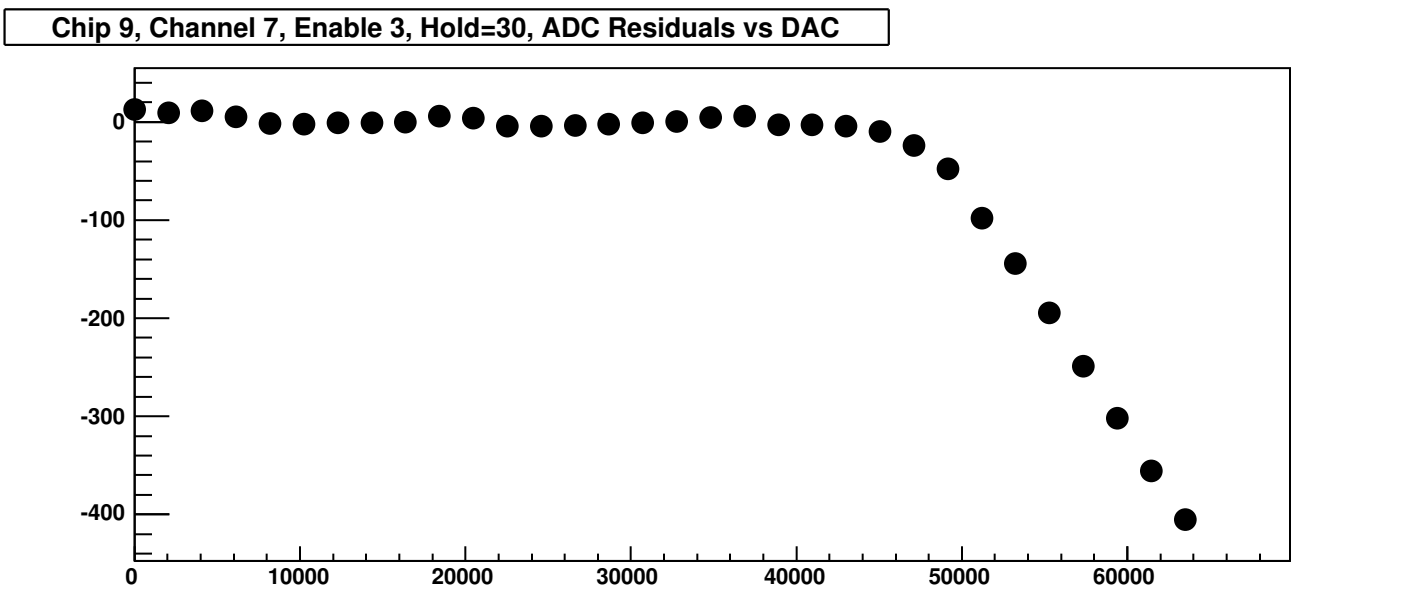

Chip 9, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 8914 / 15
p0 1085 ± 0.2818
p1 0.5983 ± 2.818e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

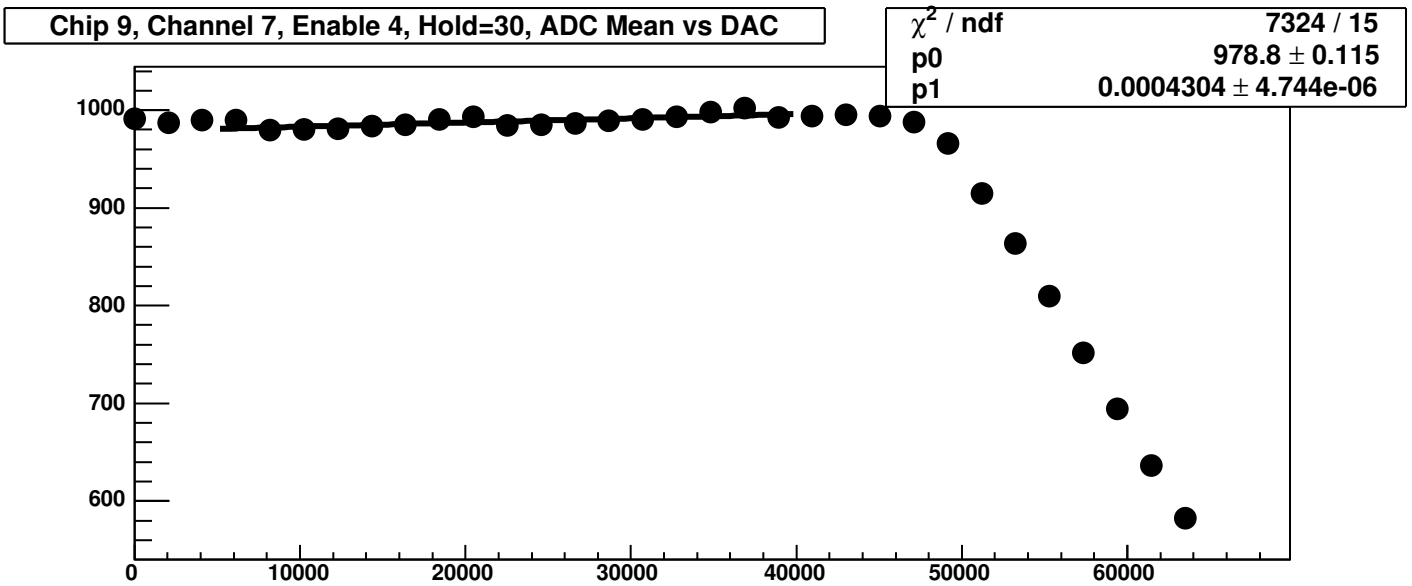

Chip 9, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

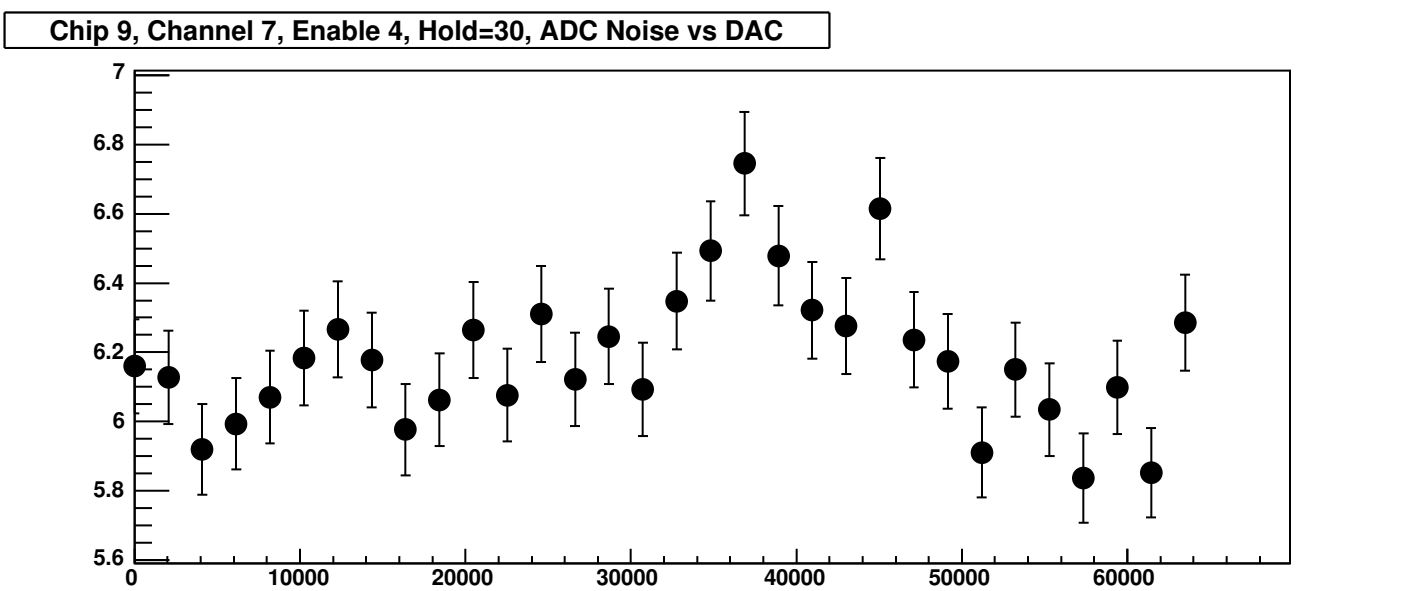

Chip 9, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

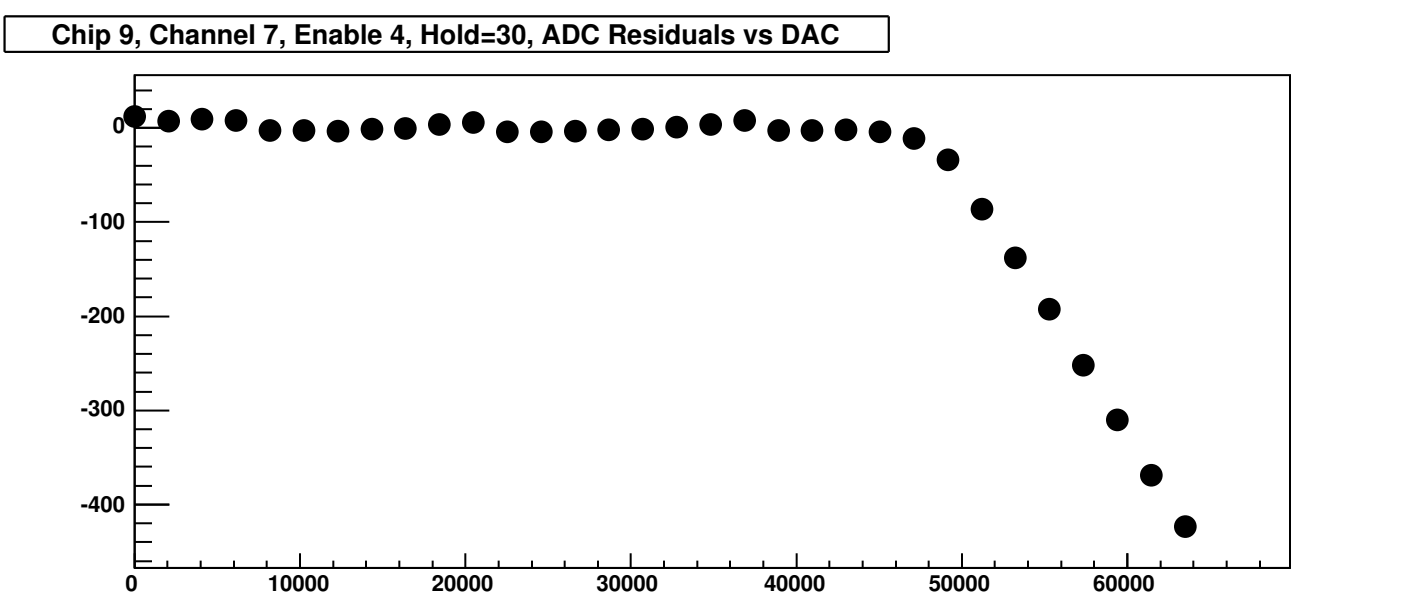

Chip 9, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5633 / 15
p0 978.4 ± 0.1161
p1 0.0002279 ± 4.761e-06
1000
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

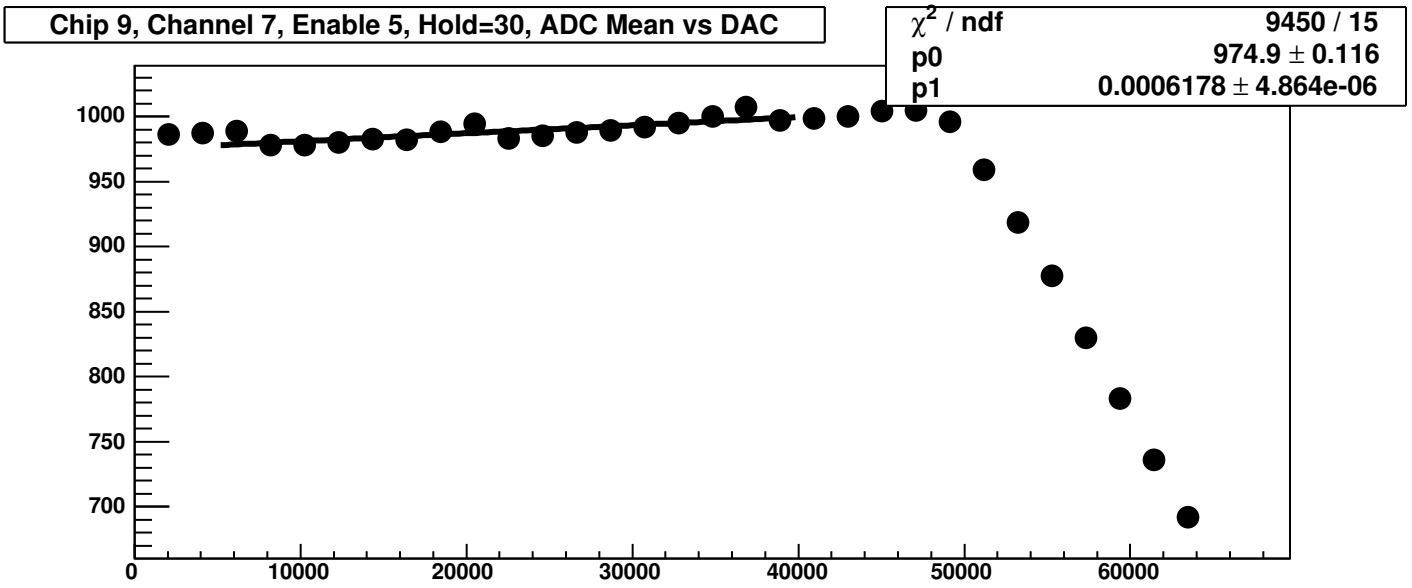

Chip 9, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

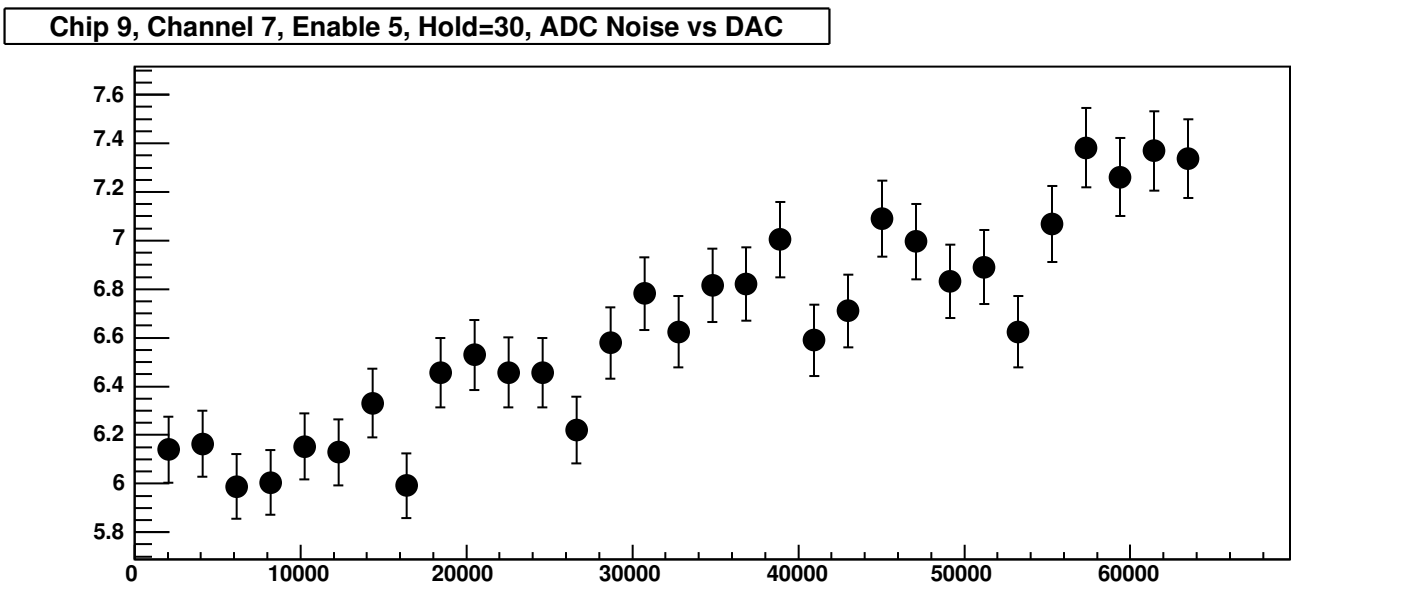

Chip 9, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

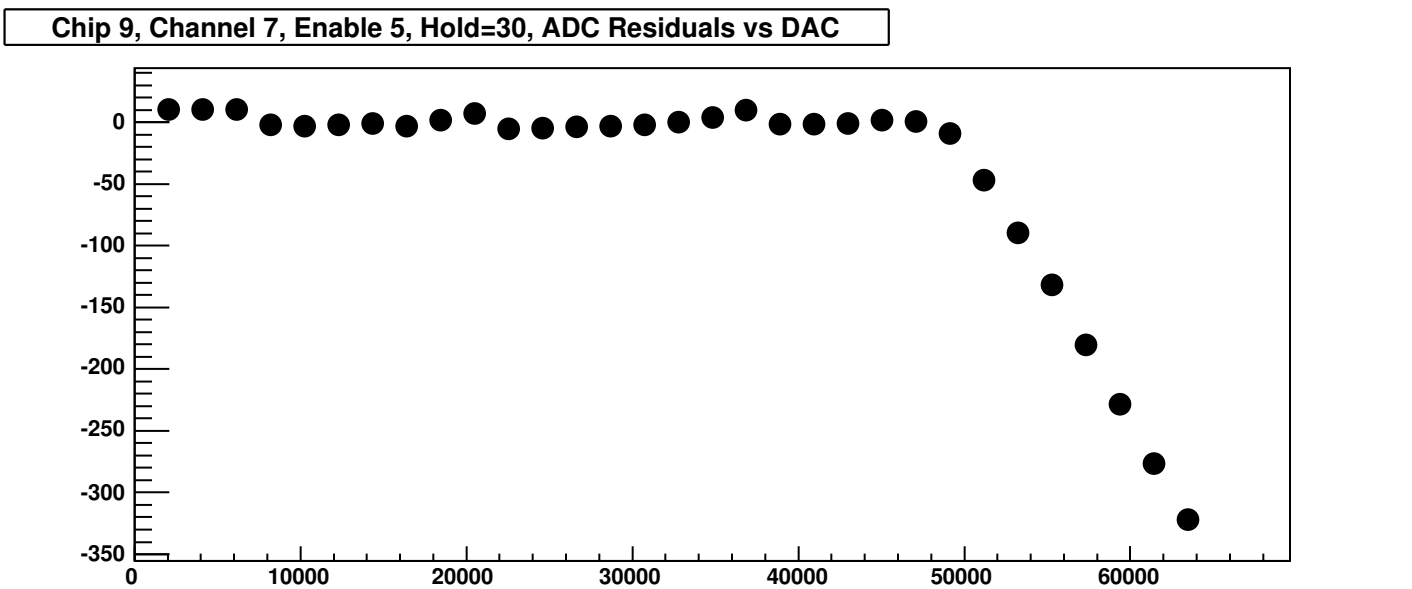

Chip 9, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7324 / 15
p0 978.8 ± 0.115
p1 0.0004304 ± 4.744e-06
1000
900
800
700
600
0
10000
20000
30000
40000
50000
60000

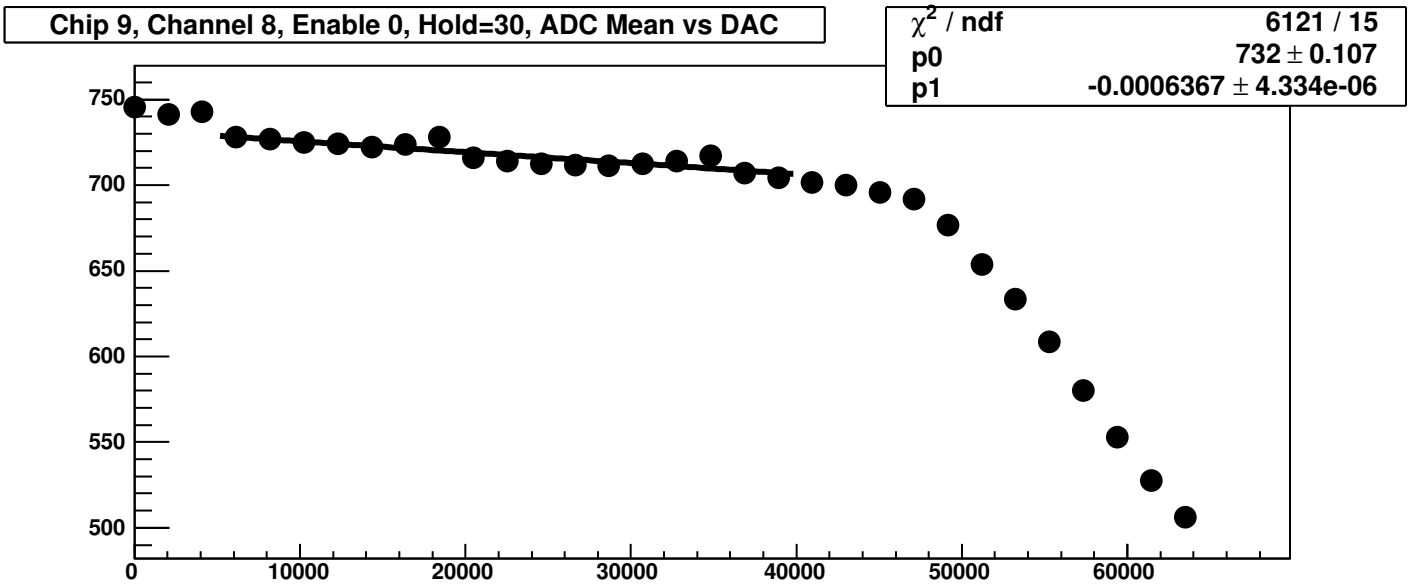

Chip 9, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

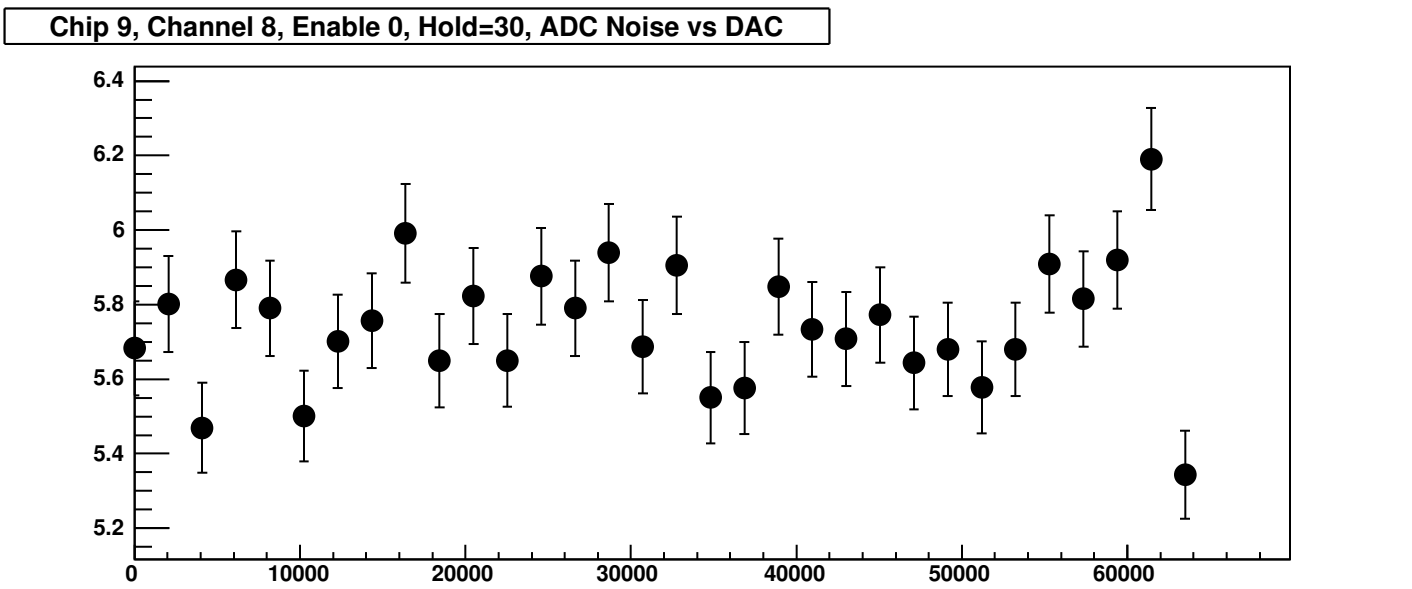

Chip 9, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

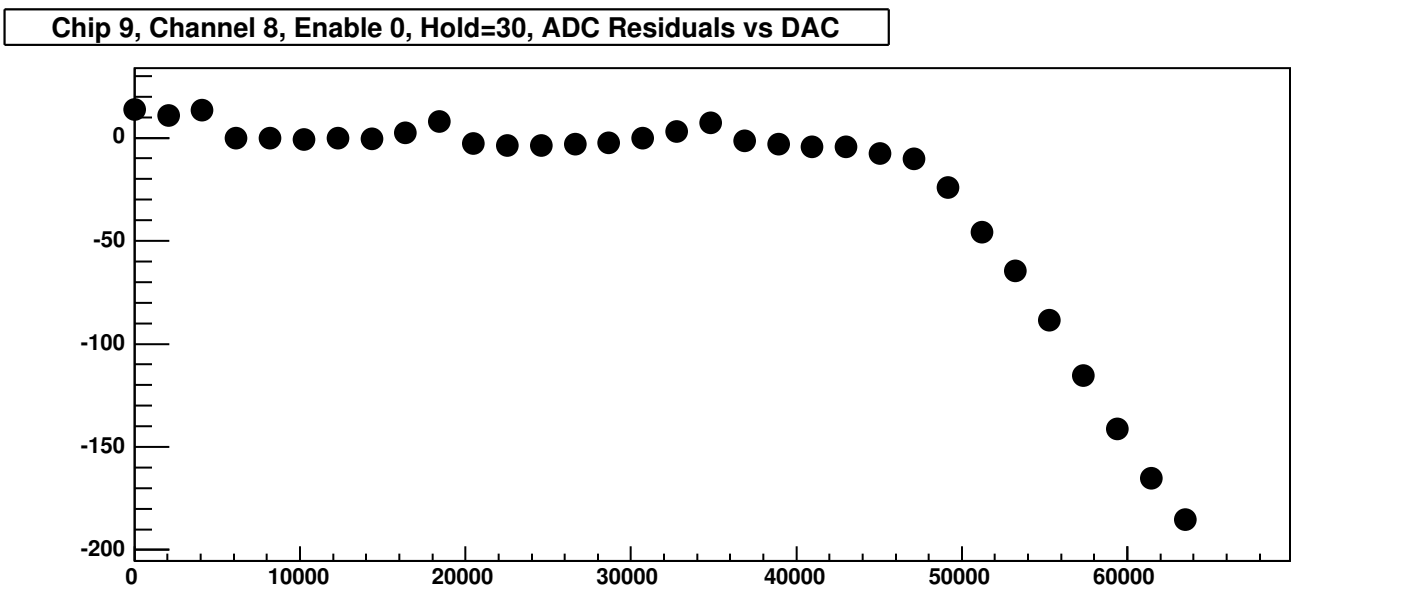

Chip 9, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9450 / 15
p0 974.9 ± 0.116
p1 0.0006178 ± 4.864e-06
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

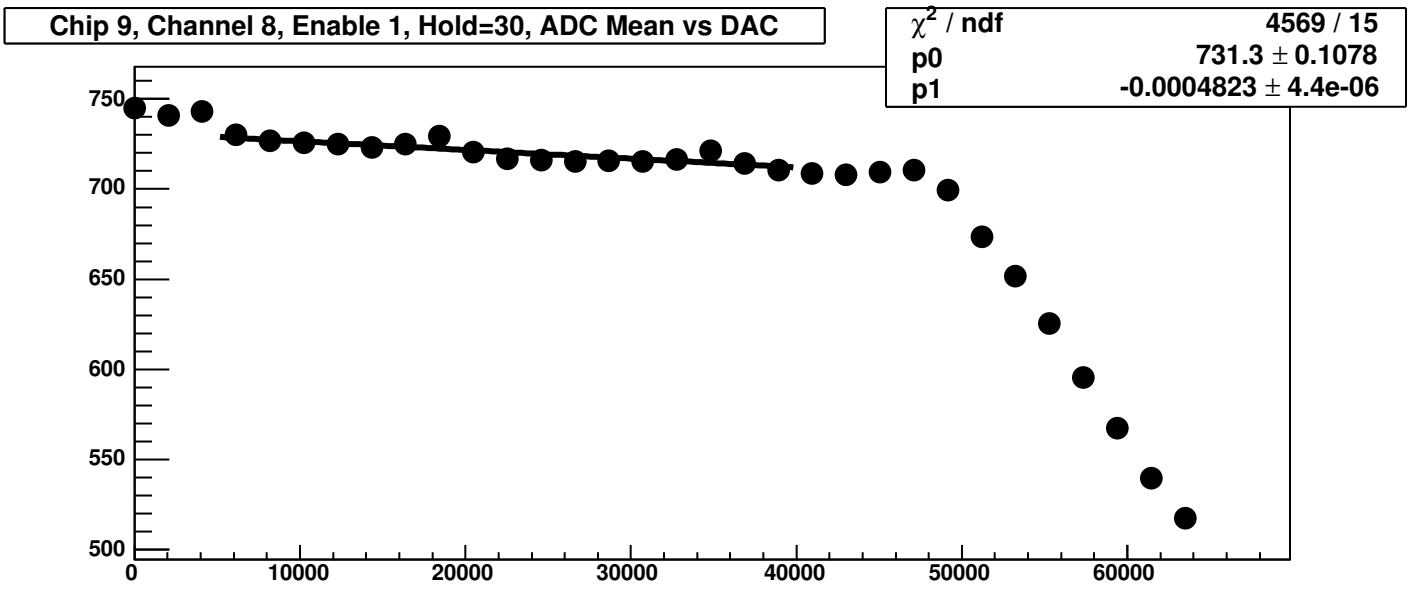

Chip 9, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

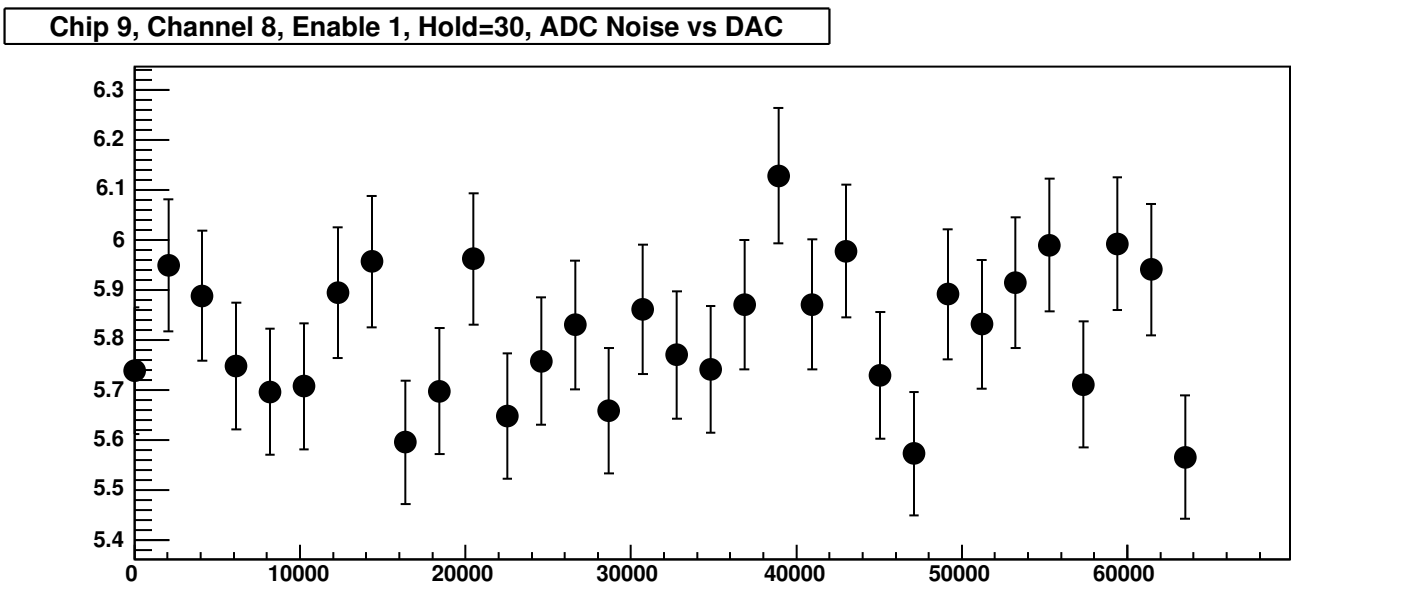

Chip 9, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

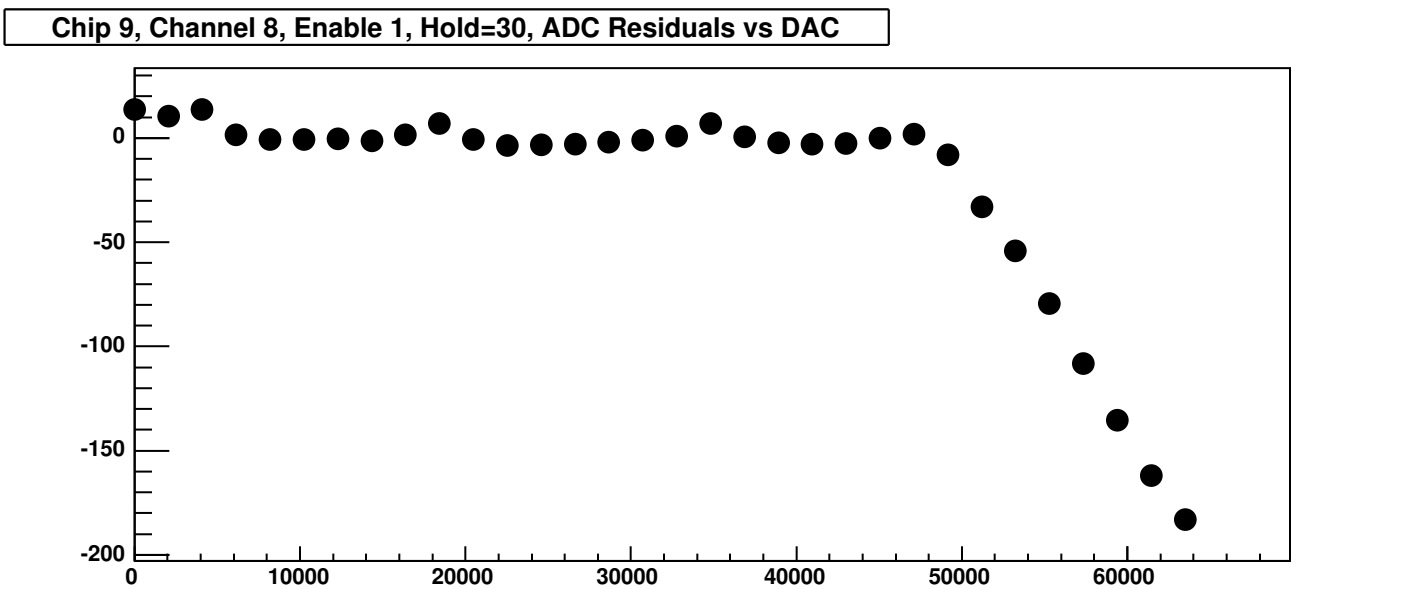

Chip 9, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6121 / 15
p0 732 ± 0.107
p1 -0.0006367 ± 4.334e-06
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

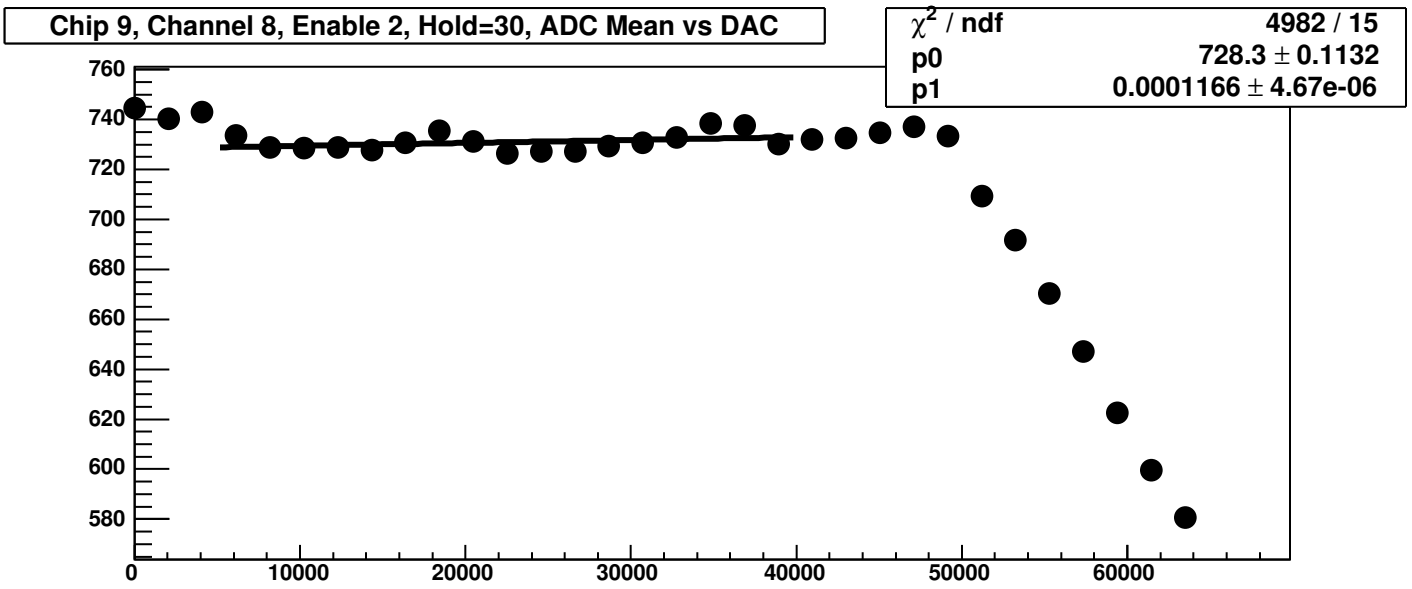

Chip 9, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

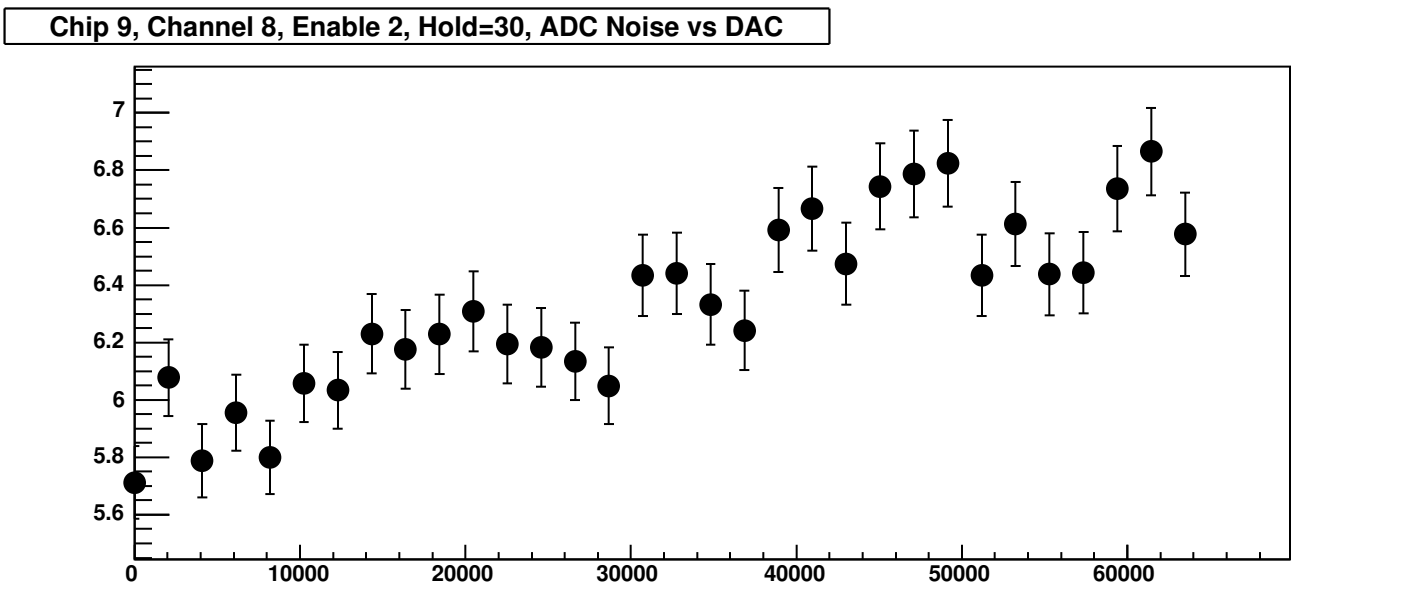

Chip 9, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

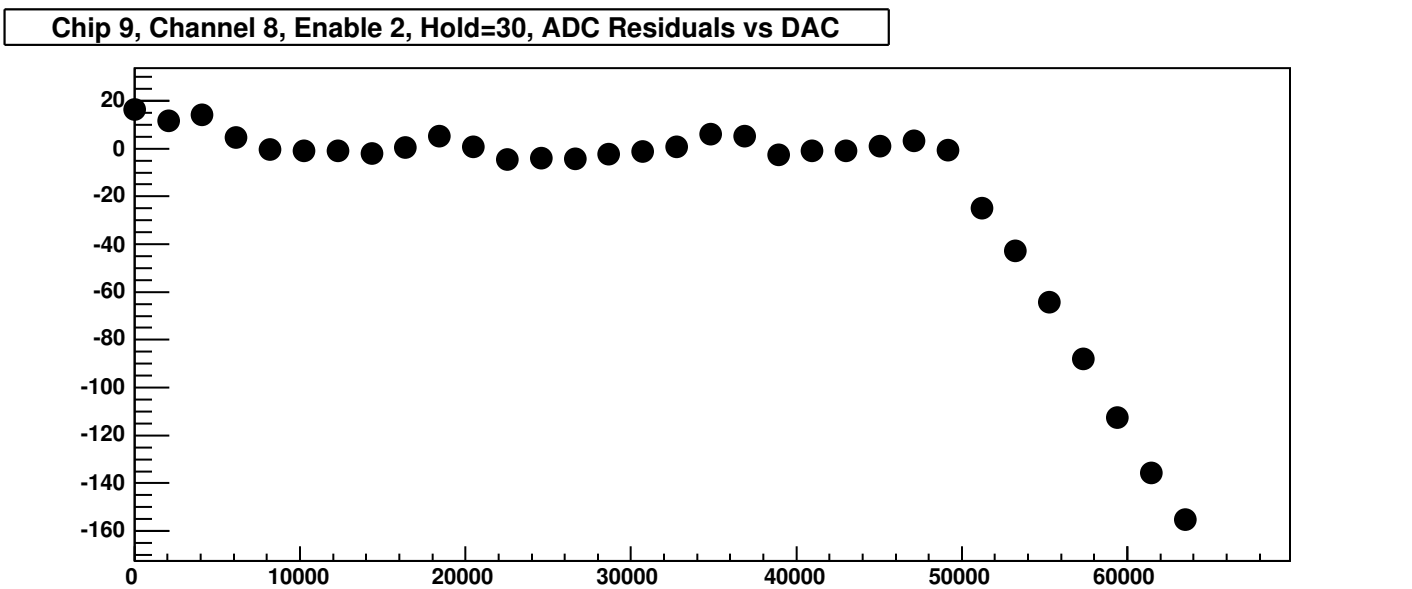

Chip 9, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4569 / 15
p0 731.3 ± 0.1078
p1 -0.0004823 ± 4.4e-06
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

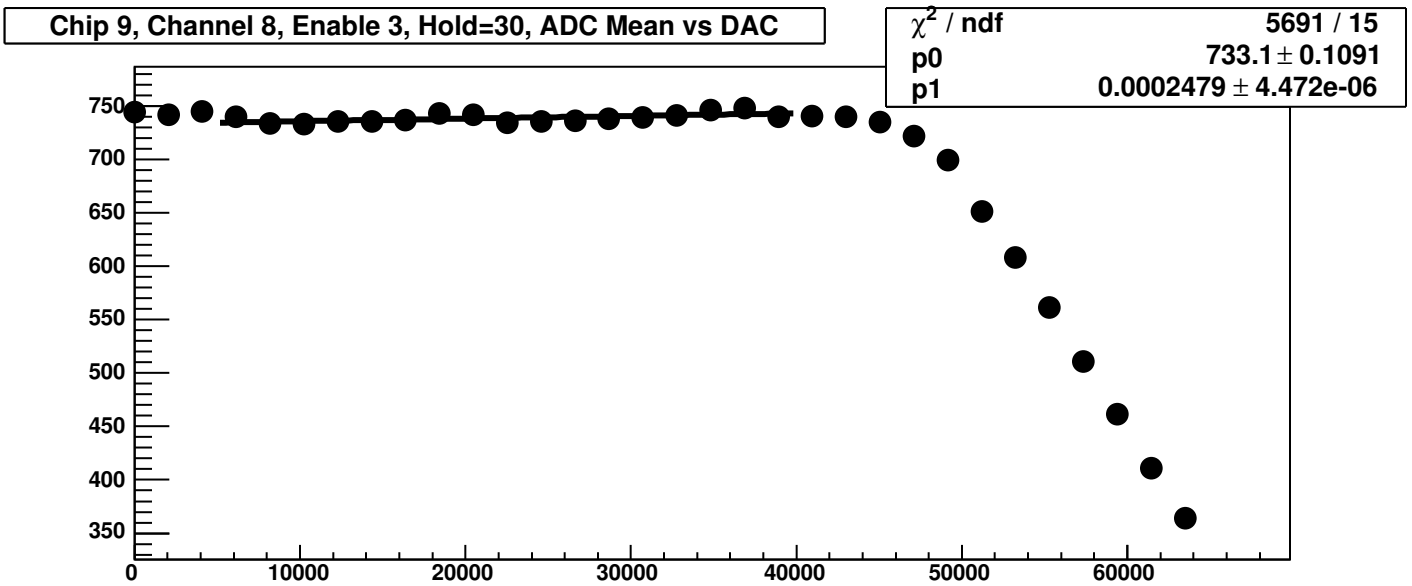

Chip 9, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

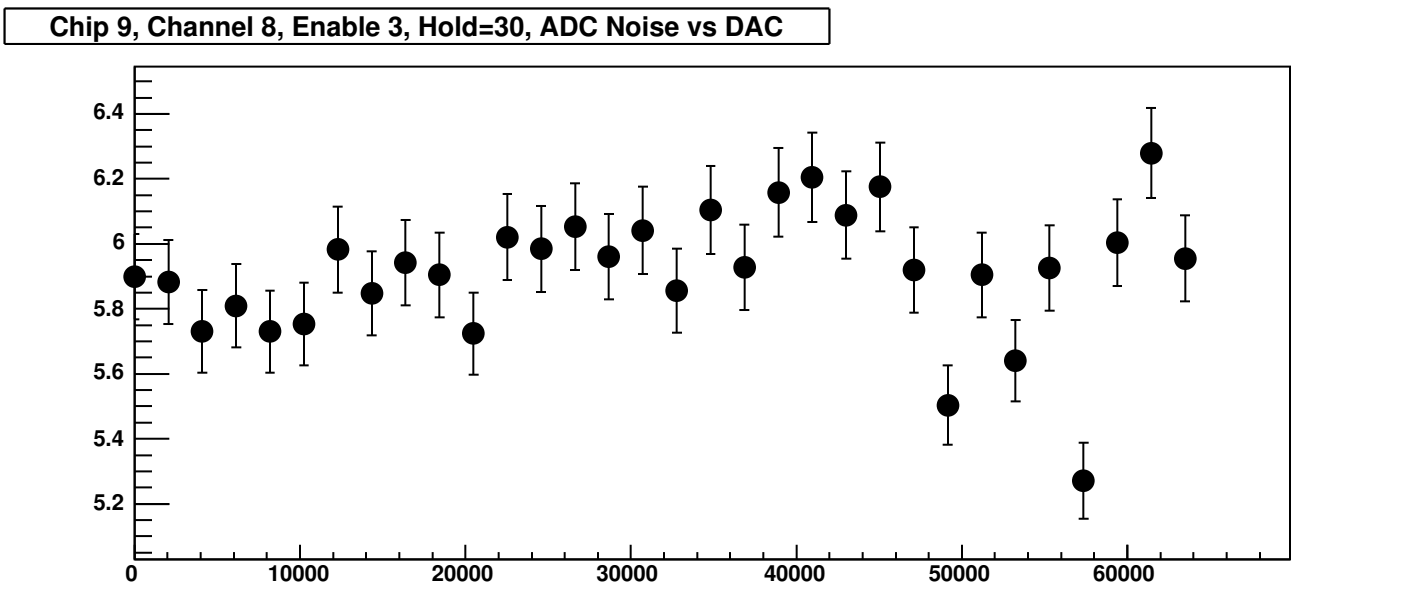

Chip 9, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

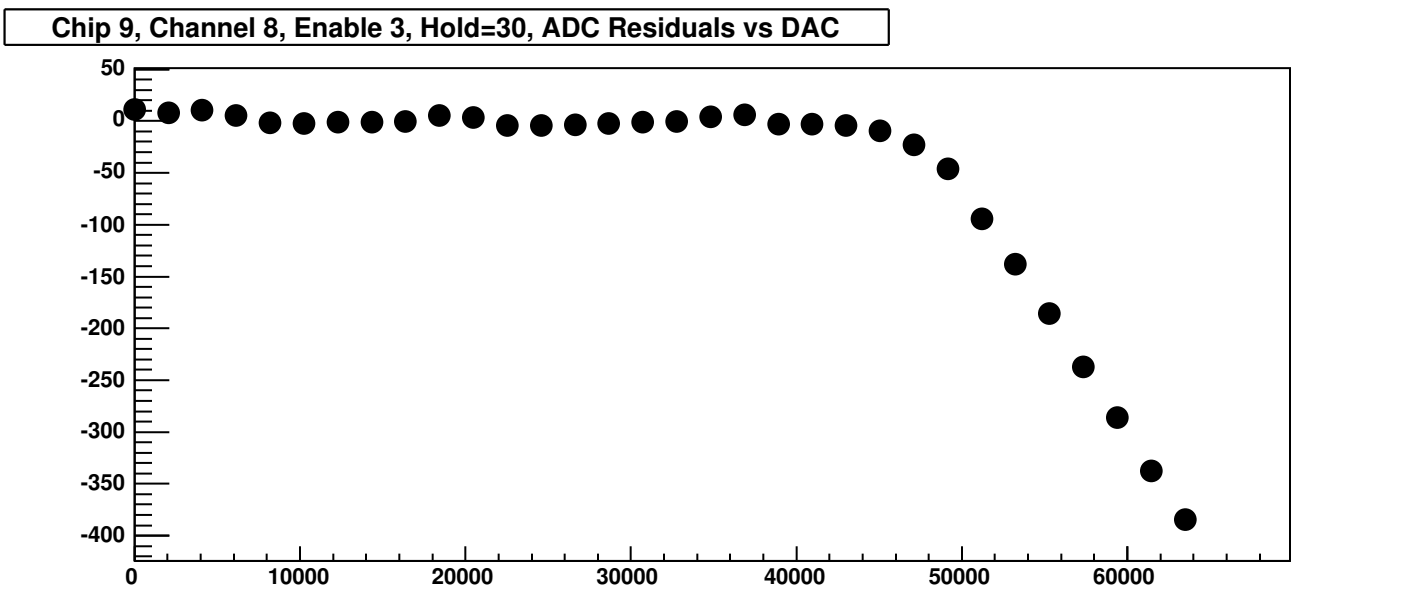

Chip 9, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4982 / 15
p0 728.3 ± 0.1132
p1 0.0001166 ± 4.67e-06
760
740
720
700
680
660
640
620
600
580
0
10000
20000
30000
40000
50000
60000

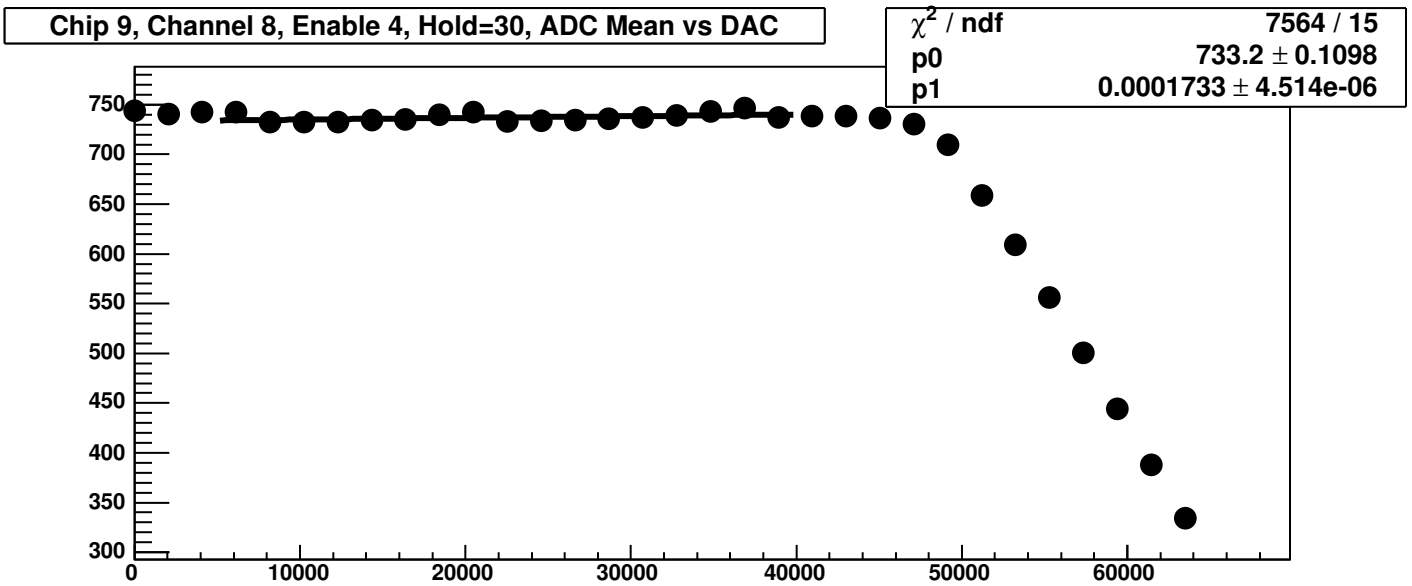

Chip 9, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

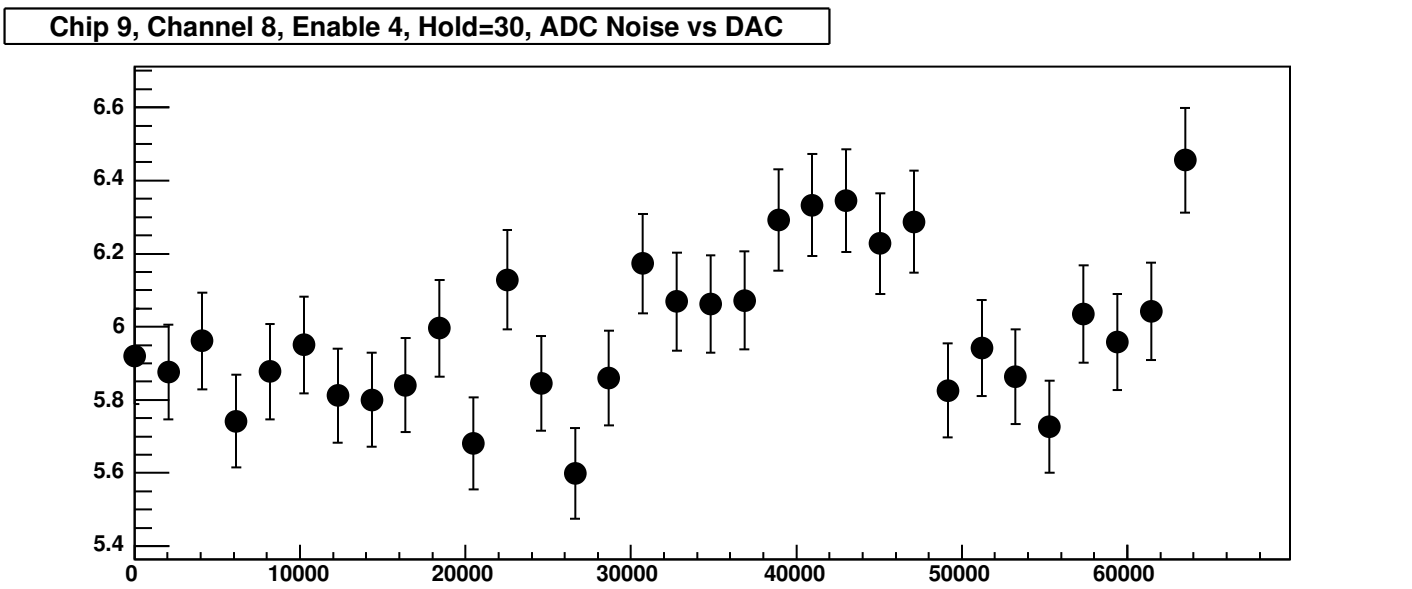

Chip 9, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

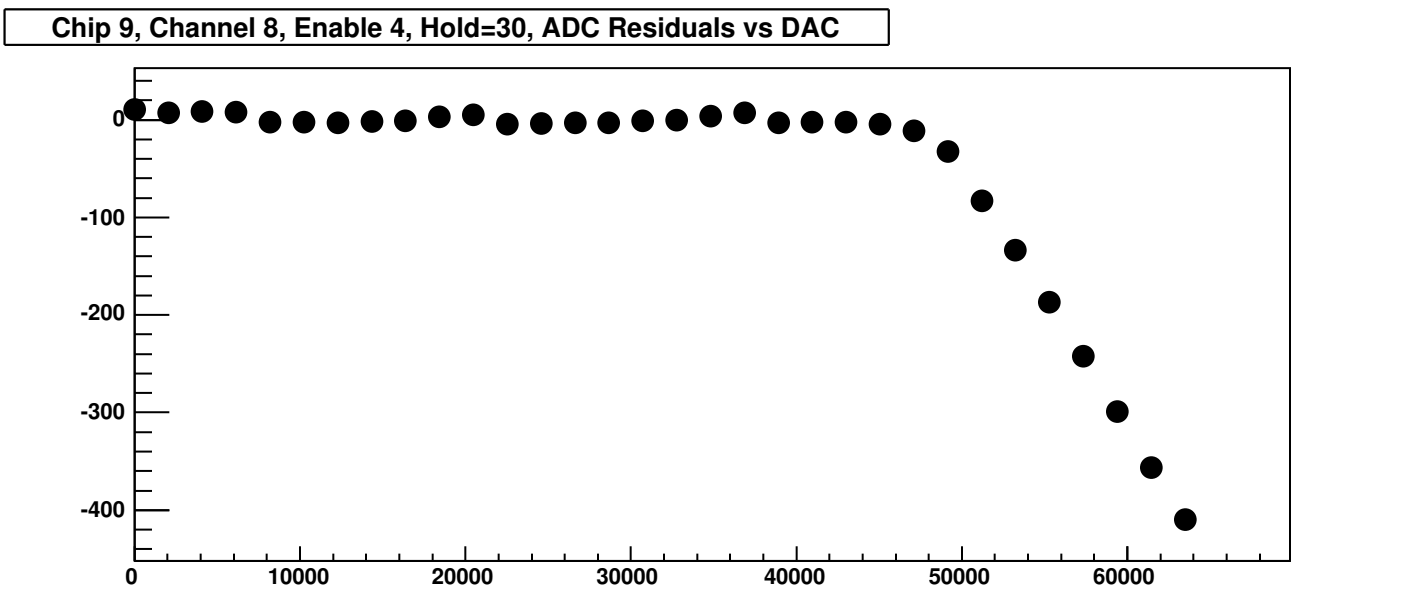

Chip 9, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5691 / 15
p0 733.1 ± 0.1091
p1 0.0002479 ± 4.472e-06
750
700
650
600
550
500
450
400
350
0
10000
20000
30000
40000
50000
60000

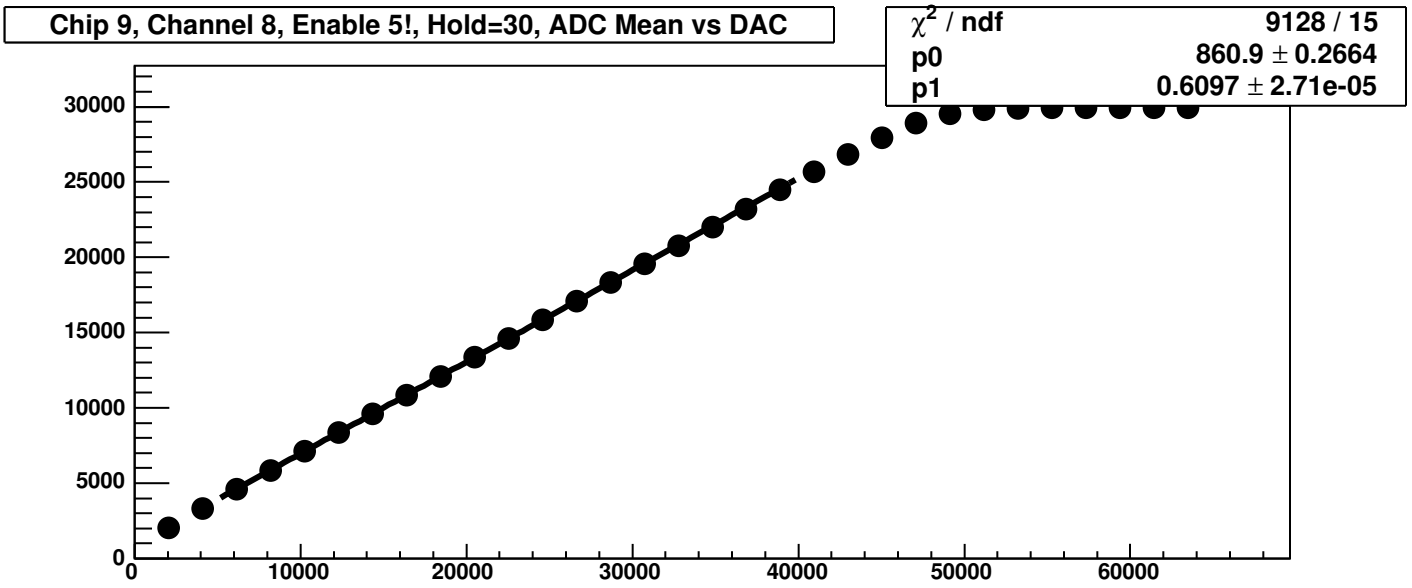

Chip 9, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

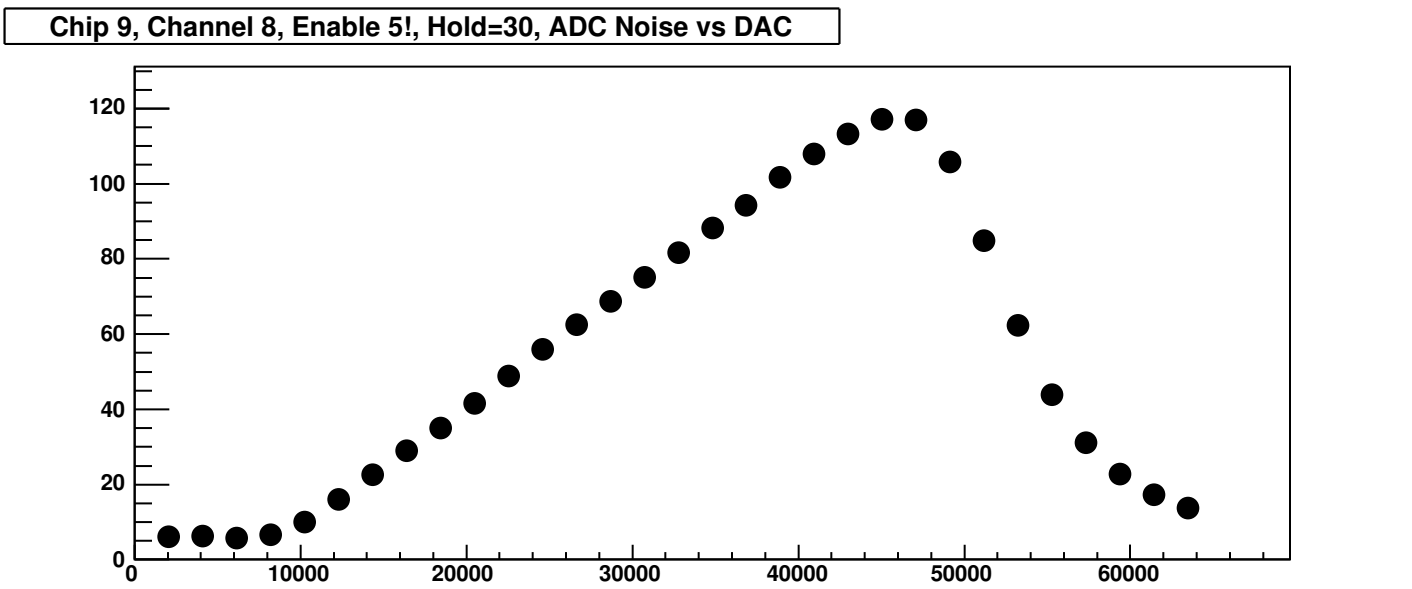

Chip 9, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

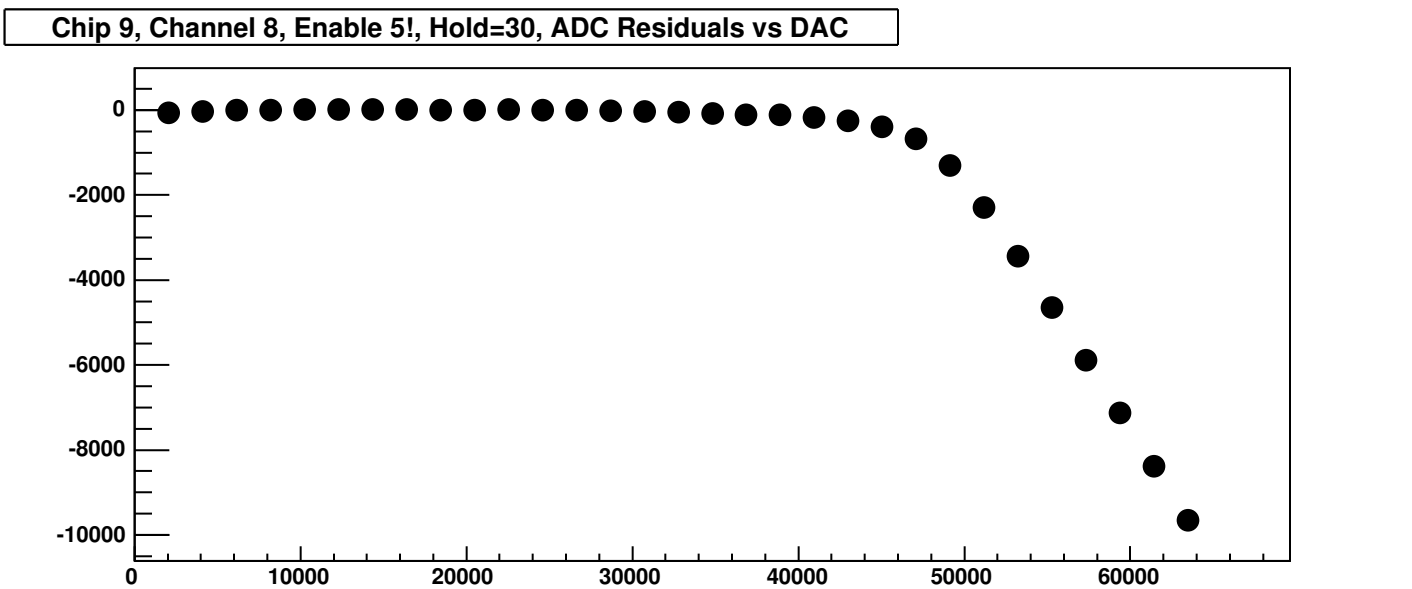

Chip 9, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7564 / 15
p0 733.2 ± 0.1098
p1 0.0001733 ± 4.514e-06
750
700
650
600
550
500
450
400
350
300
0
10000
20000
30000
40000
50000
60000

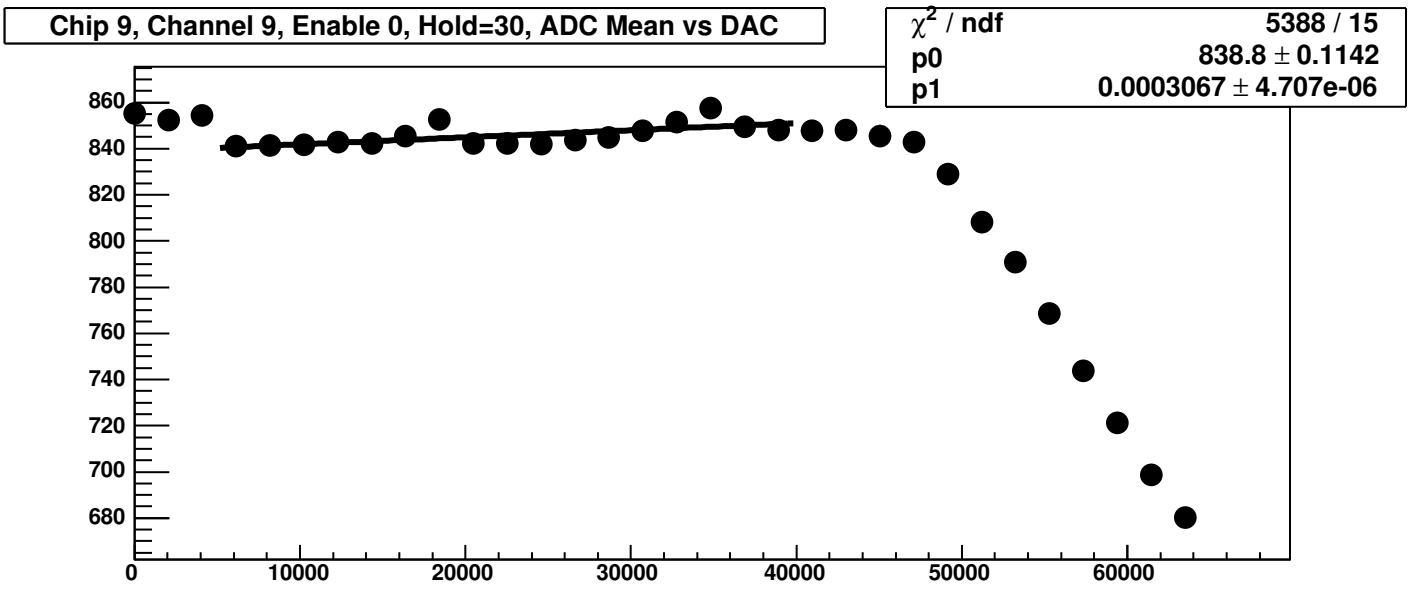

Chip 9, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

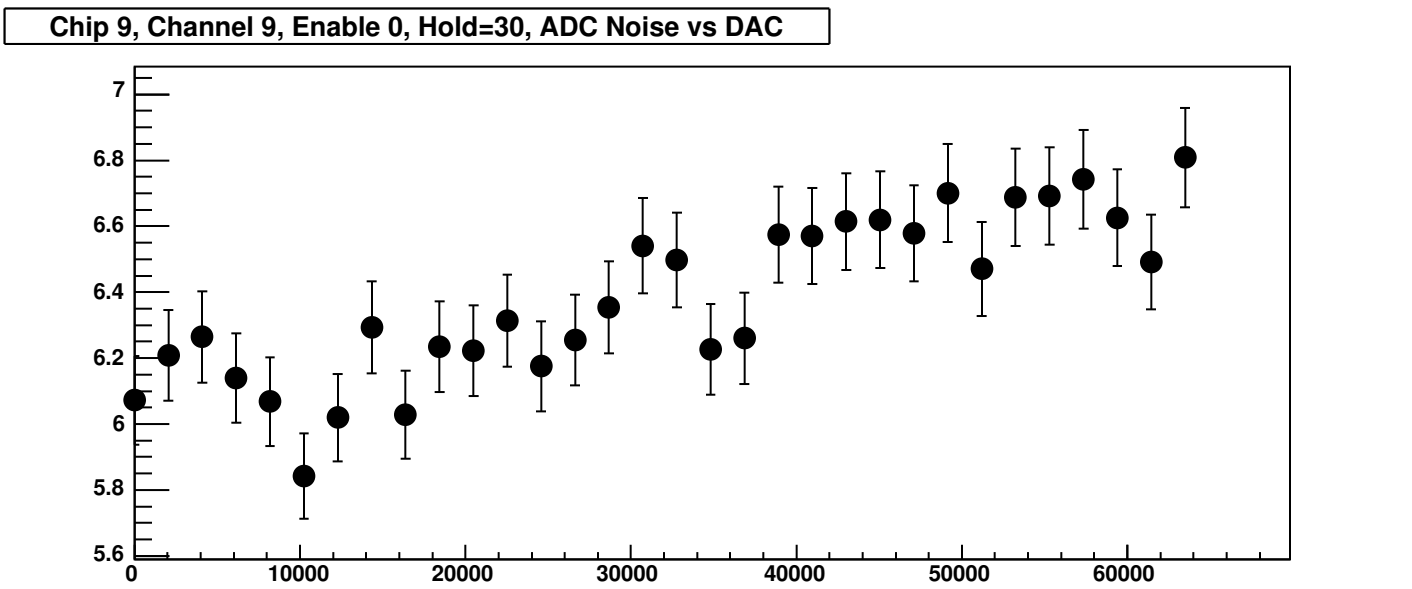

Chip 9, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

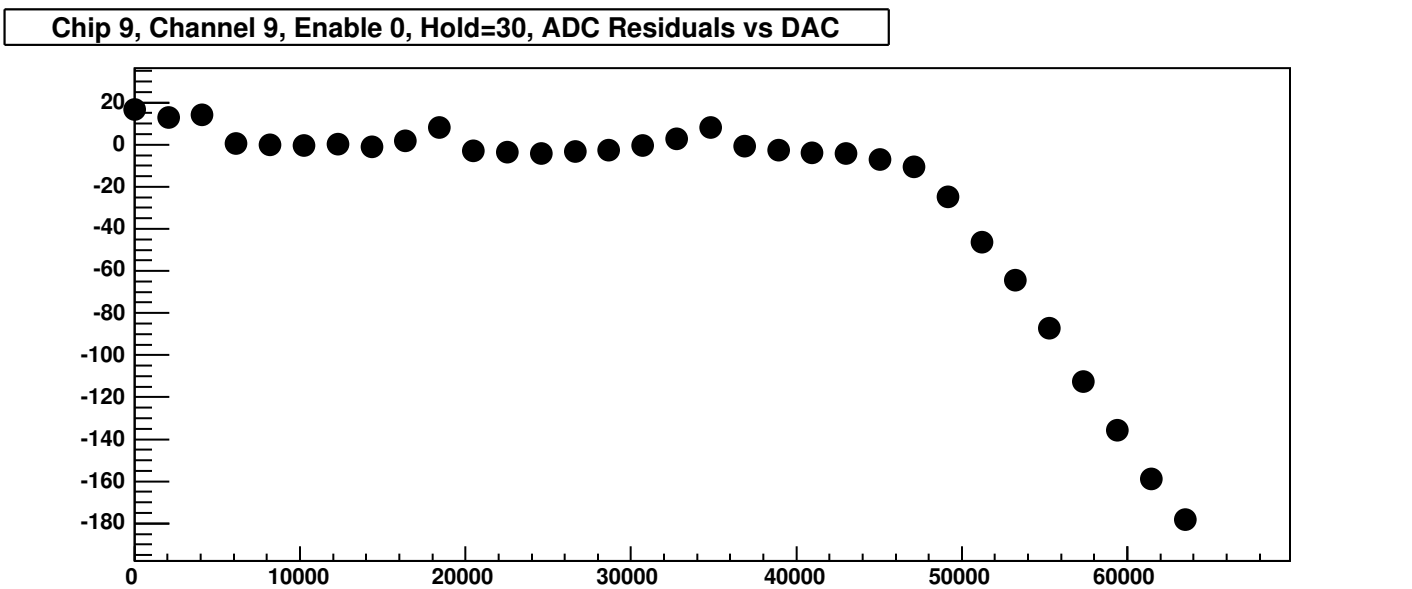

Chip 9, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 9128 / 15
p0 860.9 ± 0.2664
p1 0.6097 ± 2.71e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

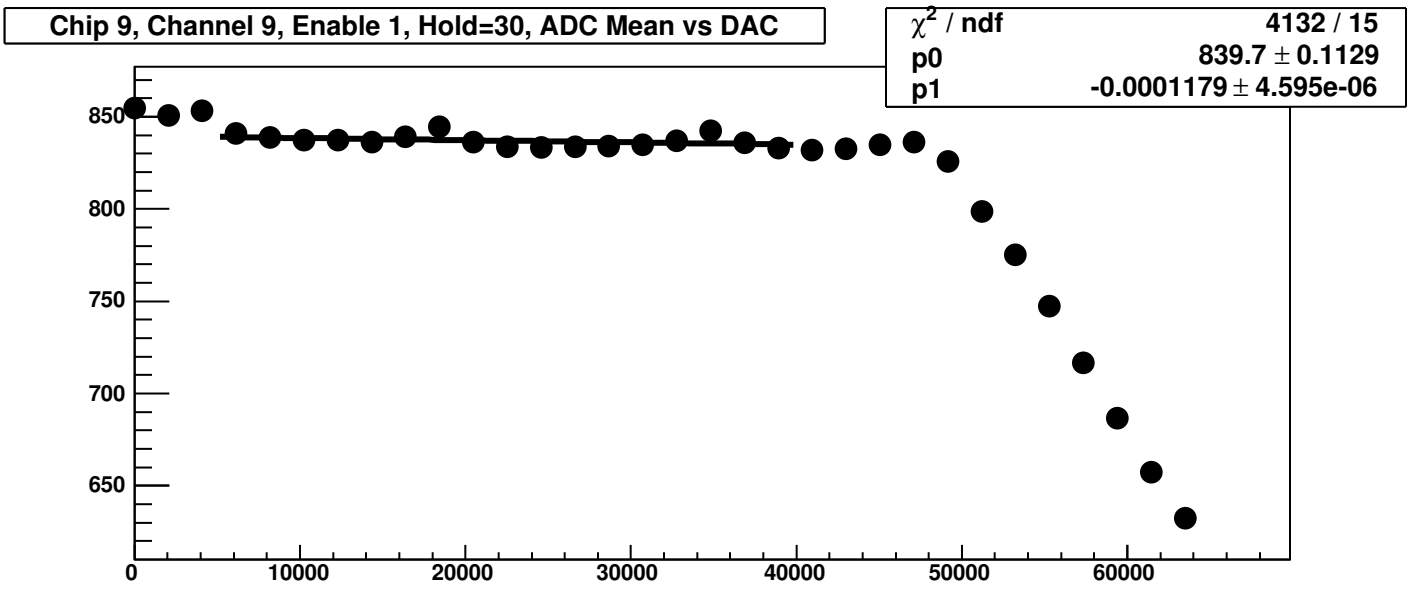

Chip 9, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

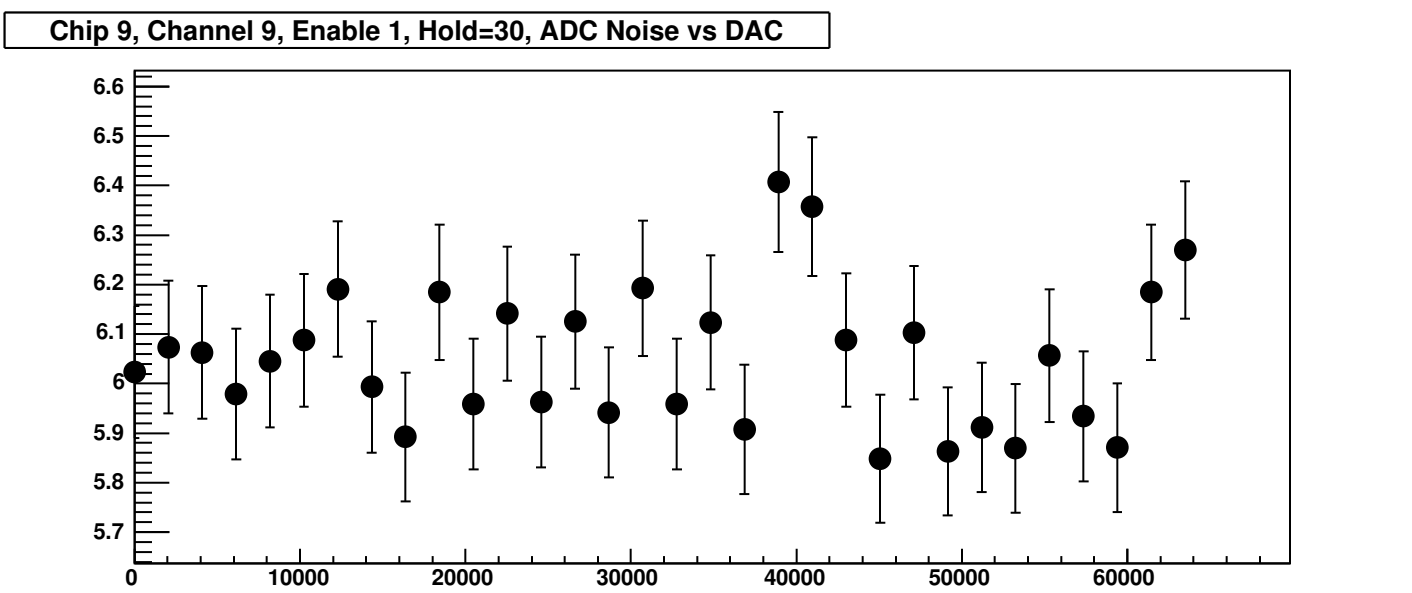

Chip 9, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

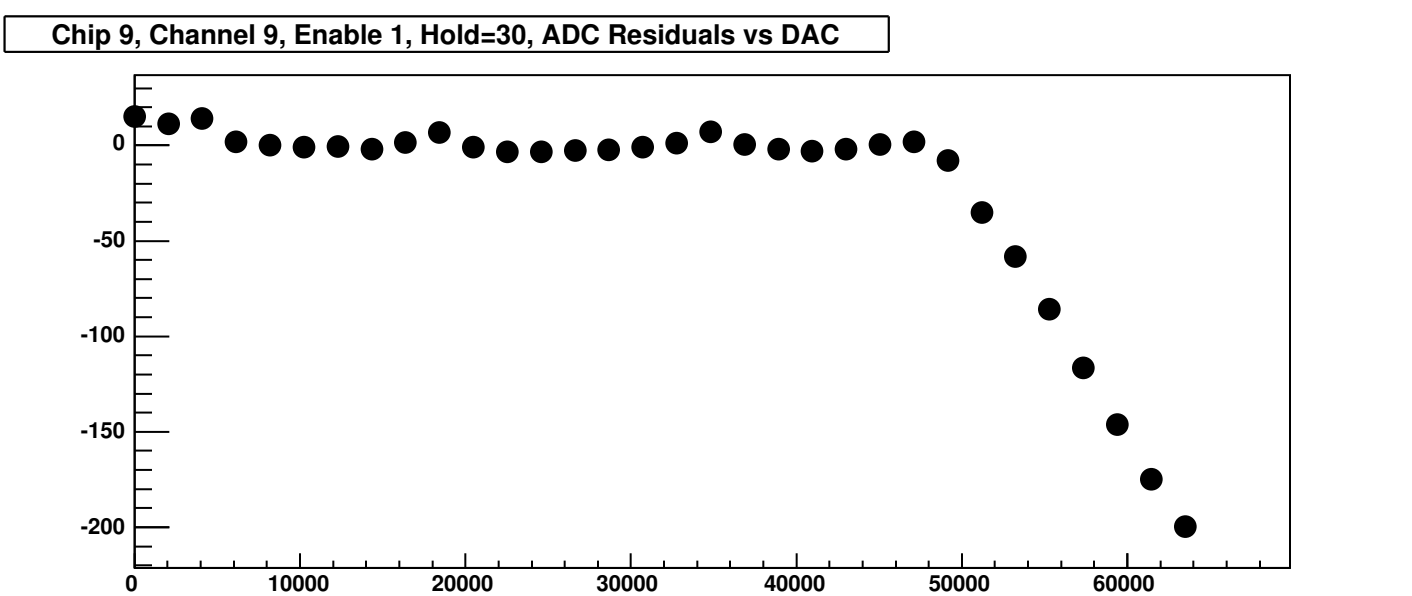

Chip 9, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5388 / 15
p0 838.8 ± 0.1142
p1 0.0003067 ± 4.707e-06
860
840
820
800
780
760
740
720
700
680
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

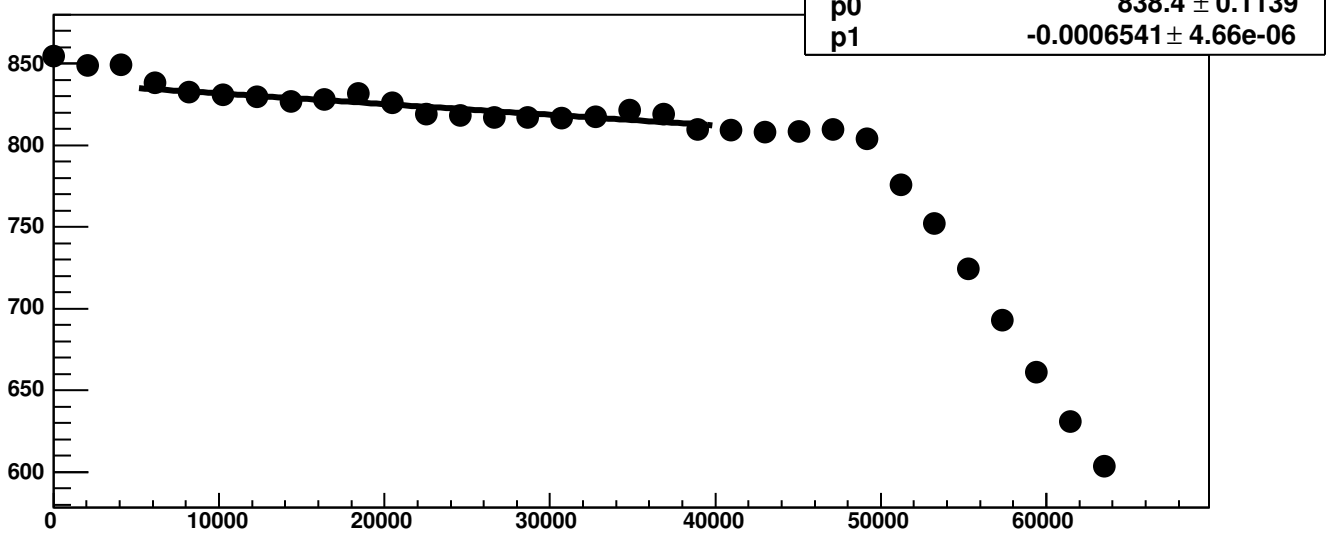

Chip 9, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4132 / 15
p0 839.7 ± 0.1129
p1 -0.0001179 ± 4.595e-06
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

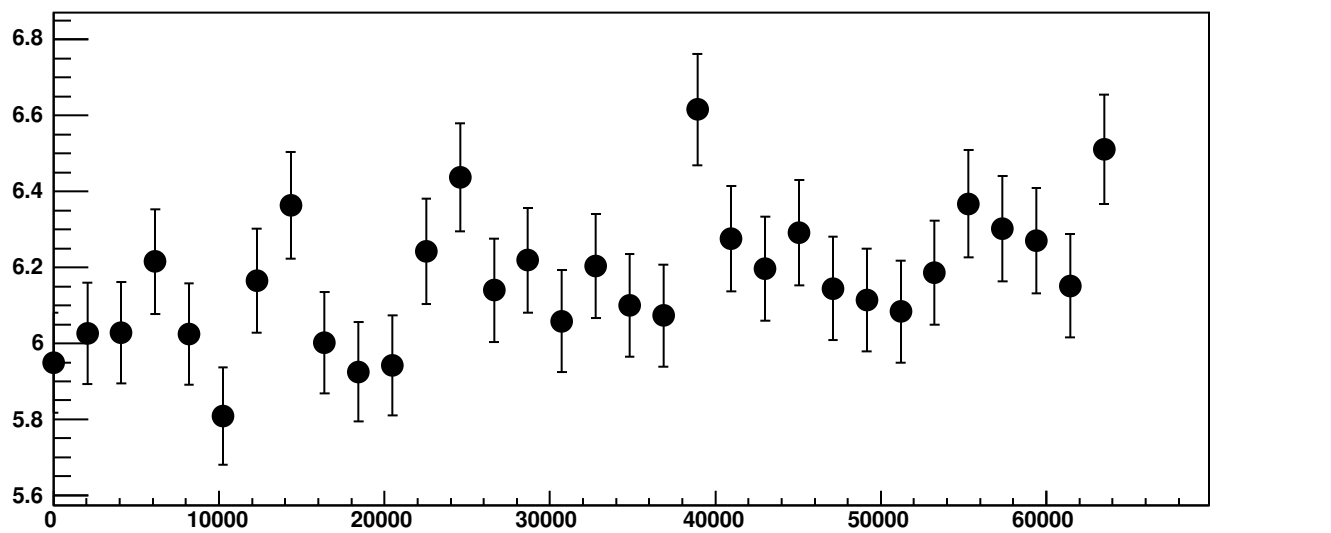

Chip 9, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

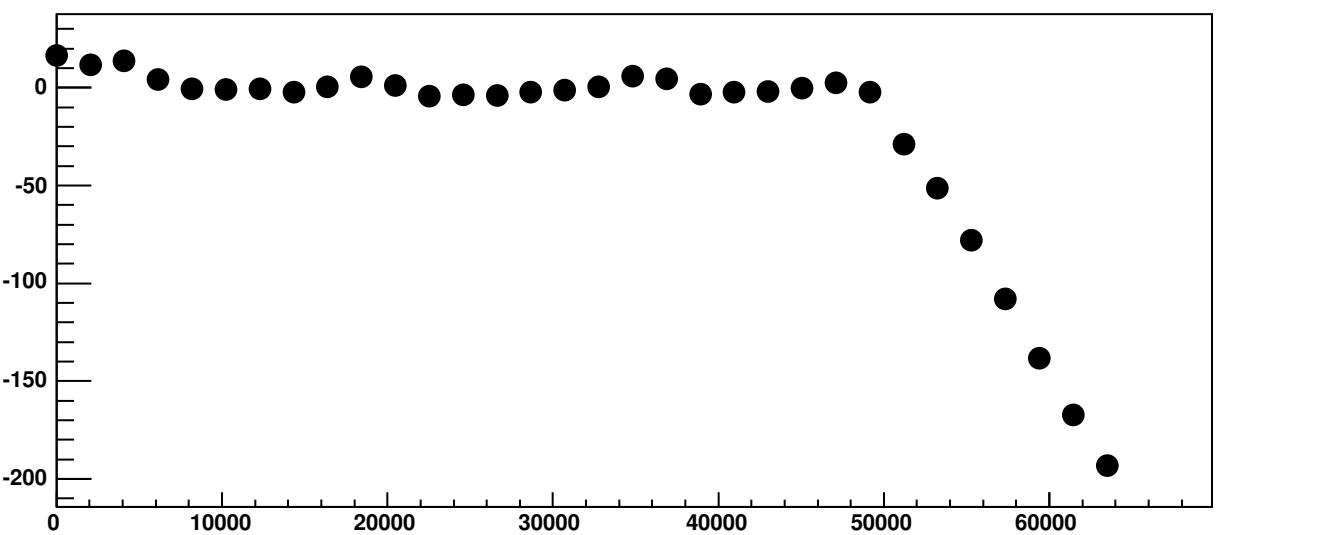
Chip 9, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC

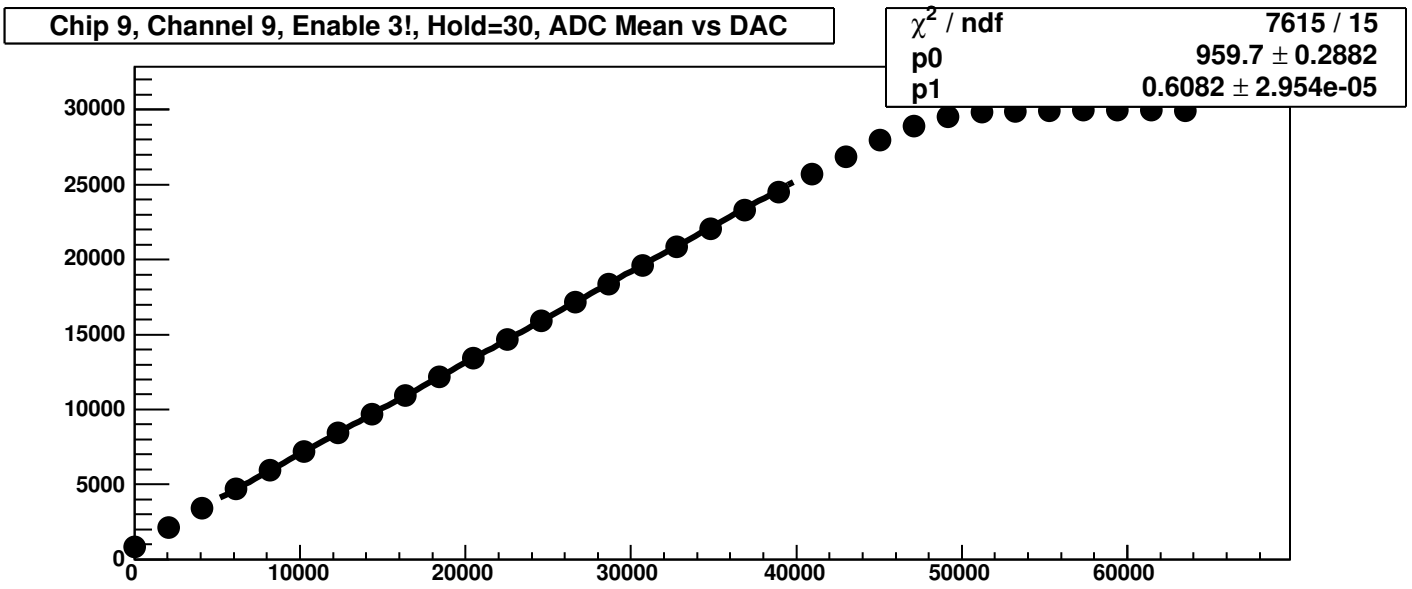


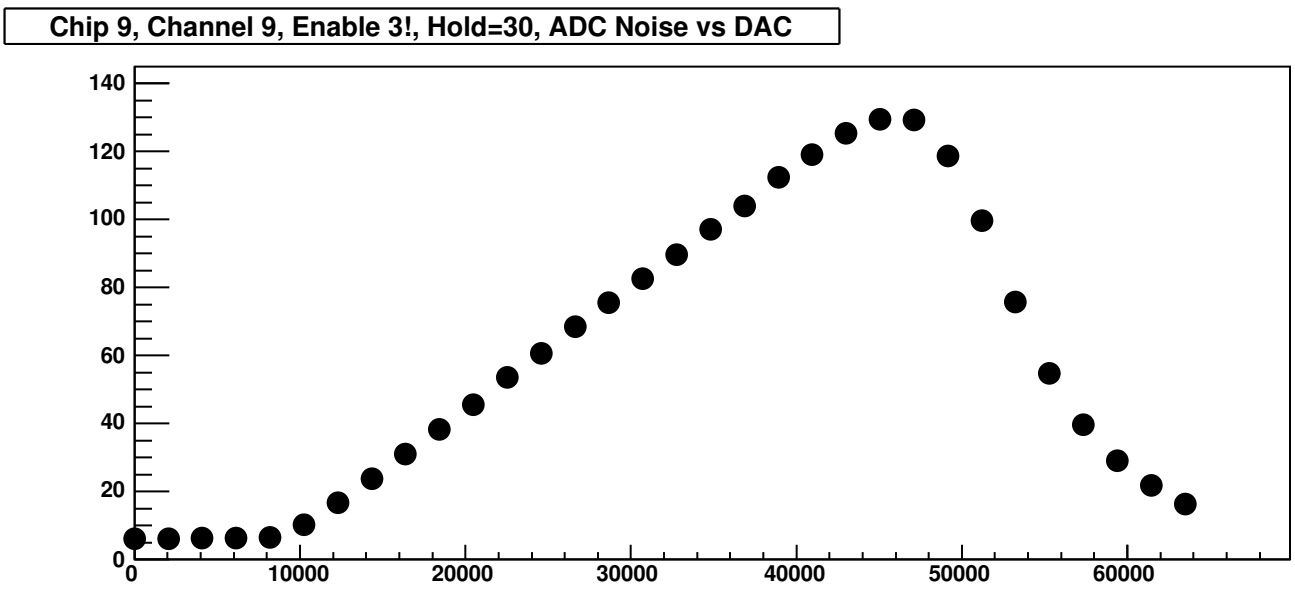
Chip 9, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC

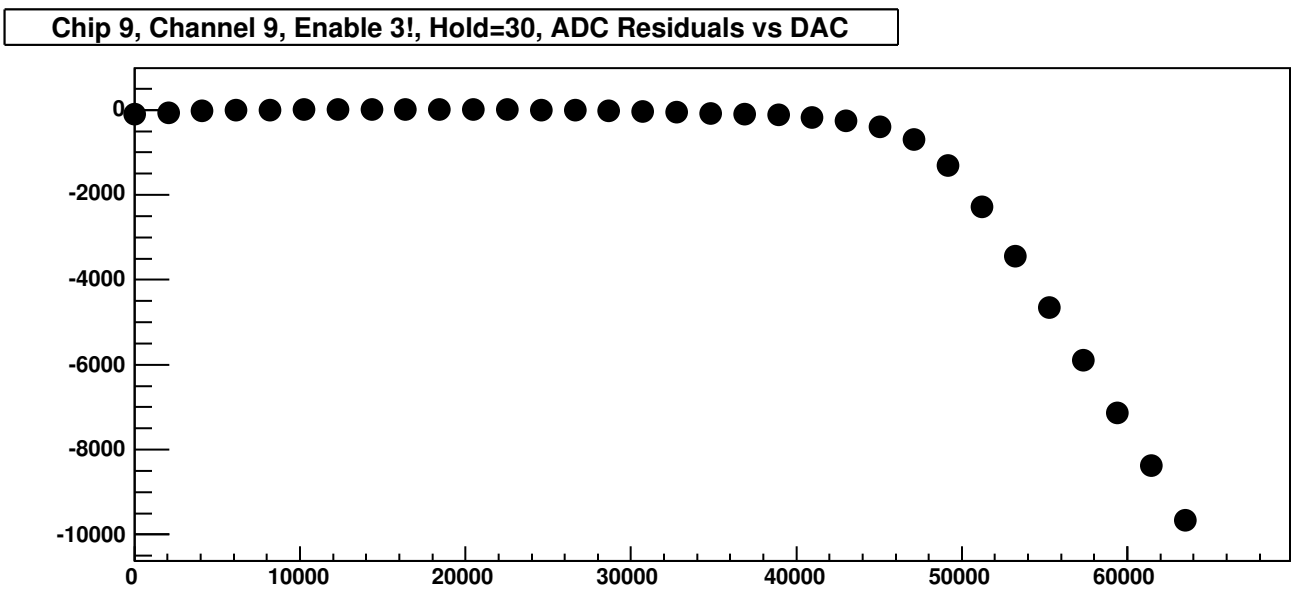


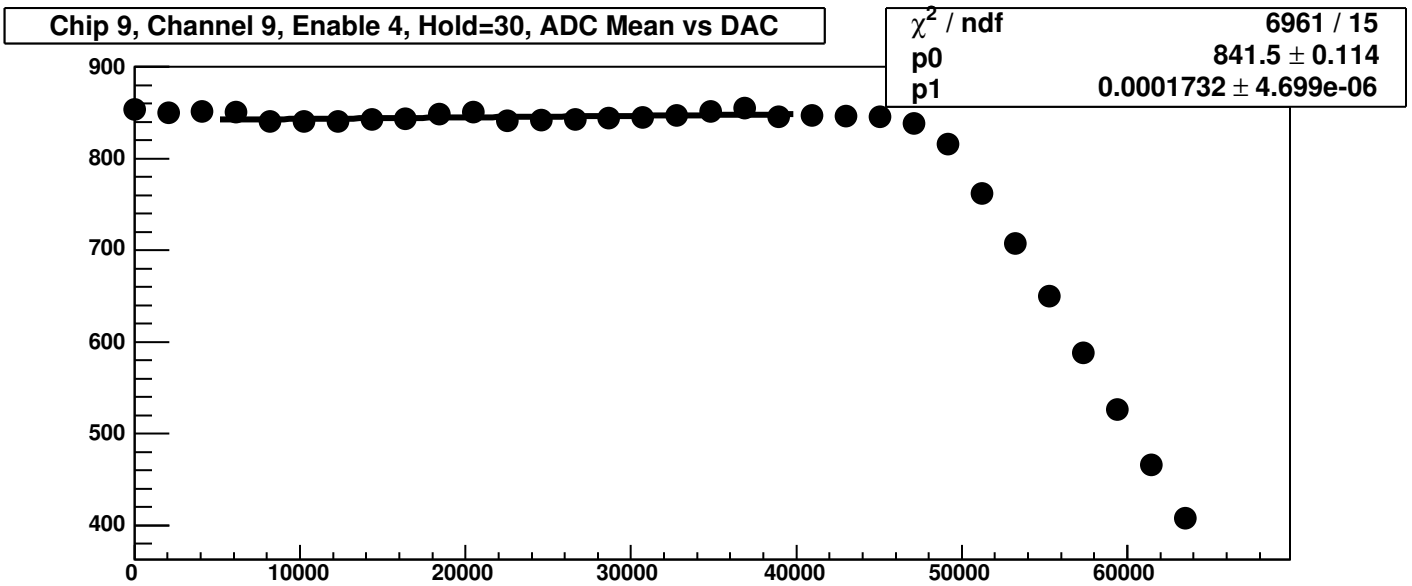
Chip 9, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

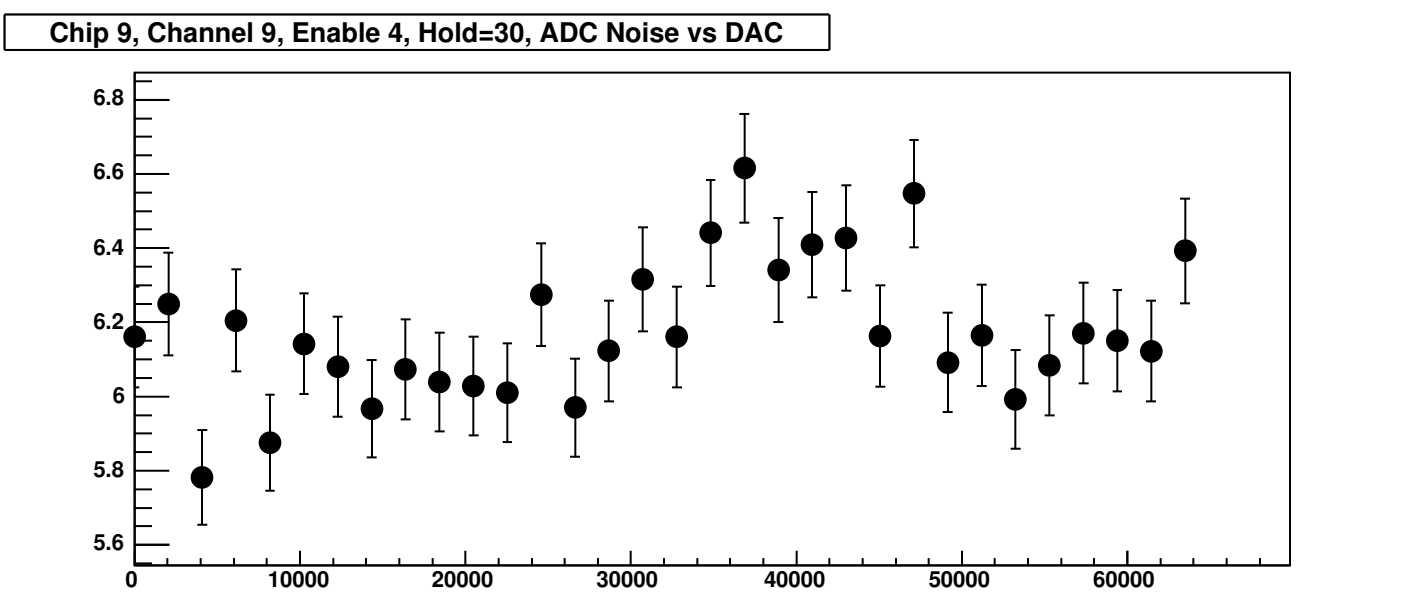


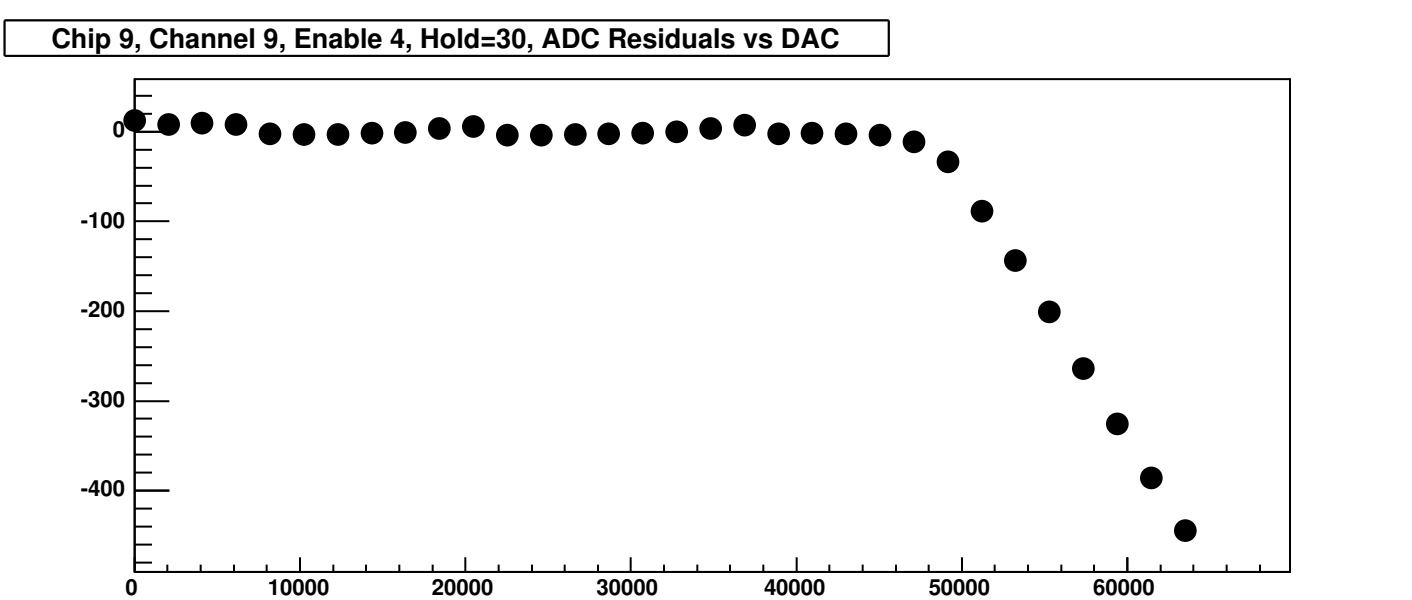

Chip 9, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 7615 / 15
p0 959.7 ± 0.2882
p1 0.6082 ± 2.954e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

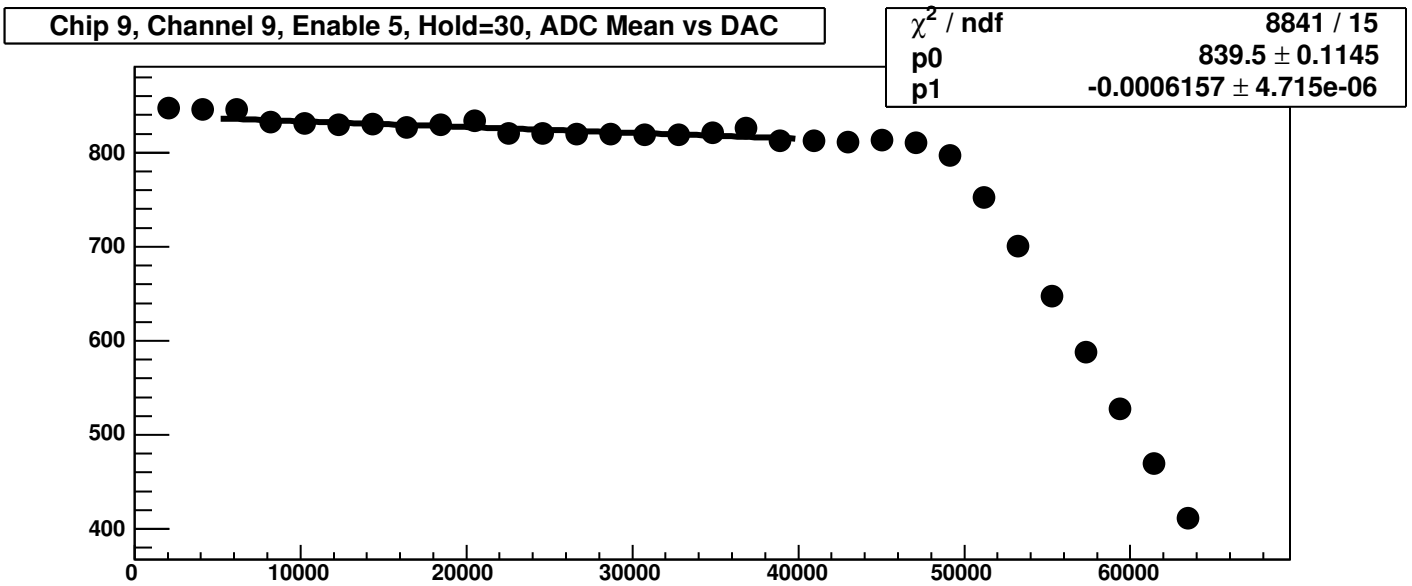

Chip 9, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

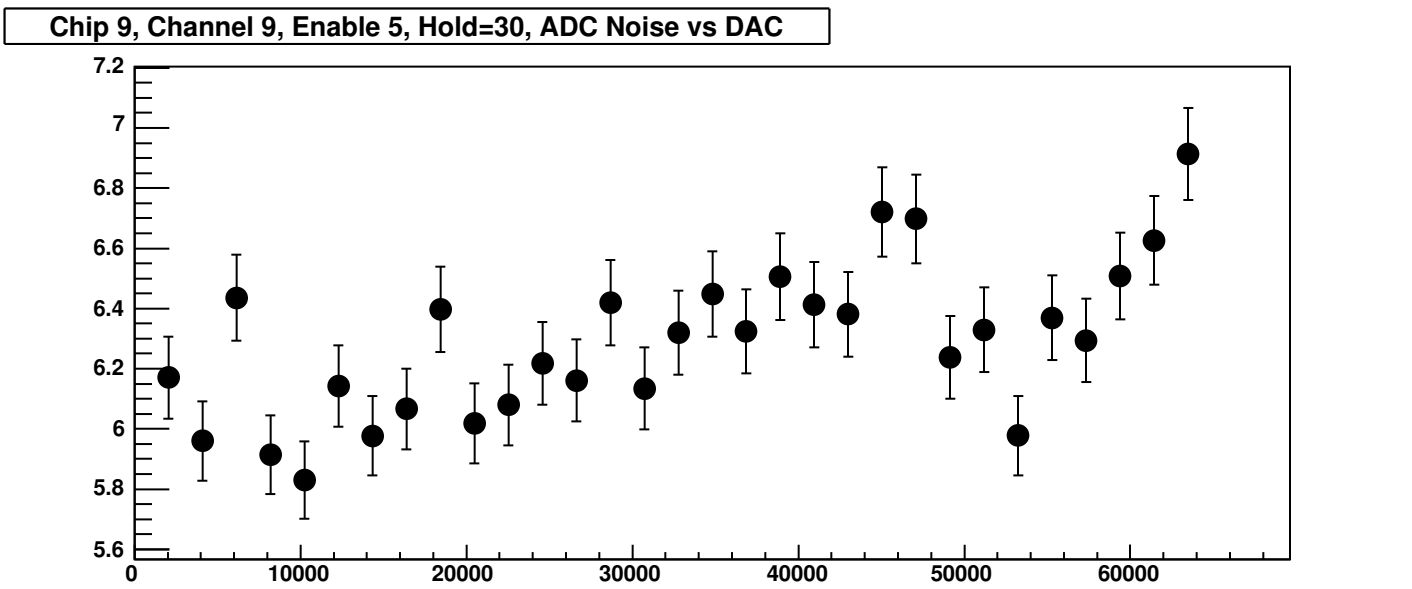

Chip 9, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

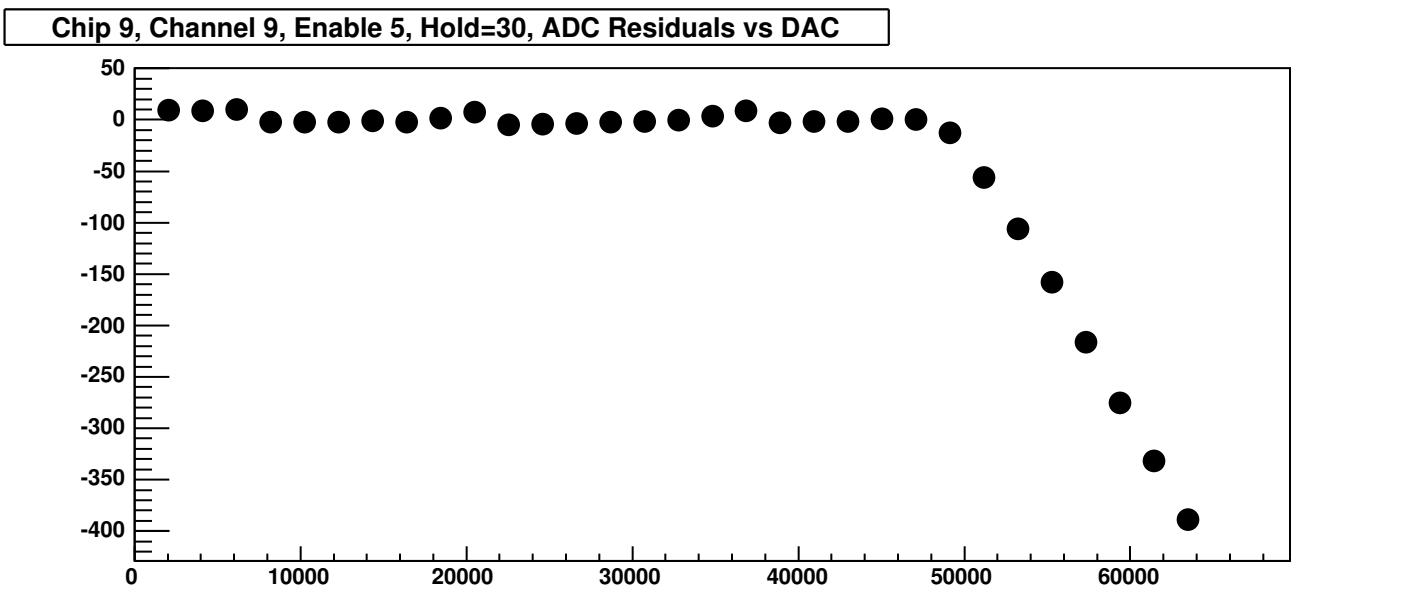

Chip 9, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6961 / 15
p0 841.5 ± 0.114
p1 0.0001732 ± 4.699e-06
900
800
700
600
500
400
0
10000
20000
30000
40000
50000
60000

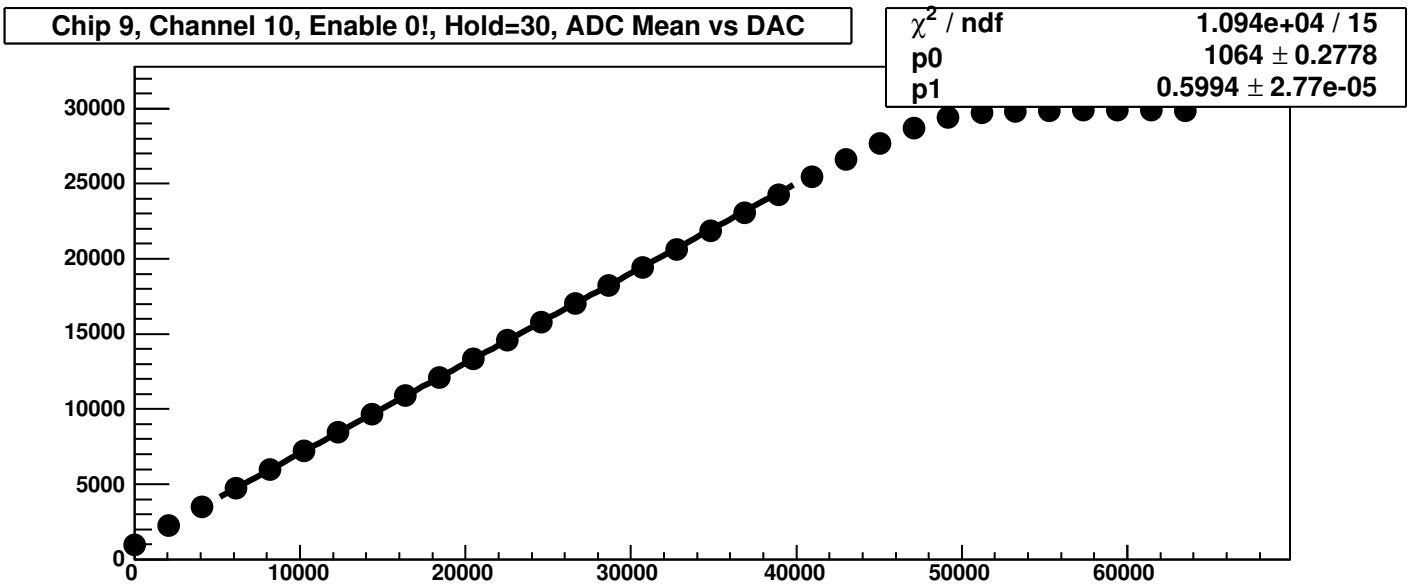

Chip 9, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

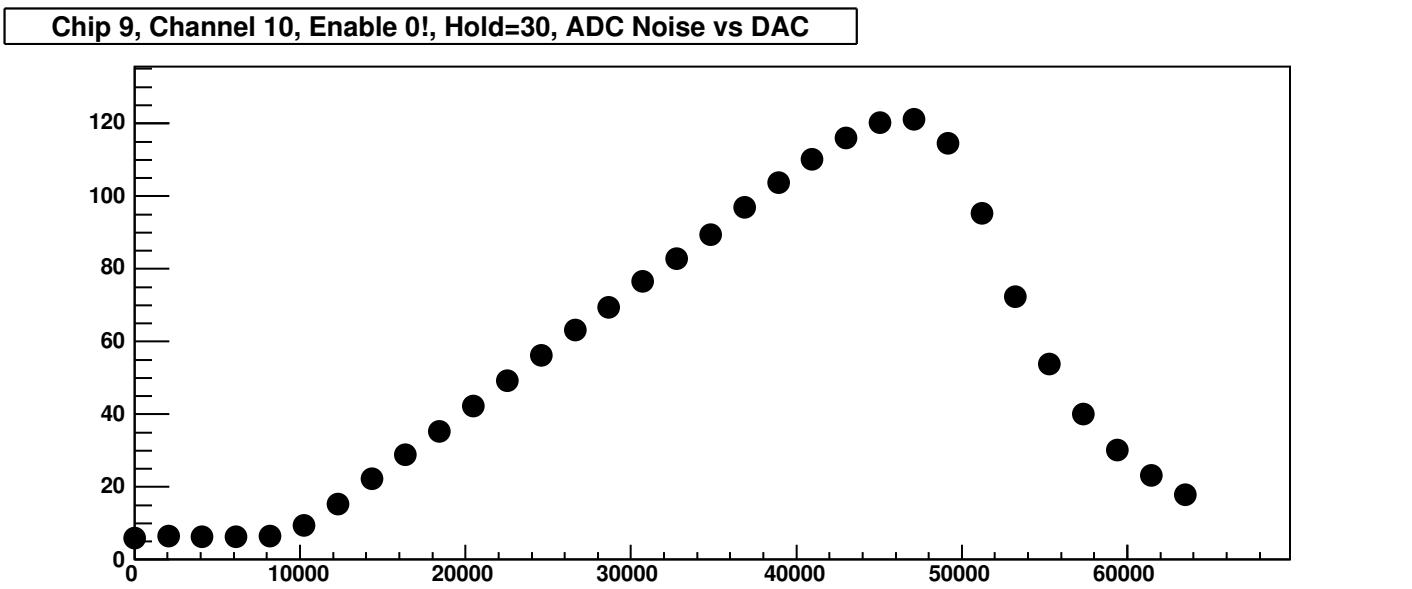

Chip 9, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

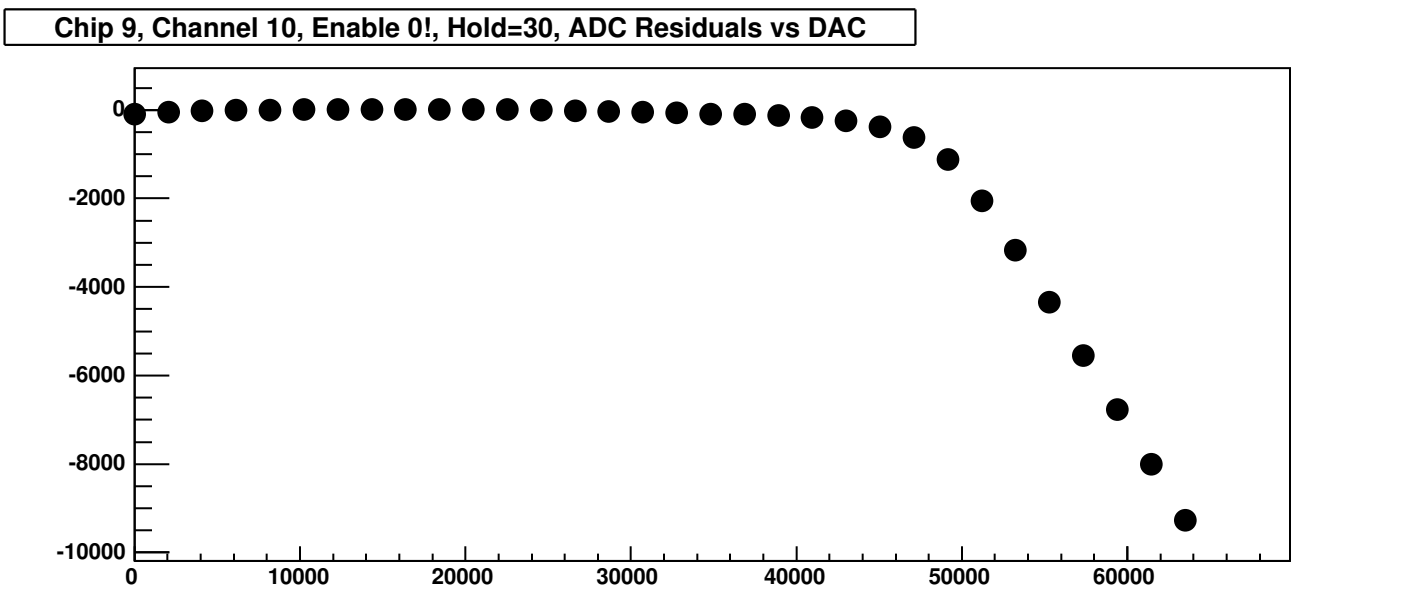

Chip 9, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8841 / 15
p0 839.5 ± 0.1145
p1 -0.0006157 ± 4.715e-06
800
700
600
500
400
0
10000
20000
30000
40000
50000
60000

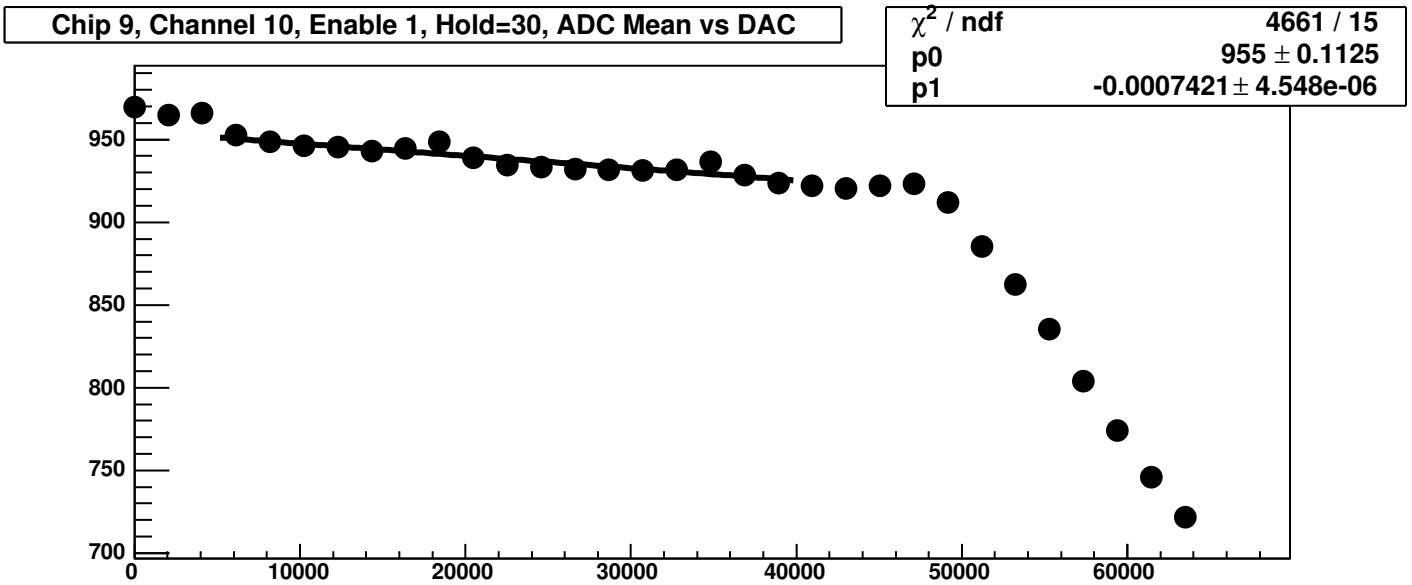

Chip 9, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

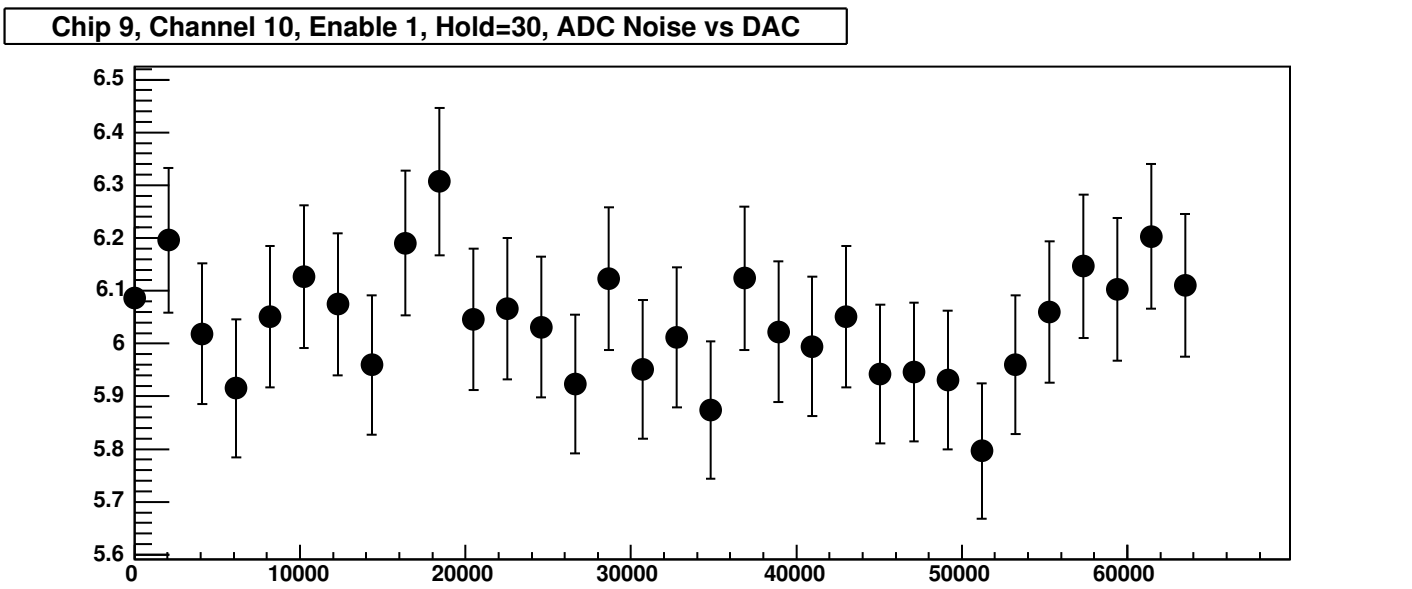

Chip 9, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

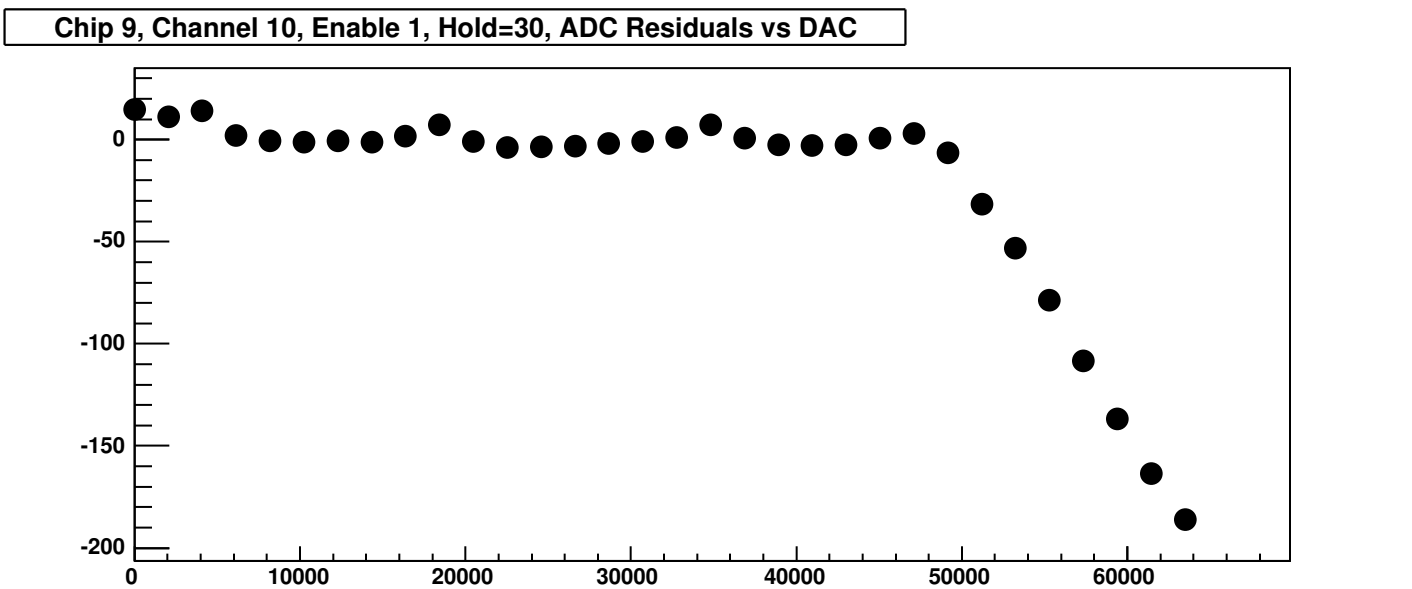

Chip 9, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.094e+04 / 15
p0 1064 ± 0.2778
p1 0.5994 ± 2.77e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

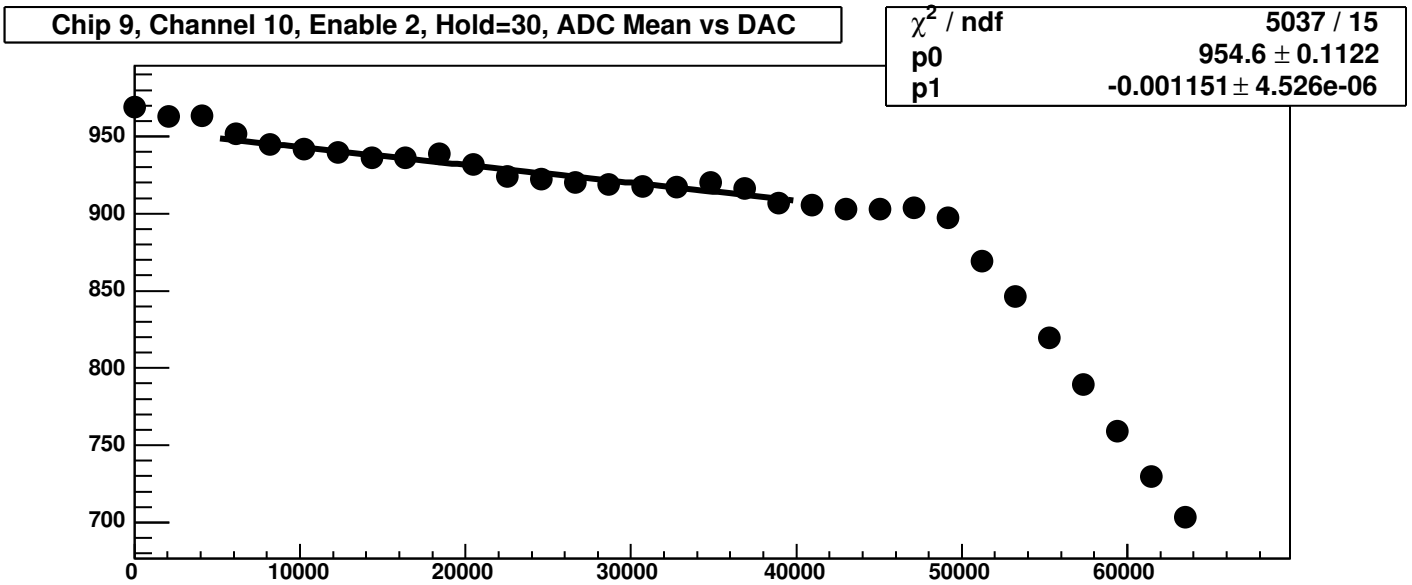

Chip 9, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

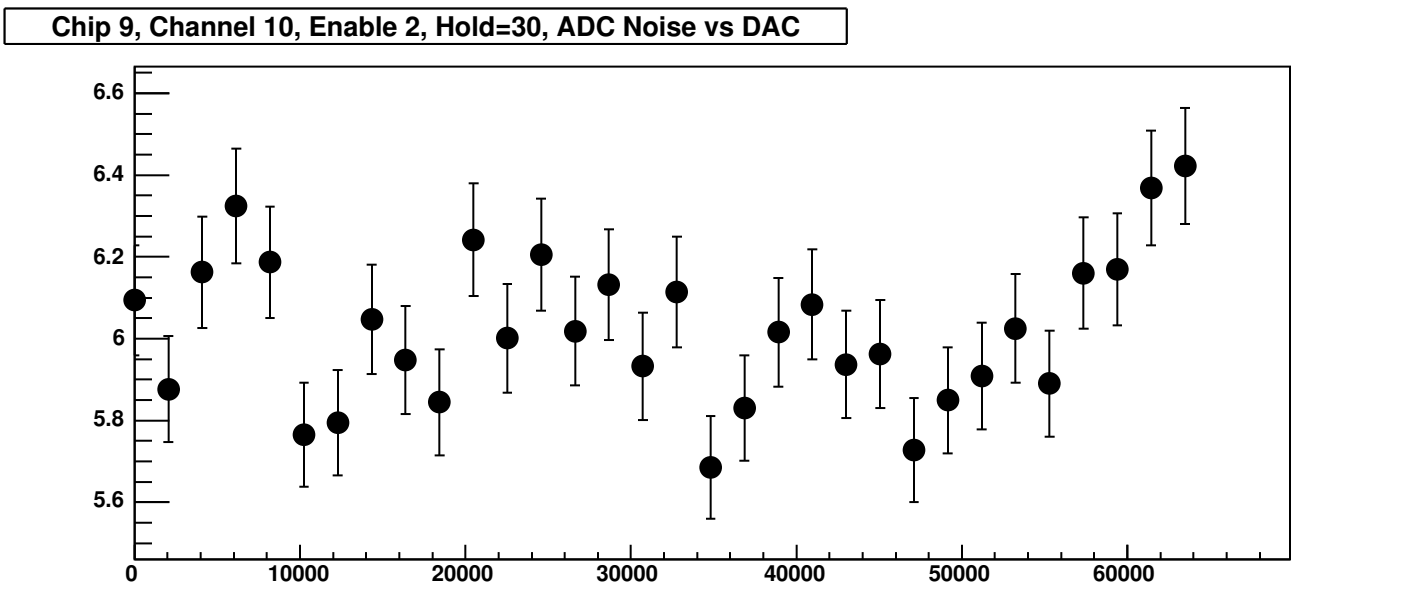

Chip 9, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

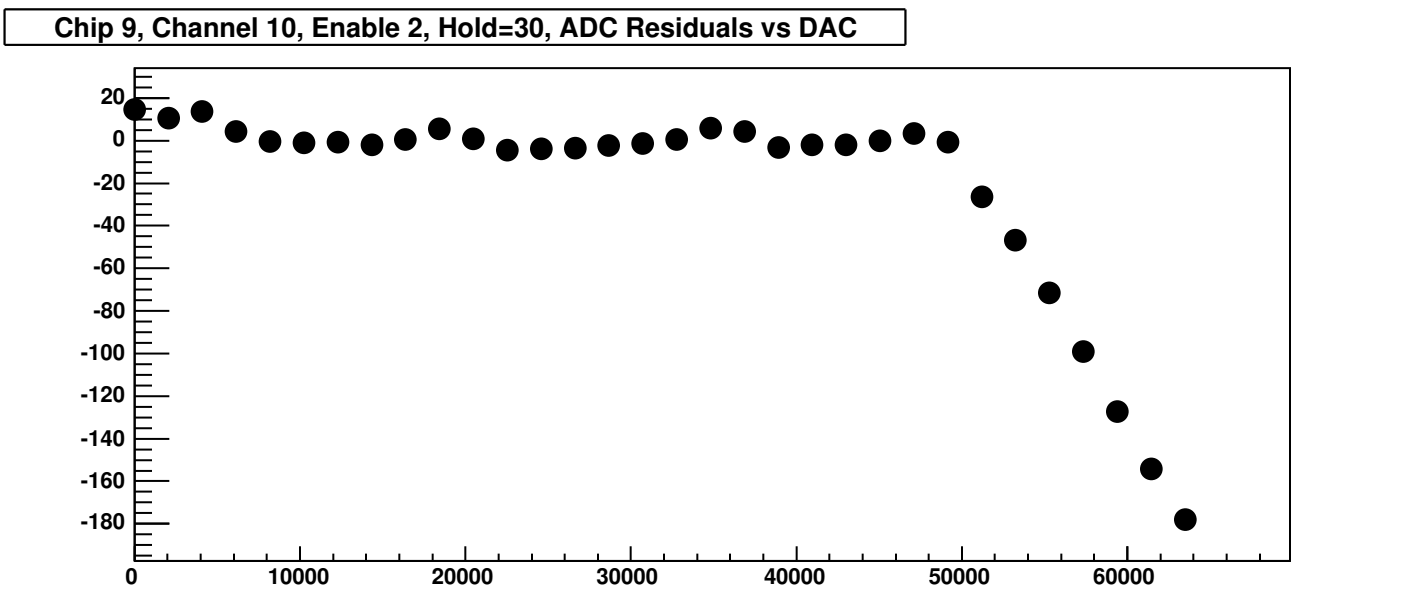

Chip 9, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4661 / 15
p0 955 ± 0.1125
p1 -0.0007421 ± 4.548e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

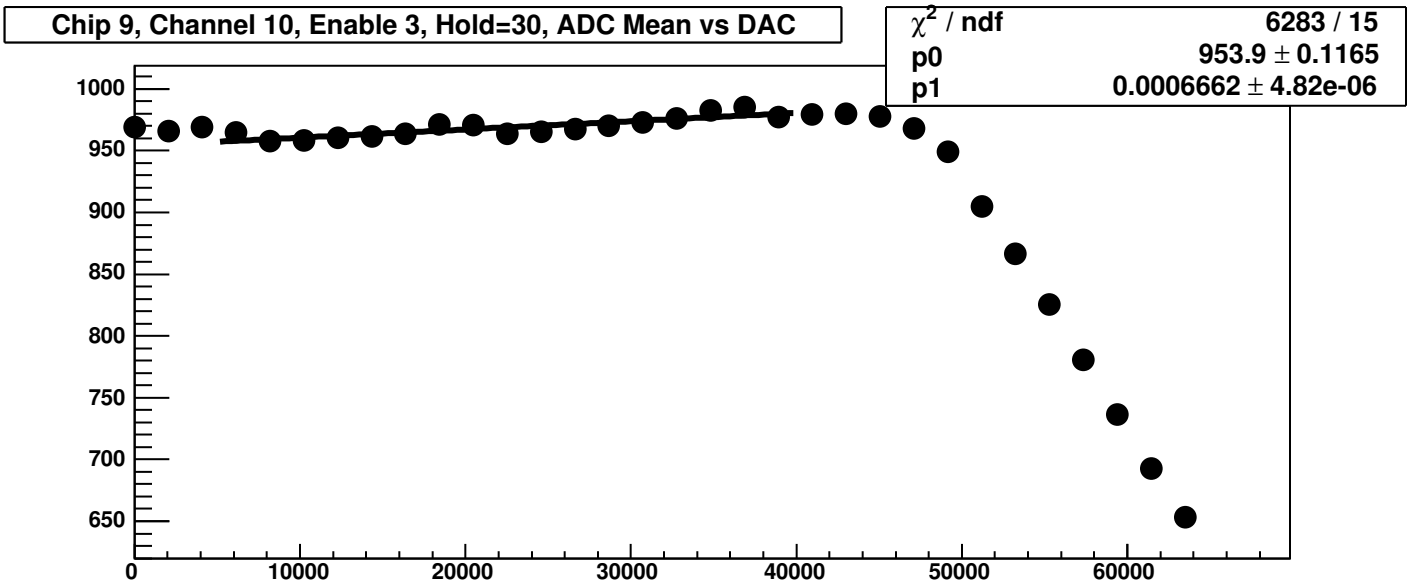

Chip 9, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
0
10000
20000
30000
40000
50000
60000

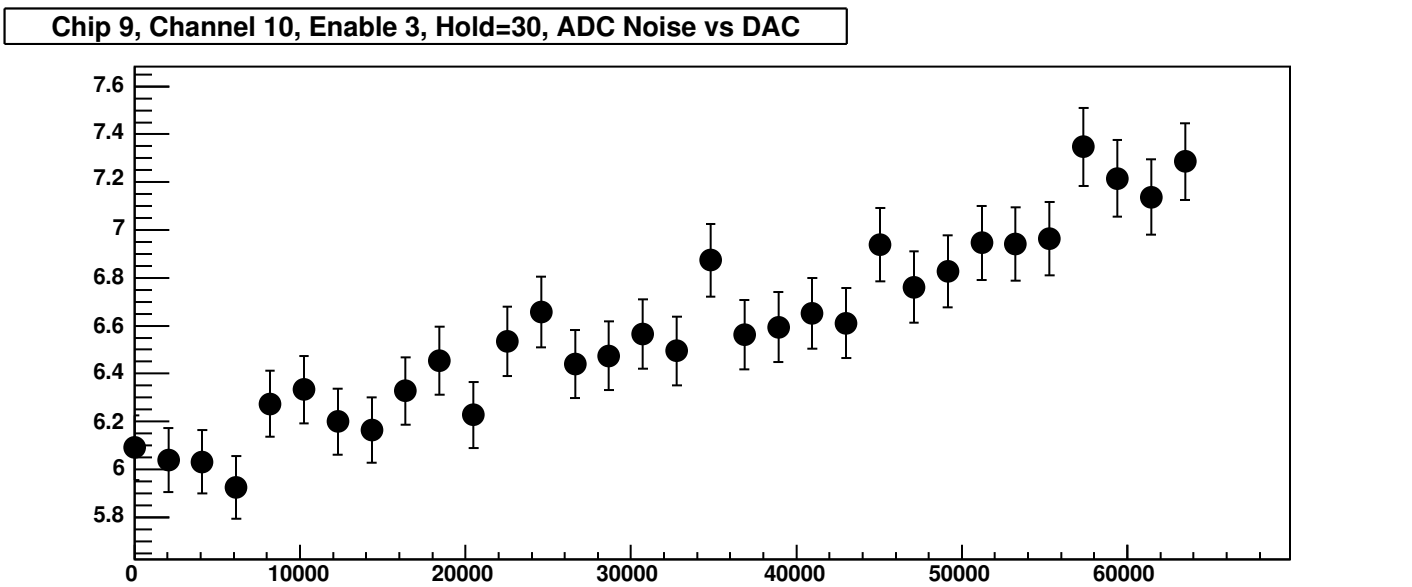

Chip 9, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

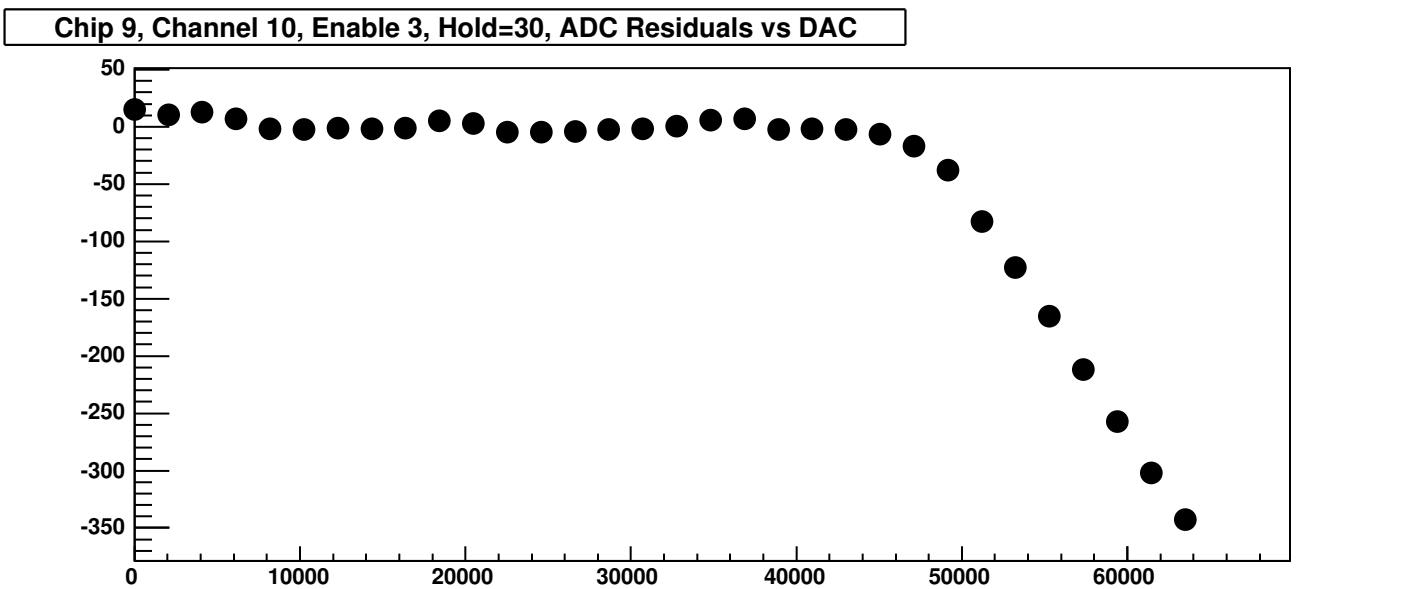

Chip 9, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5037 / 15
p0 954.6 ± 0.1122
p1 -0.001151 ± 4.526e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

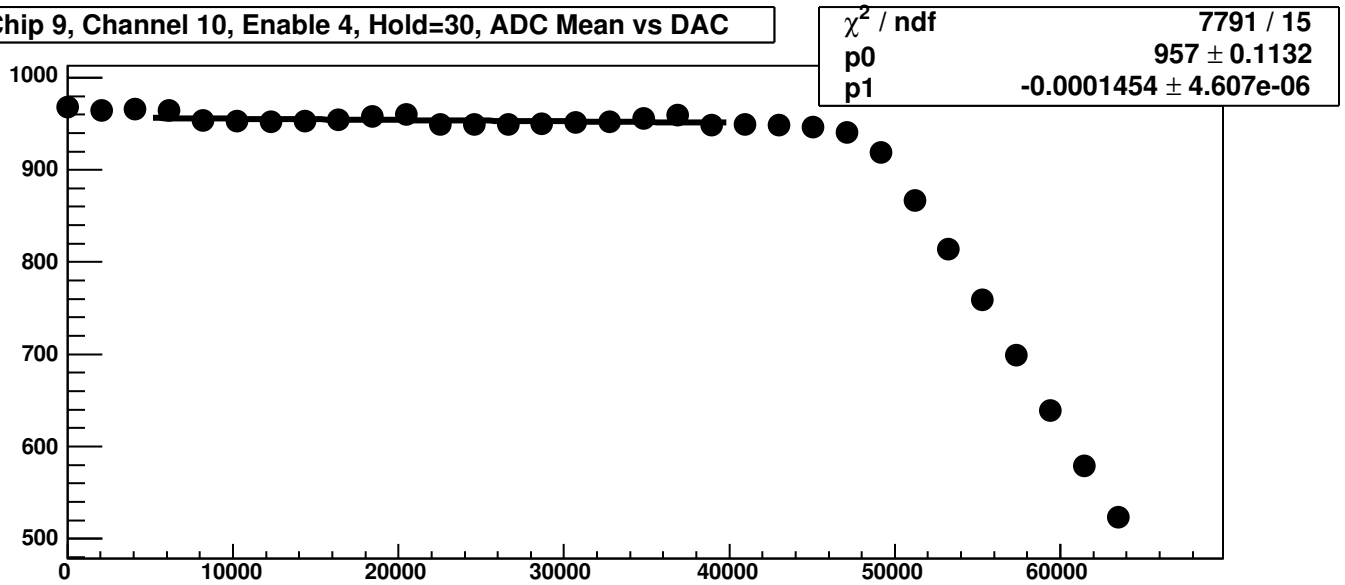

Chip 9, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 6283 / 15
p0 953.9 ± 0.1165
p1 0.0006662 ± 4.82e-06
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

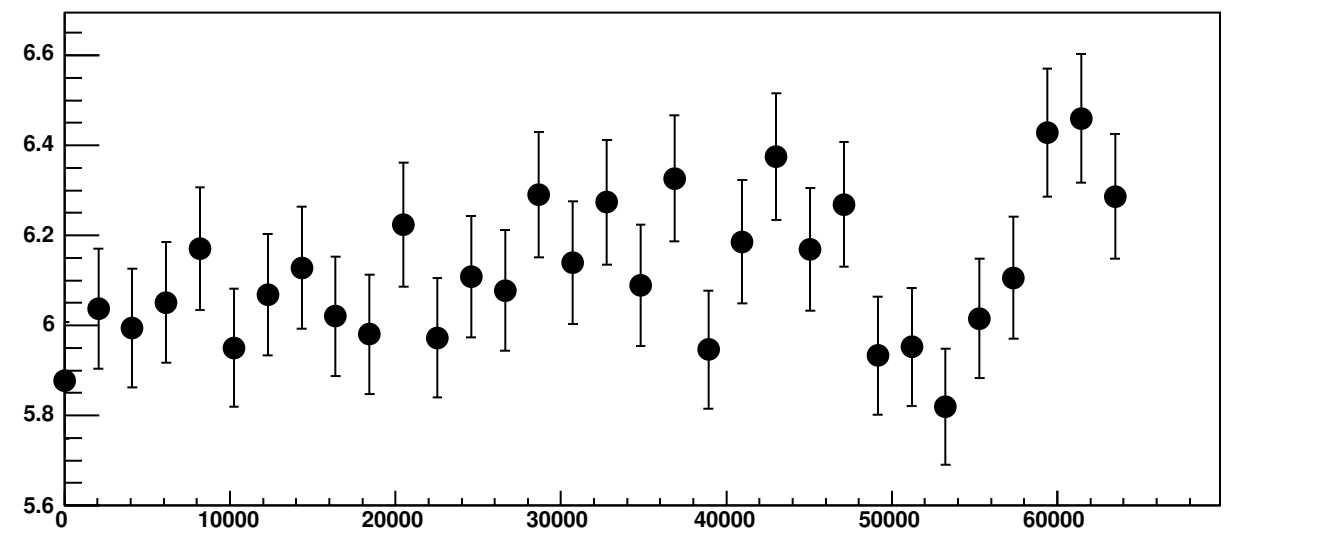

Chip 9, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

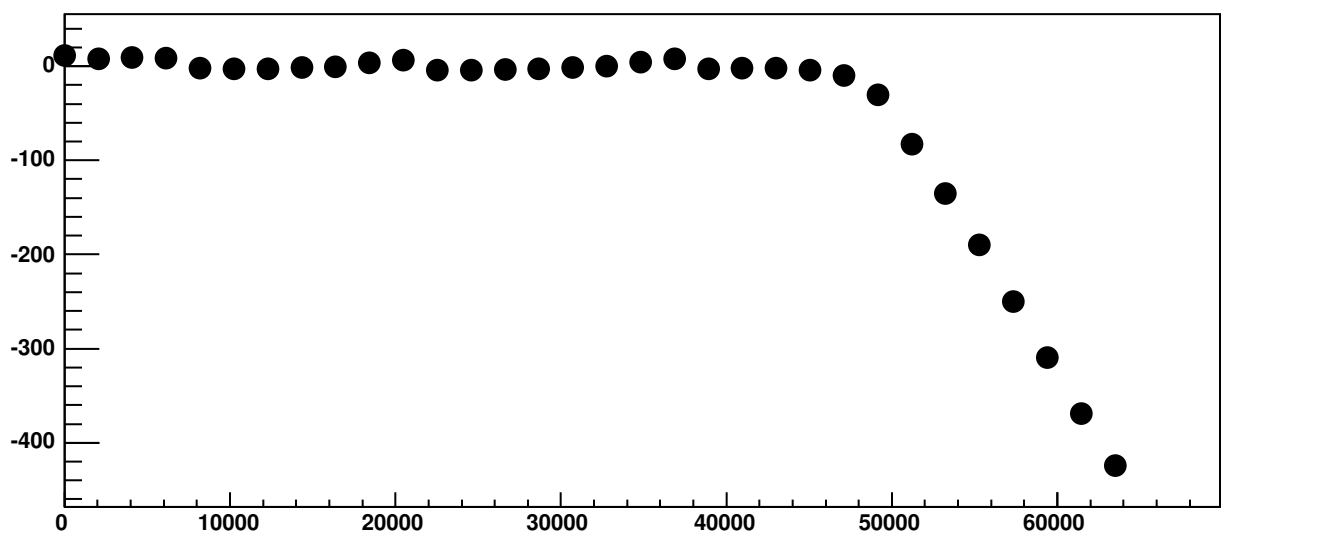
Chip 9, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

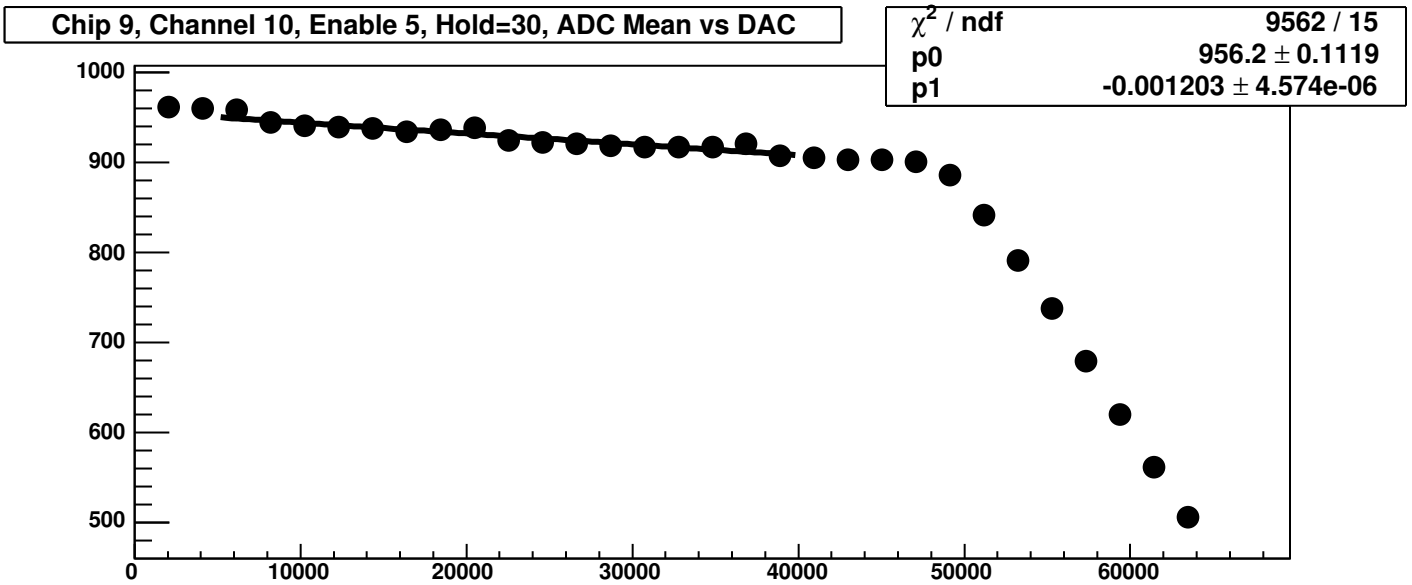


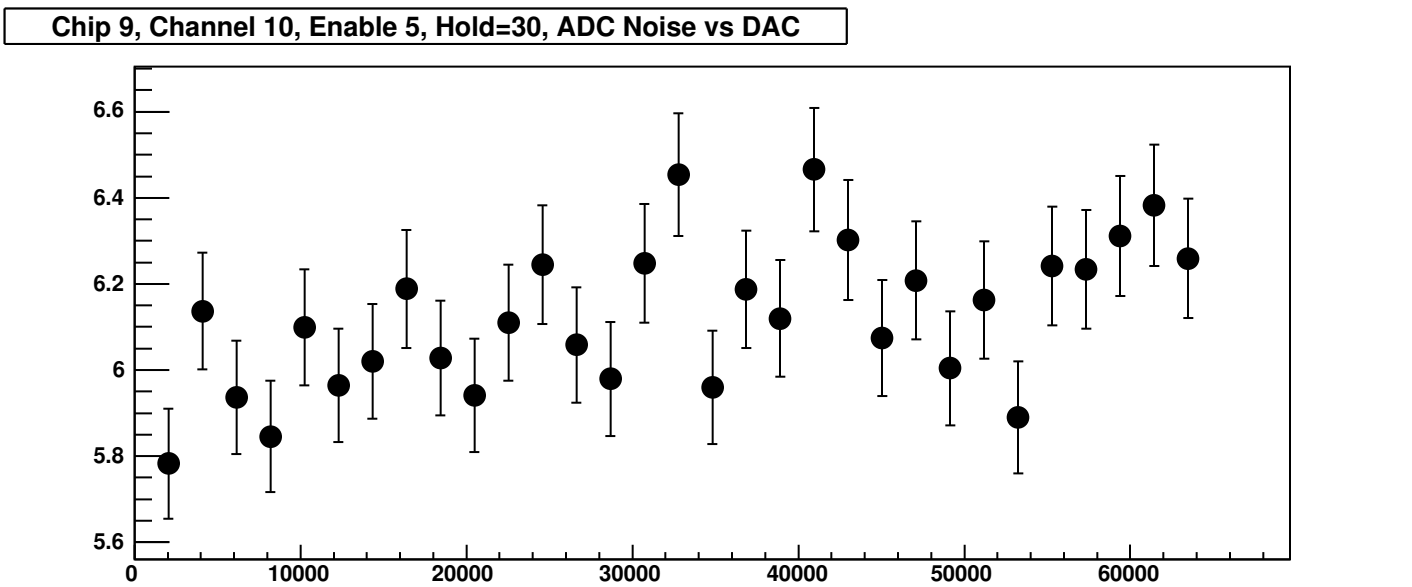
Chip 9, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC

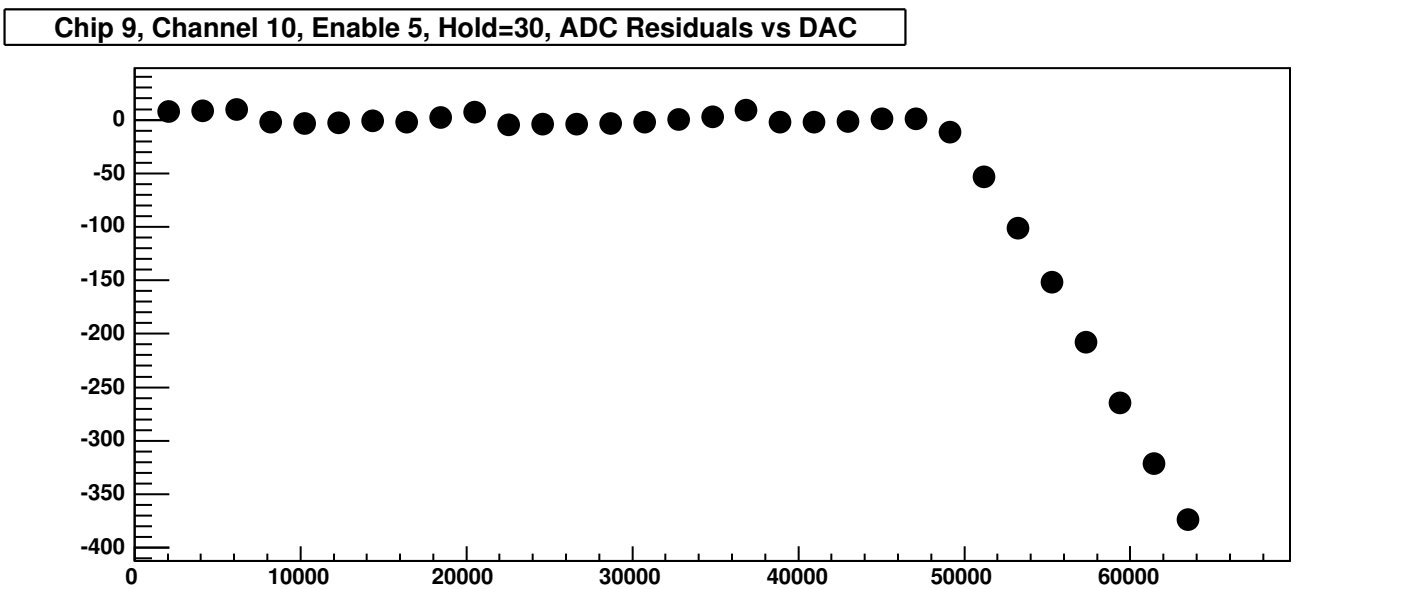


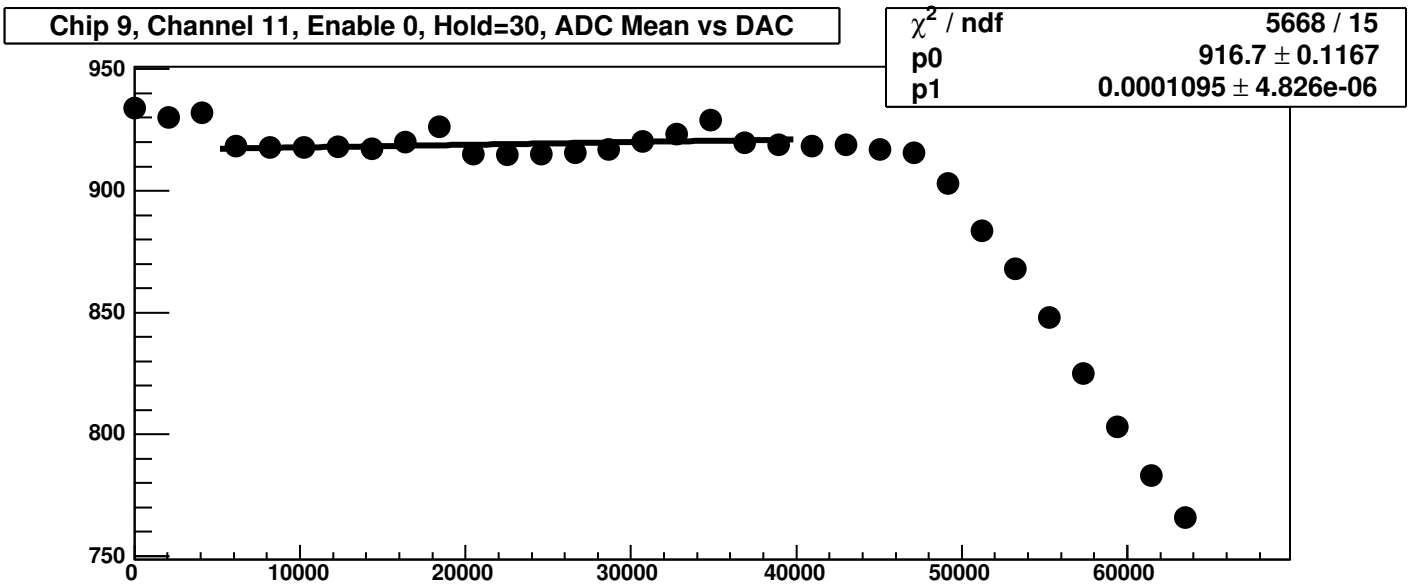
Chip 9, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC

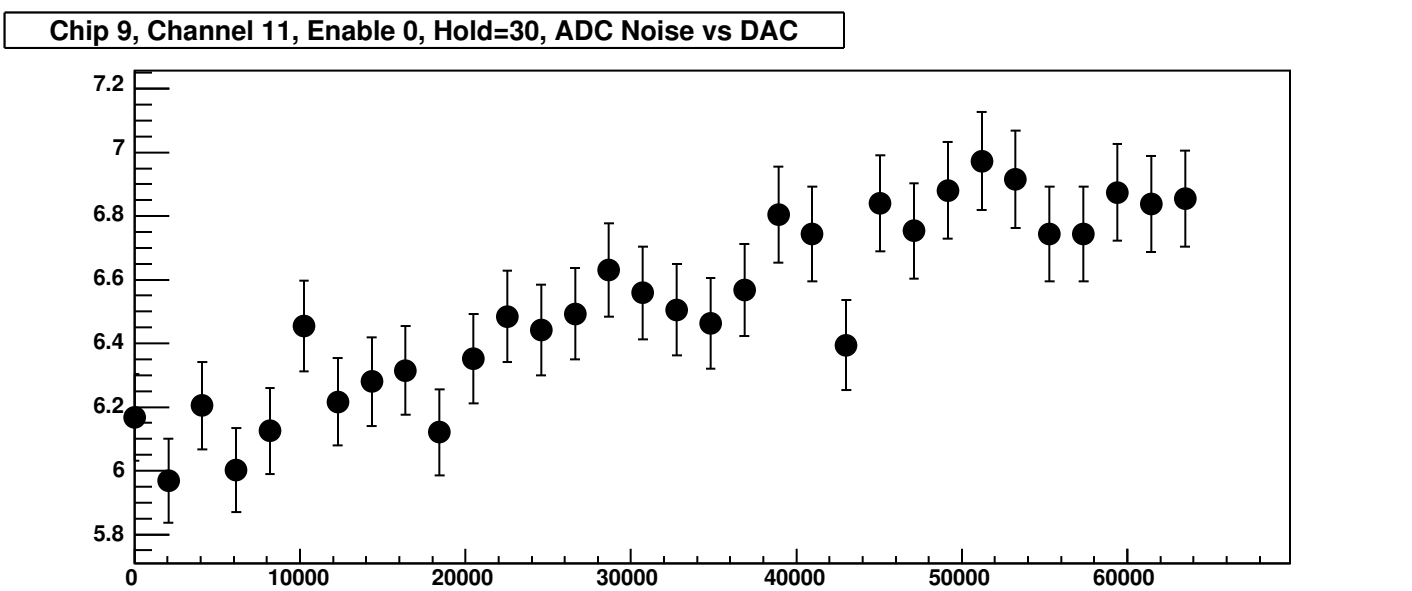


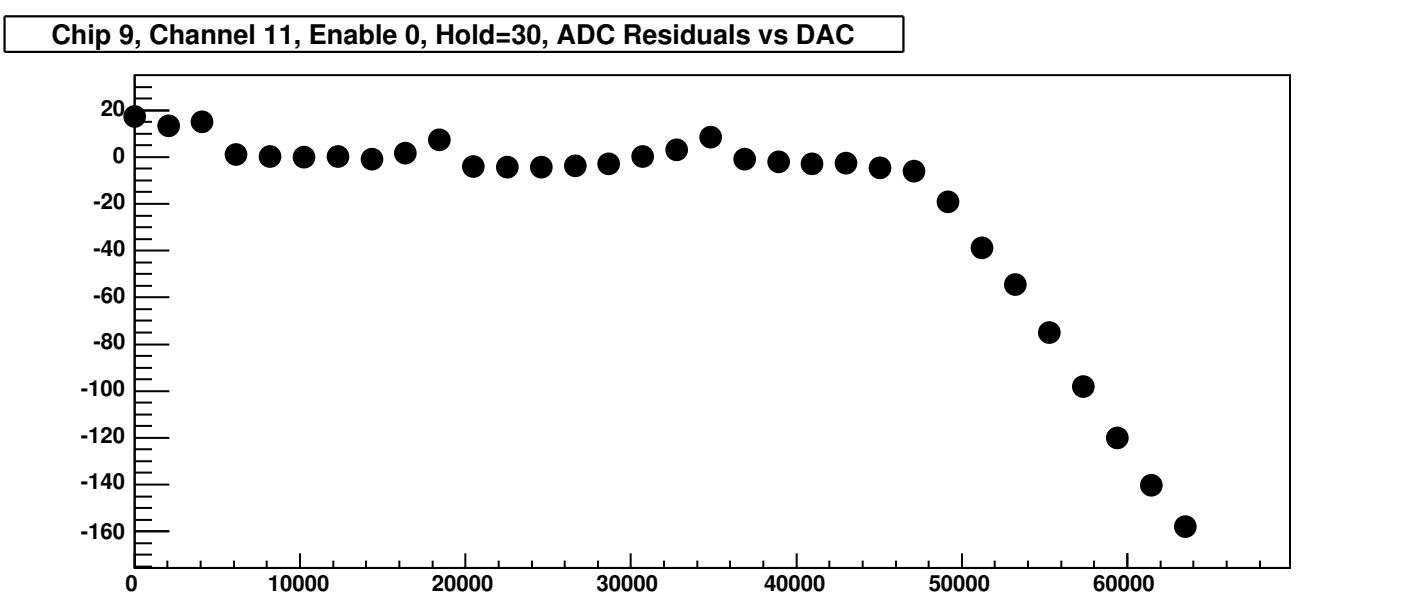

Chip 9, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9562 / 15
p0 956.2 ± 0.1119
p1 -0.001203 ± 4.574e-06
1000
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

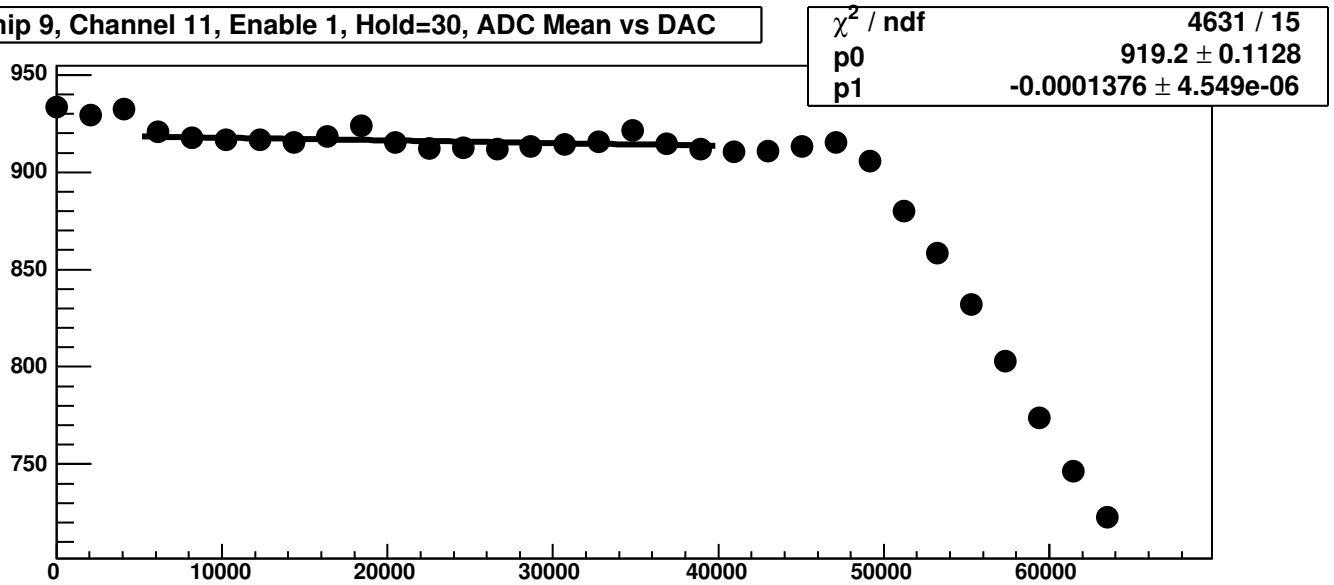

Chip 9, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5668 / 15
p0 916.7 ± 0.1167
p1 0.0001095 ± 4.826e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

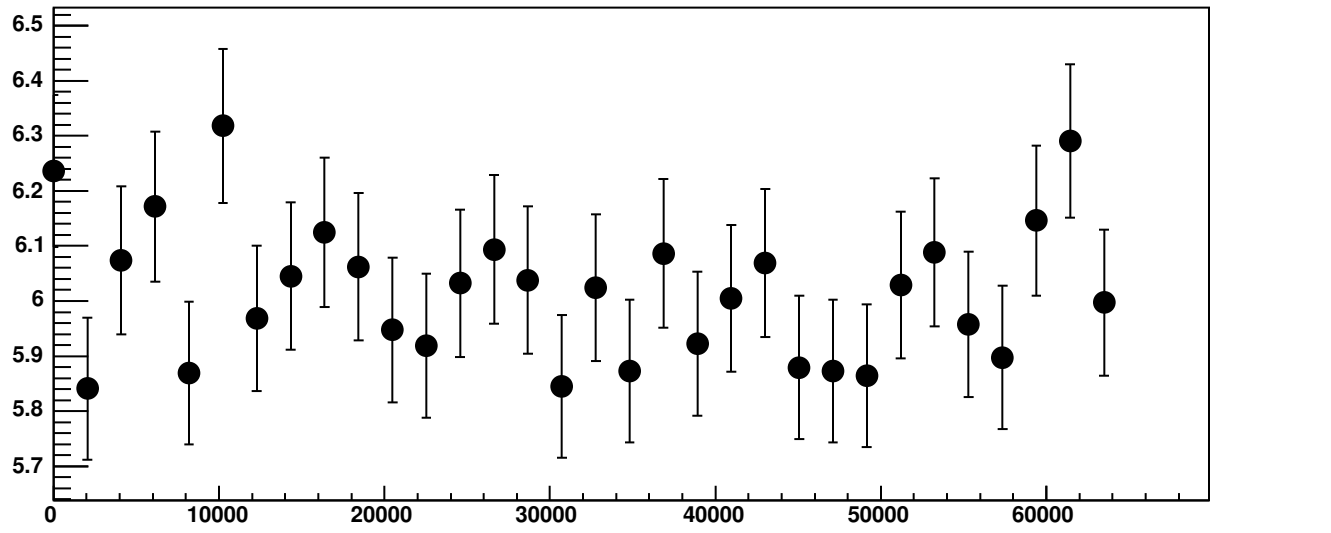

Chip 9, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

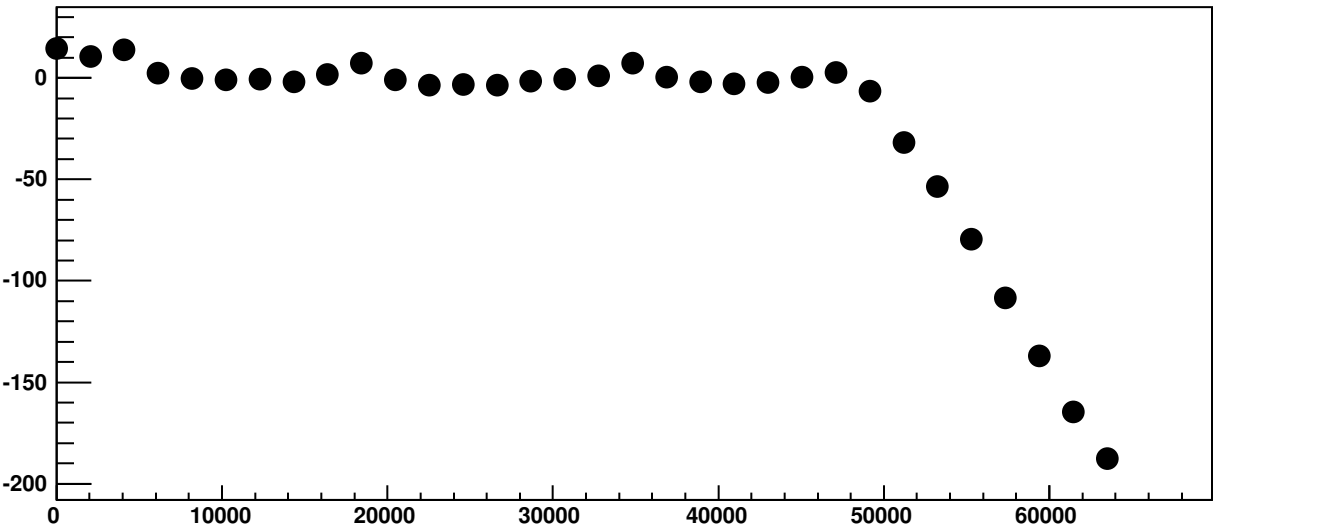
Chip 9, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC



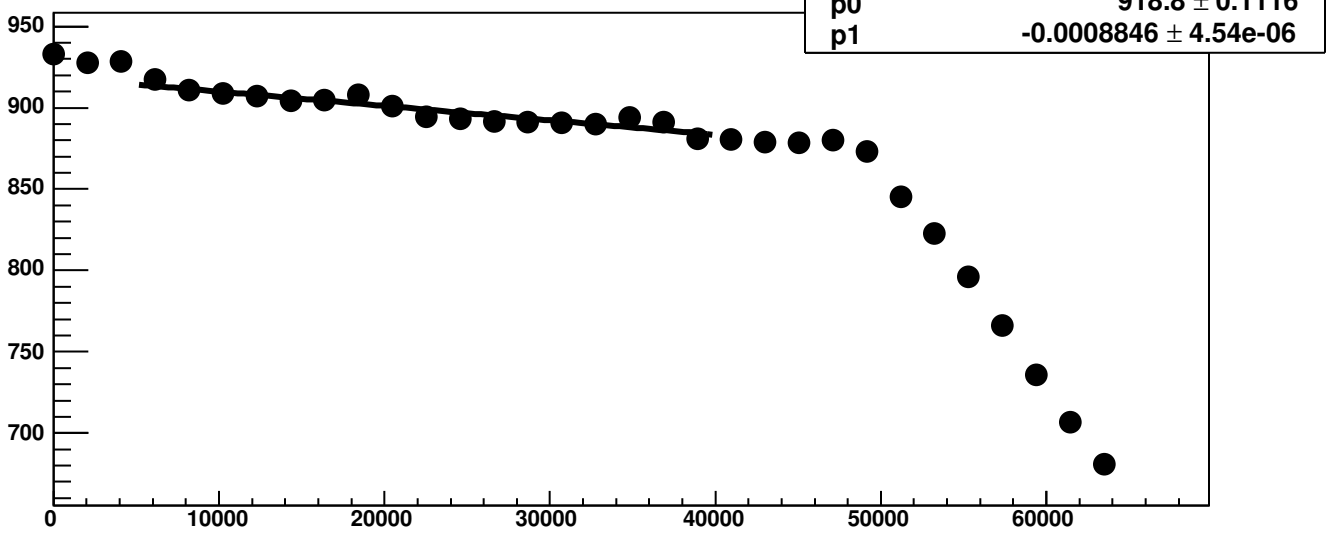
Chip 9, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



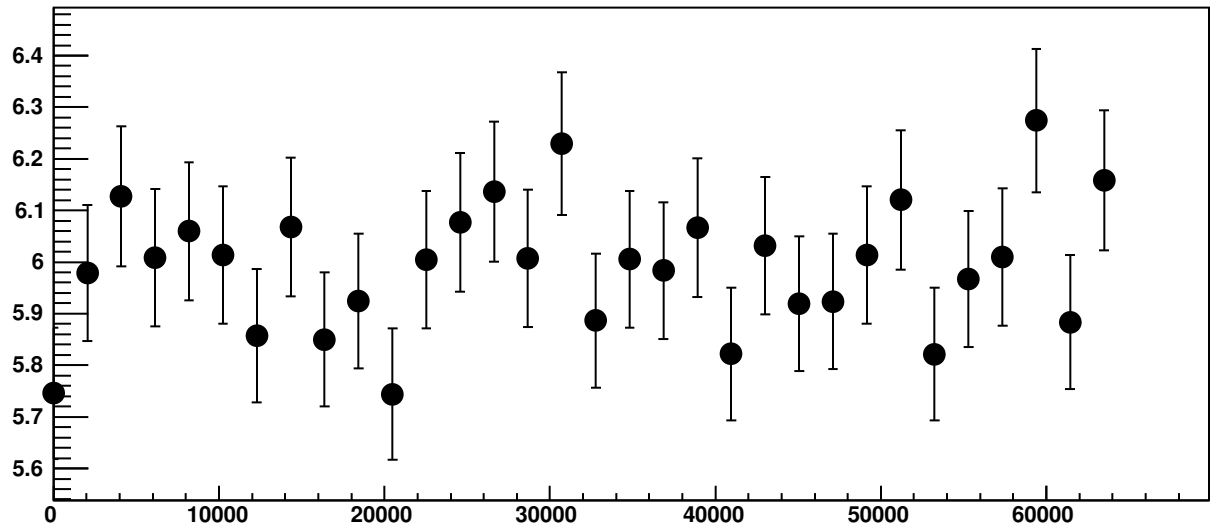
Chip 9, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC



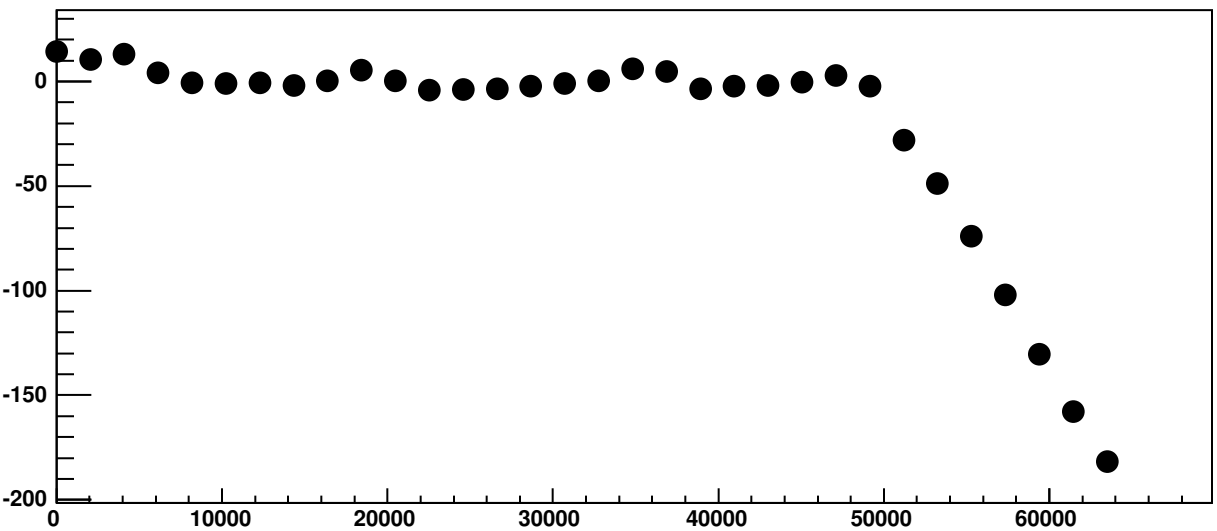
Chip 9, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

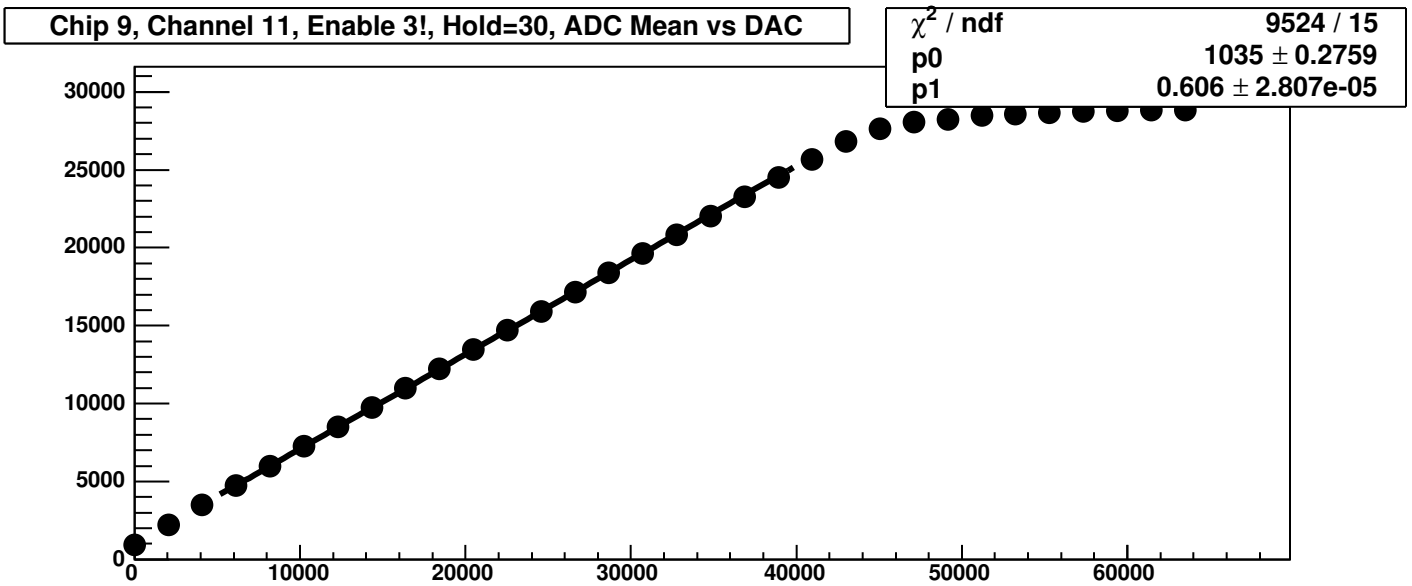


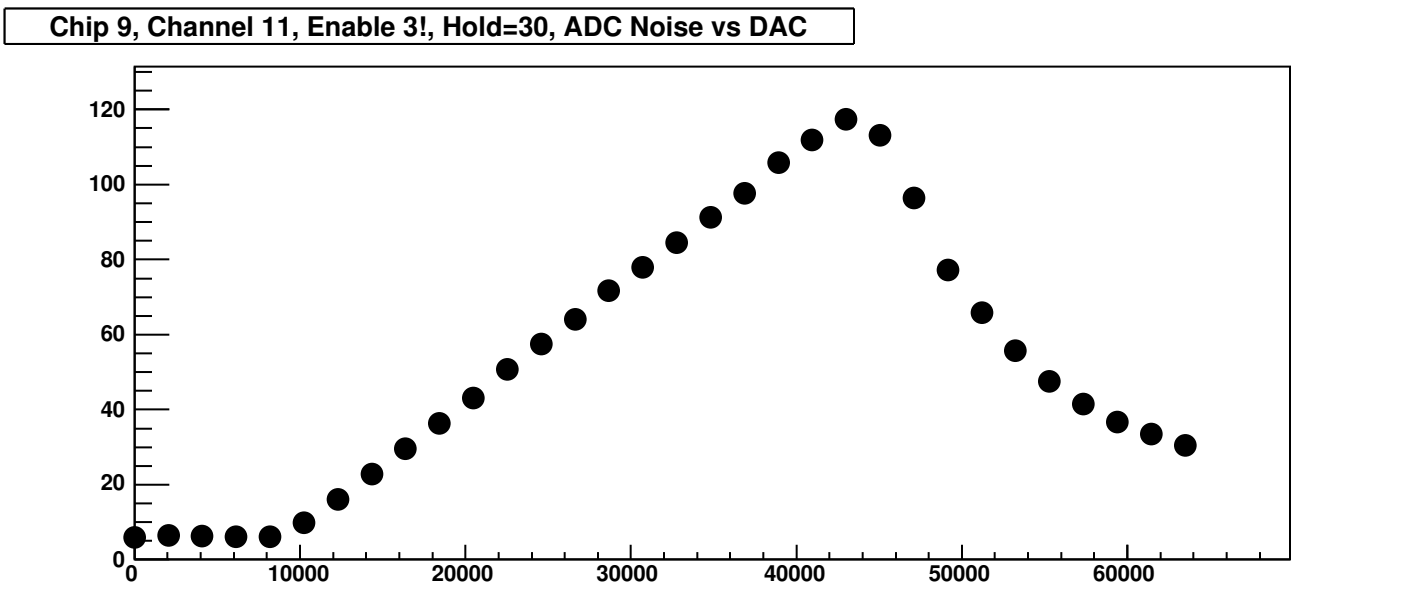
Chip 9, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC

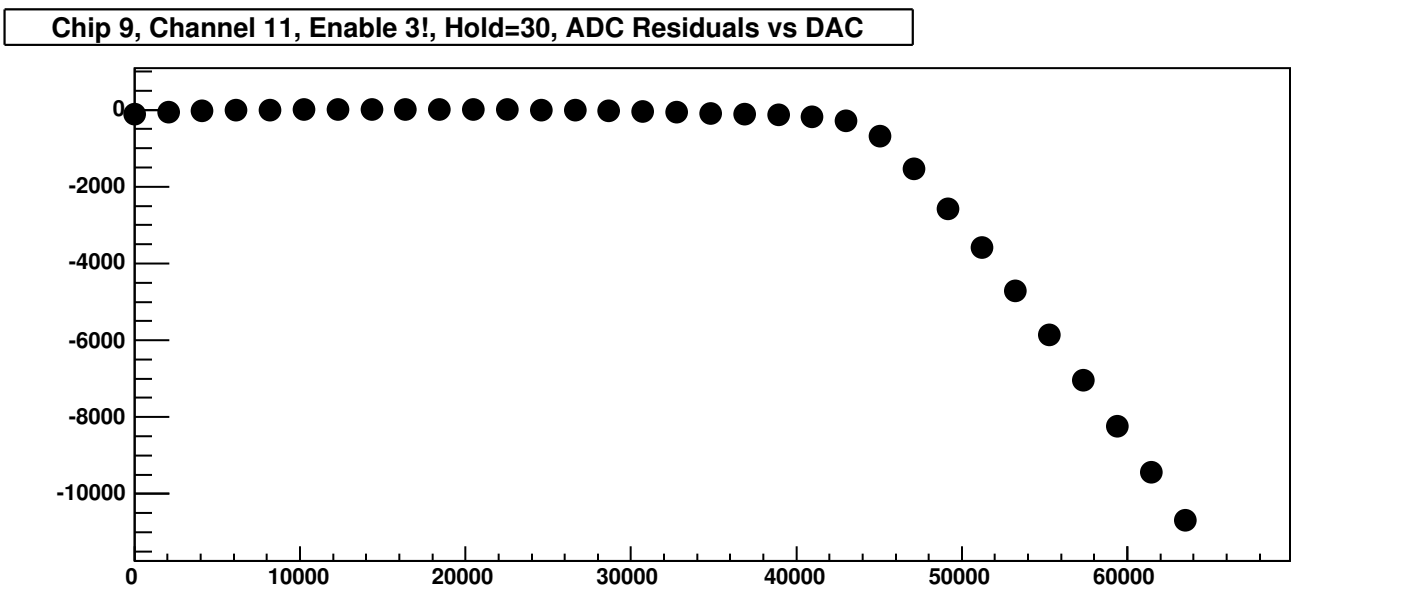


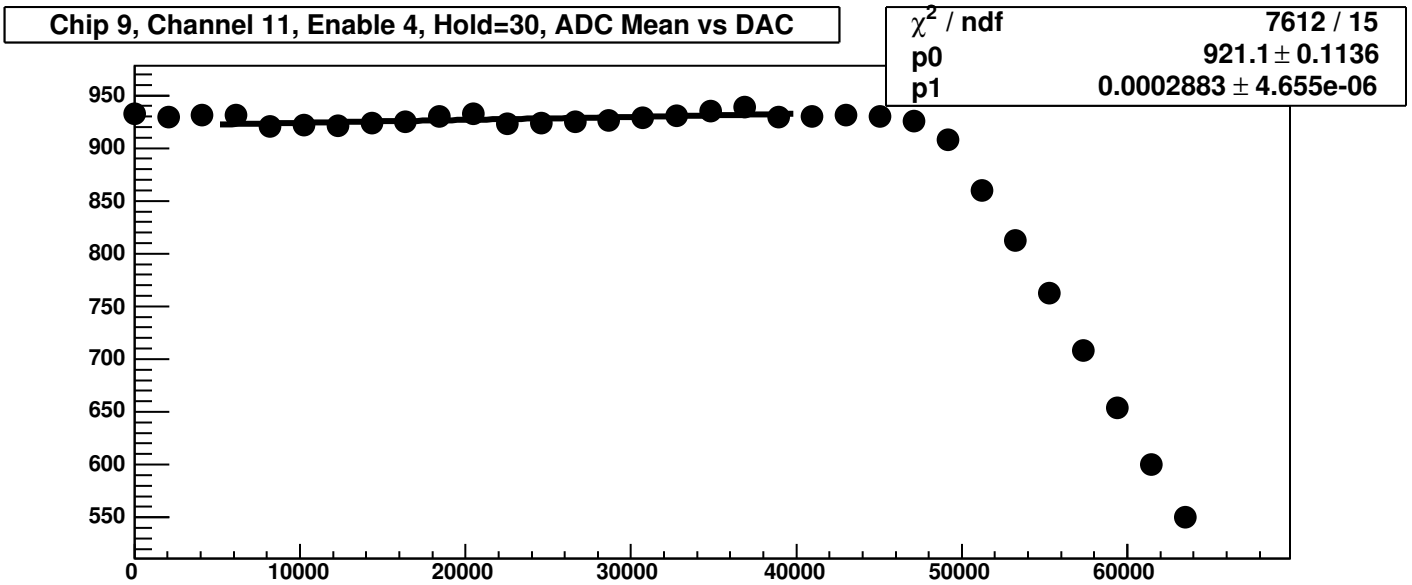
Chip 9, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC

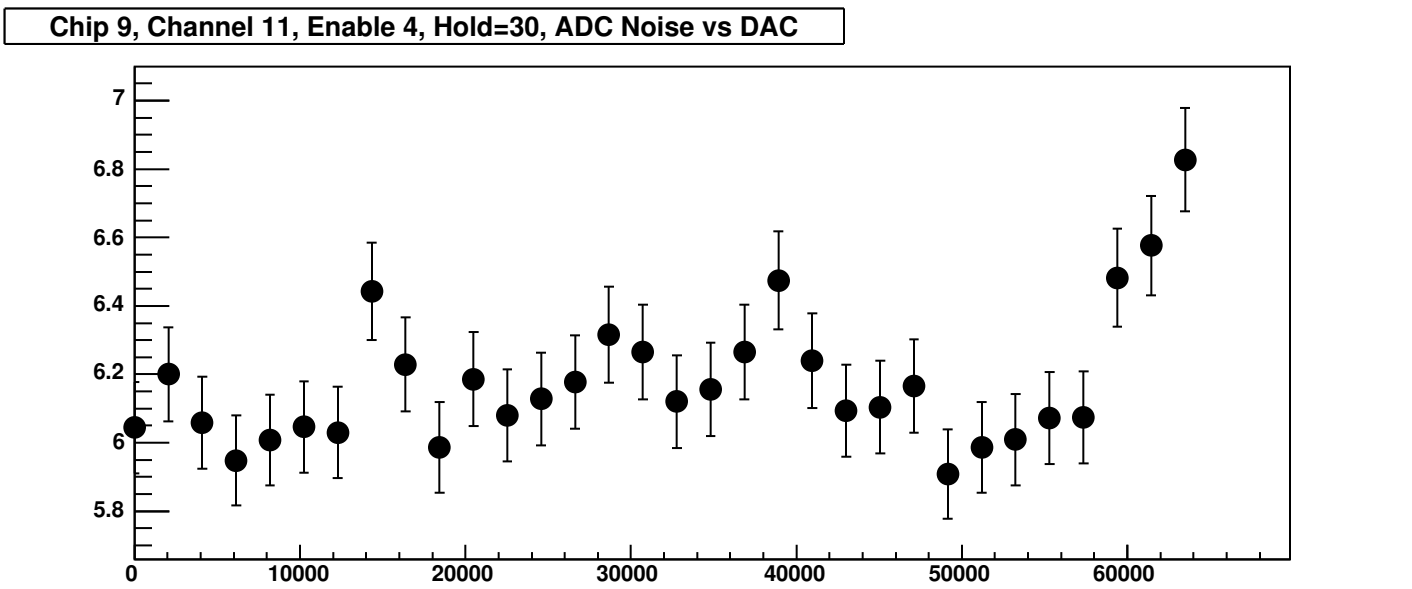


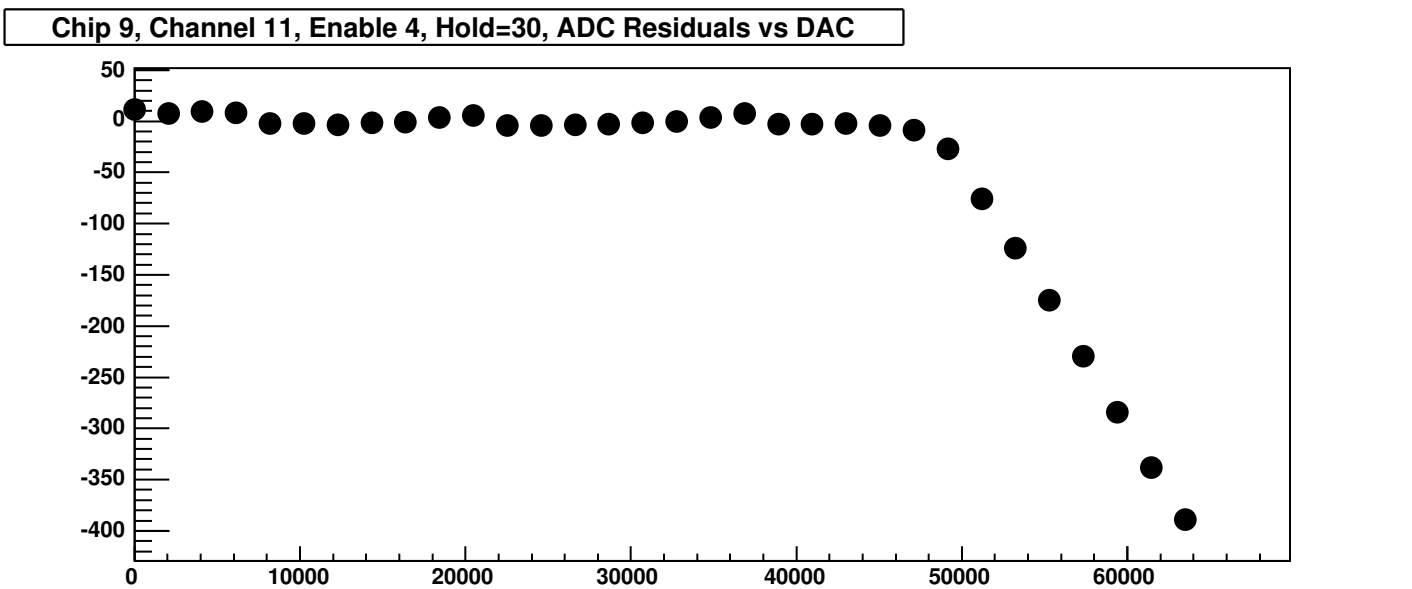

Chip 9, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 9524 / 15
p0 1035 ± 0.2759
p1 0.606 ± 2.807e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

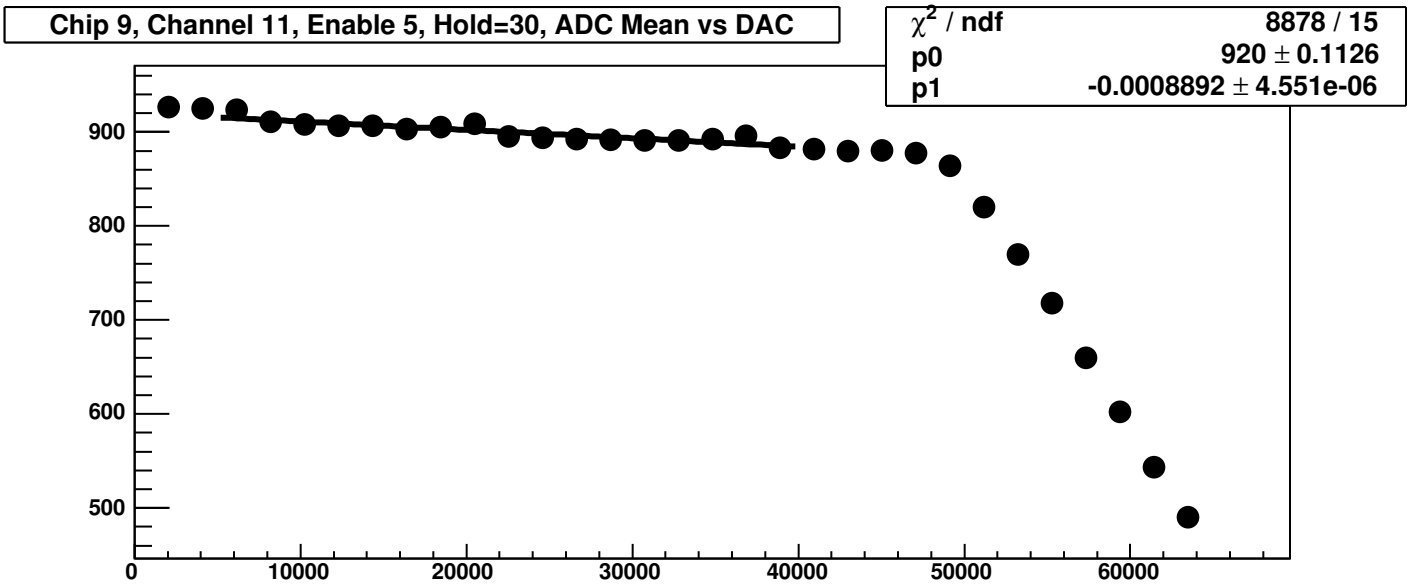

Chip 9, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

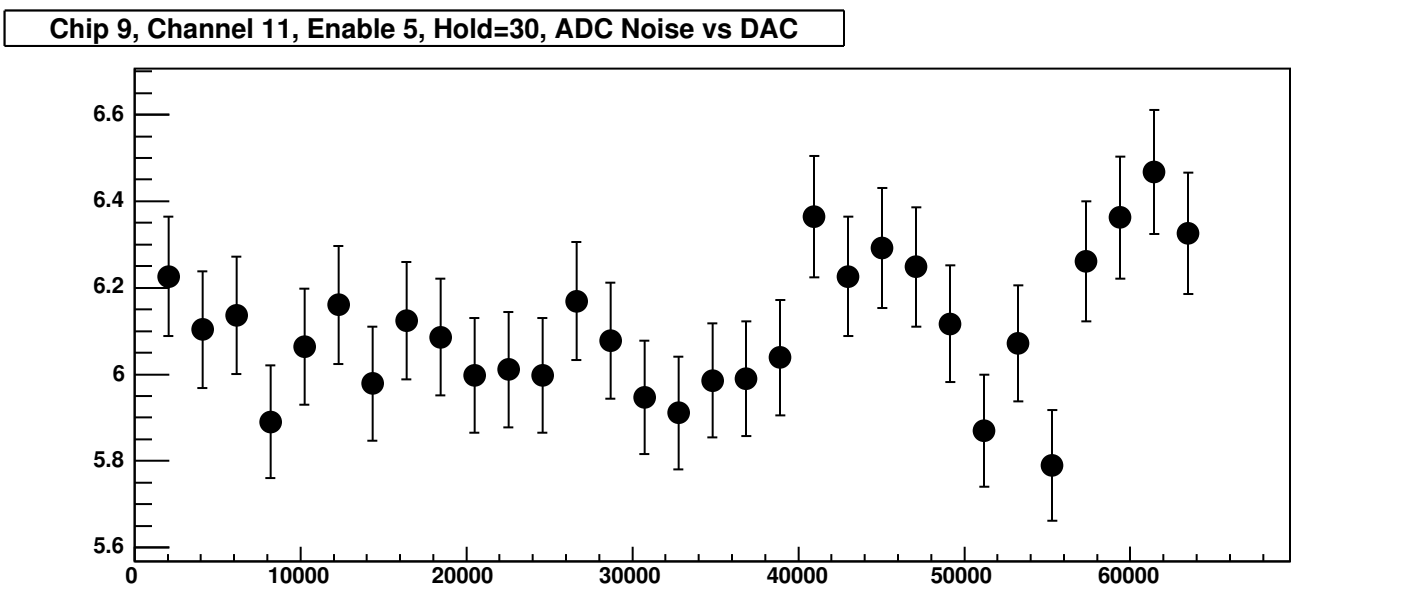

Chip 9, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

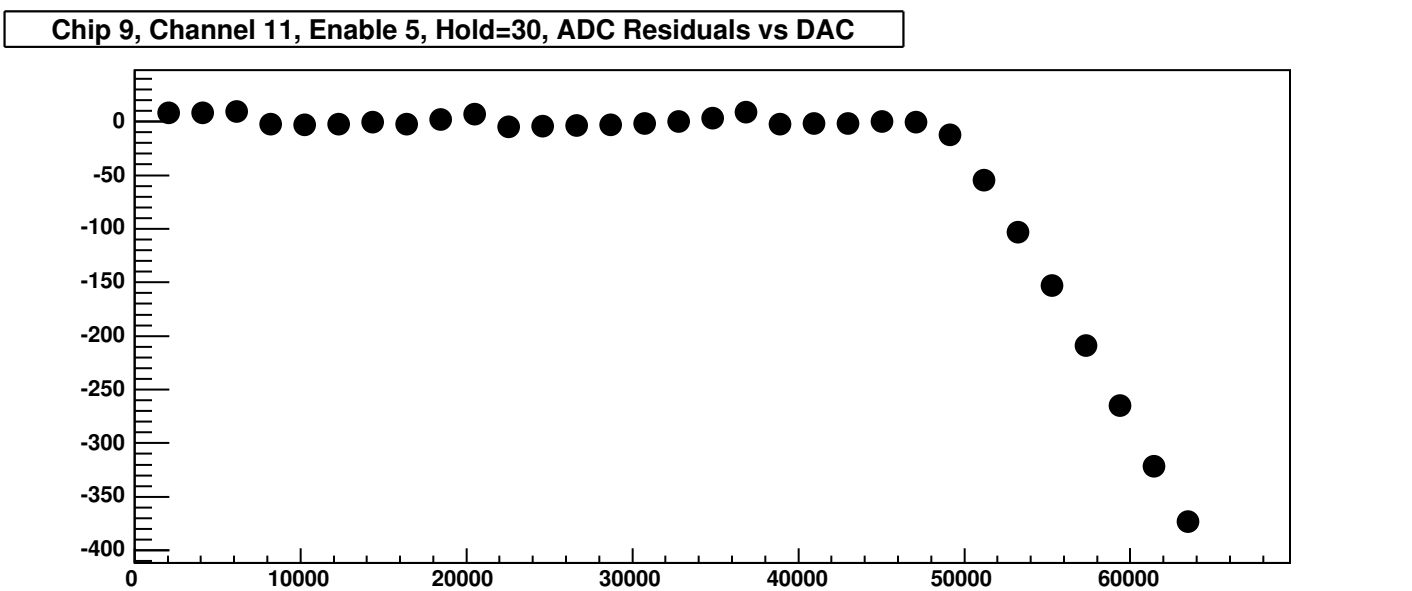

Chip 9, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7612 / 15
p0 921.1 ± 0.1136
p1 0.0002883 ± 4.655e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

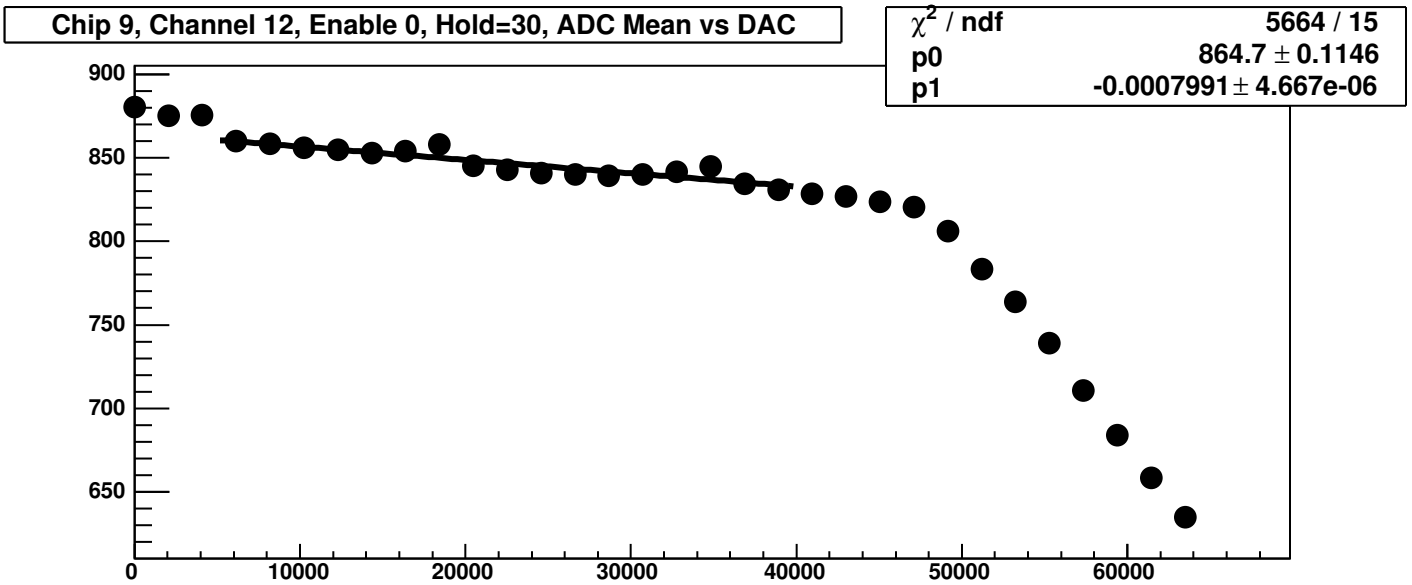

Chip 9, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

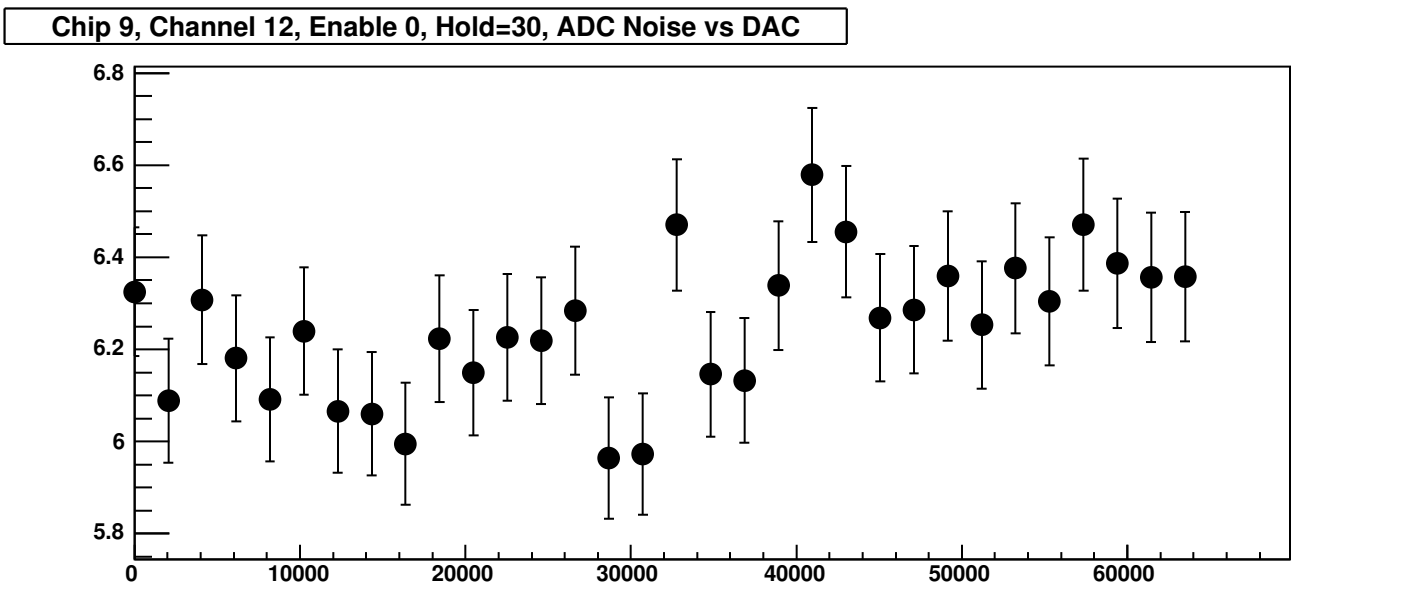

Chip 9, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

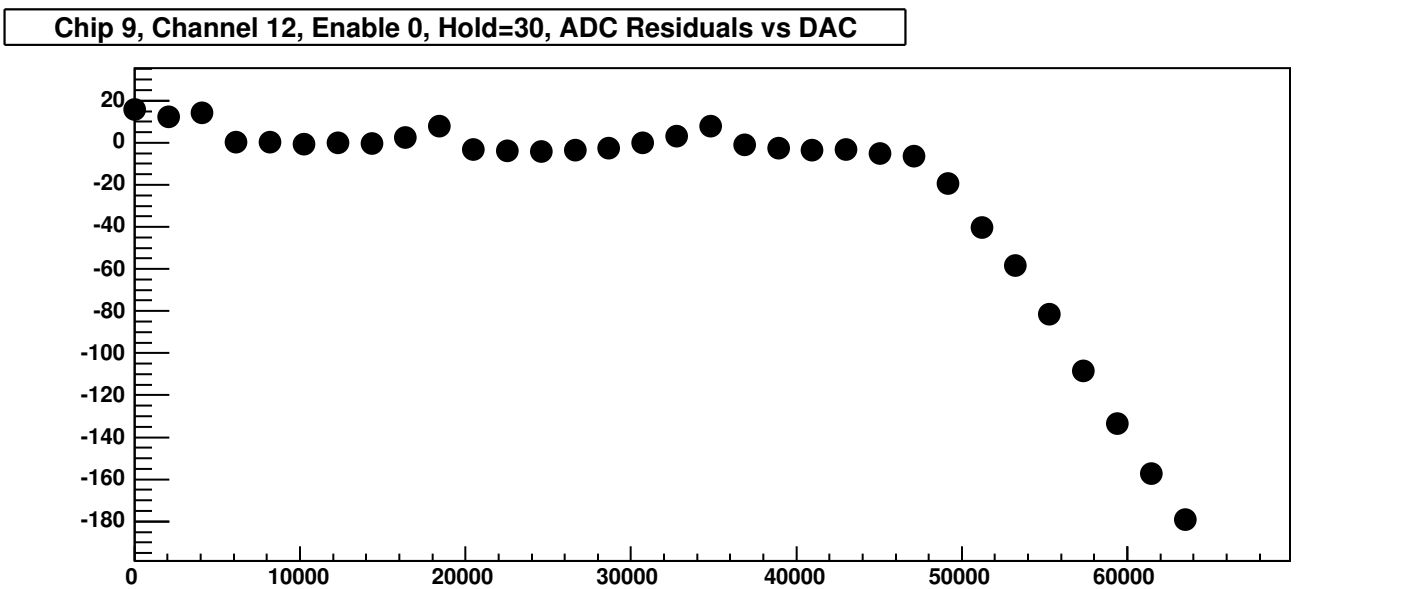

Chip 9, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8878 / 15
p0 920 ± 0.1126
p1 -0.0008892 ± 4.551e-06
900
800
700
600
500
0
10000
20000
30000
40000
50000
60000

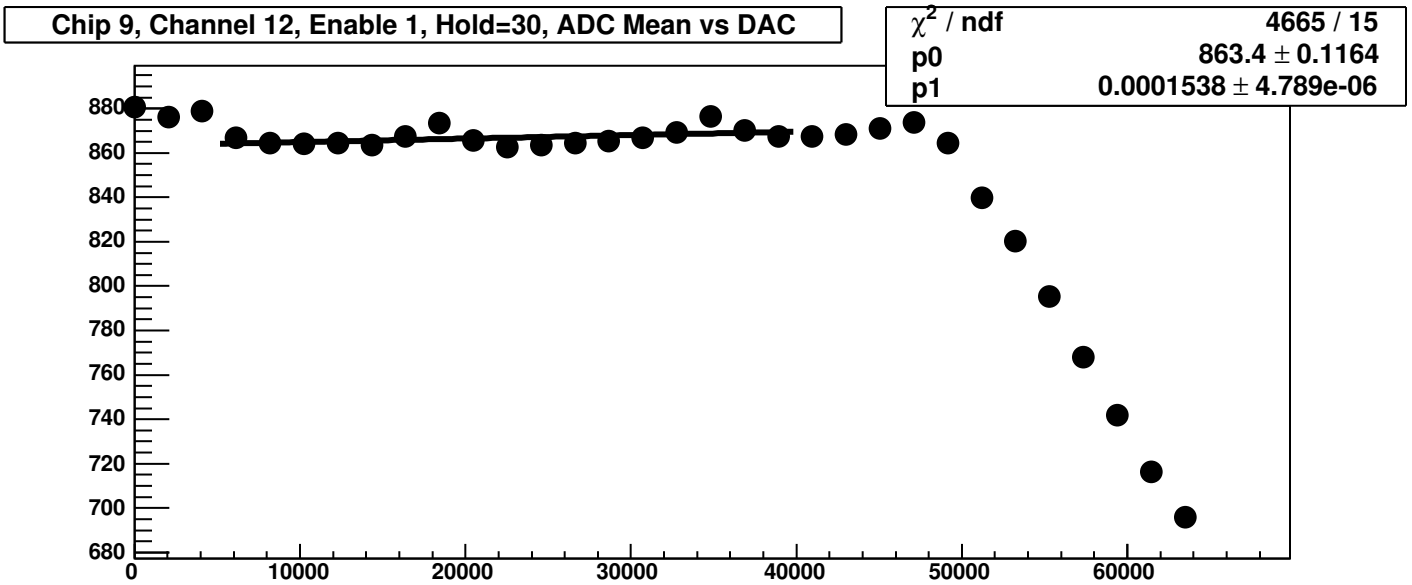

Chip 9, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

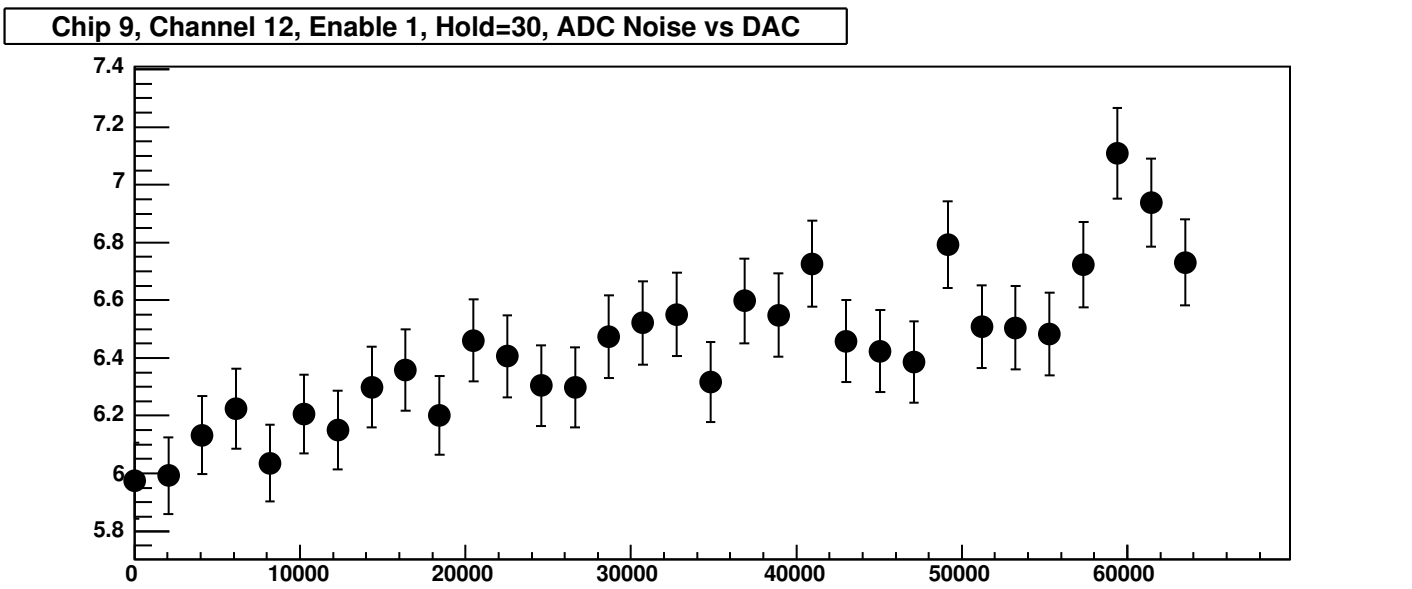

Chip 9, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

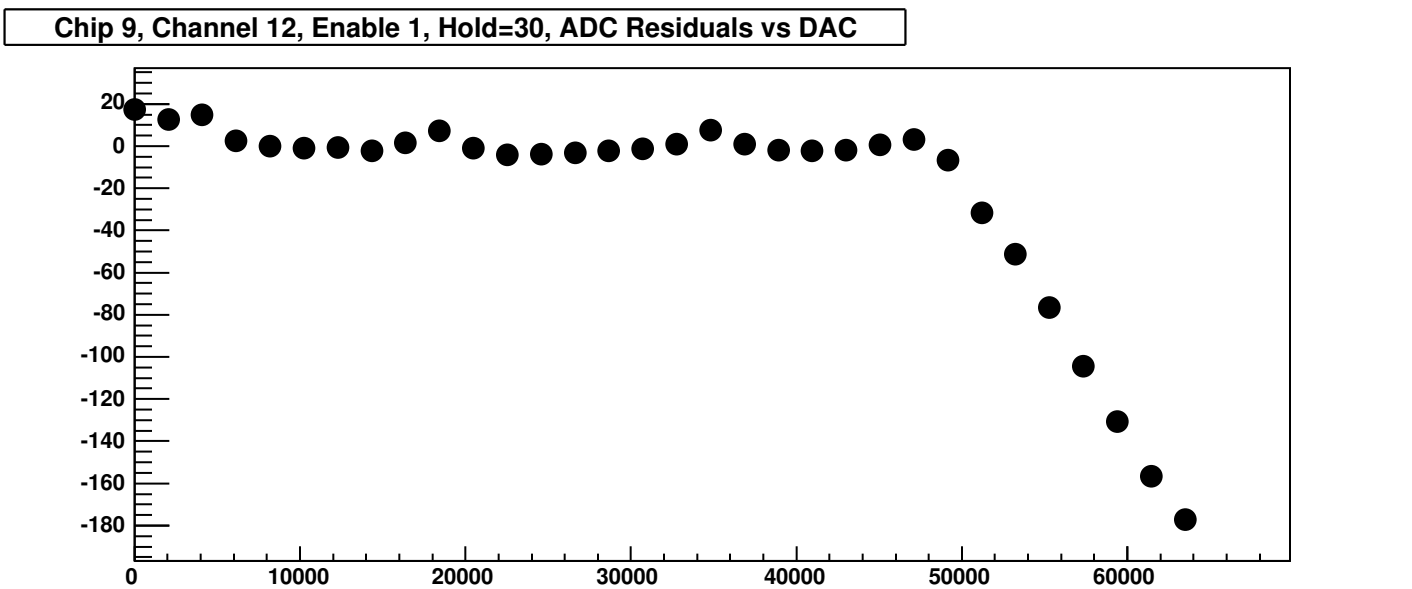

Chip 9, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5664 / 15
p0 864.7 ± 0.1146
p1 -0.0007991 ± 4.667e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

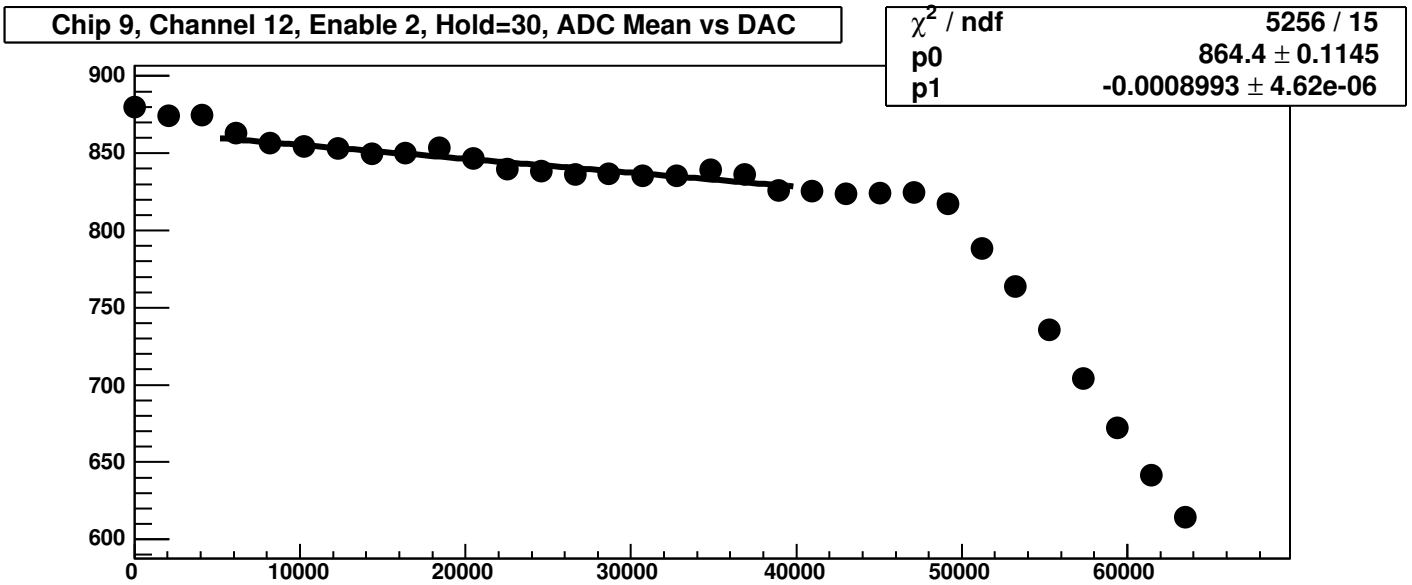

Chip 9, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

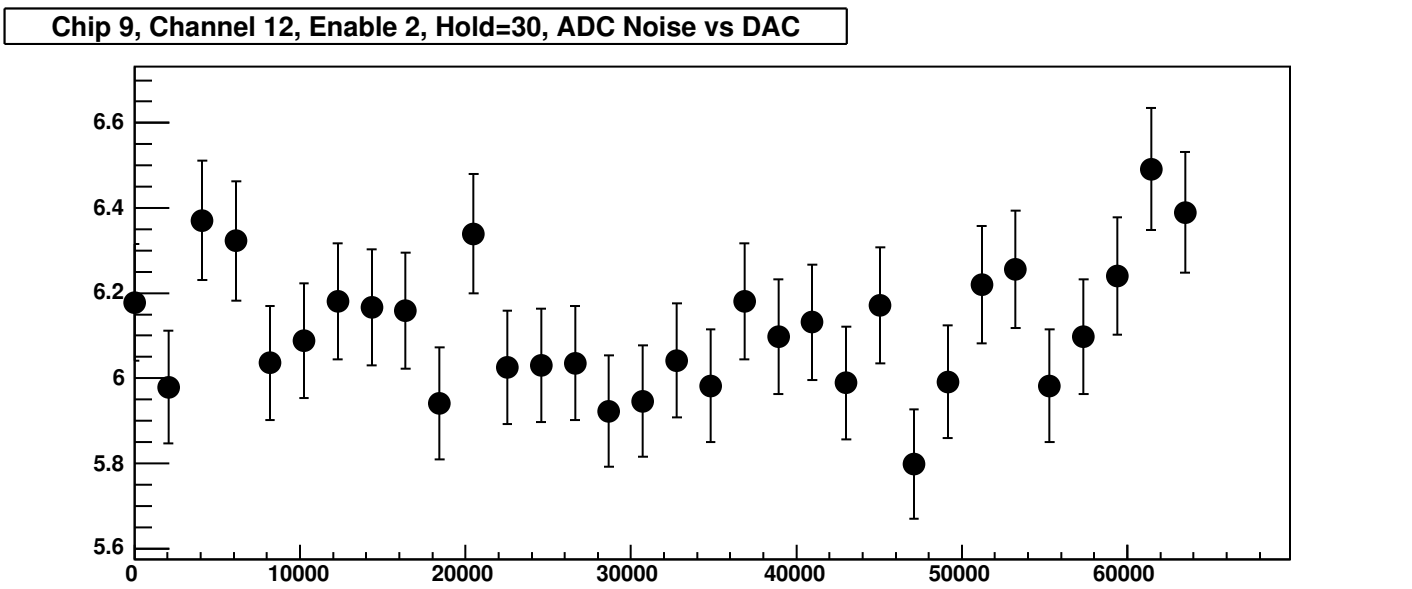

Chip 9, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

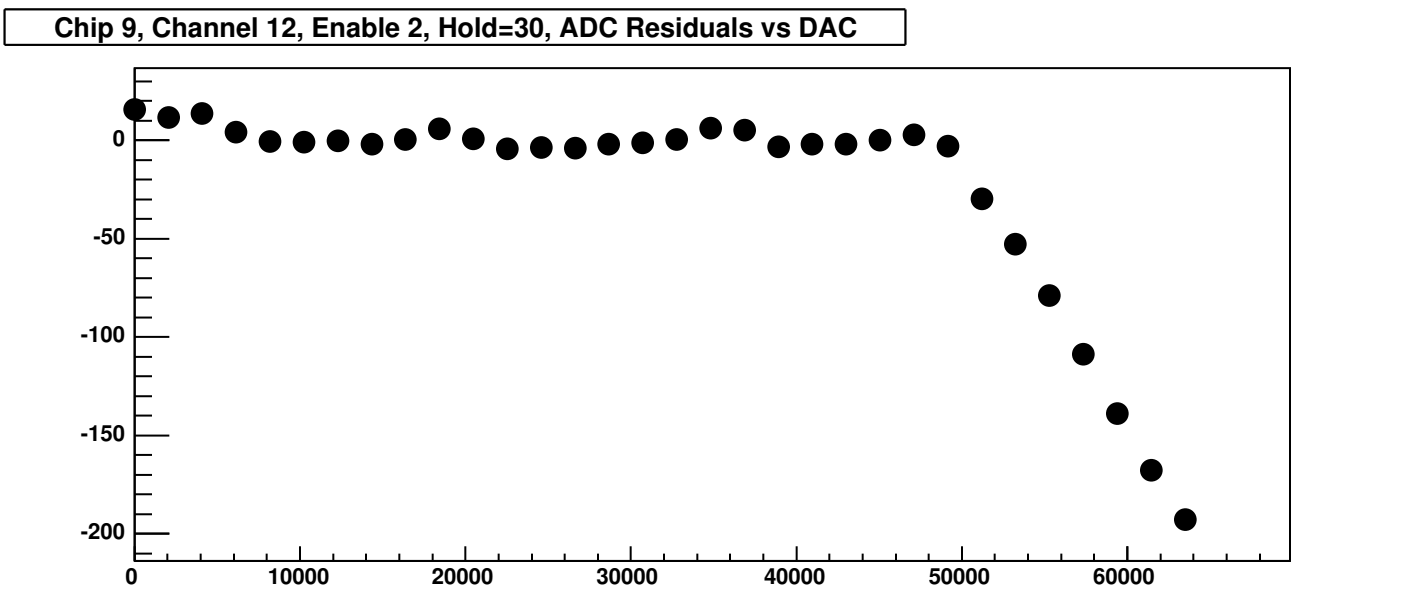

Chip 9, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4665 / 15
p0 863.4 ± 0.1164
p1 0.0001538 ± 4.789e-06
880
860
840
820
800
780
760
740
720
700
680
0
10000
20000
30000
40000
50000
60000

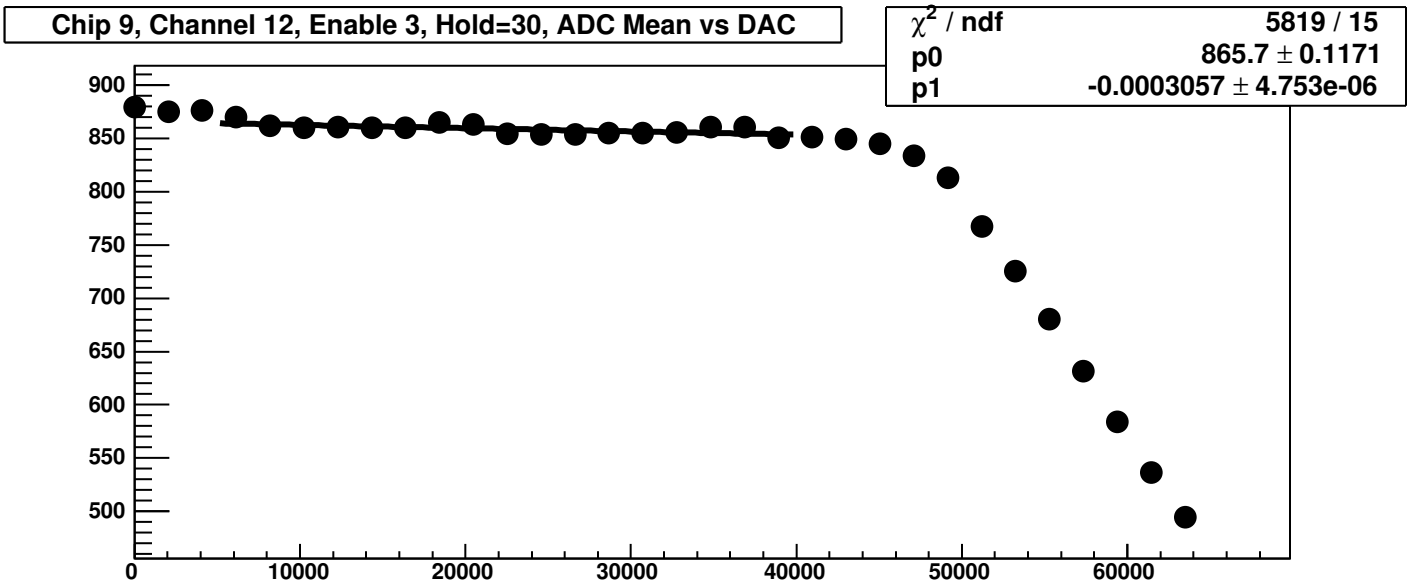

Chip 9, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

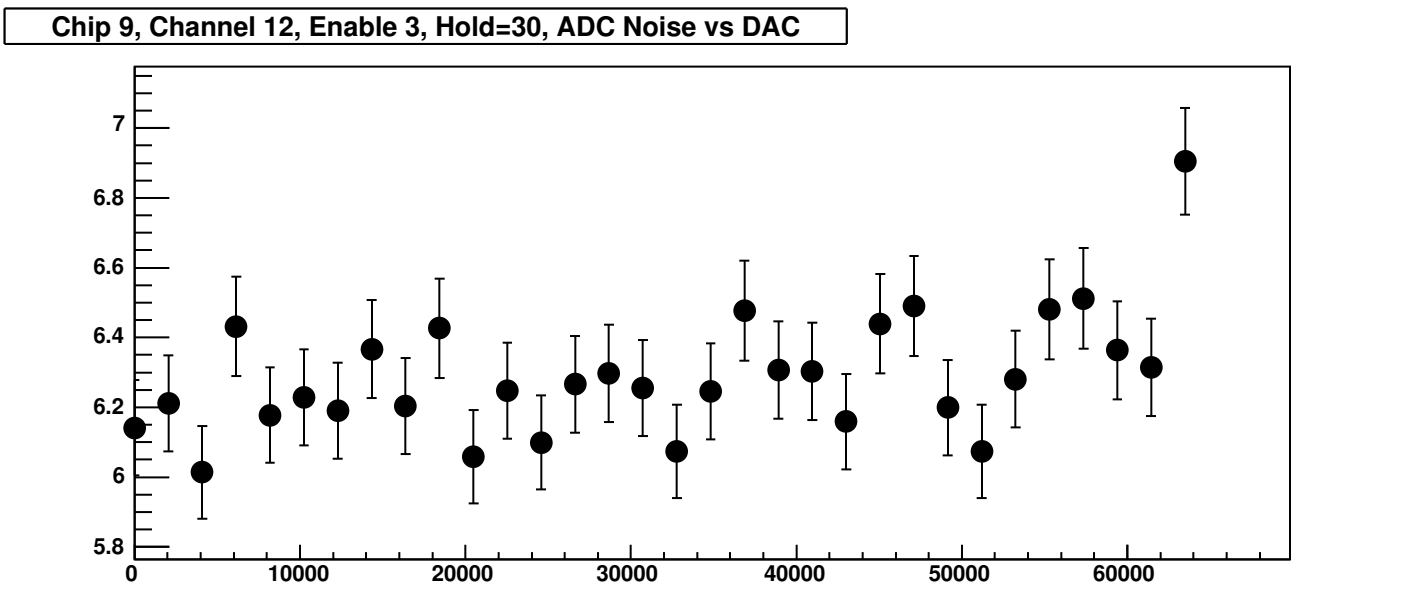

Chip 9, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

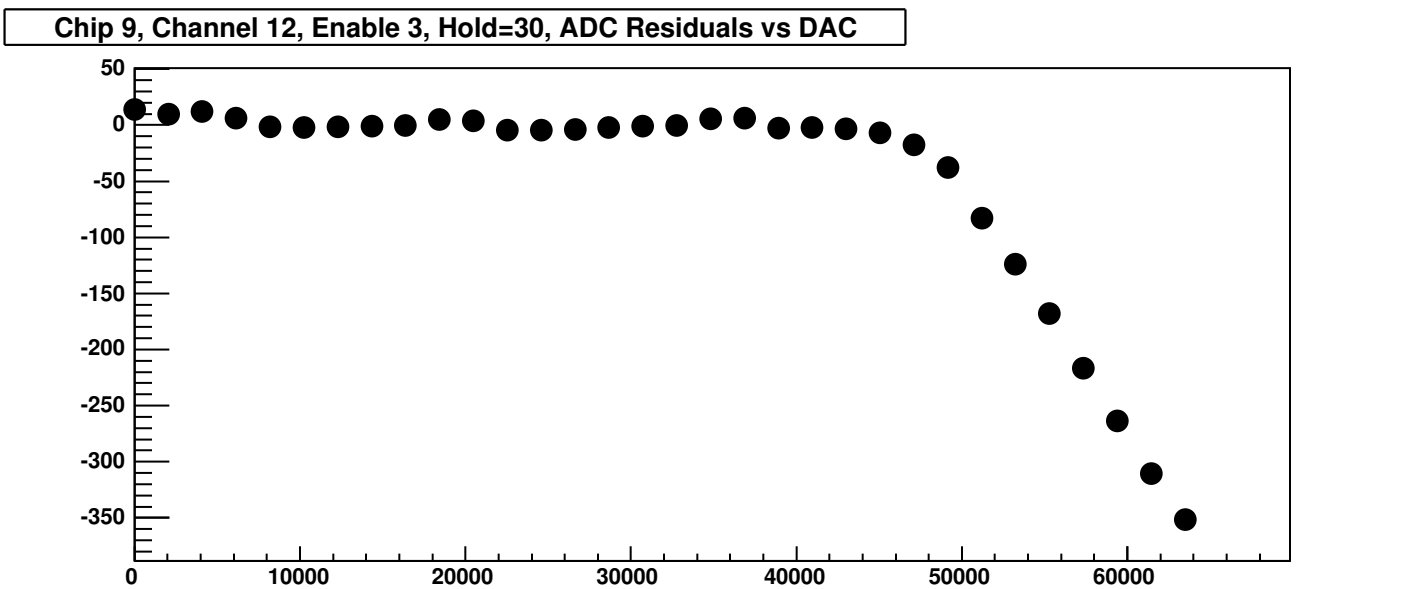

Chip 9, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5256 / 15
p0 864.4 ± 0.1145
p1 -0.0008993 ± 4.62e-06
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

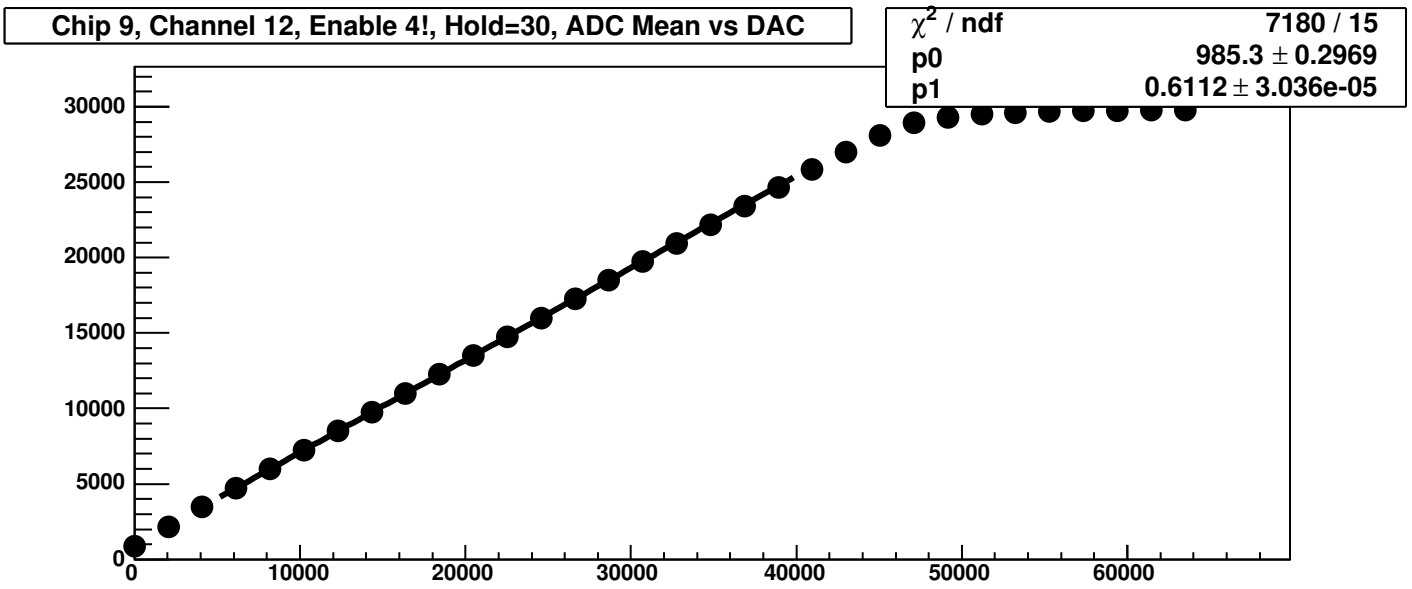

Chip 9, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

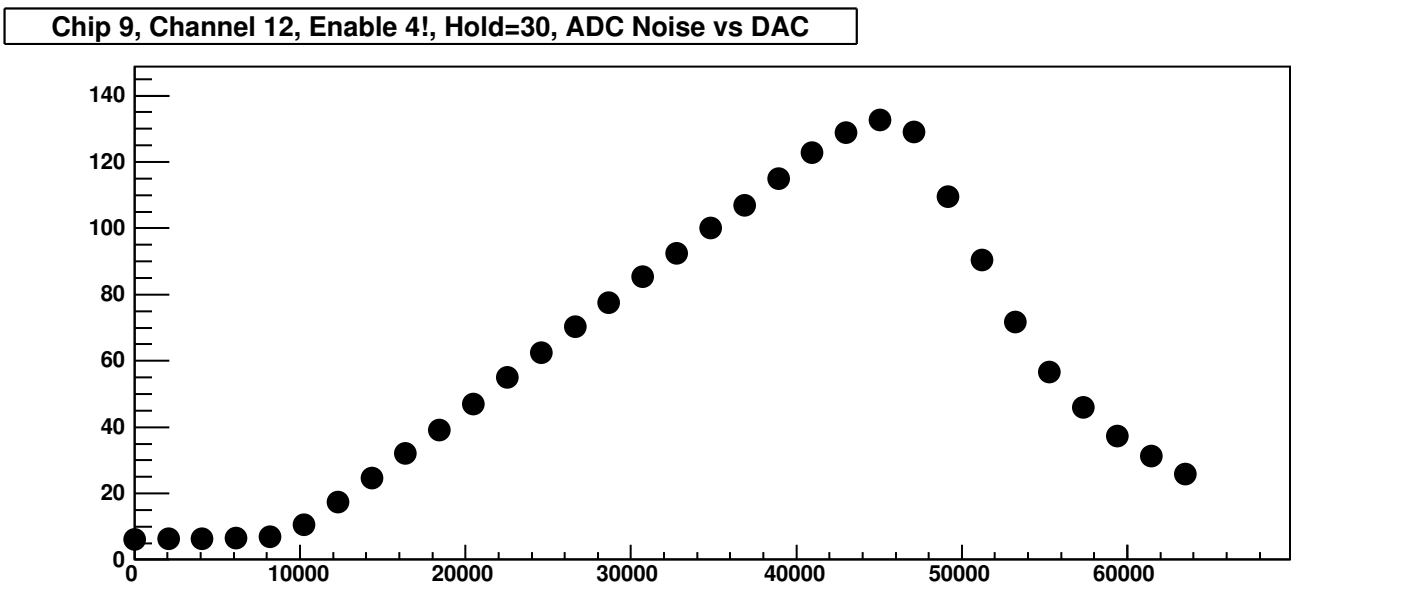

Chip 9, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

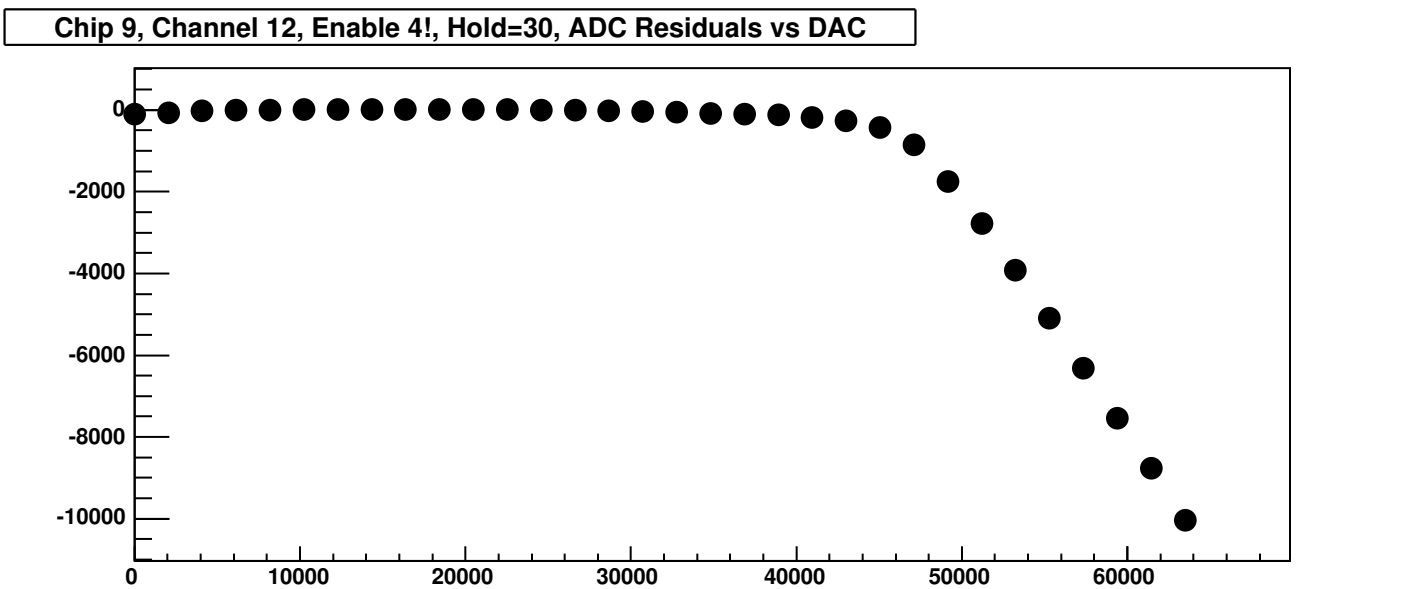

Chip 9, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5819 / 15
p0 865.7 ± 0.1171
p1 -0.0003057 ± 4.753e-06
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

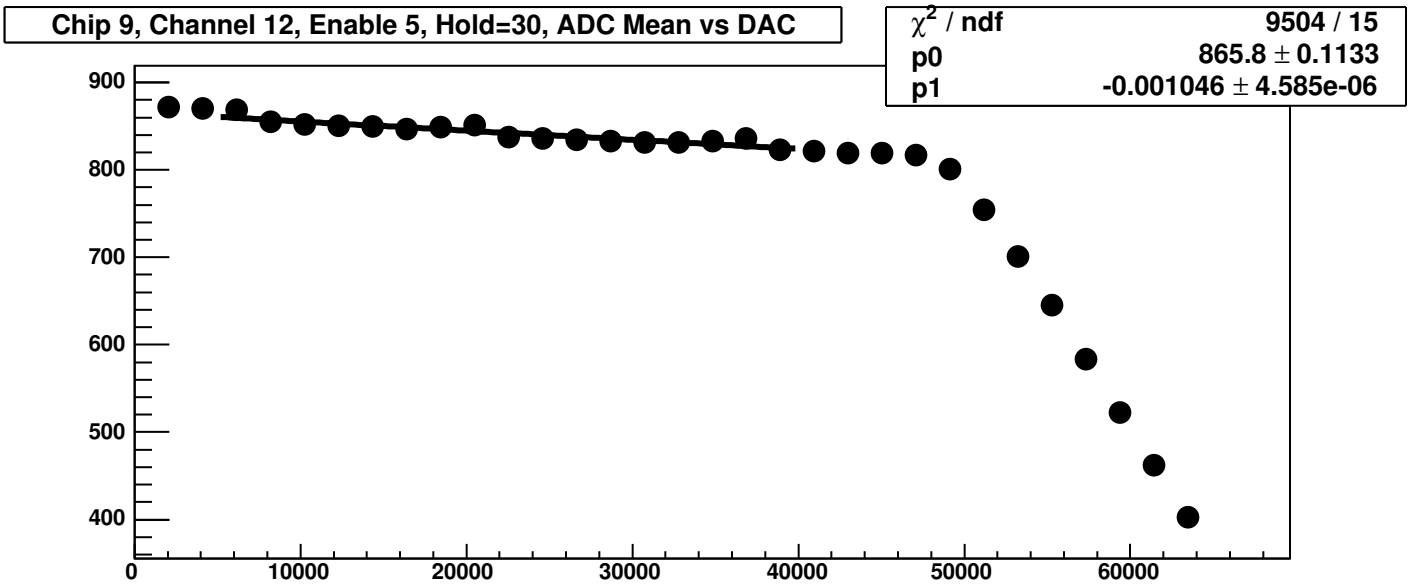

Chip 9, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

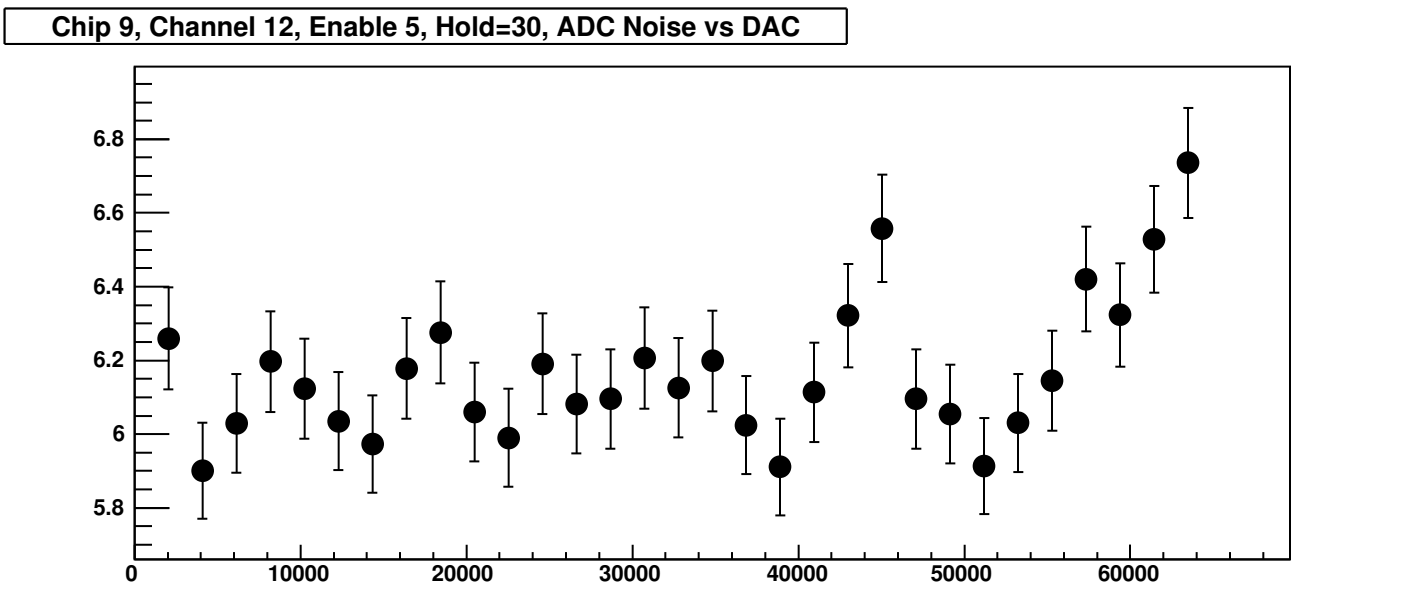

Chip 9, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

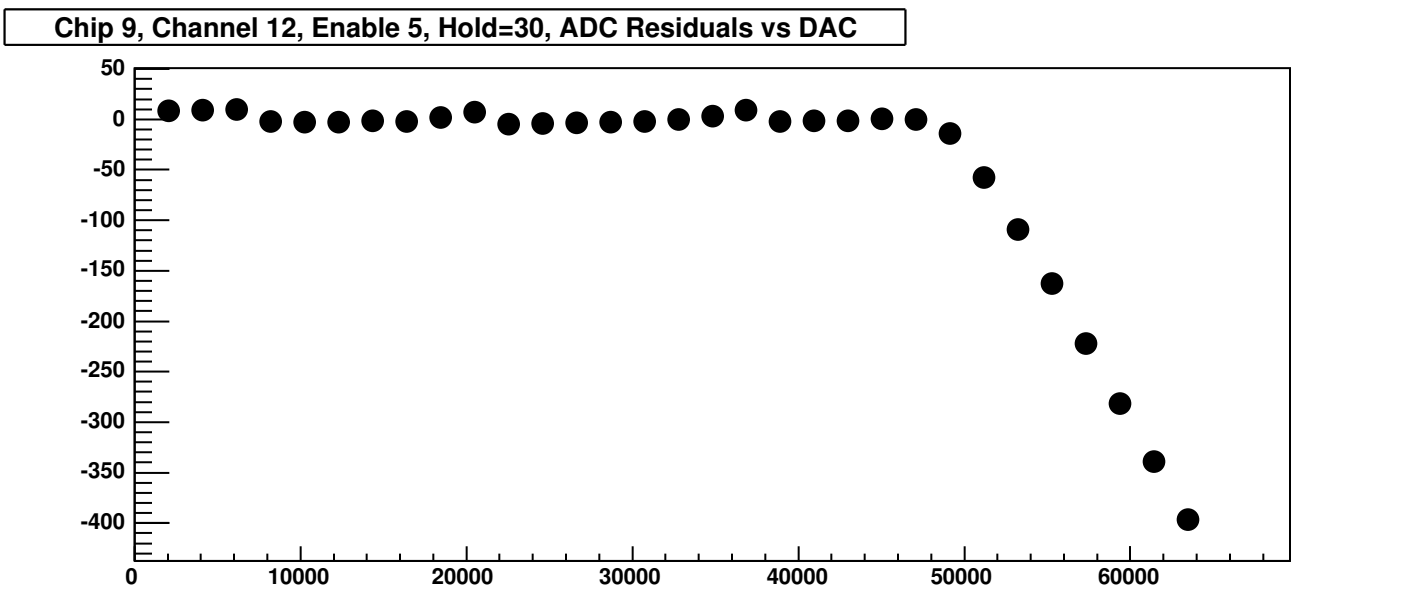

Chip 9, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 7180 / 15
p0 985.3 ± 0.2969
p1 0.6112 ± 3.036e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

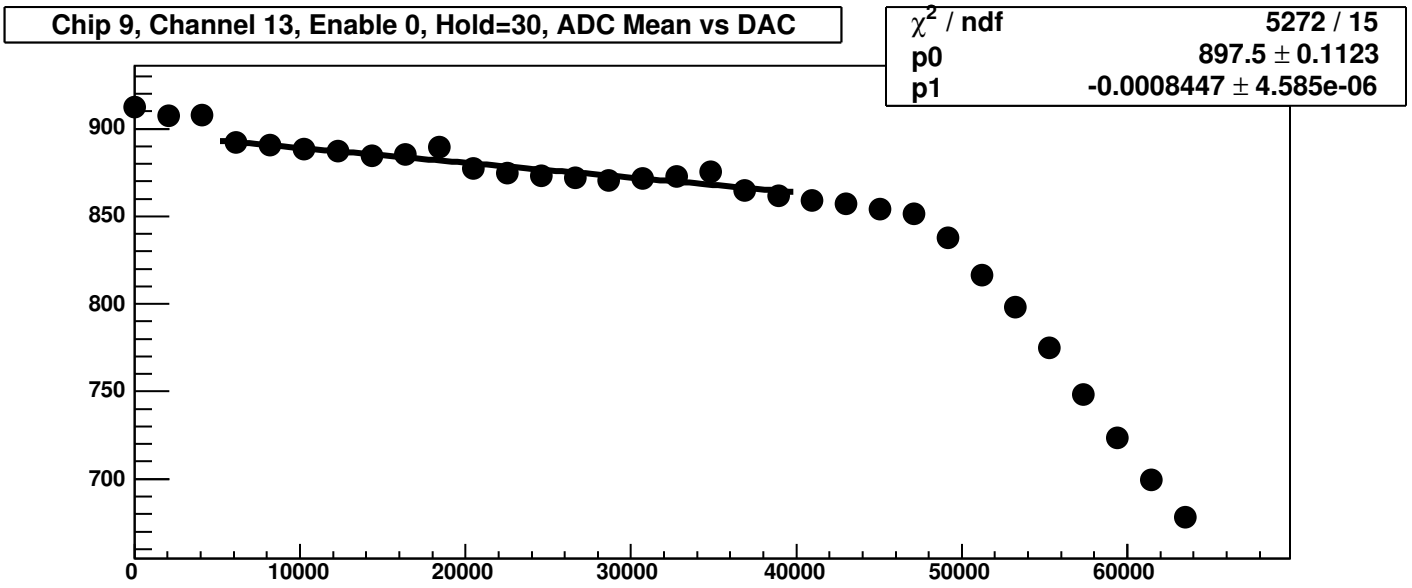

Chip 9, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

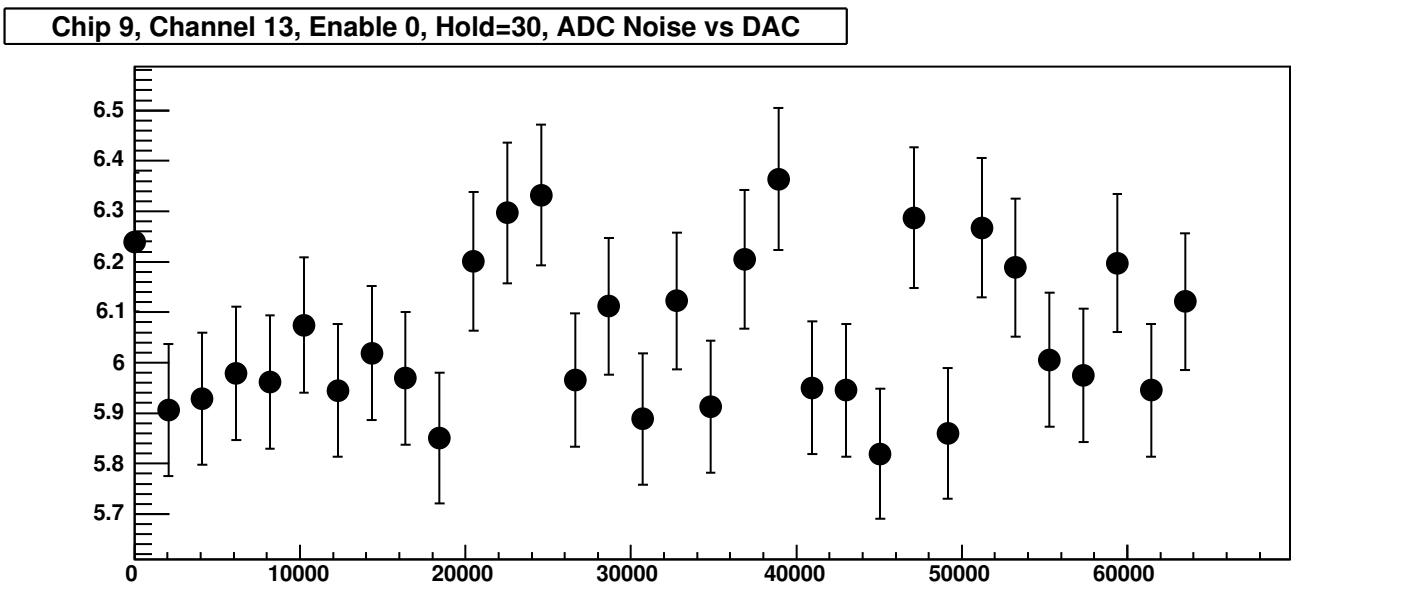

Chip 9, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

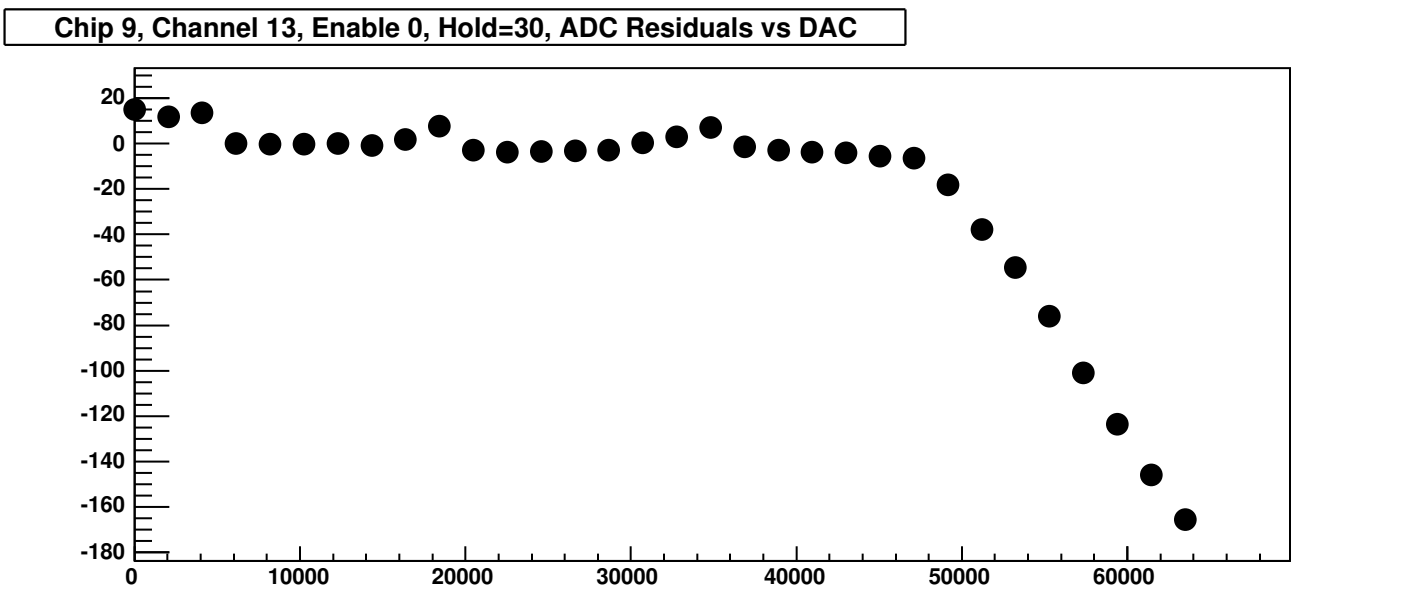

Chip 9, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9504 / 15
p0 865.8 ± 0.1133
p1 -0.001046 ± 4.585e-06
900
800
700
600
500
400
0
10000
20000
30000
40000
50000
60000

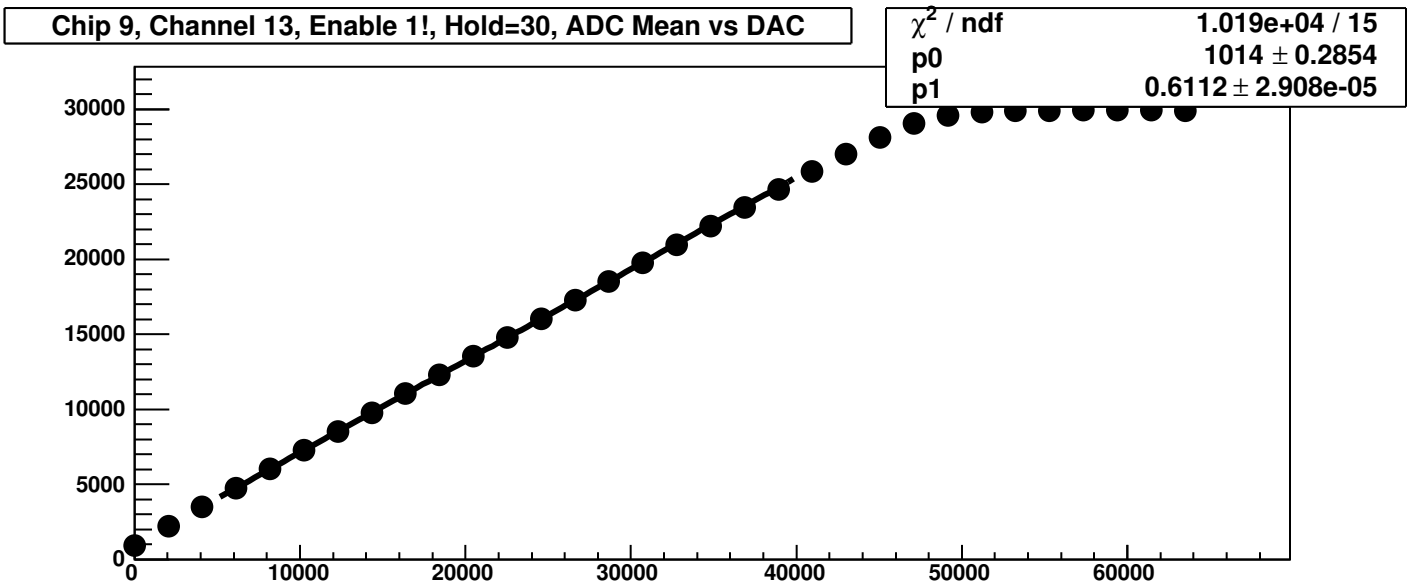

Chip 9, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

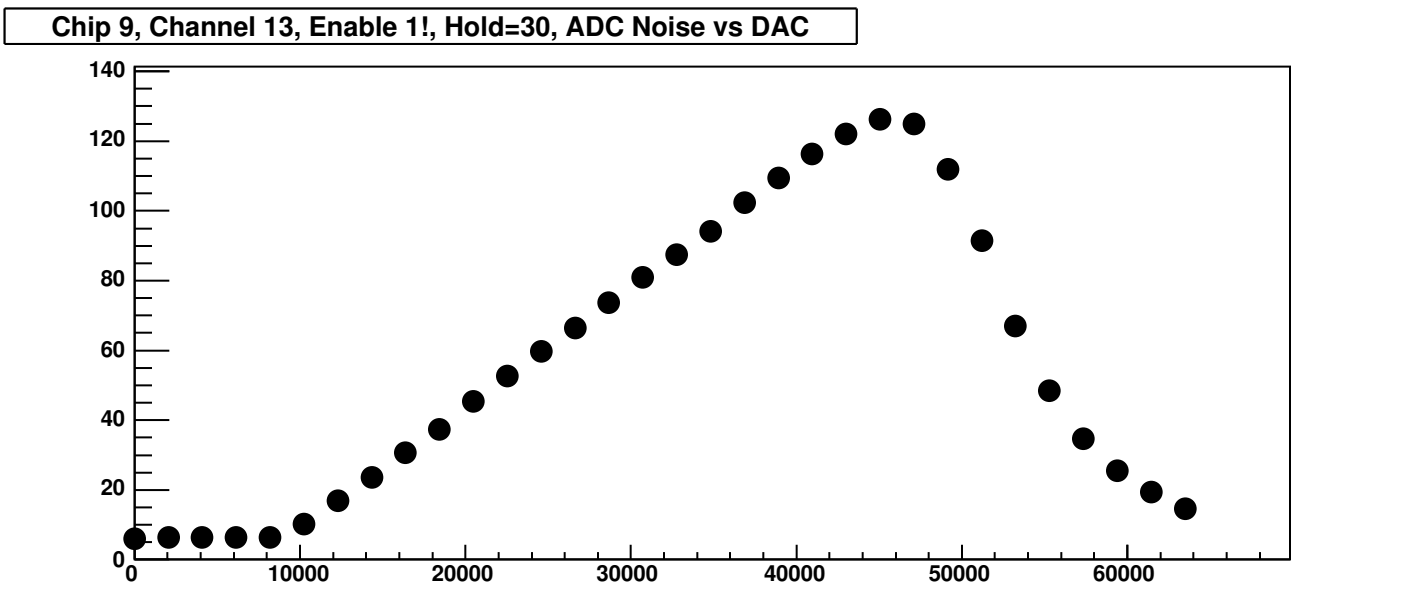

Chip 9, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

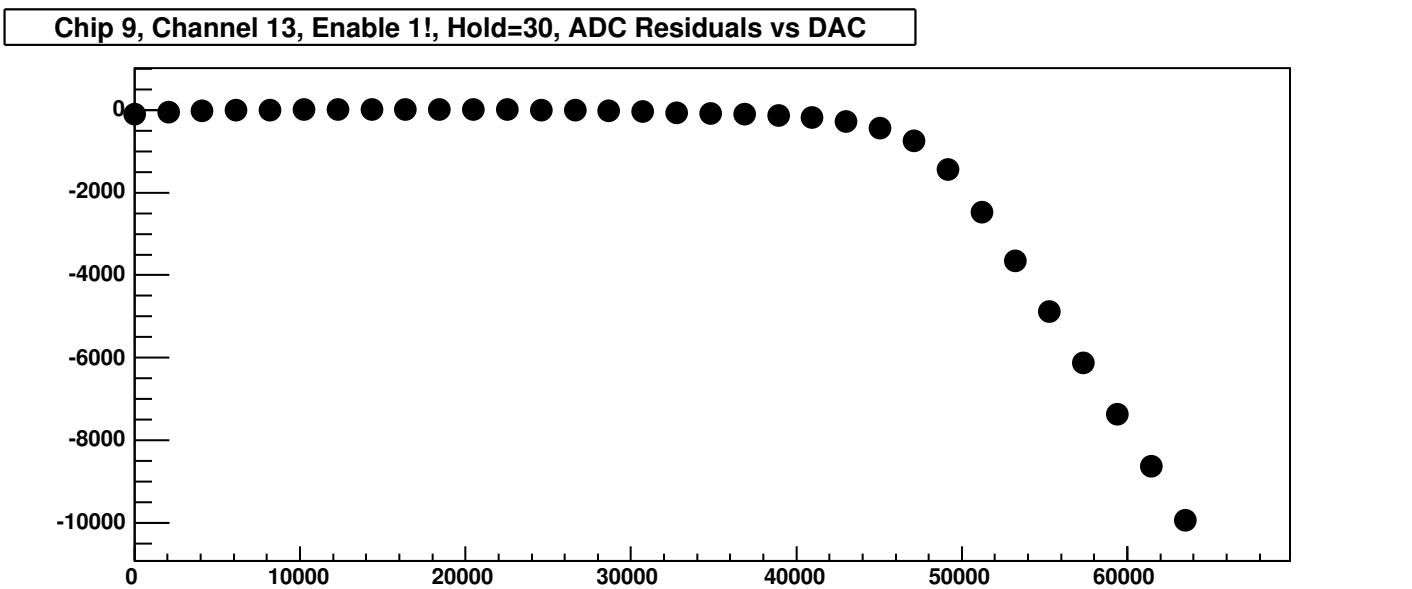

Chip 9, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5272 / 15
p0 897.5 ± 0.1123
p1 -0.0008447 ± 4.585e-06
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
0
10000
20000
30000
40000
50000
60000

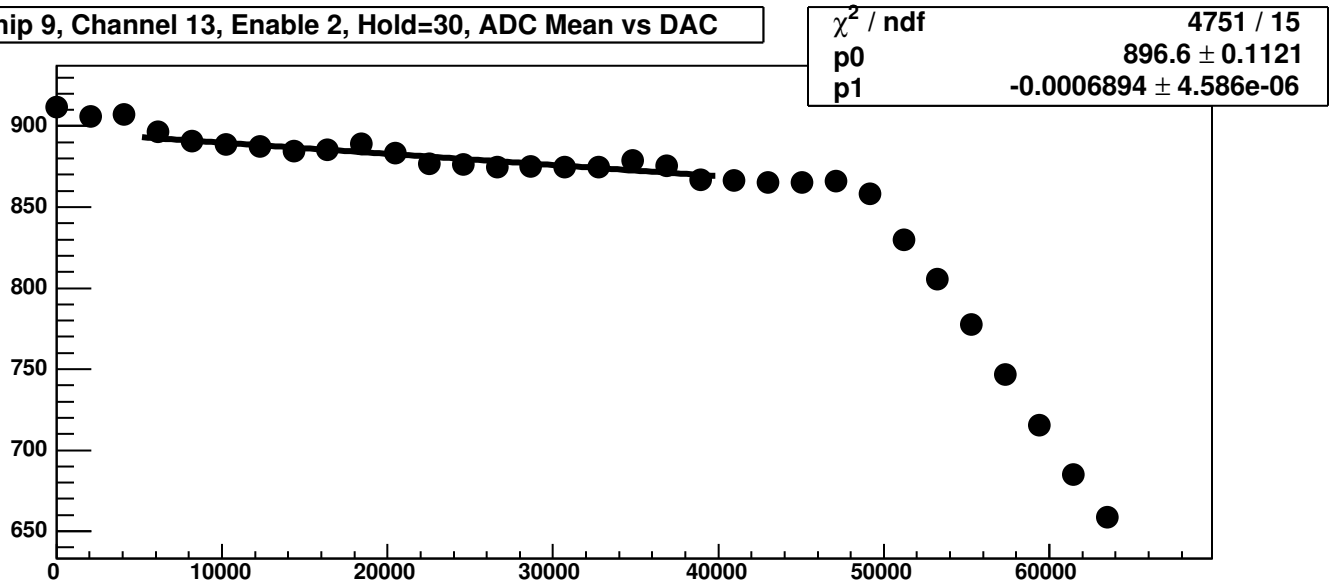

Chip 9, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.019e+04 / 15
p0 1014 ± 0.2854
p1 0.6112 ± 2.908e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

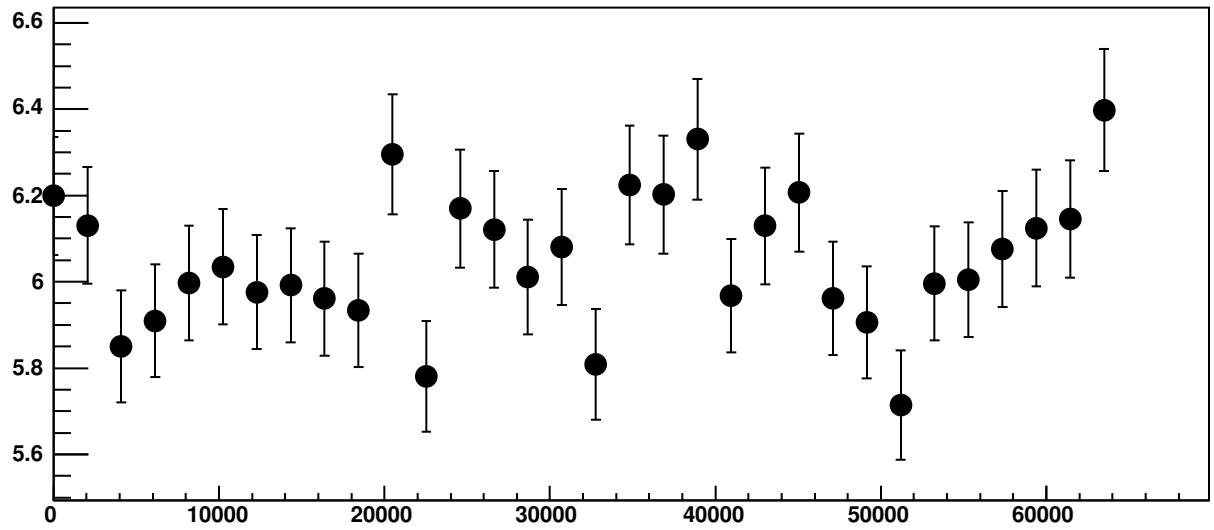

Chip 9, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

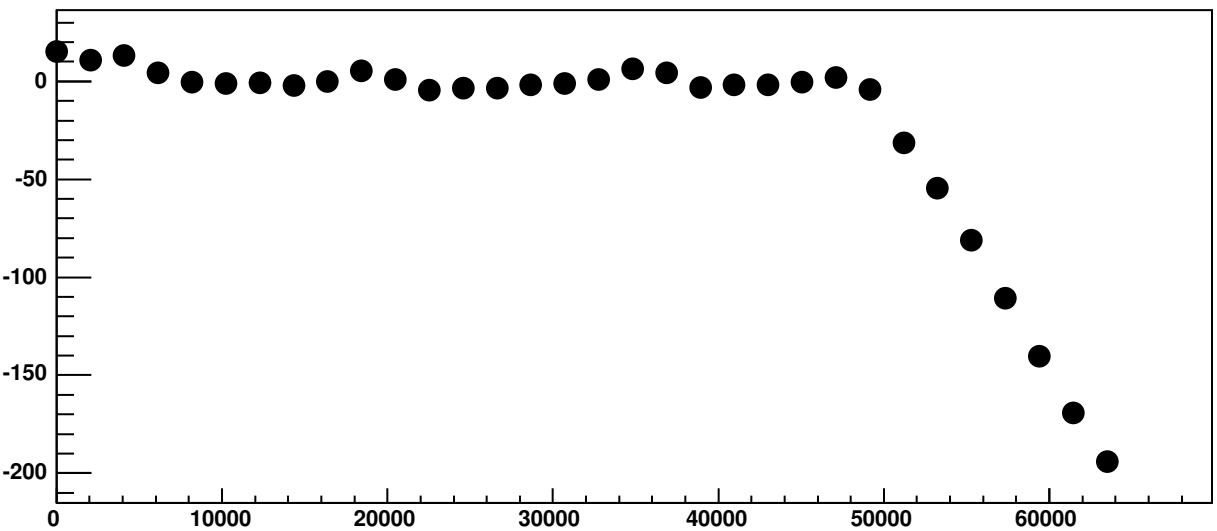
Chip 9, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC

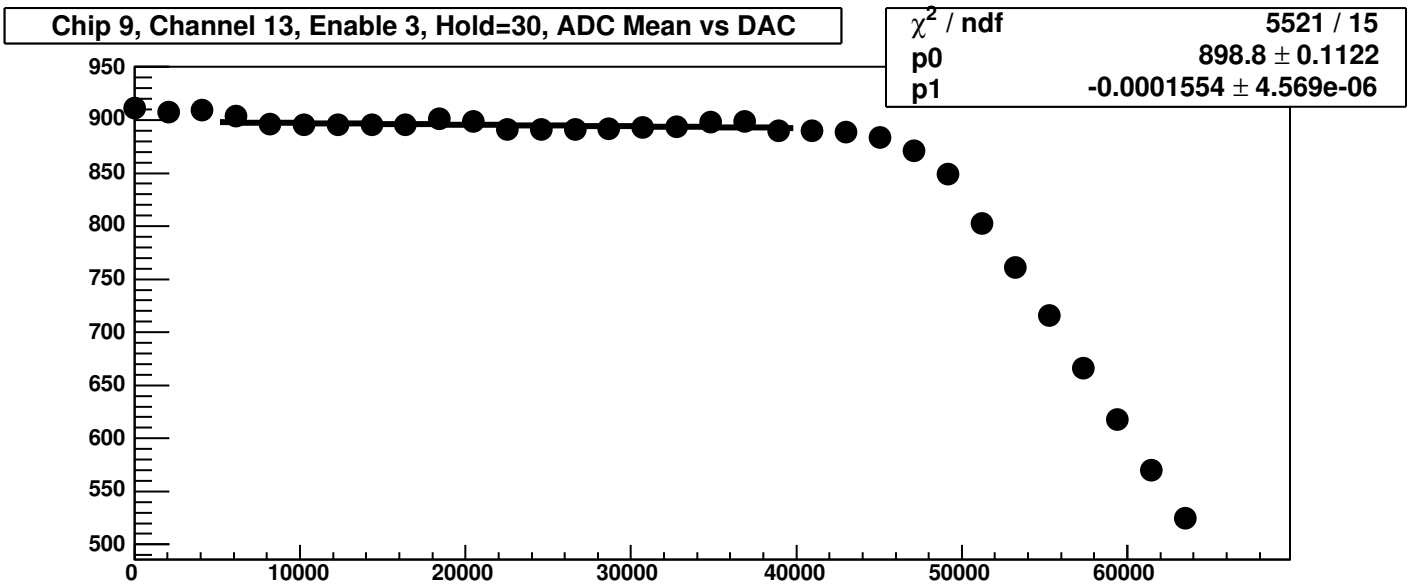


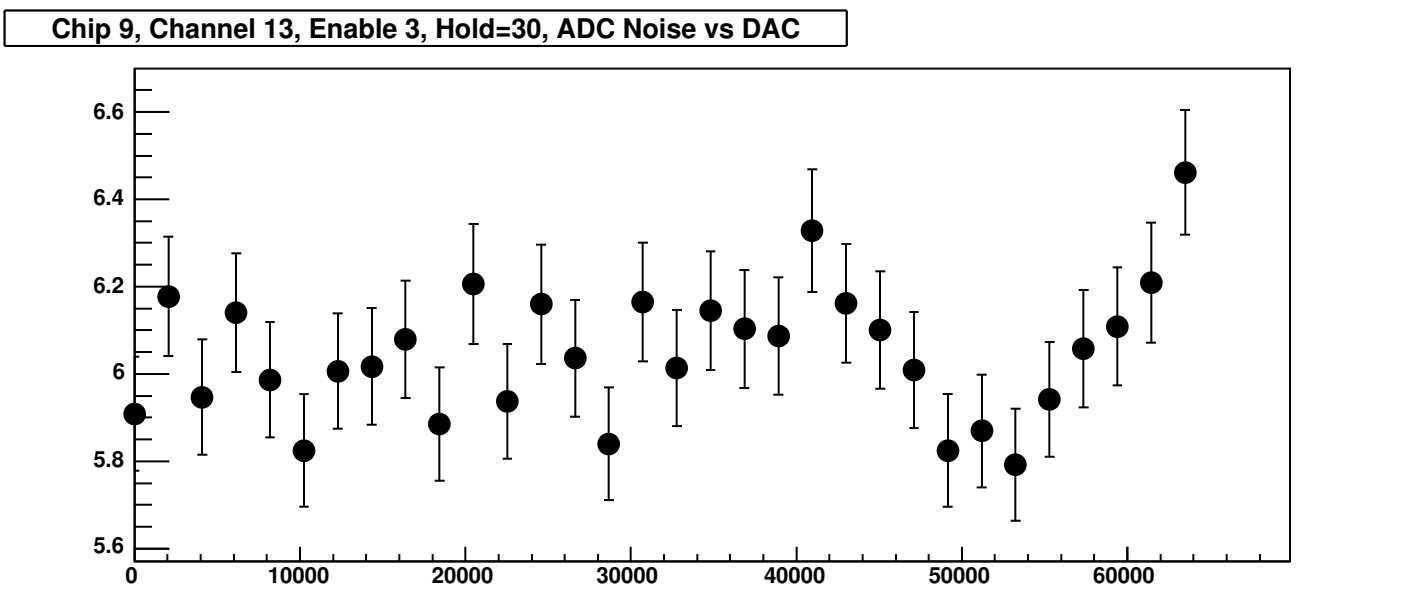
Chip 9, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC

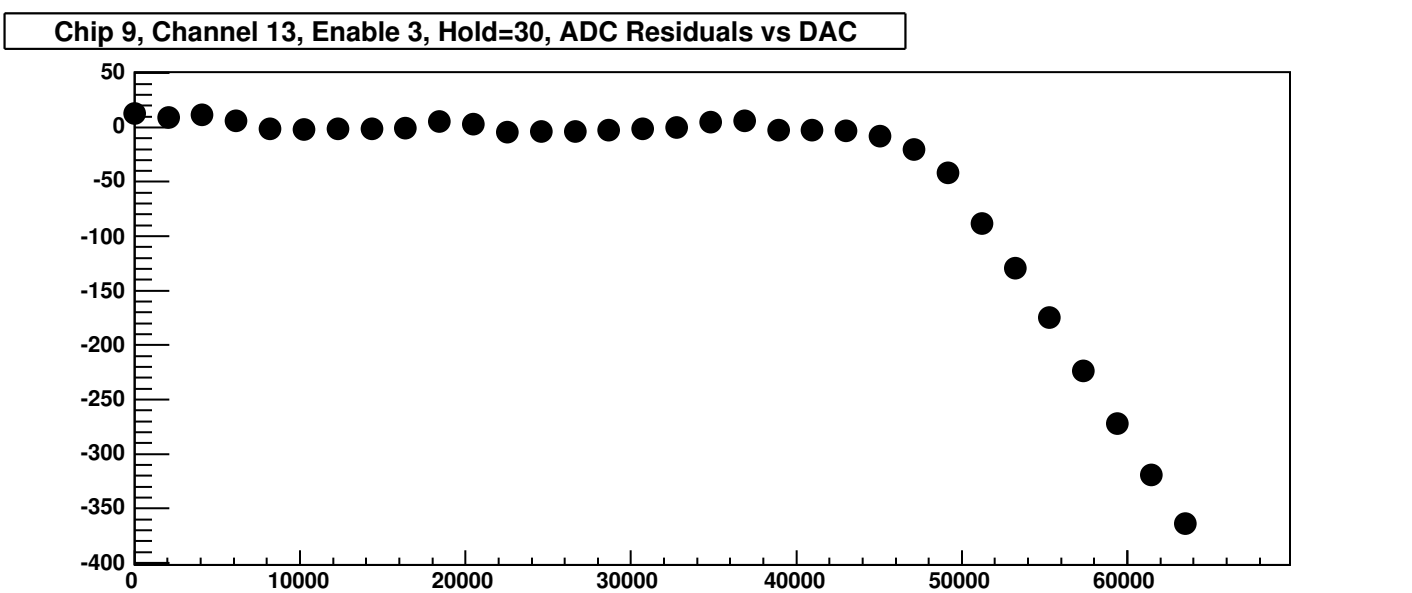


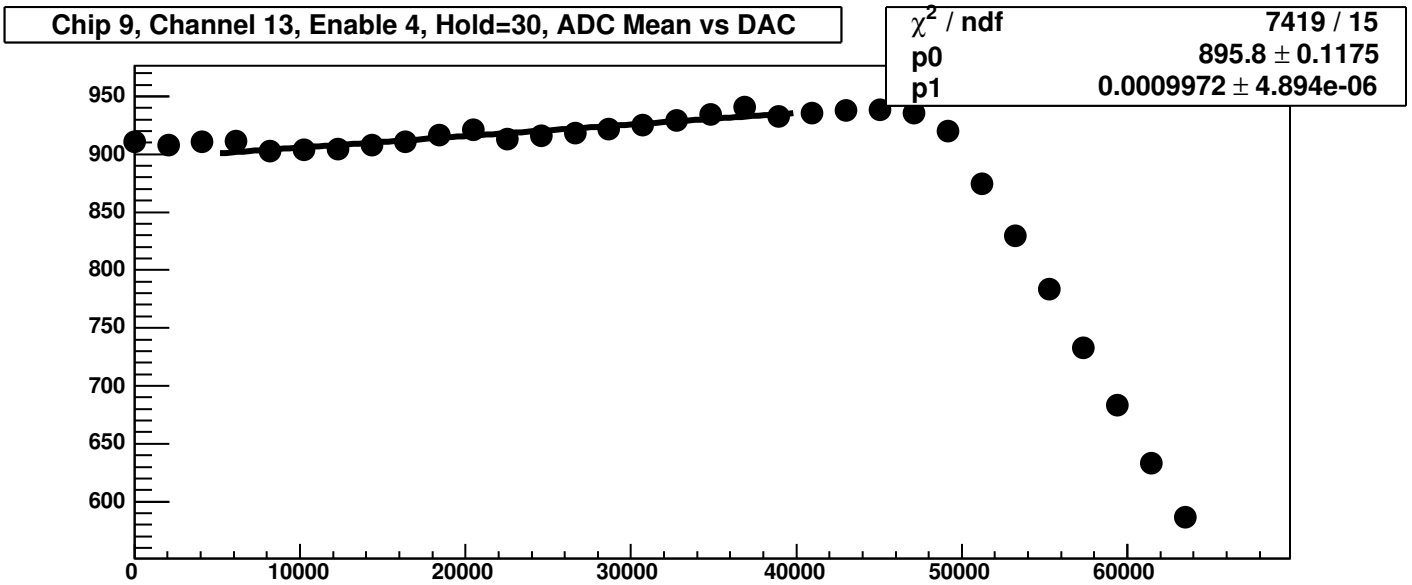
Chip 9, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC

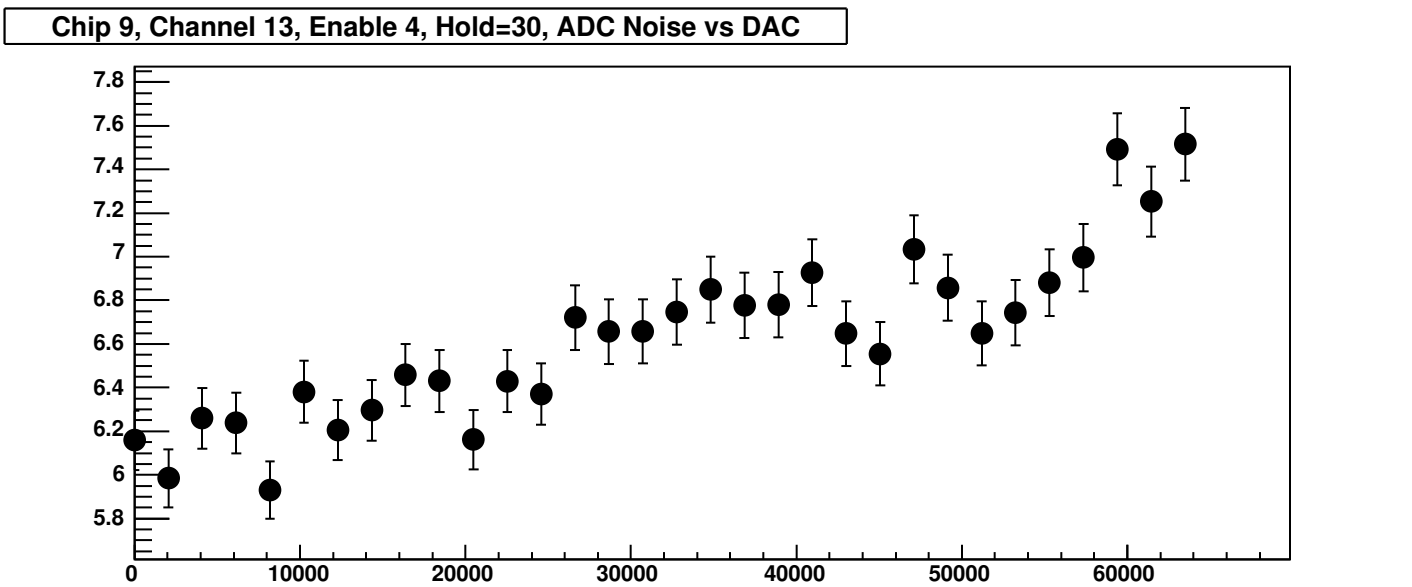


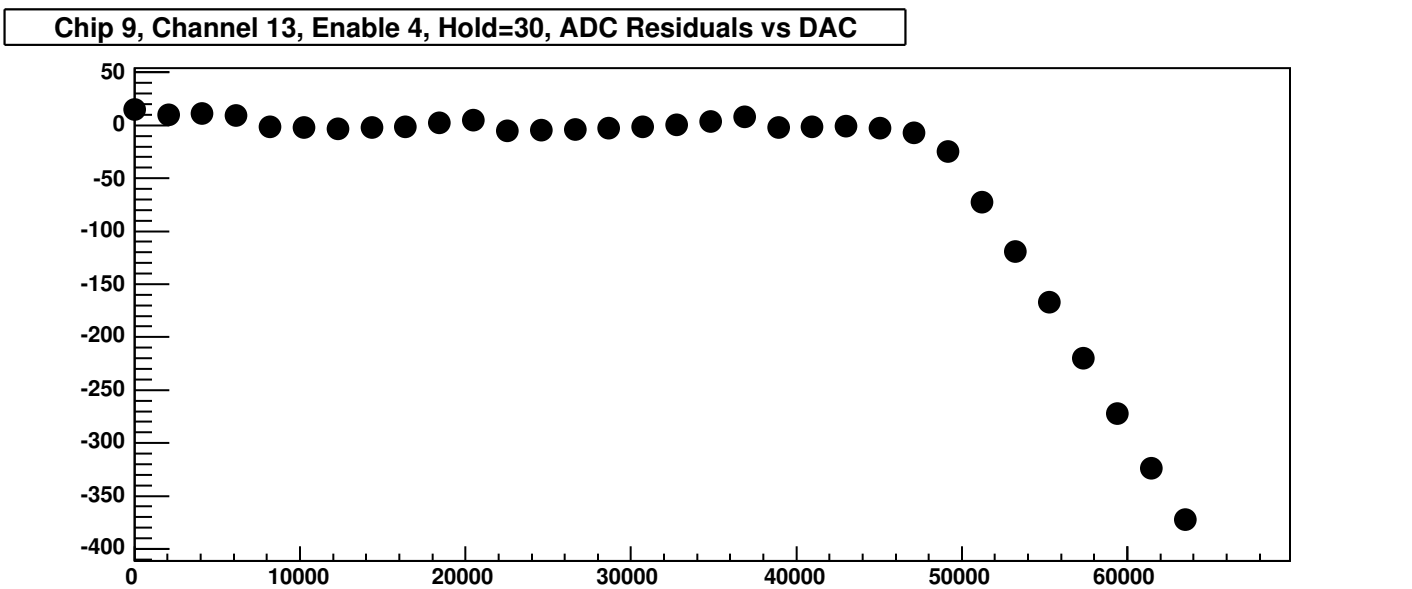

Chip 9, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5521 / 15
p0 898.8 ± 0.1122
p1 -0.0001554 ± 4.569e-06
950
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

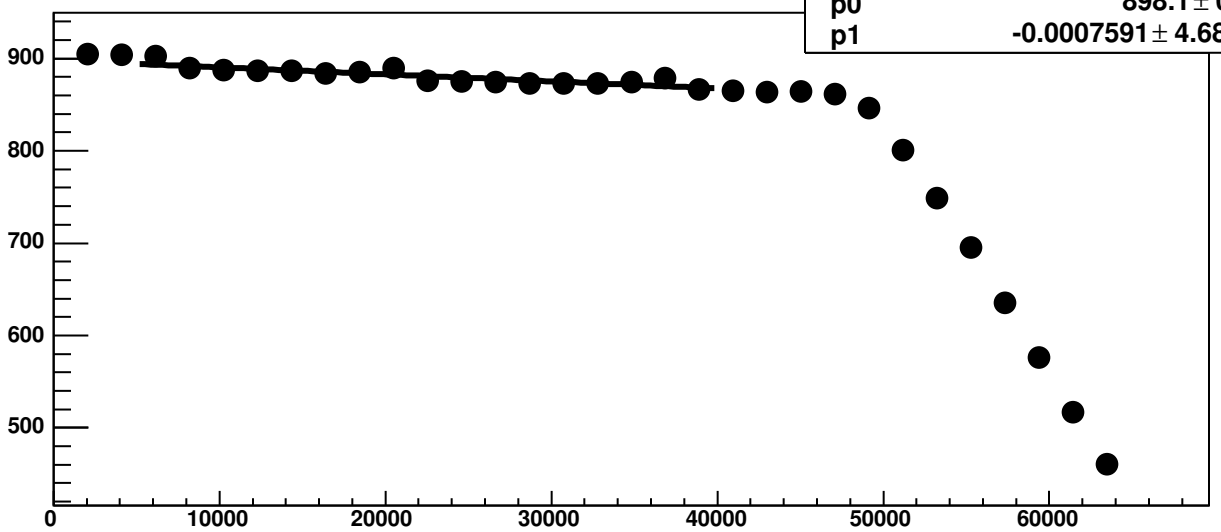

Chip 9, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7419 / 15
p0 895.8 ± 0.1175
p1 0.0009972 ± 4.894e-06
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

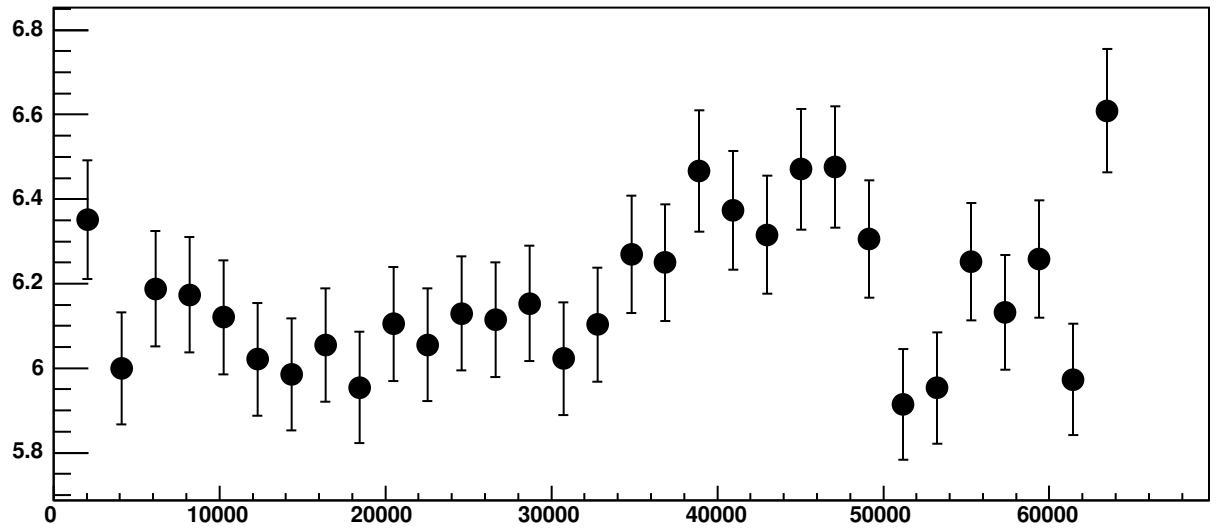

Chip 9, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

Chip 9, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC

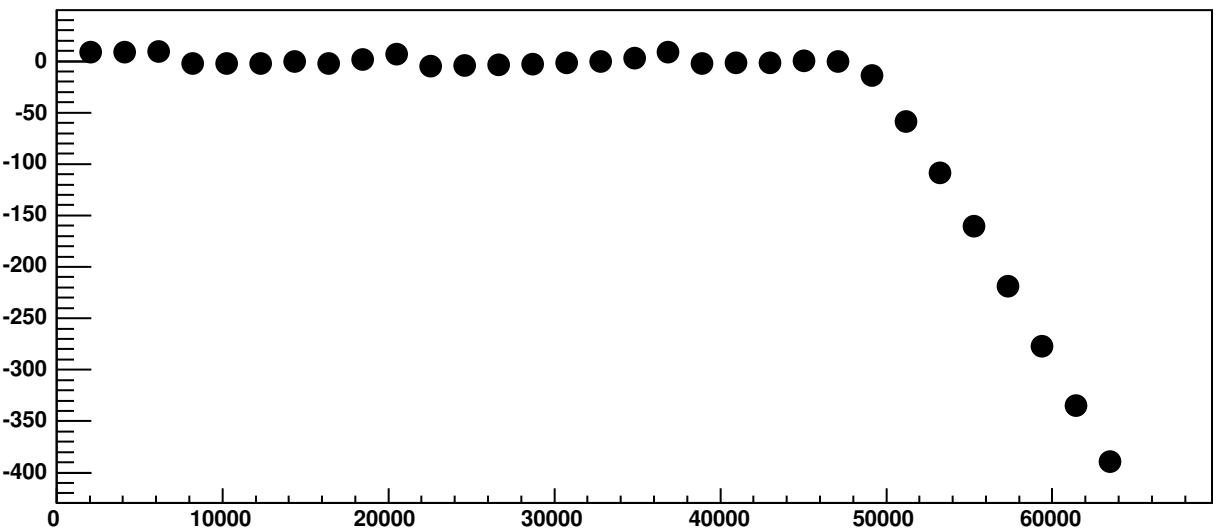
χ^2 / ndf	8583 / 15
p0	898.1 ± 0.1145
p1	-0.0007591 ± 4.682e-06

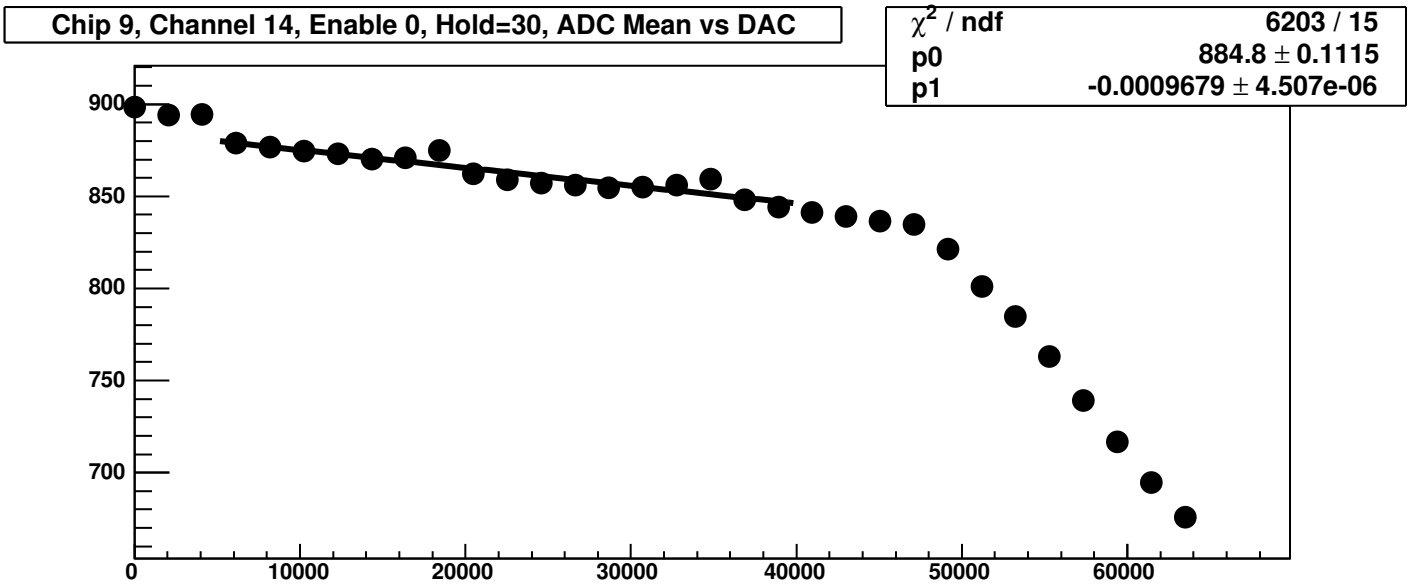


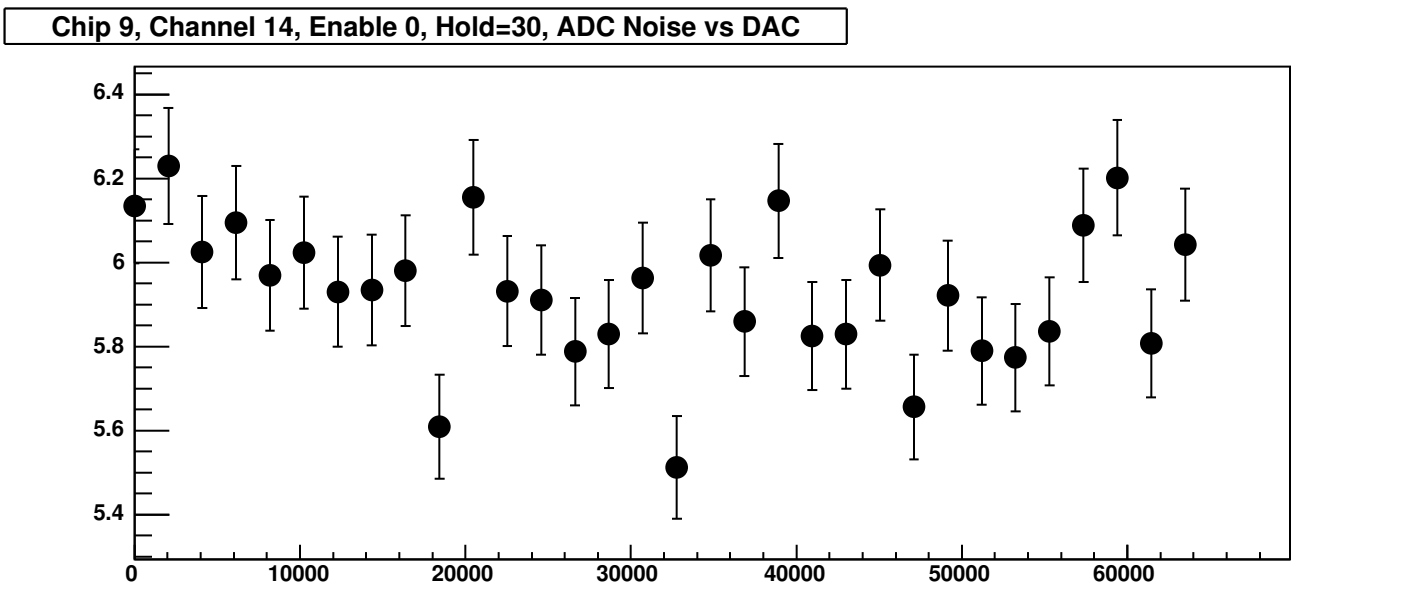
Chip 9, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC

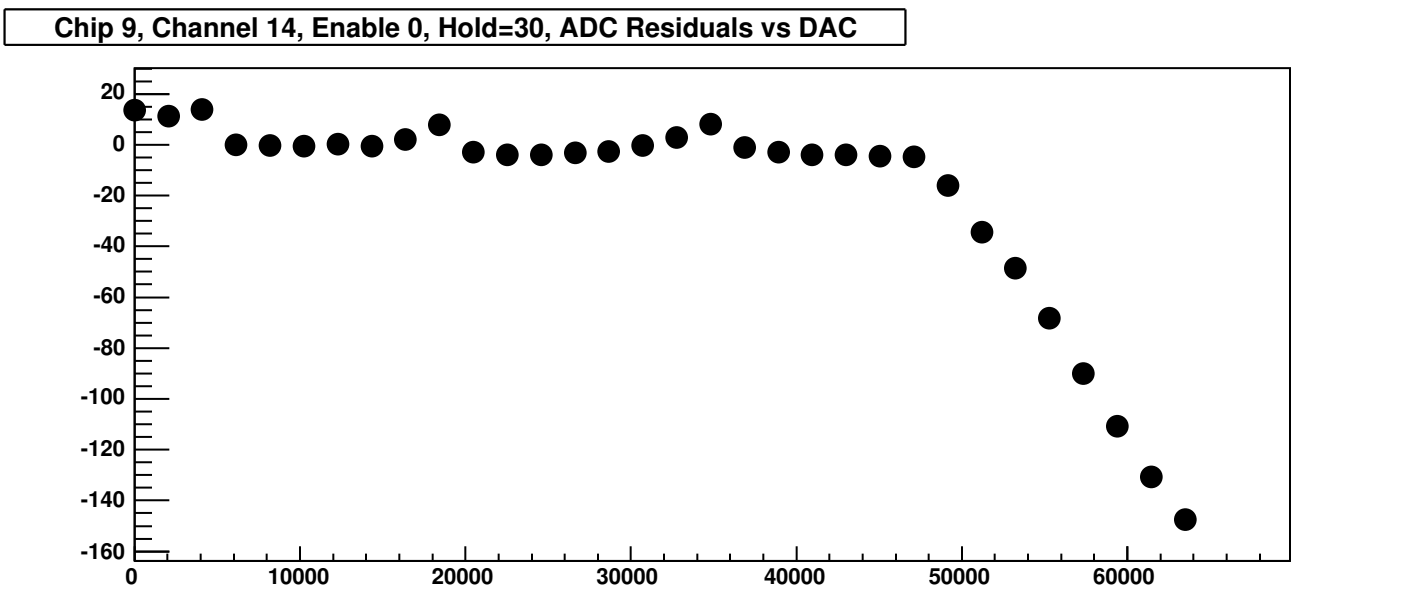


Chip 9, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC

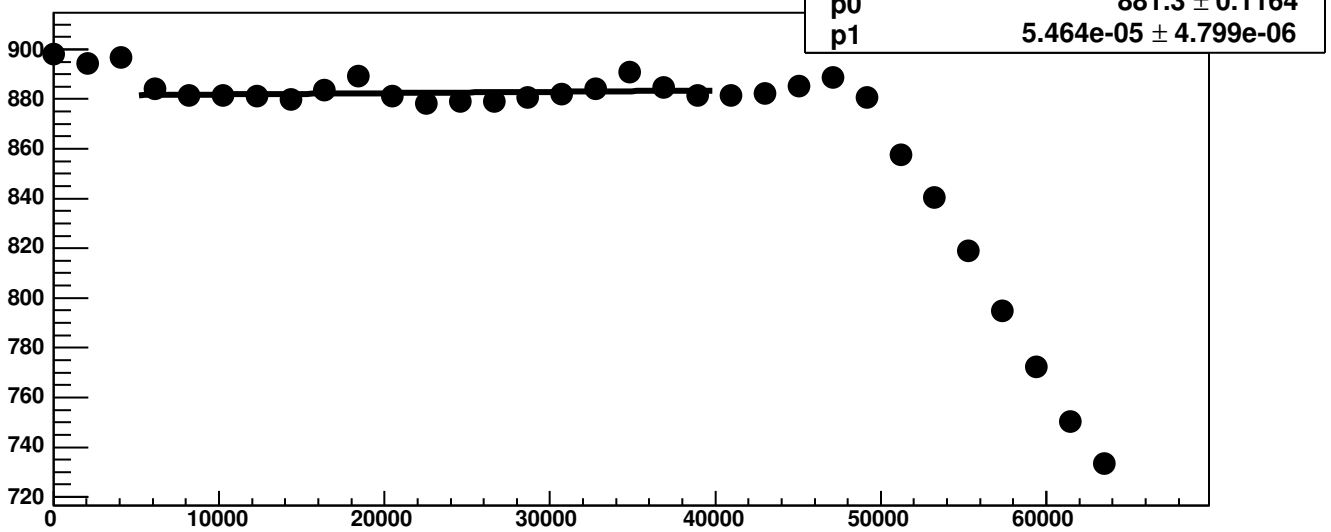



Chip 9, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6203 / 15
p0 884.8 ± 0.1115
p1 -0.0009679 ± 4.507e-06
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

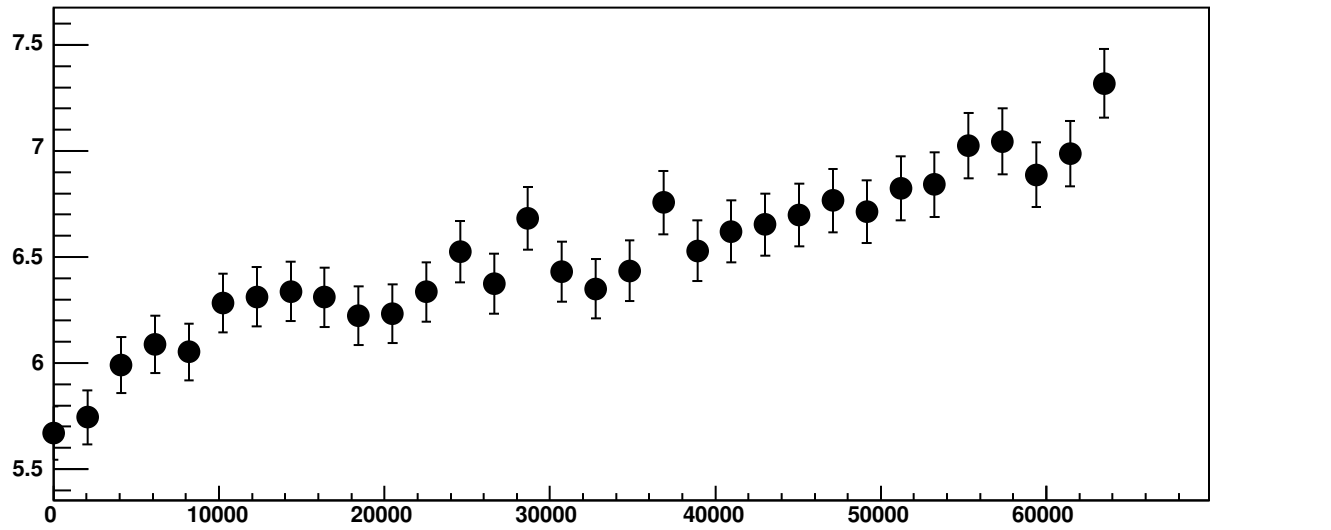

Chip 9, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

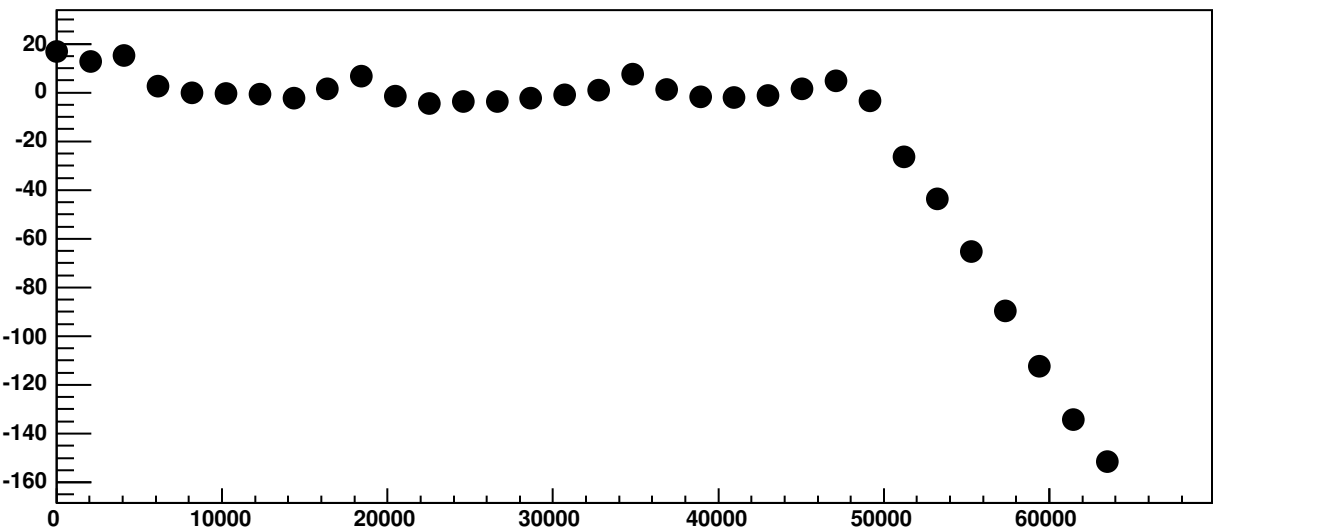
Chip 9, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC



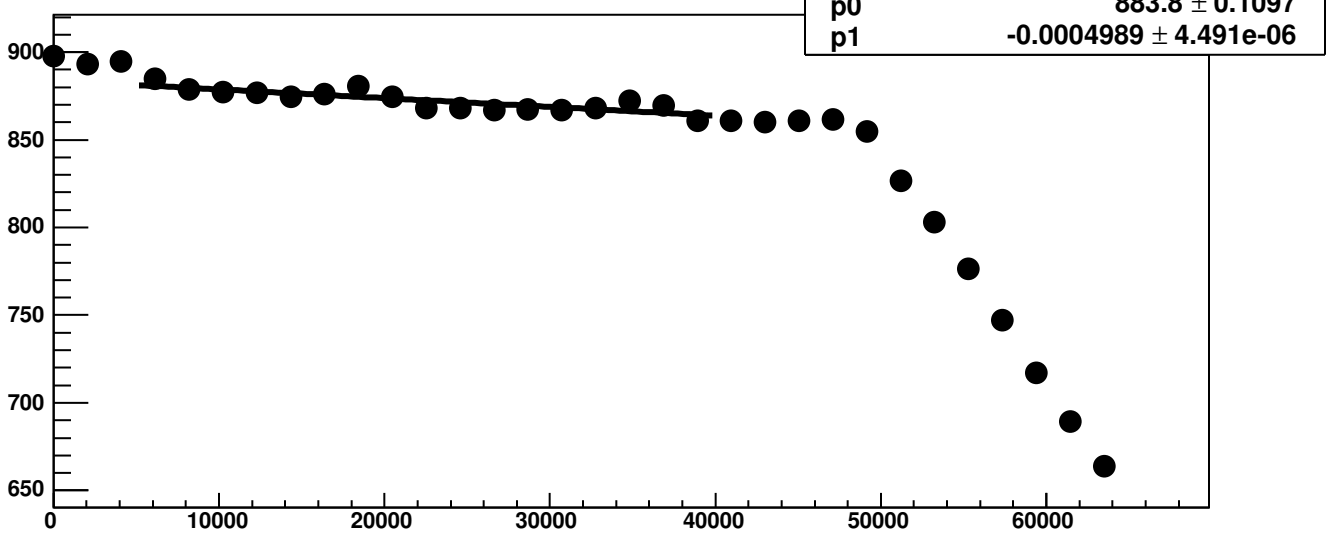
Chip 9, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC



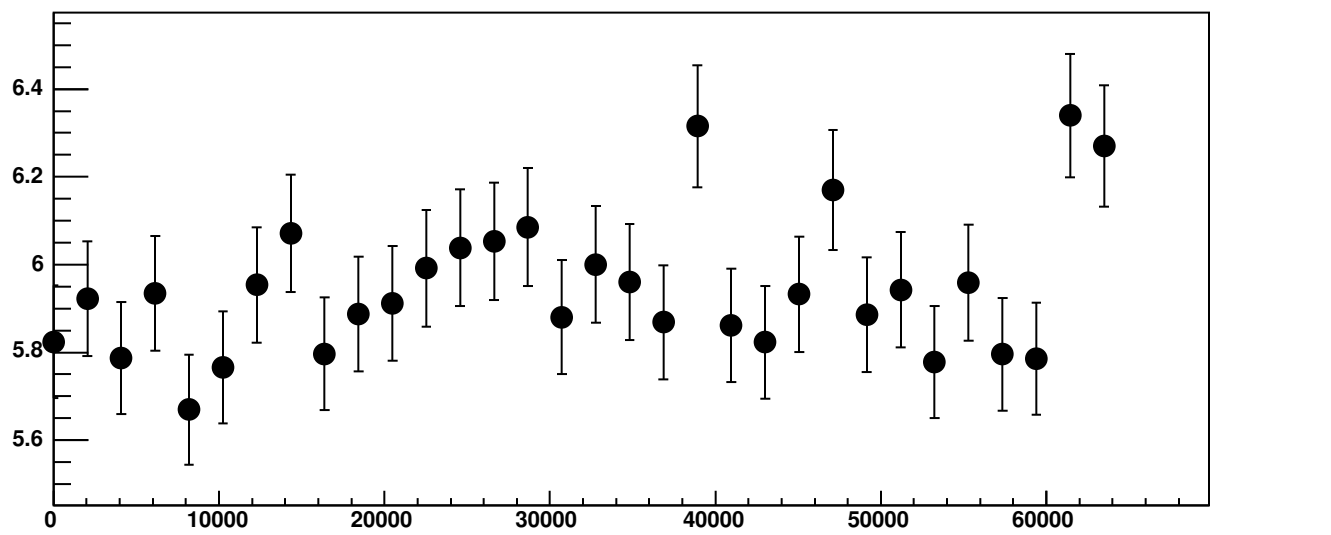
Chip 9, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC



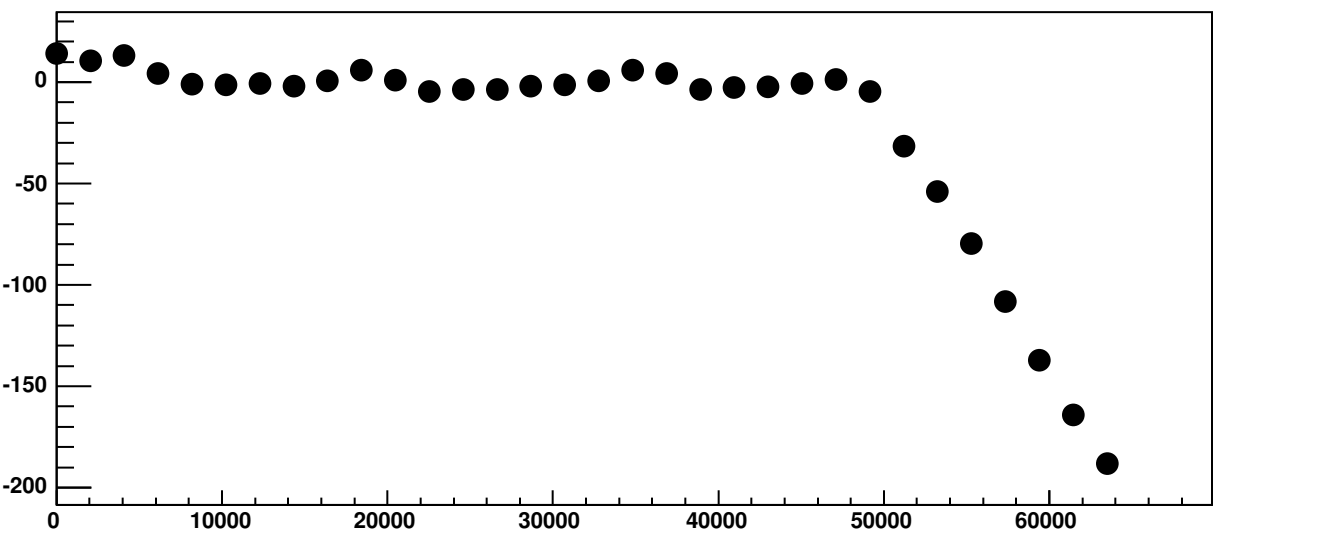
Chip 9, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC

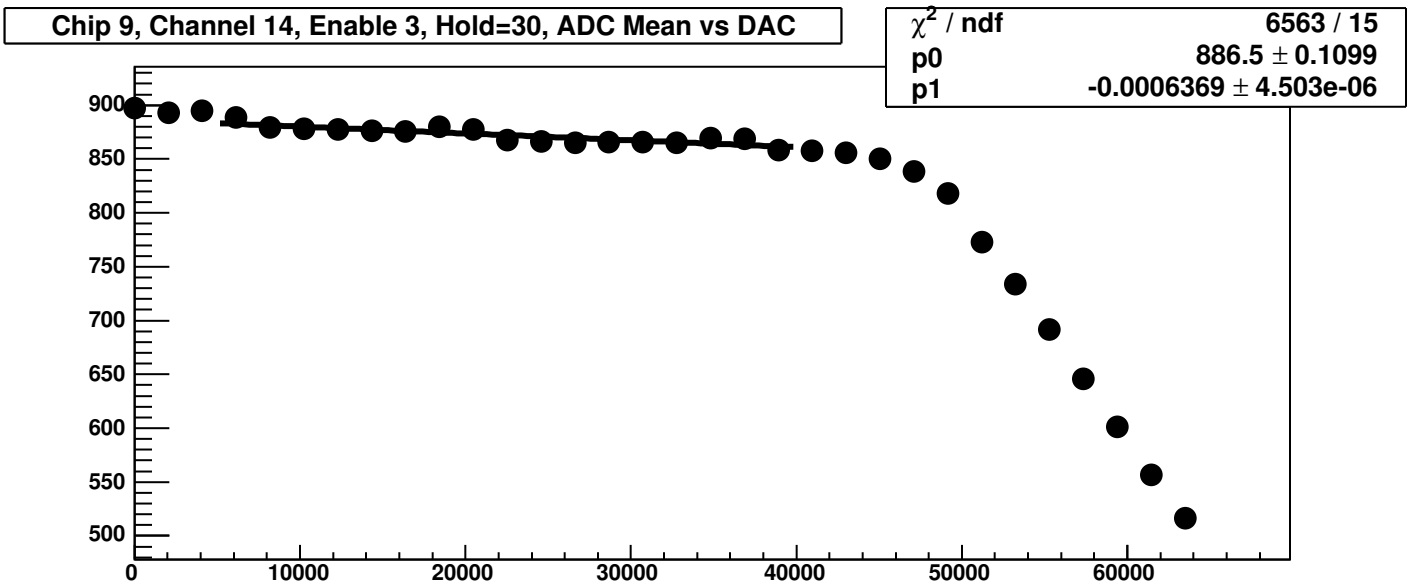


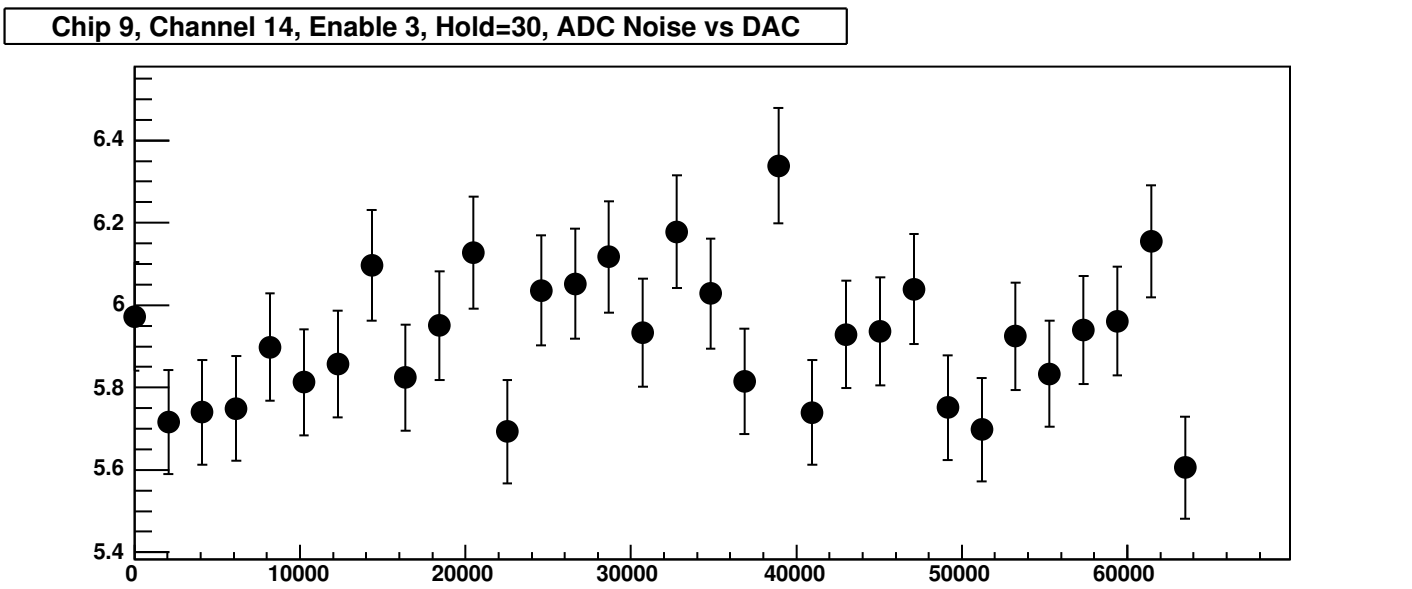
Chip 9, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC

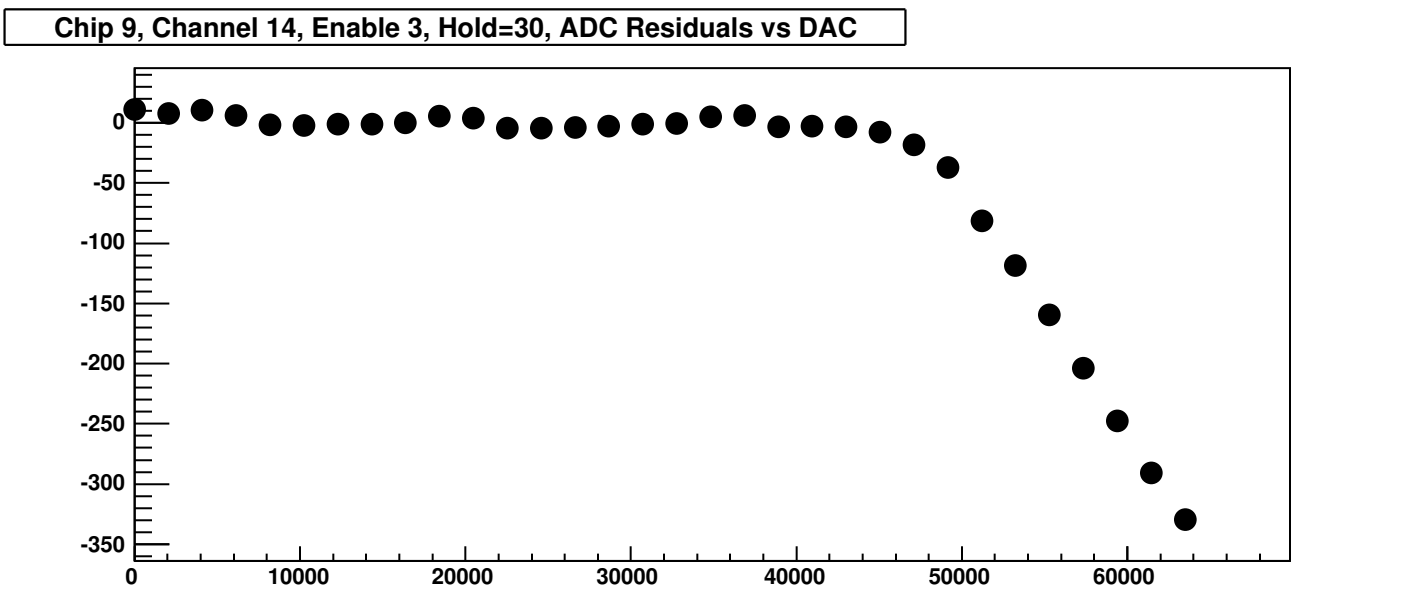


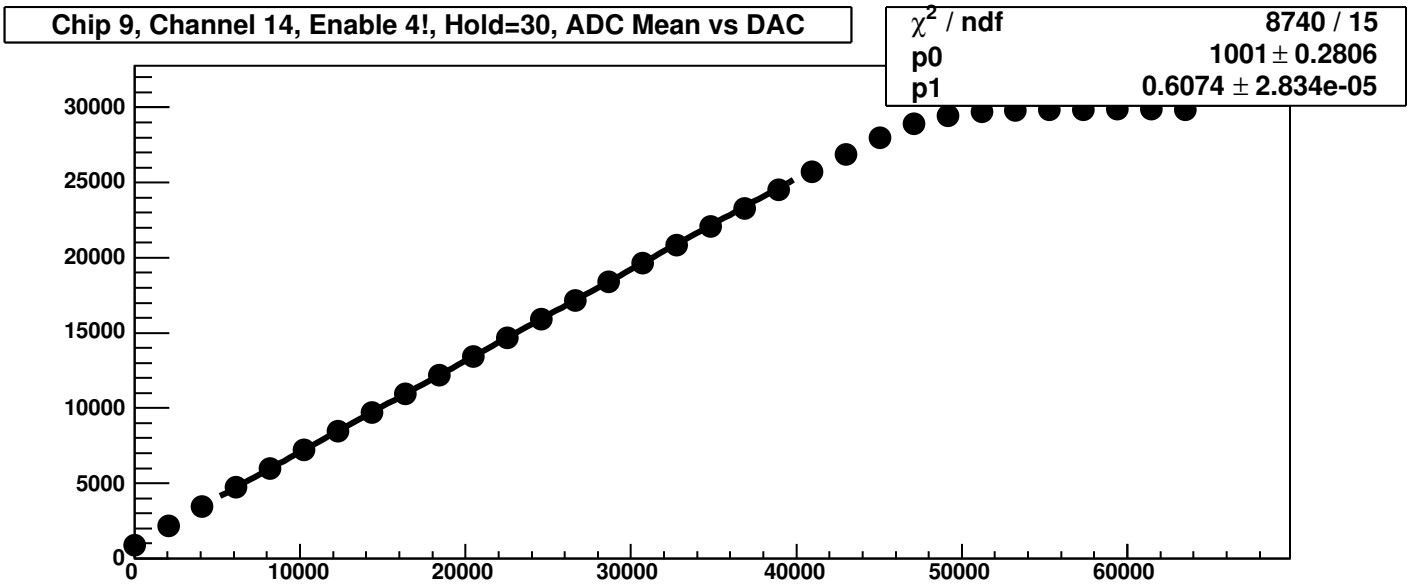
Chip 9, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

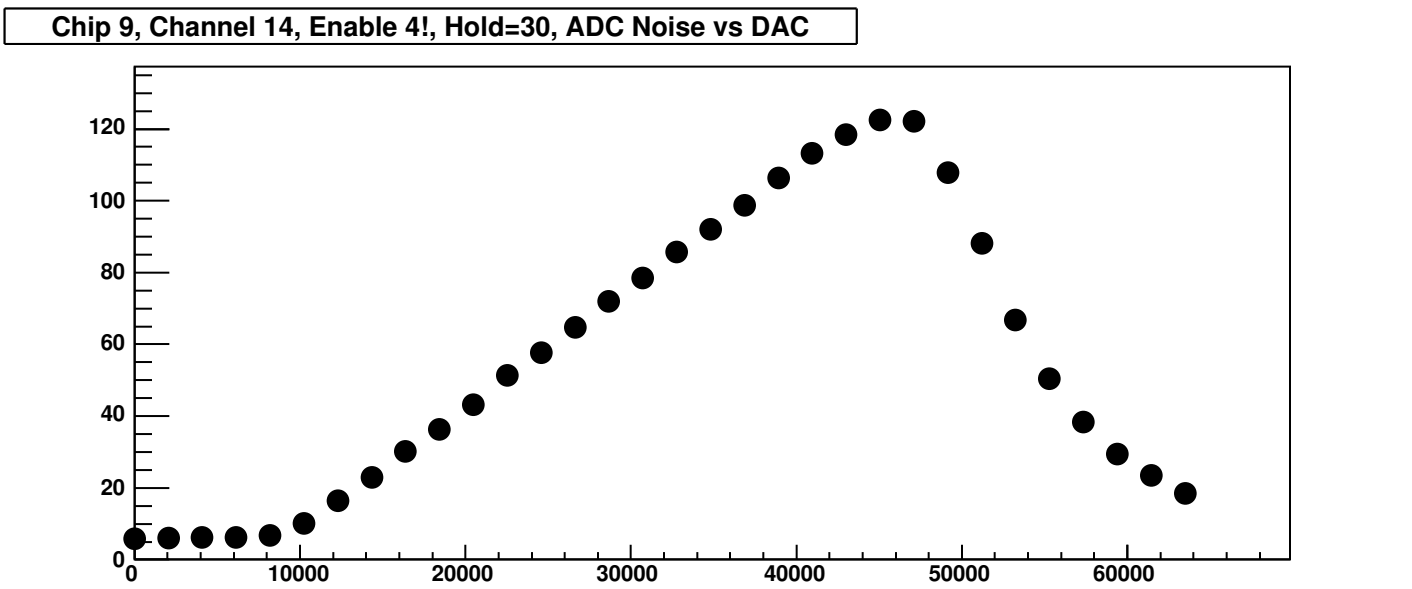


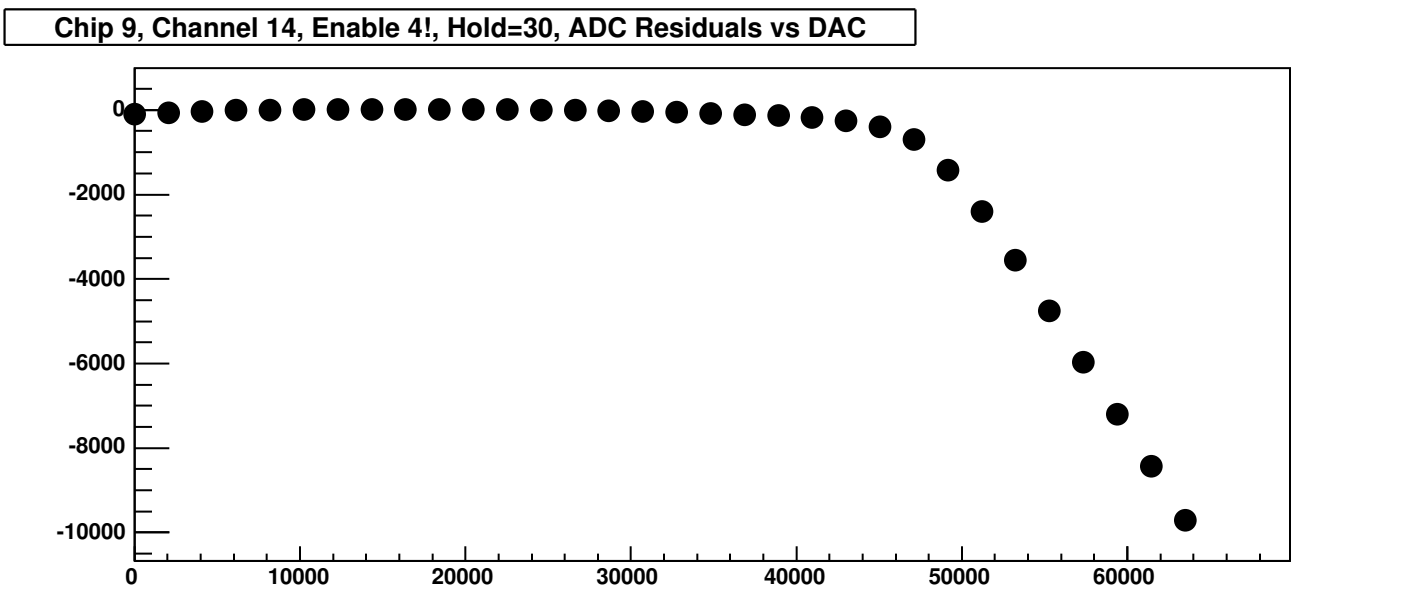

Chip 9, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 6563 / 15
p0 886.5 ± 0.1099
p1 -0.0006369 ± 4.503e-06
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

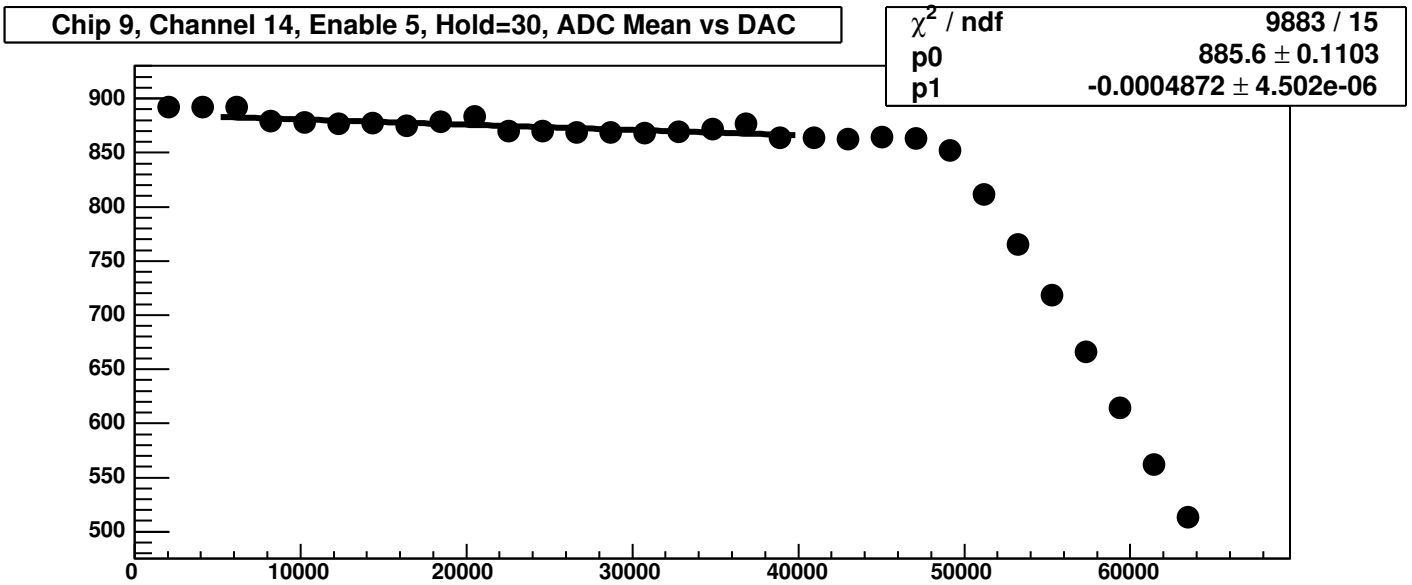

Chip 9, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

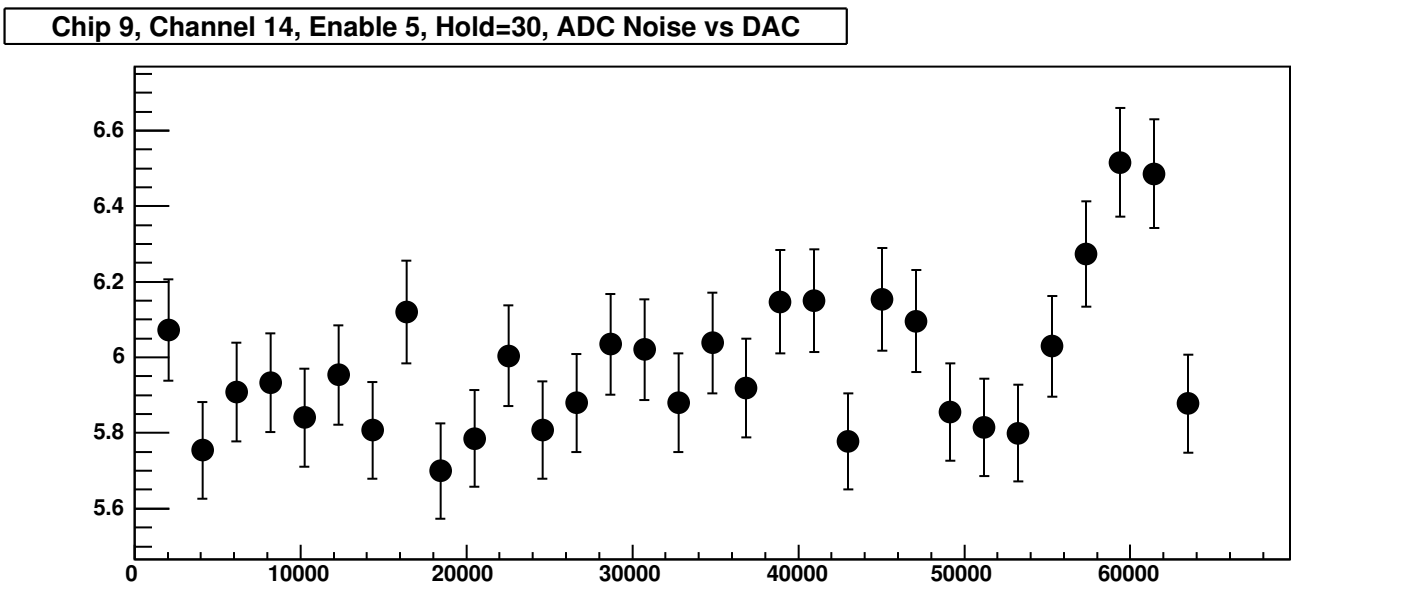

Chip 9, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

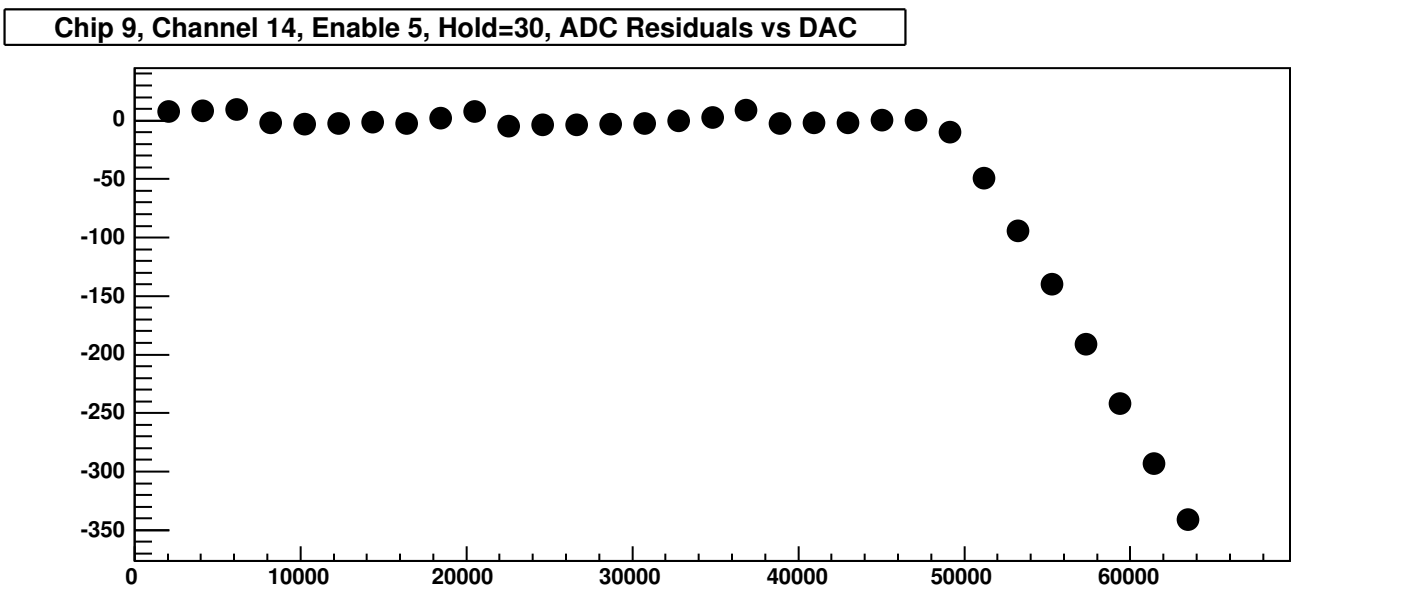

Chip 9, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 8740 / 15
p0 1001 ± 0.2806
p1 0.6074 ± 2.834e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

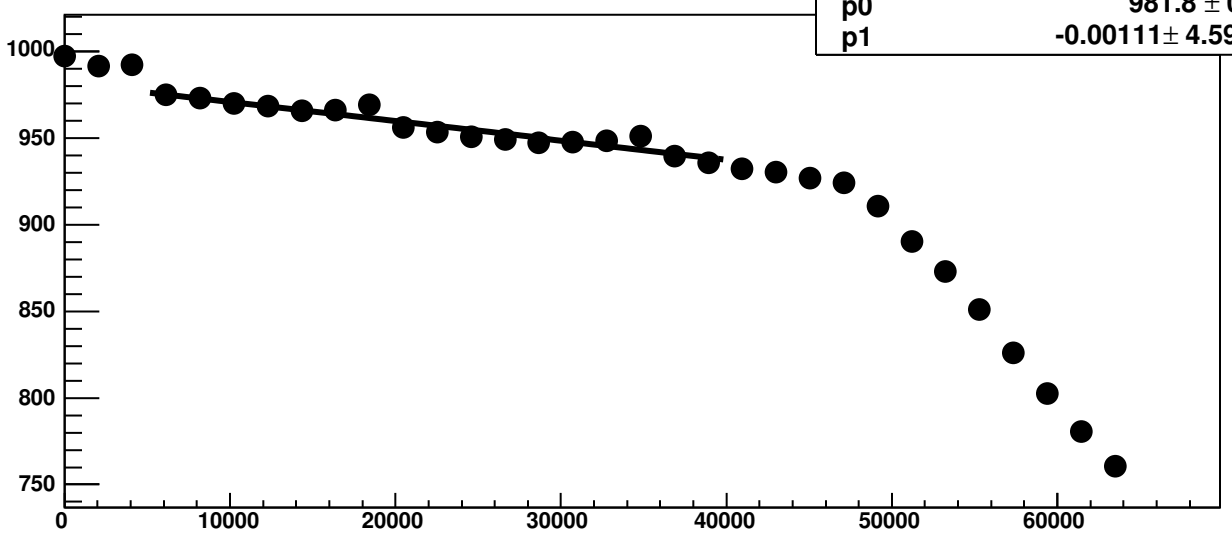

Chip 9, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9883 / 15
p0 885.6 ± 0.1103
p1 -0.0004872 ± 4.502e-06
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

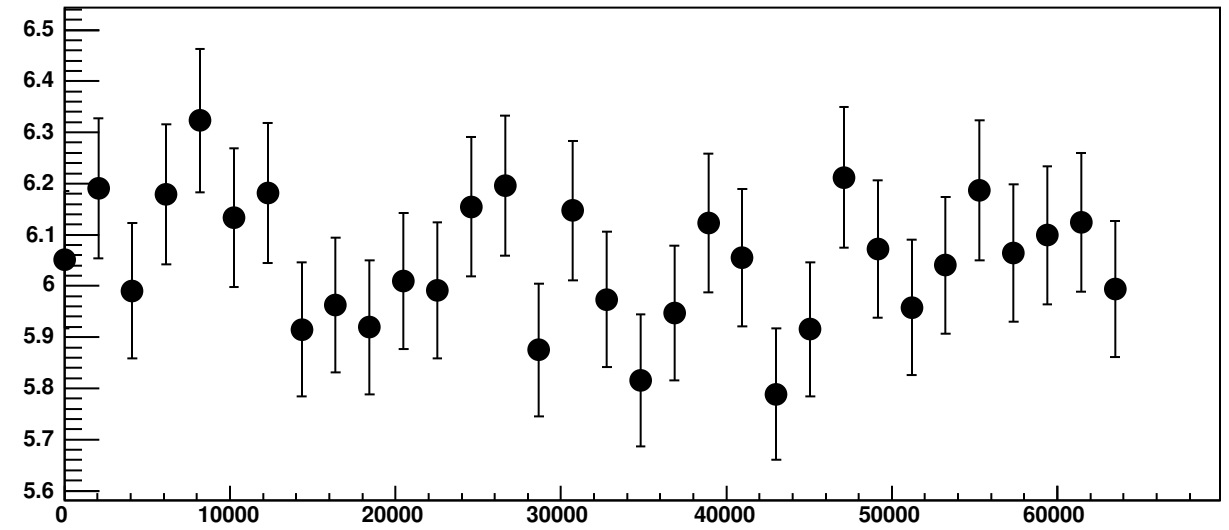

Chip 9, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

Chip 9, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC

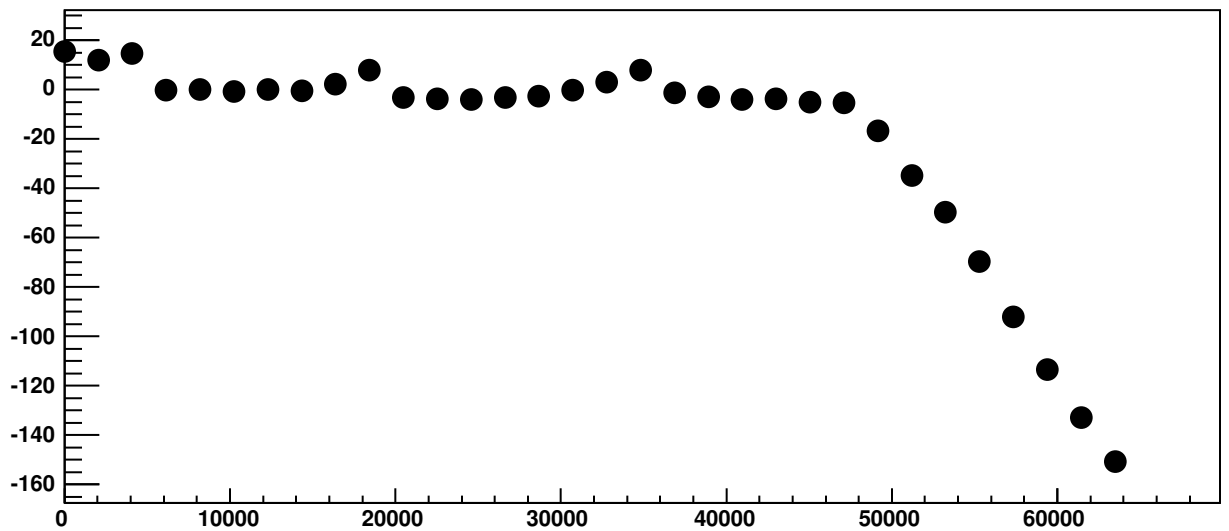
χ^2 / ndf	5938 / 15
p0	981.8 ± 0.1139
p1	-0.00111 ± 4.594e-06



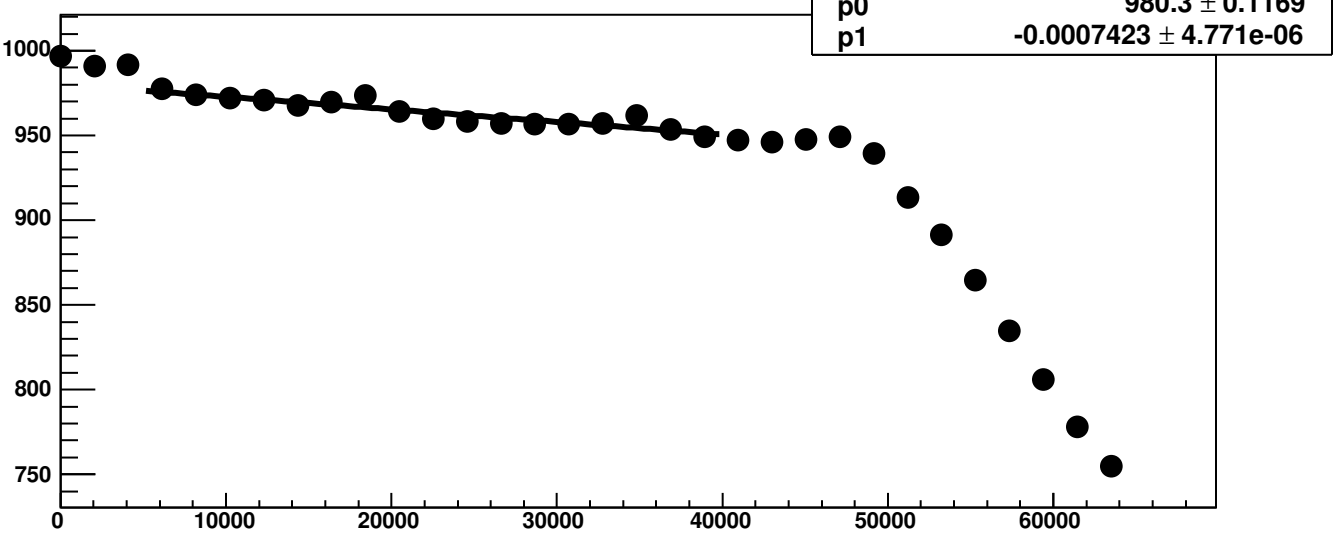
Chip 9, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



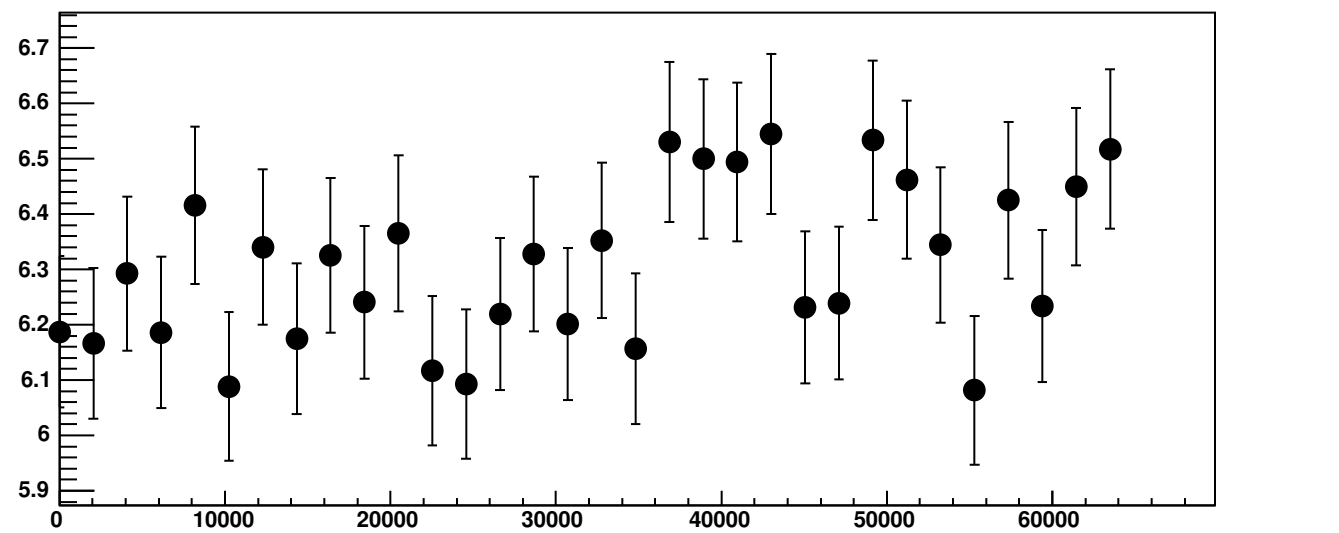
Chip 9, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC



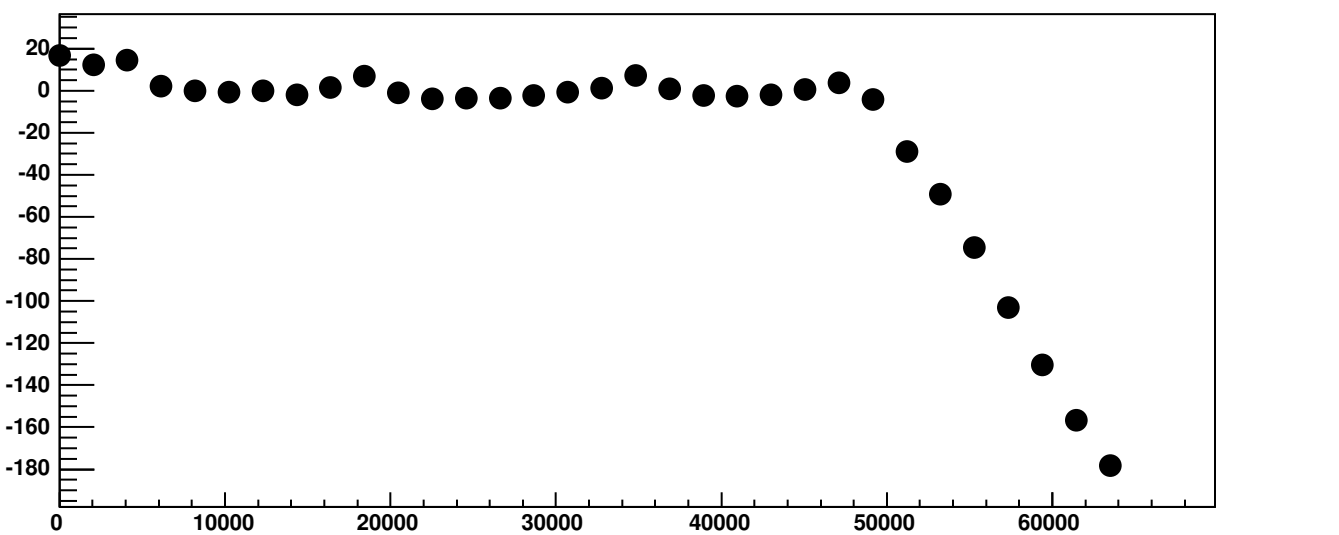
Chip 9, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

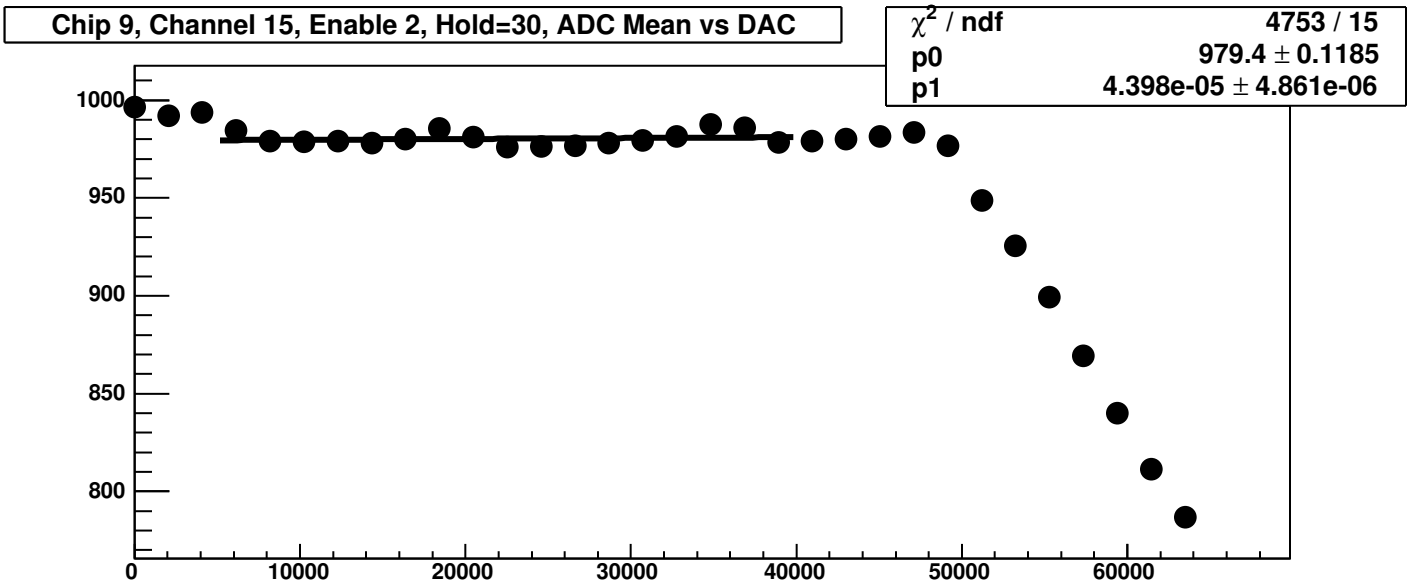


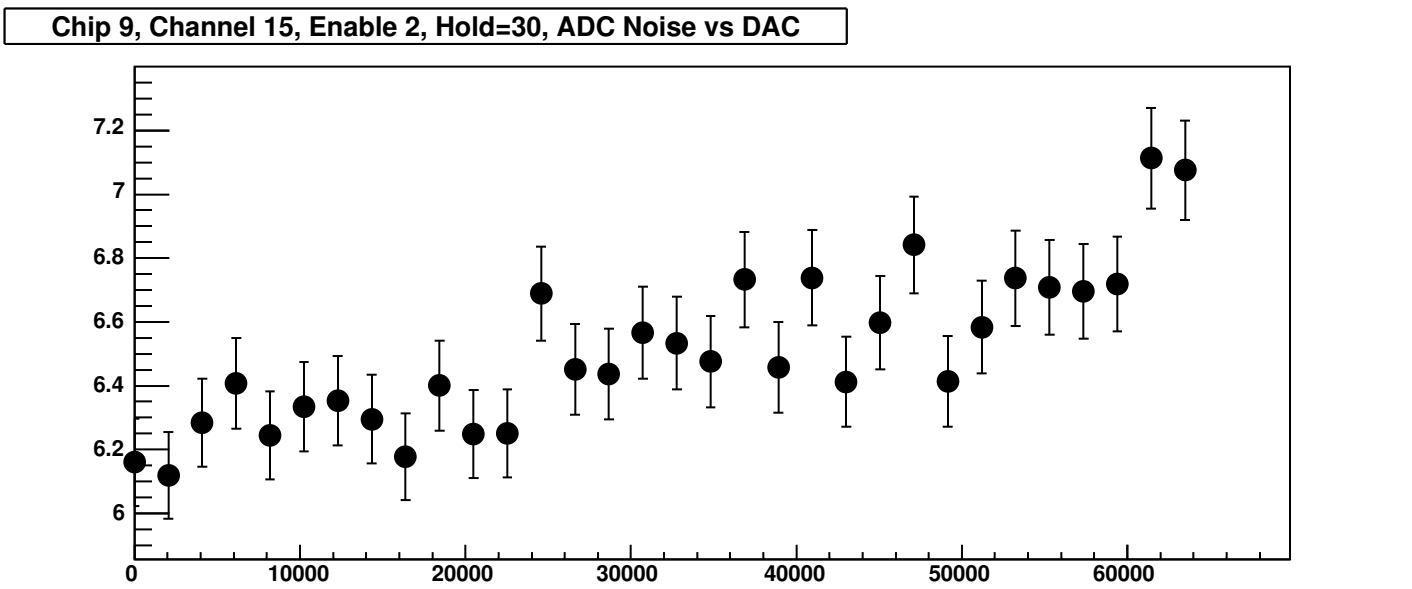
Chip 9, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC

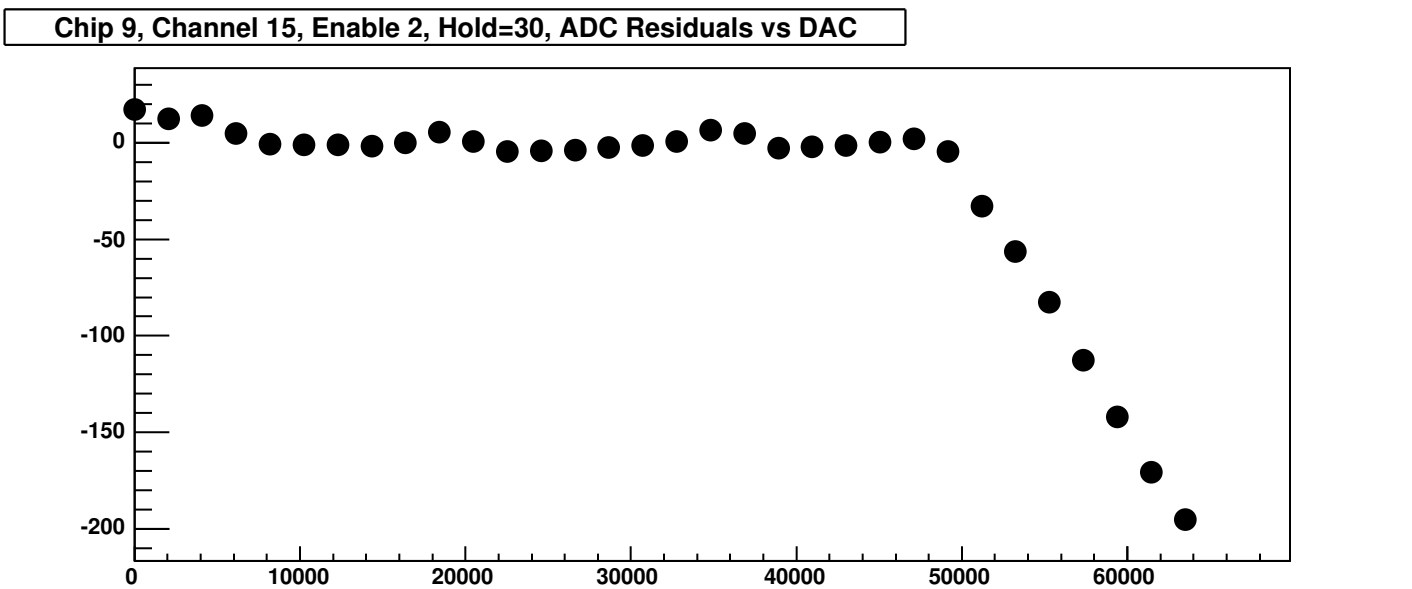


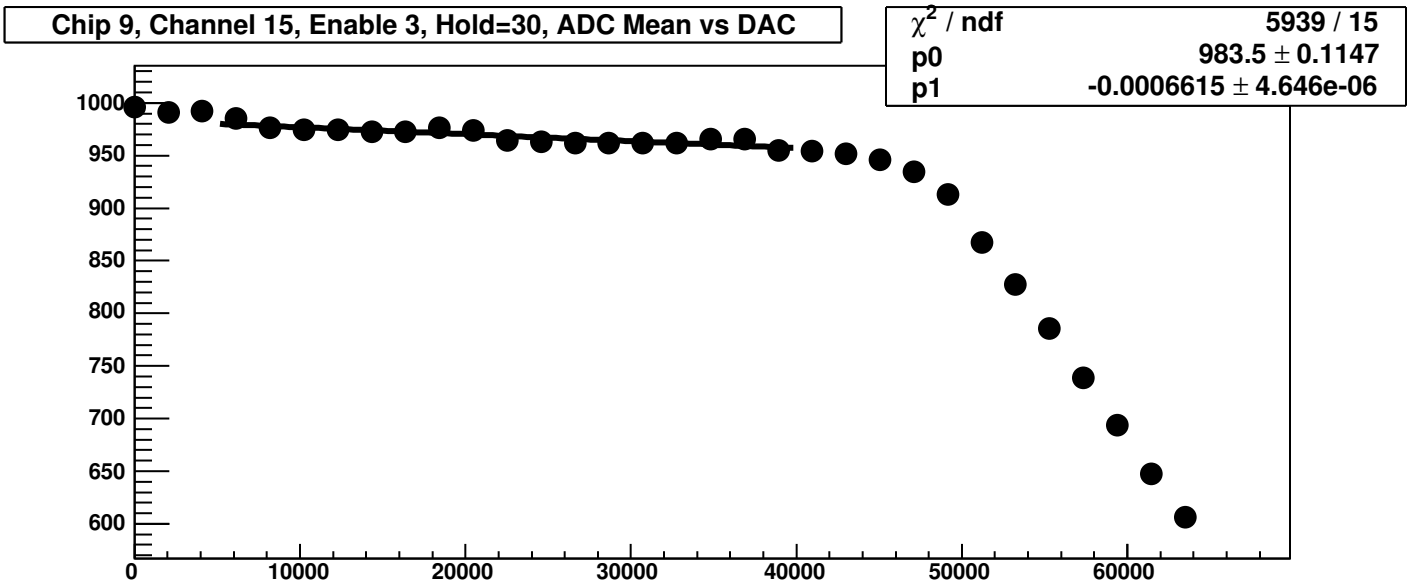
Chip 9, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

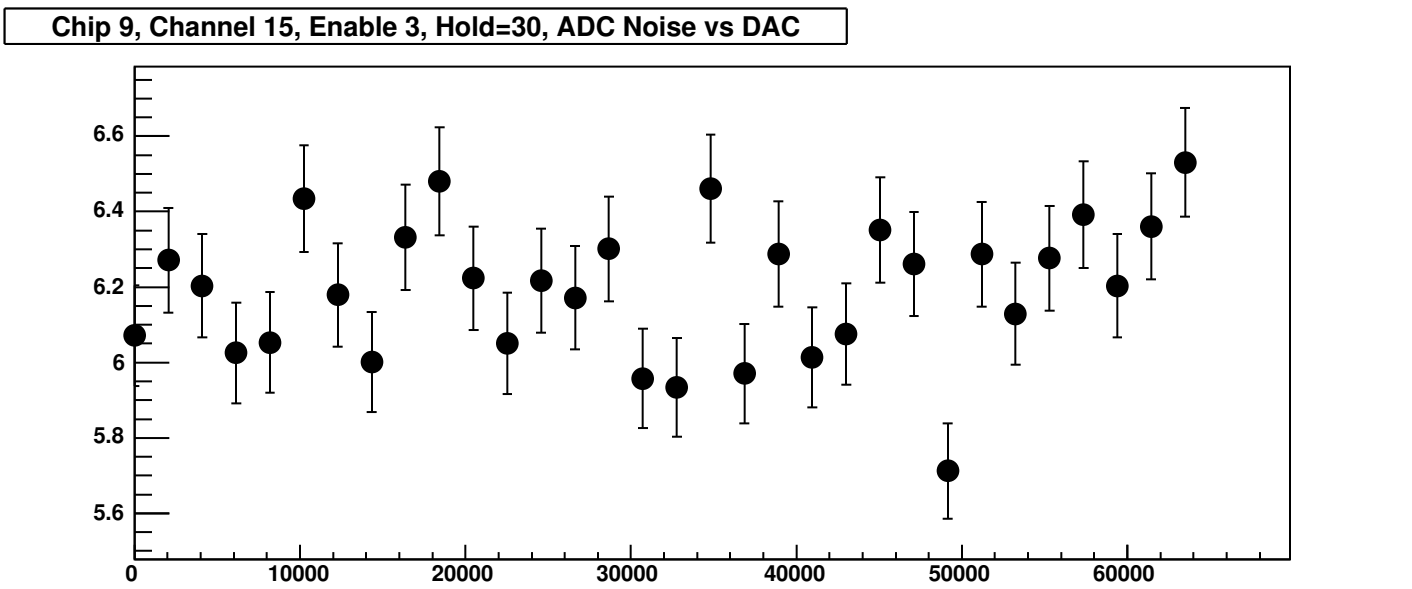


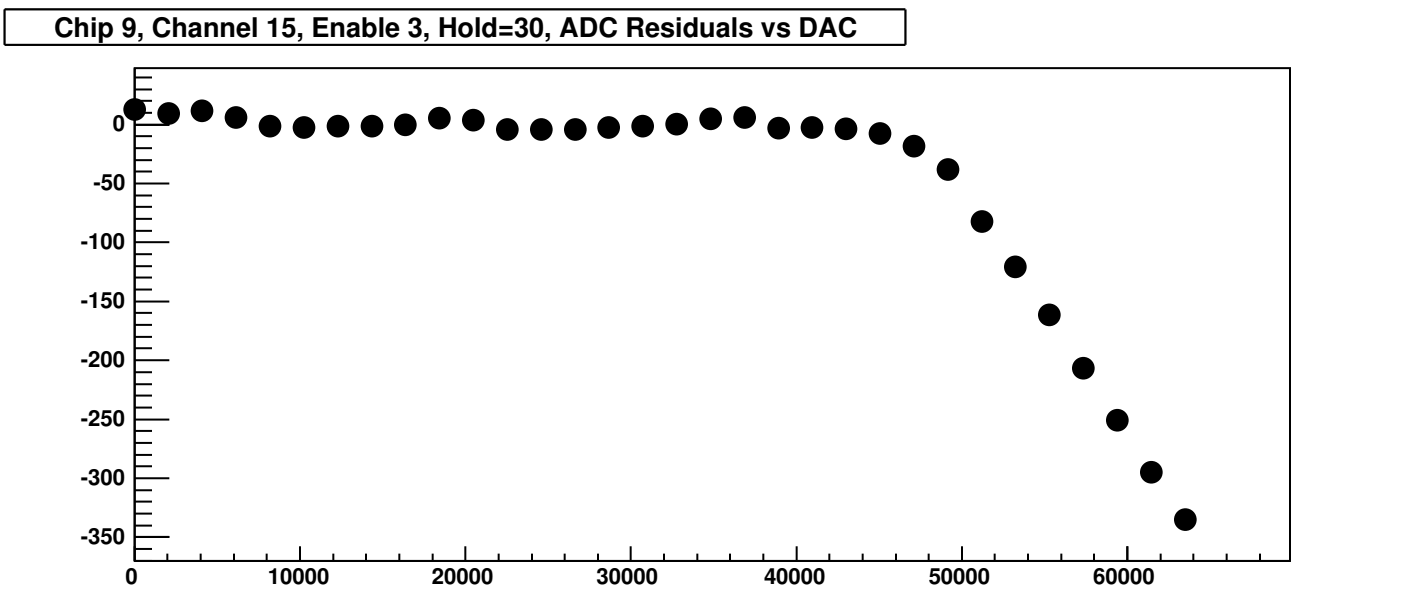

Chip 9, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4753 / 15
p0 979.4 ± 0.1185
p1 4.398e-05 ± 4.861e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

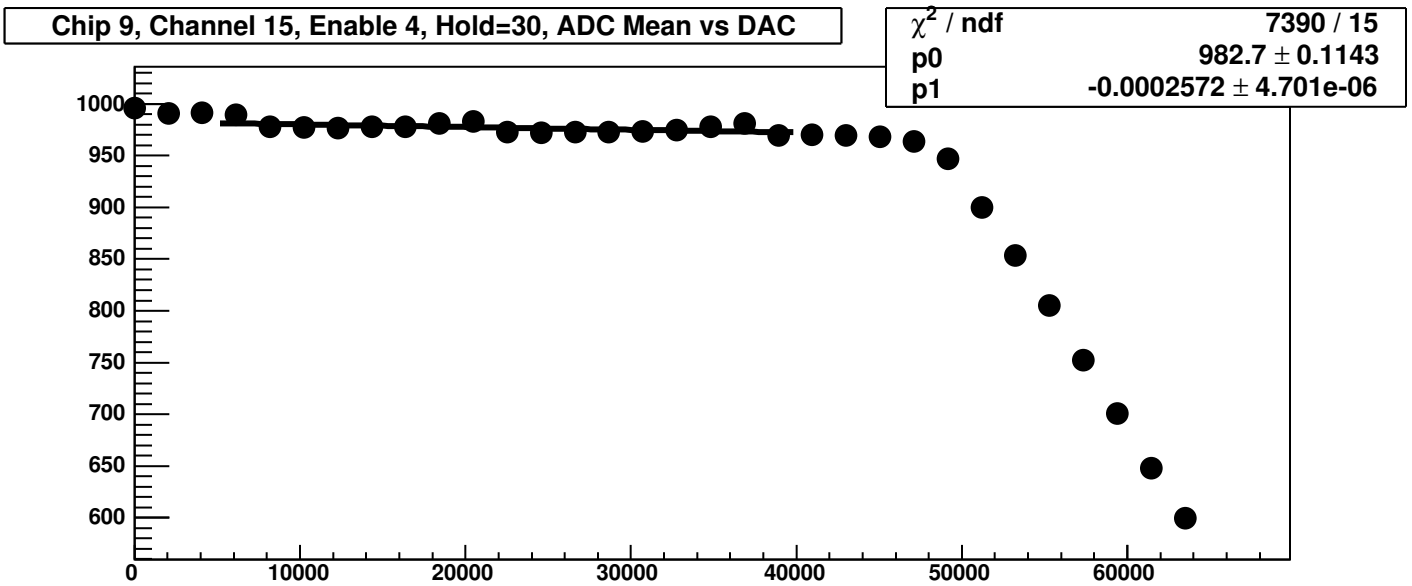

Chip 9, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
0
10000
20000
30000
40000
50000
60000

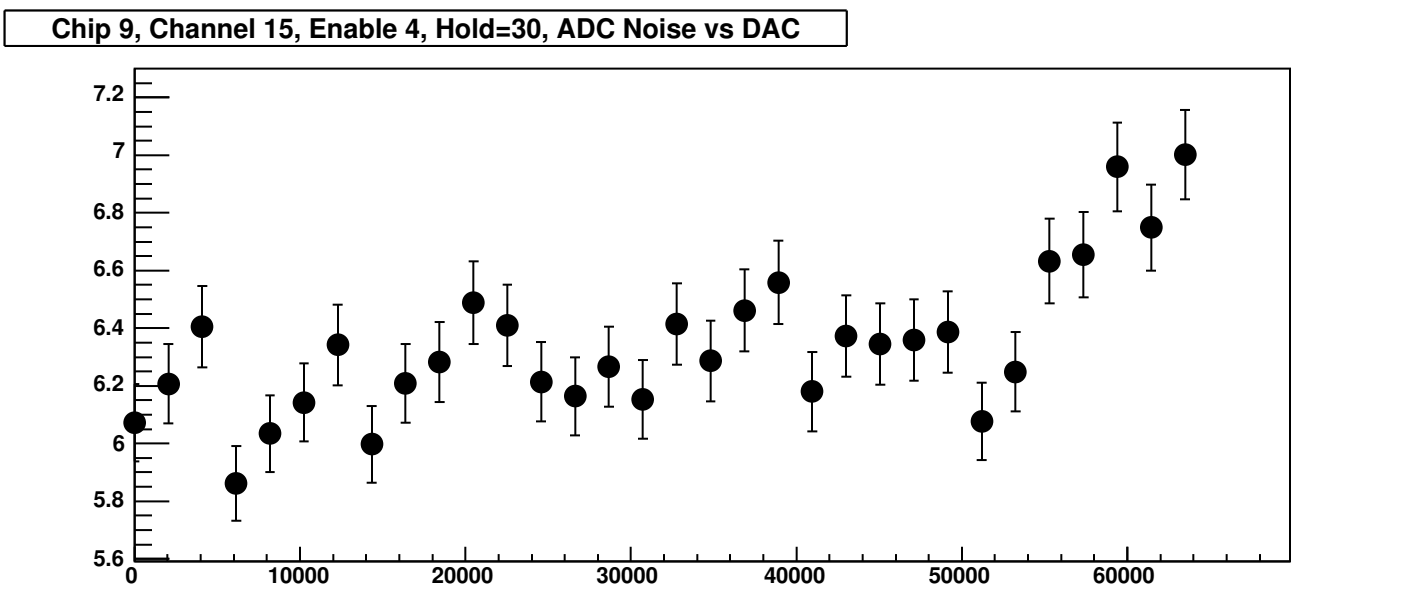

Chip 9, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

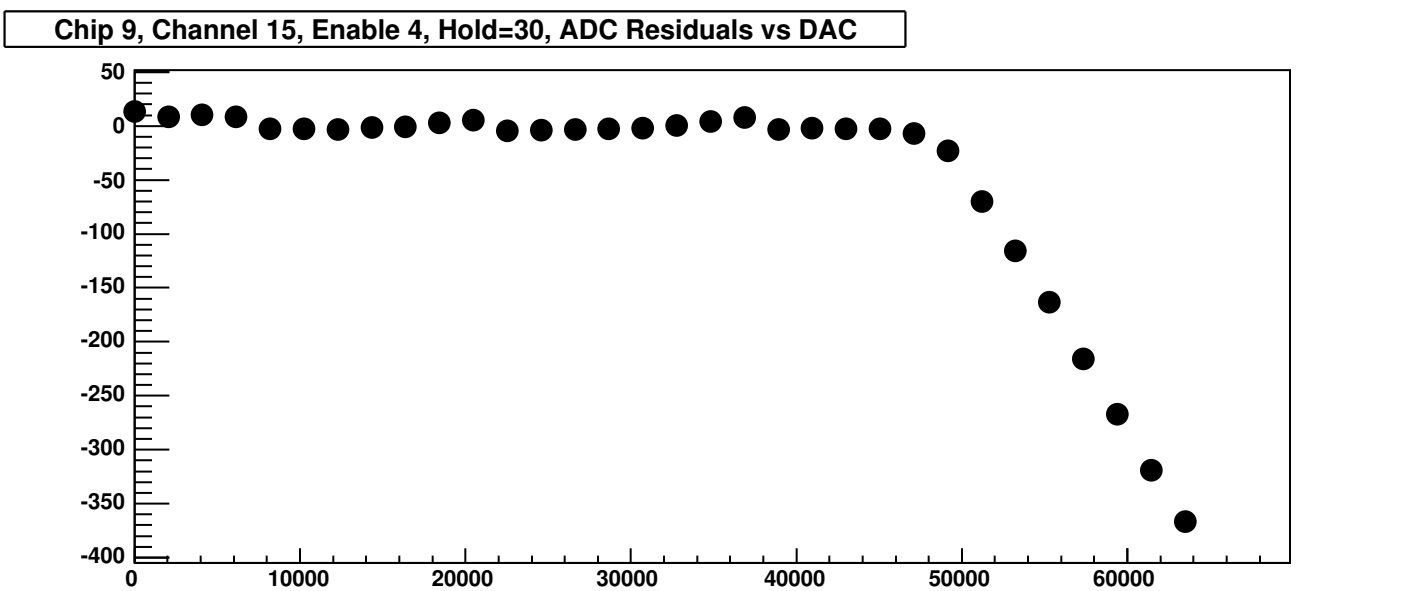

Chip 9, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5939 / 15
p0 983.5 ± 0.1147
p1 -0.0006615 ± 4.646e-06
1000
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

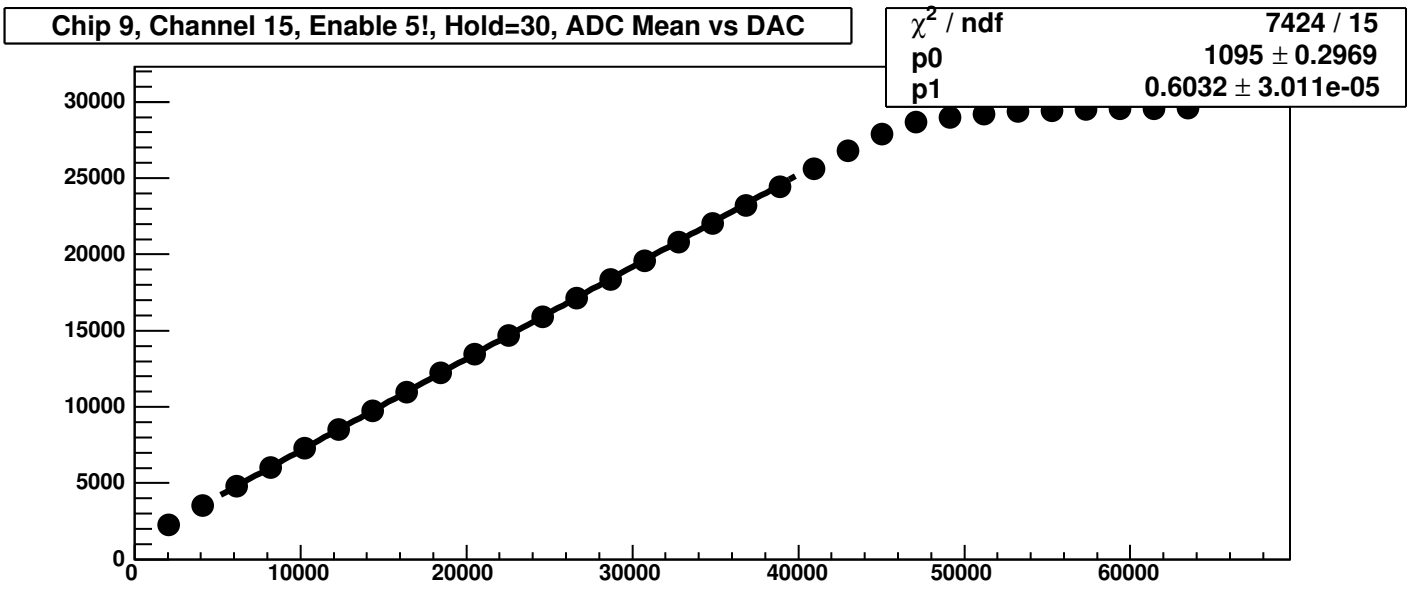

Chip 9, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

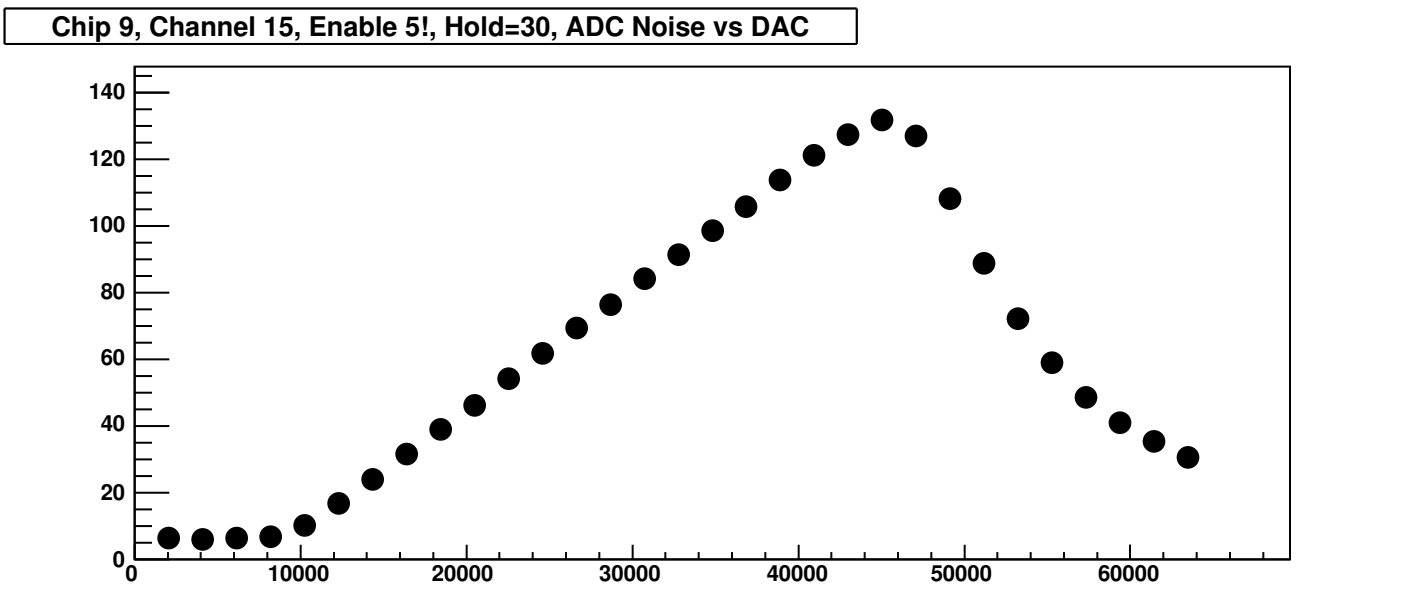

Chip 9, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

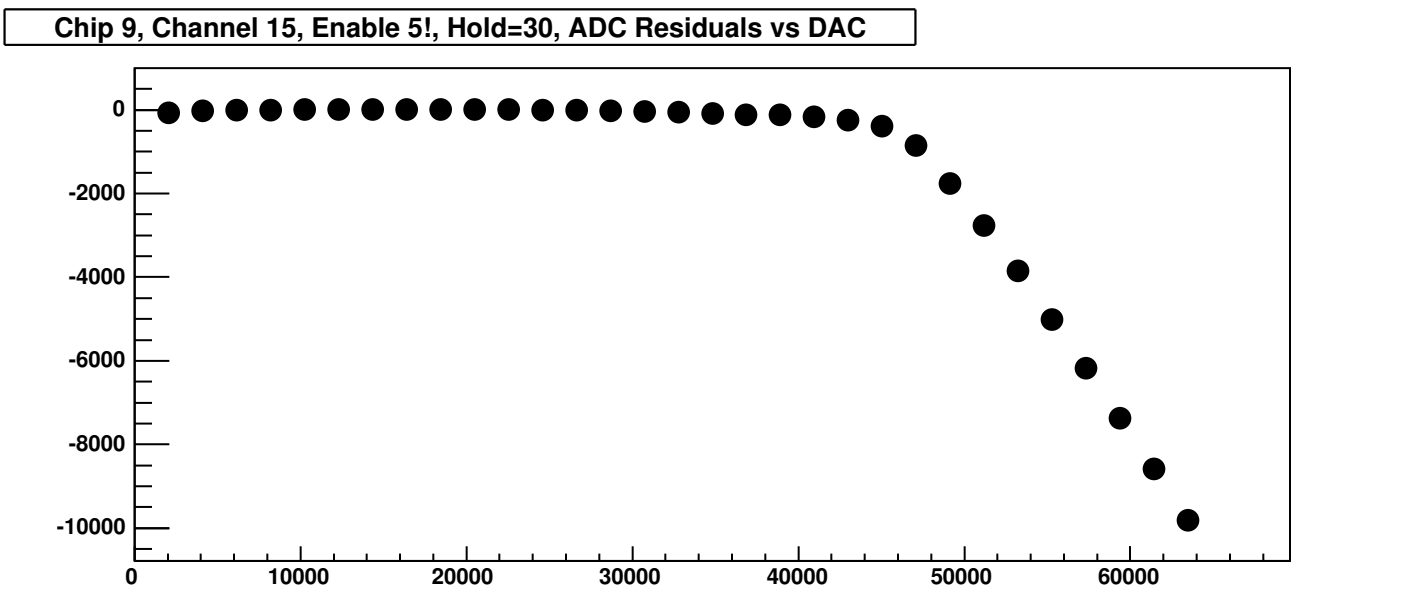

Chip 9, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7390 / 15
p0 982.7 ± 0.1143
p1 -0.0002572 ± 4.701e-06
1000
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

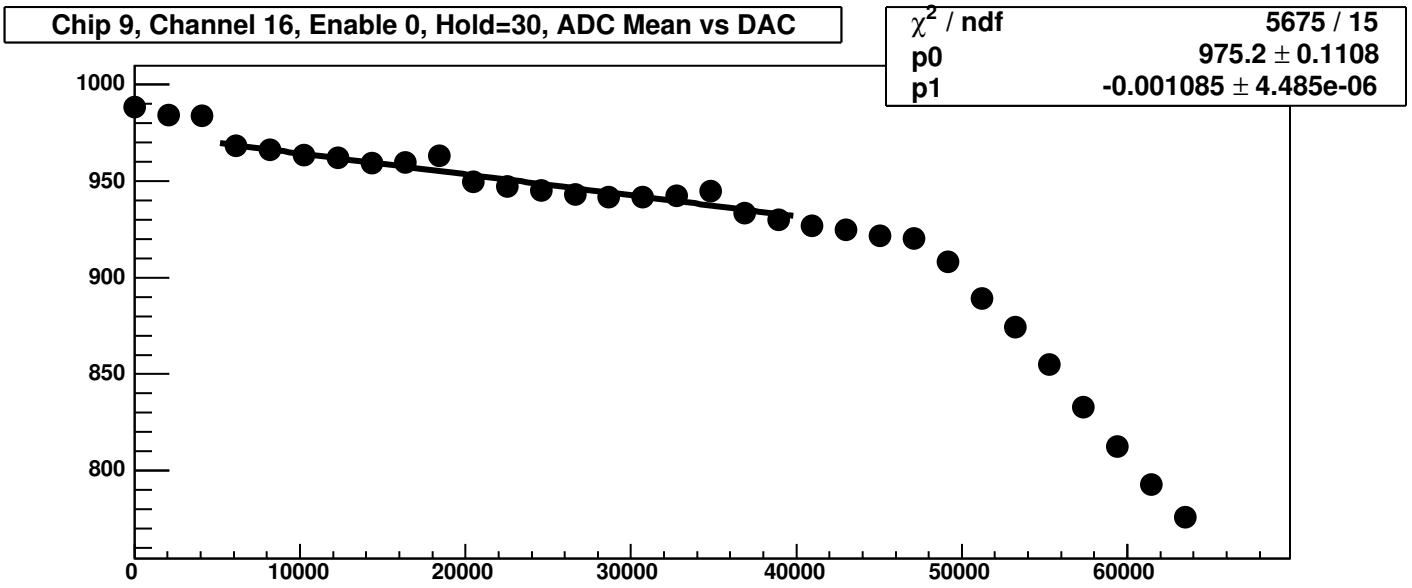

Chip 9, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

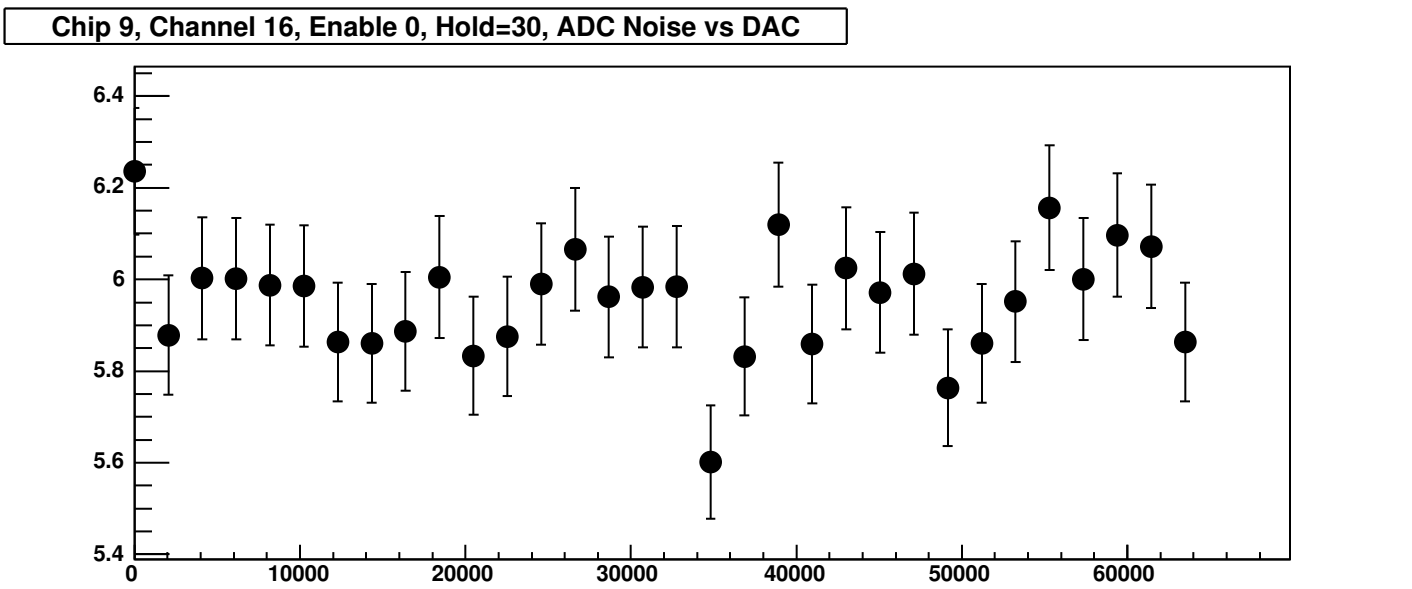

Chip 9, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

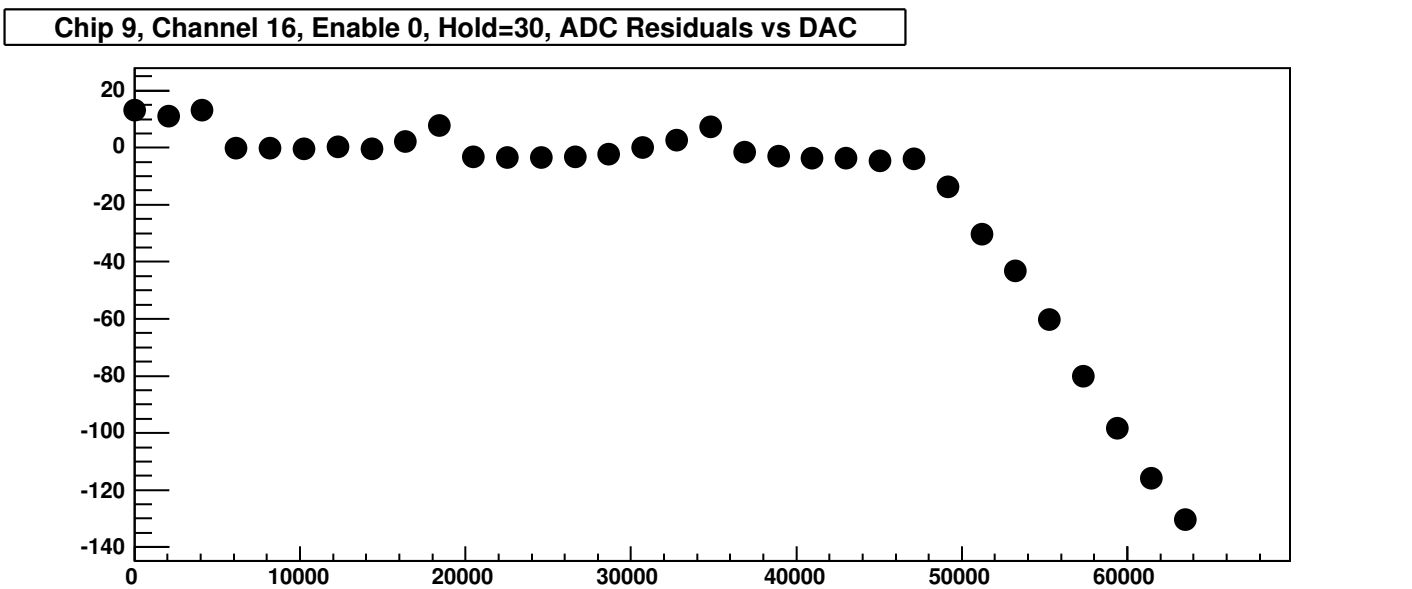

Chip 9, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7424 / 15
p0 1095 ± 0.2969
p1 0.6032 ± 3.011e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

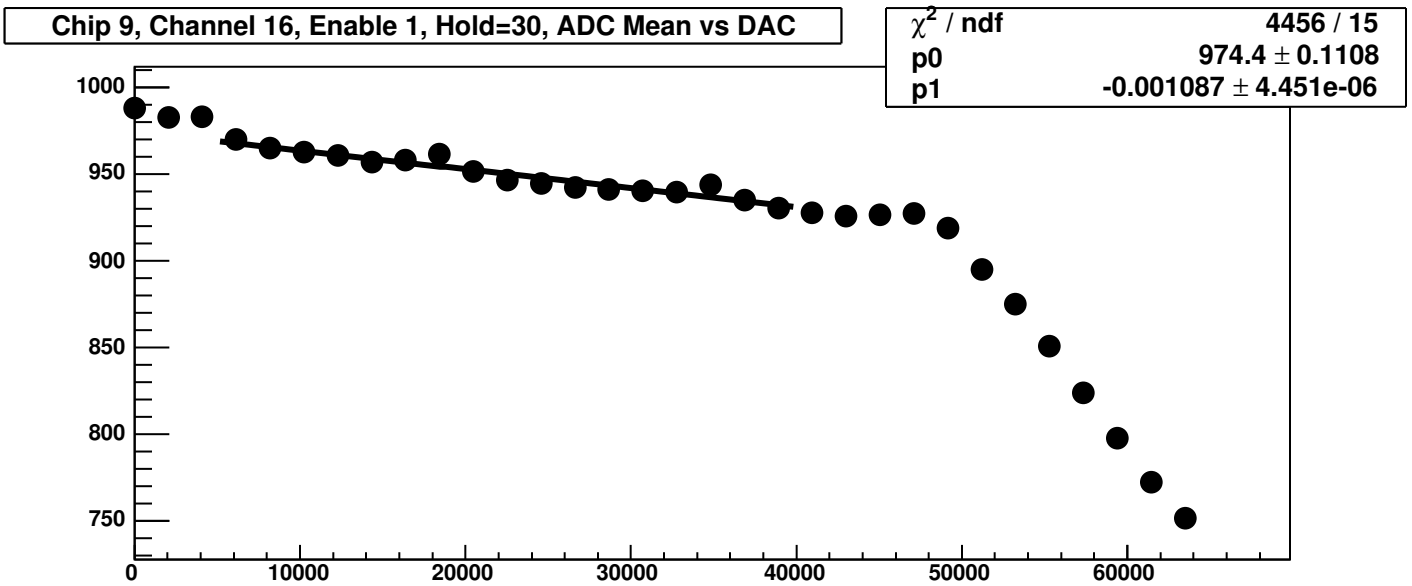

Chip 9, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

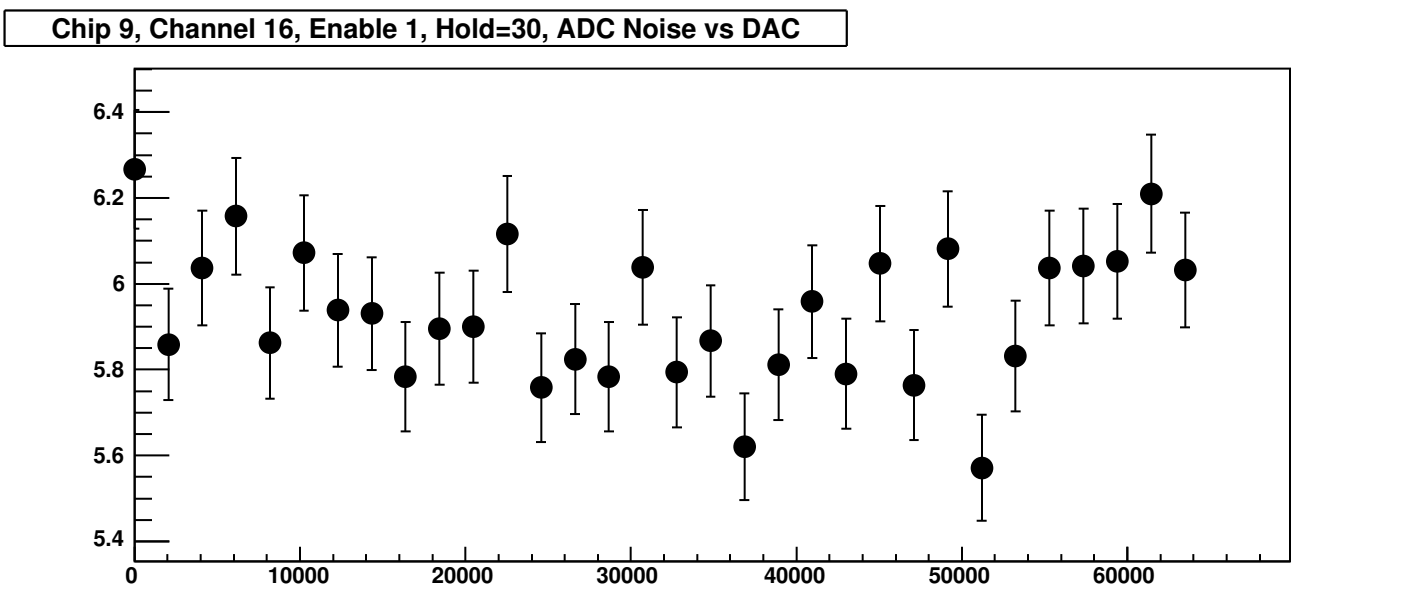

Chip 9, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

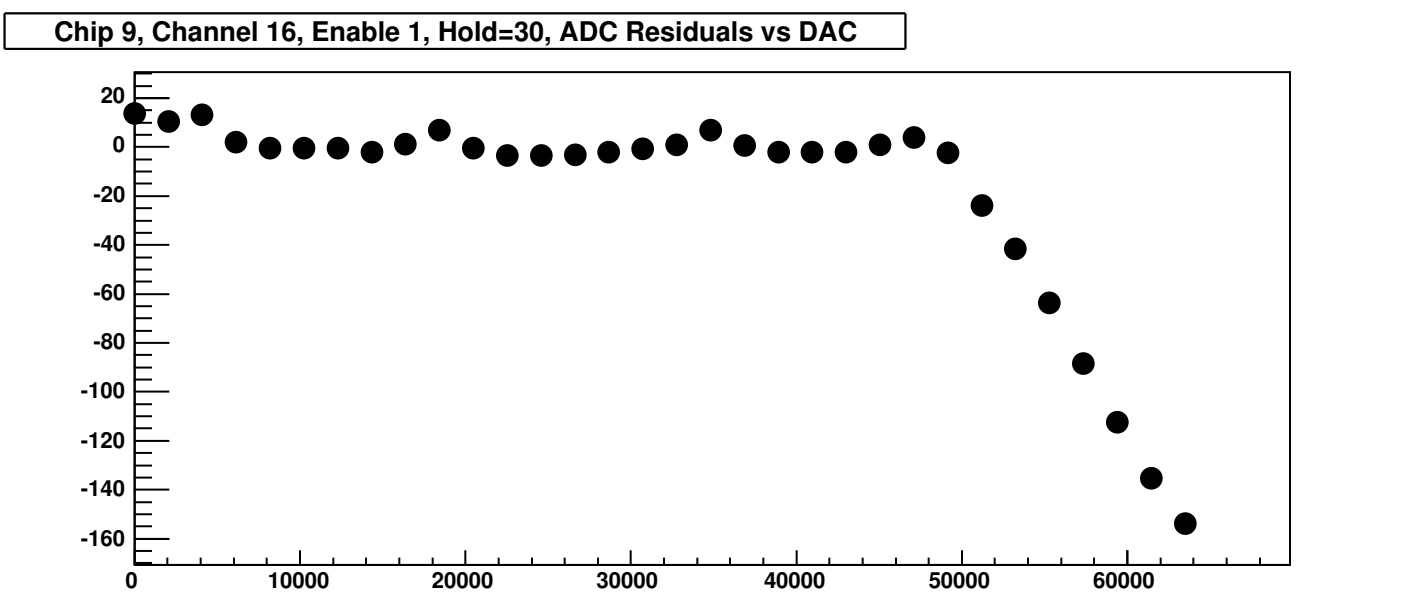

Chip 9, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5675 / 15
p0 975.2 ± 0.1108
p1 -0.001085 ± 4.485e-06
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

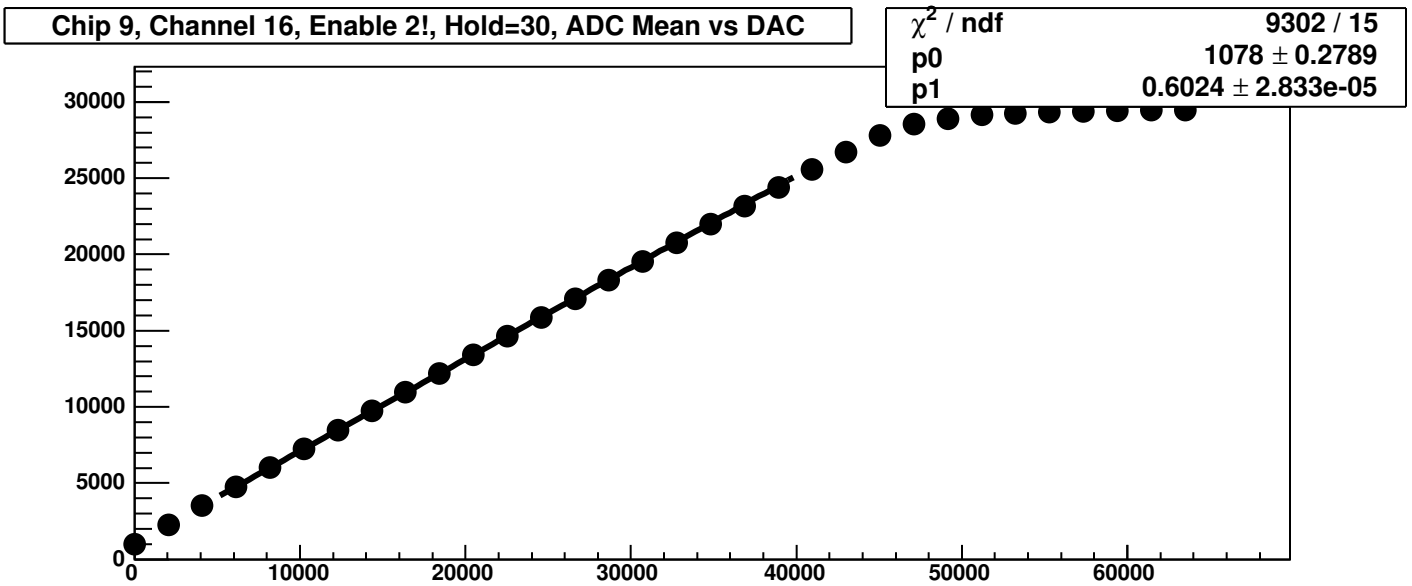

Chip 9, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

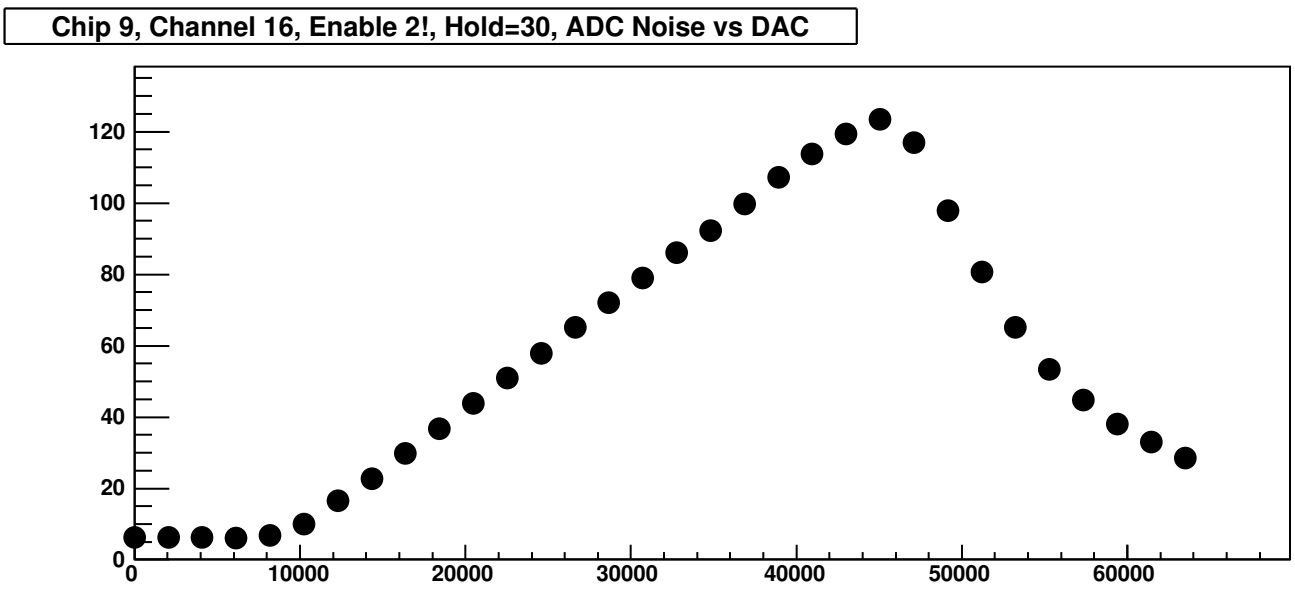

Chip 9, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

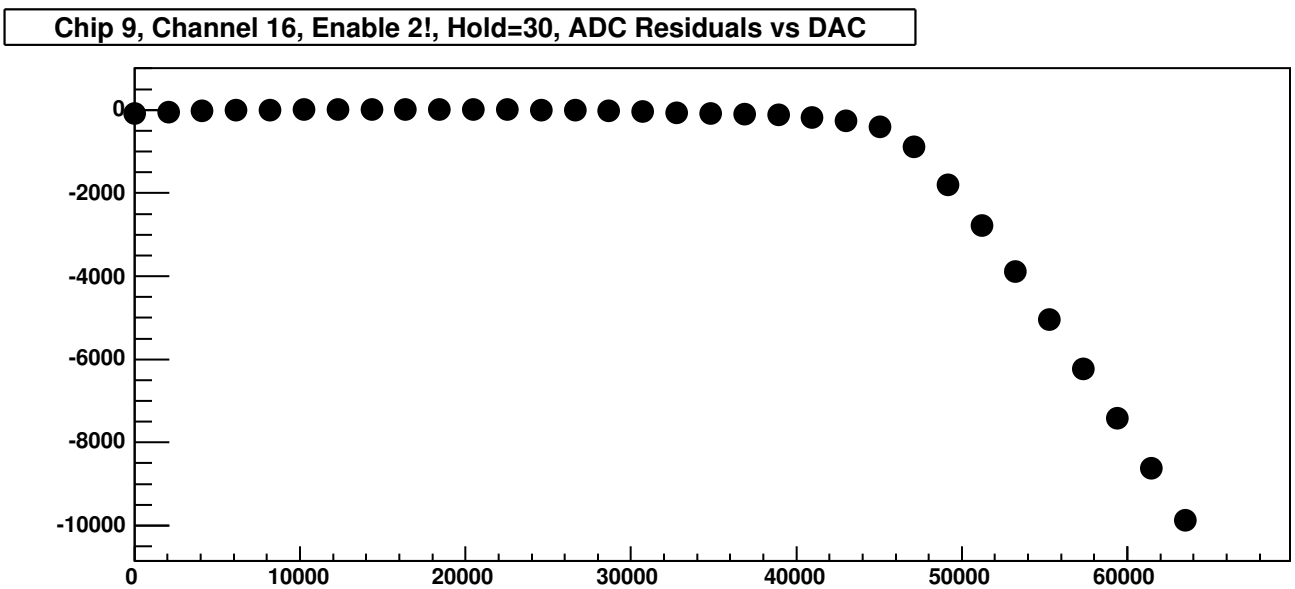

Chip 9, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4456 / 15
p0 974.4 ± 0.1108
p1 -0.001087 ± 4.451e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

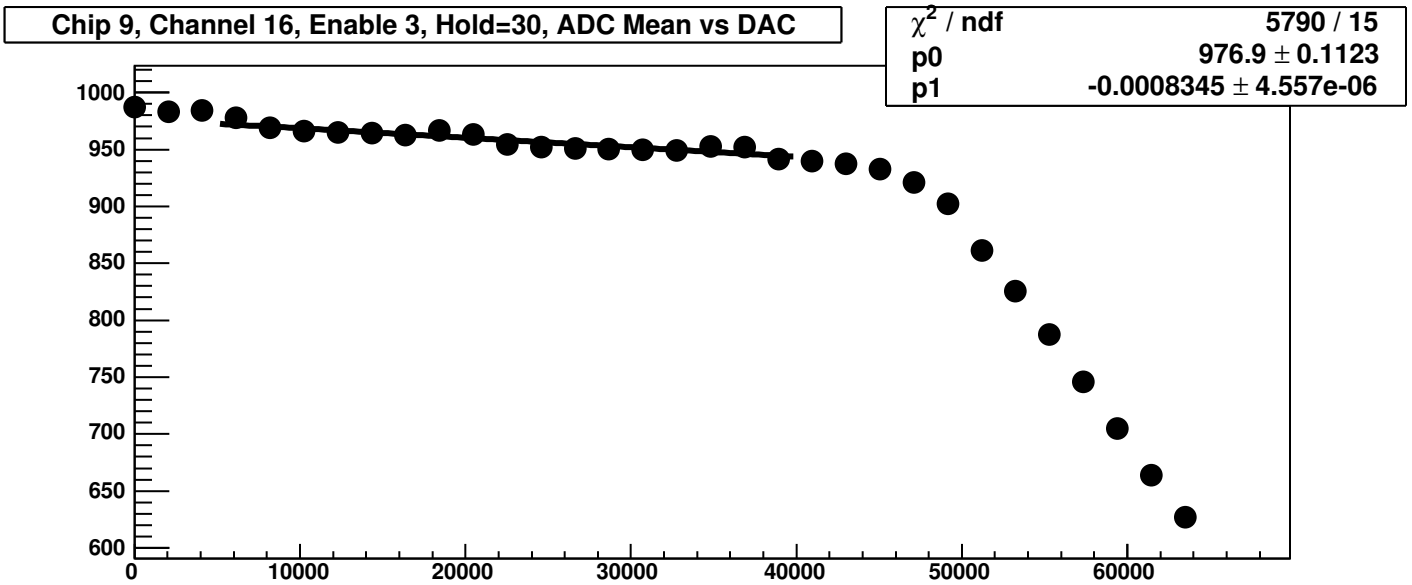

Chip 9, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

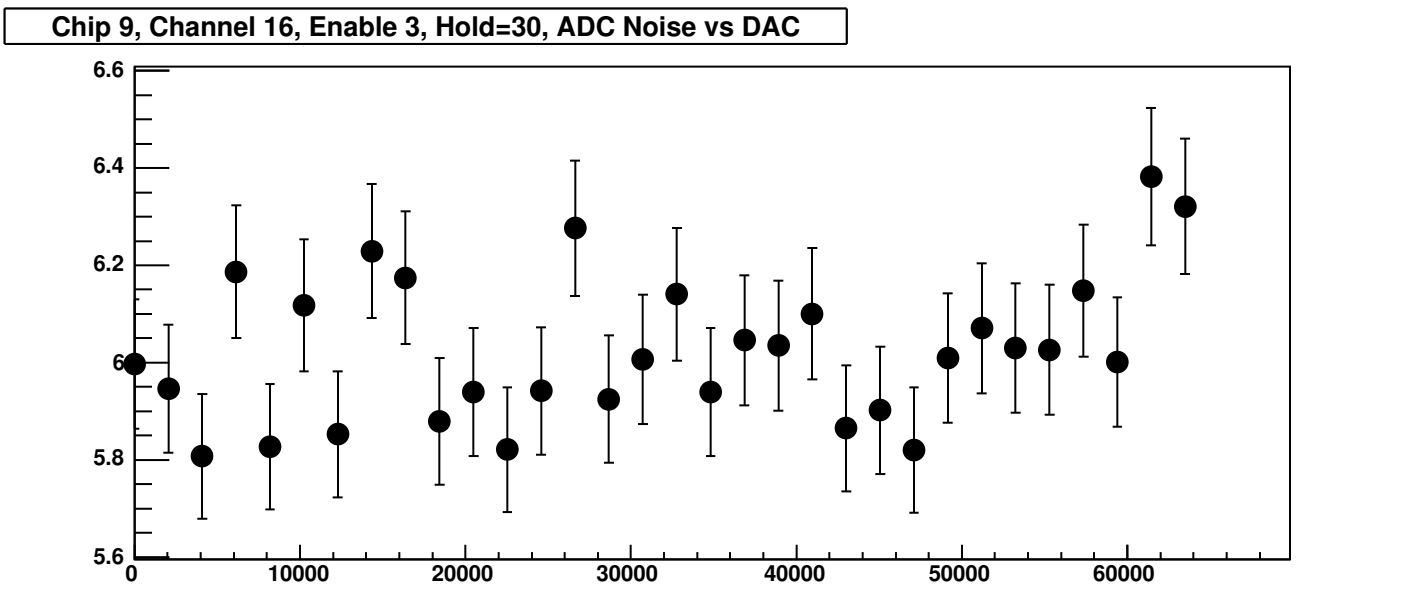

Chip 9, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

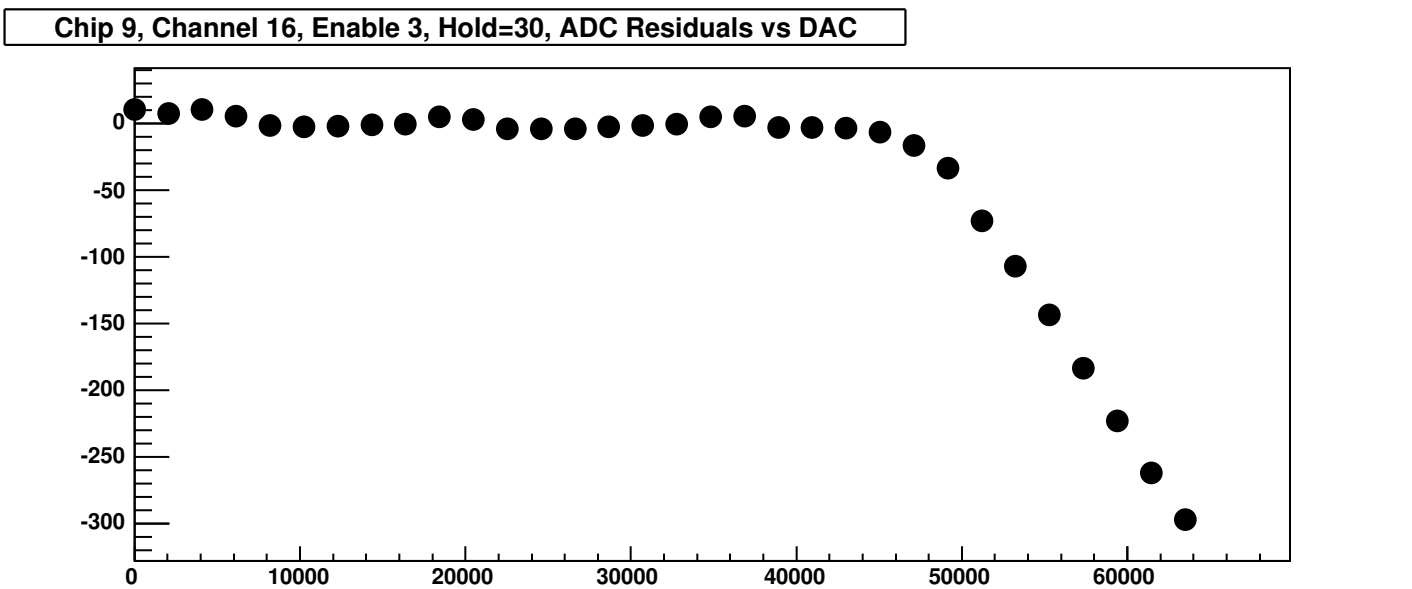

Chip 9, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 9302 / 15
p0 1078 ± 0.2789
p1 0.6024 ± 2.833e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

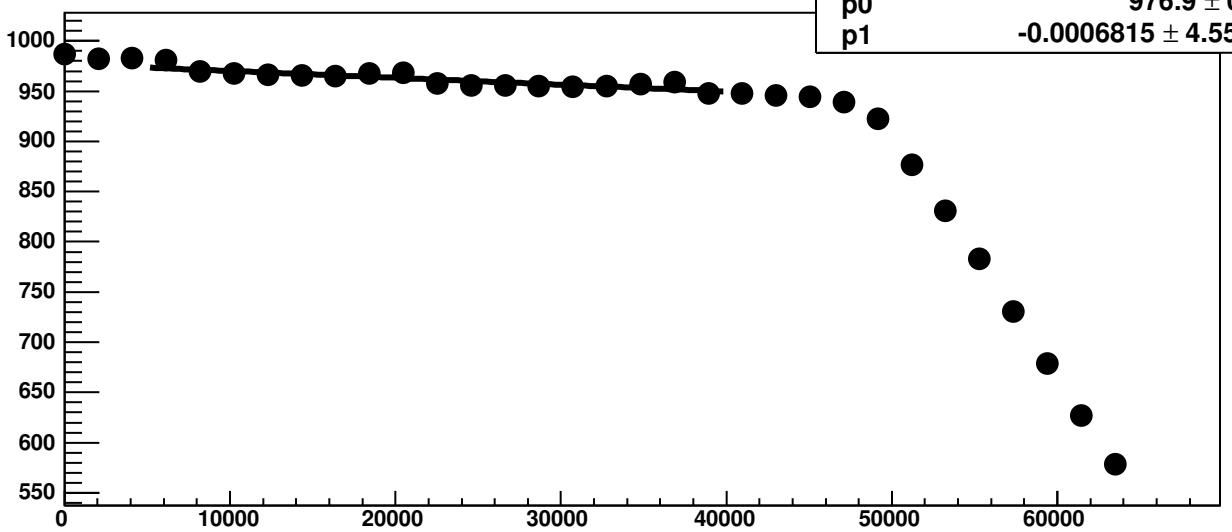

Chip 9, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5790 / 15
p0 976.9 ± 0.1123
p1 -0.0008345 ± 4.557e-06
1000
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

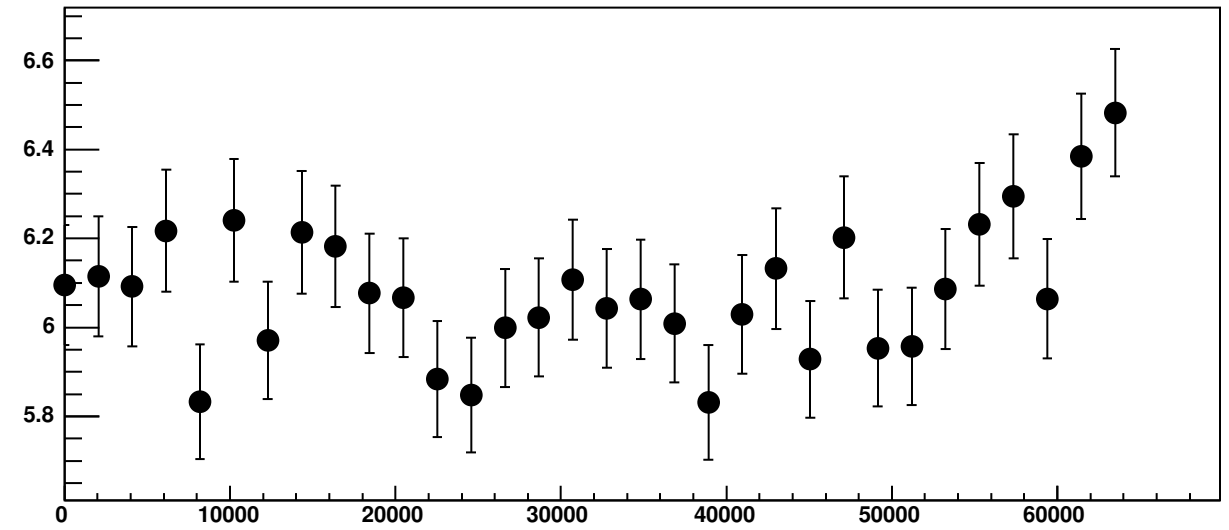

Chip 9, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

Chip 9, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

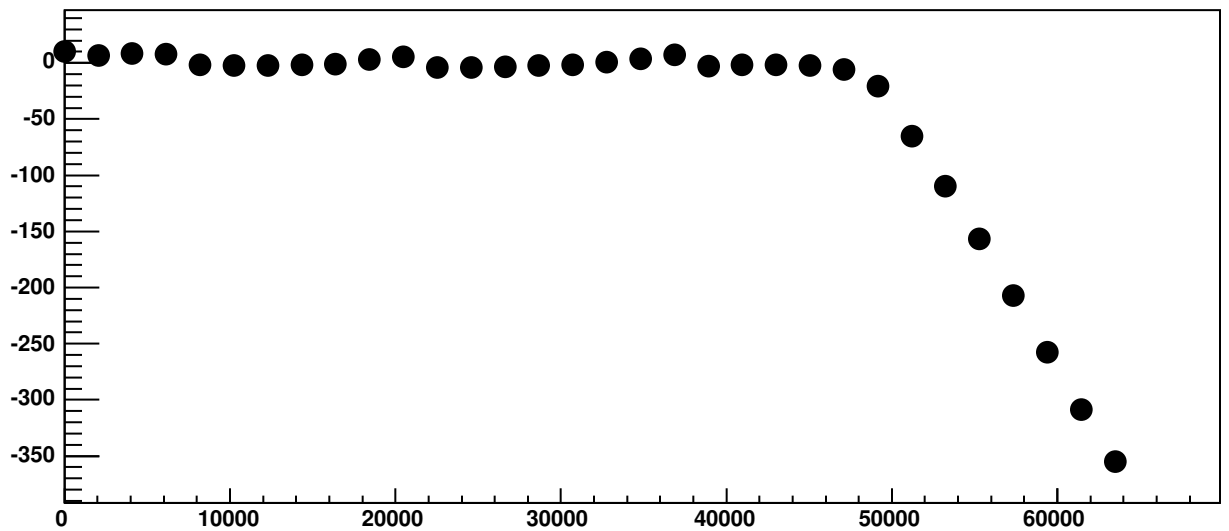
χ^2 / ndf	6849 / 15
p0	976.9 ± 0.1129
p1	-0.0006815 ± 4.554e-06

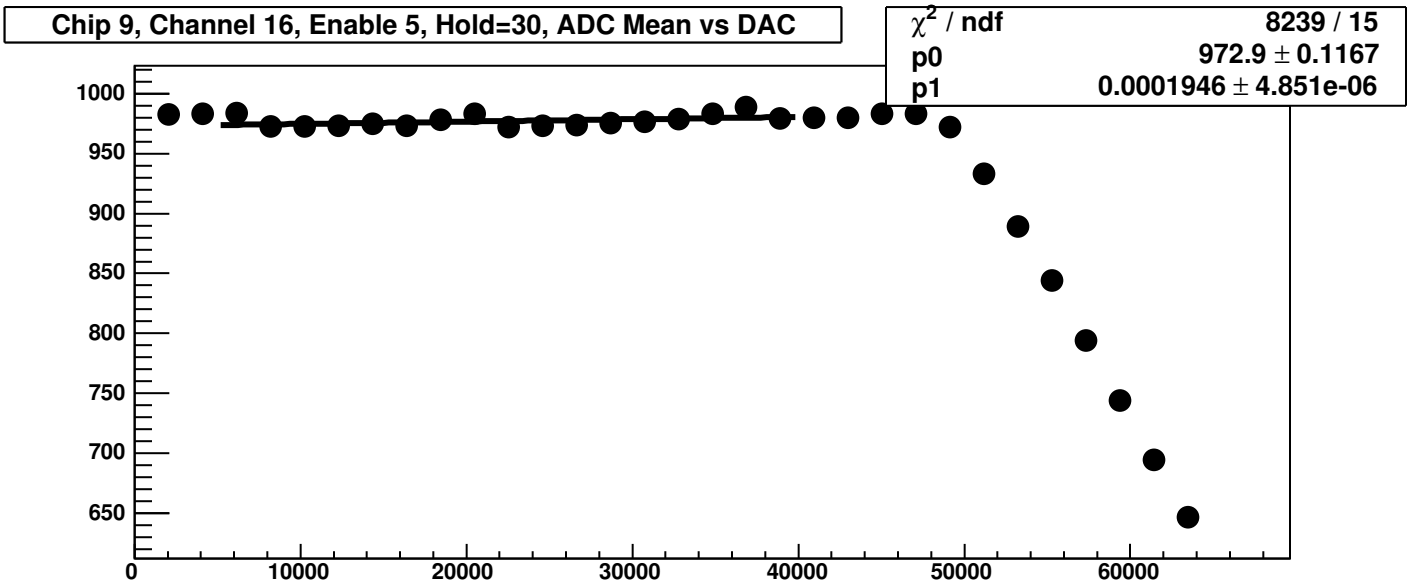


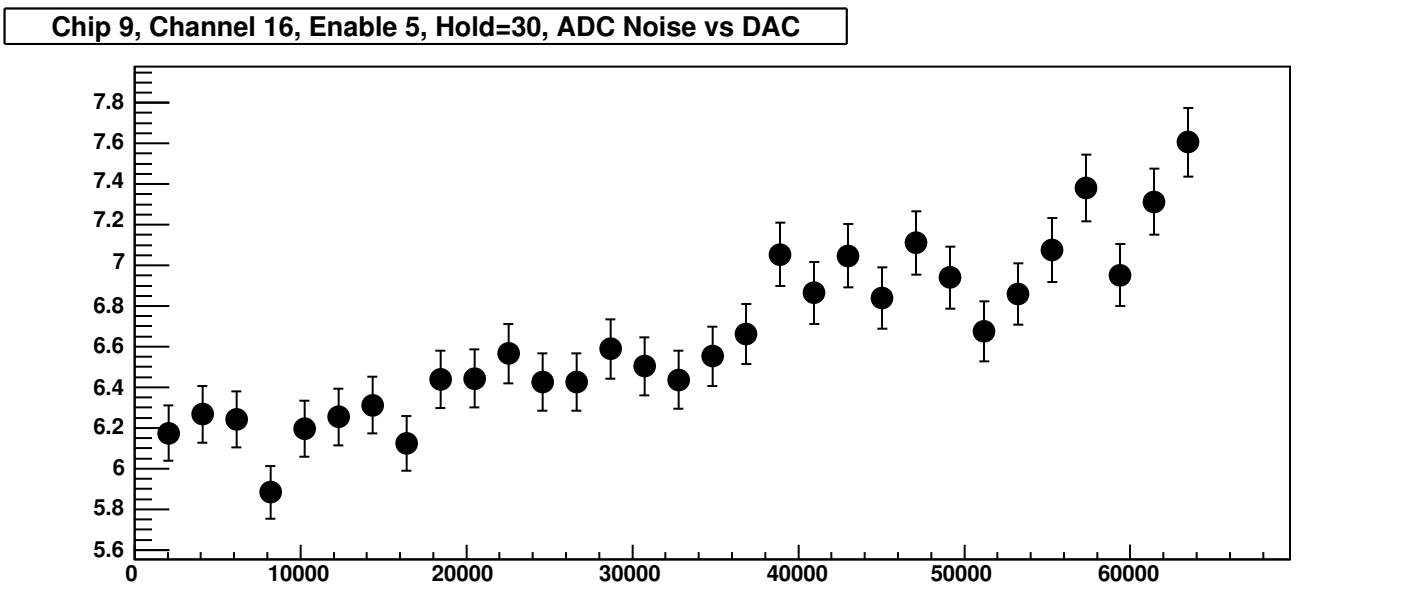
Chip 9, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC

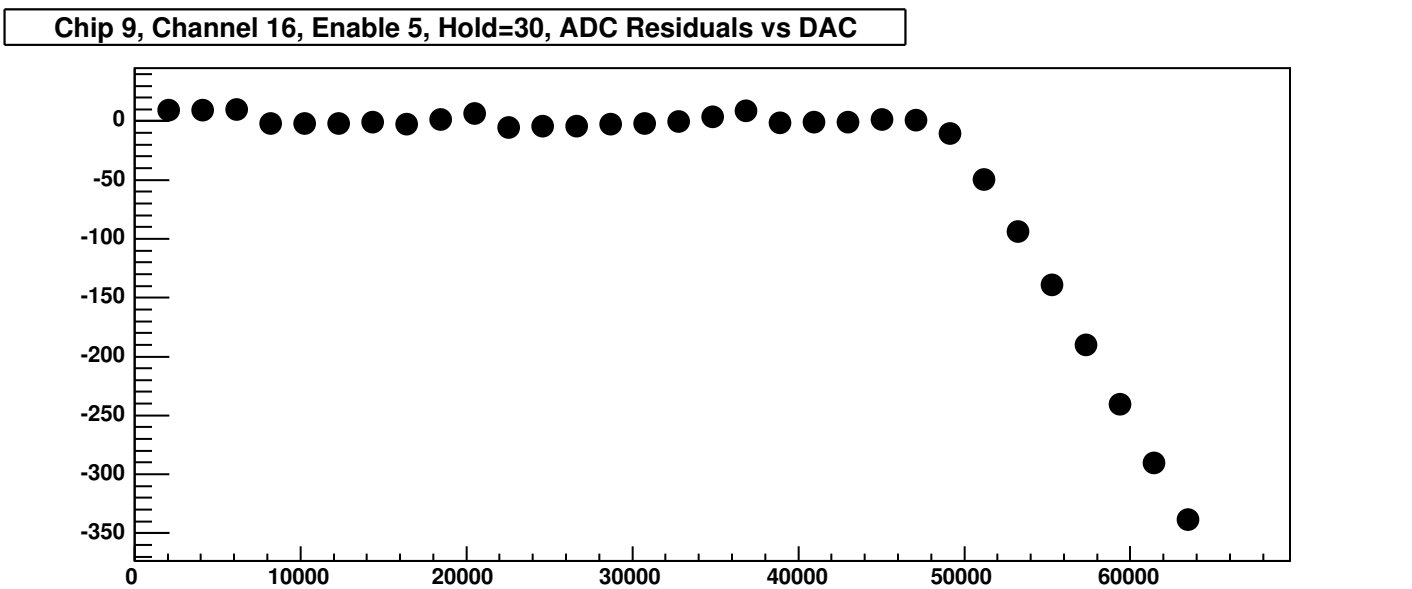


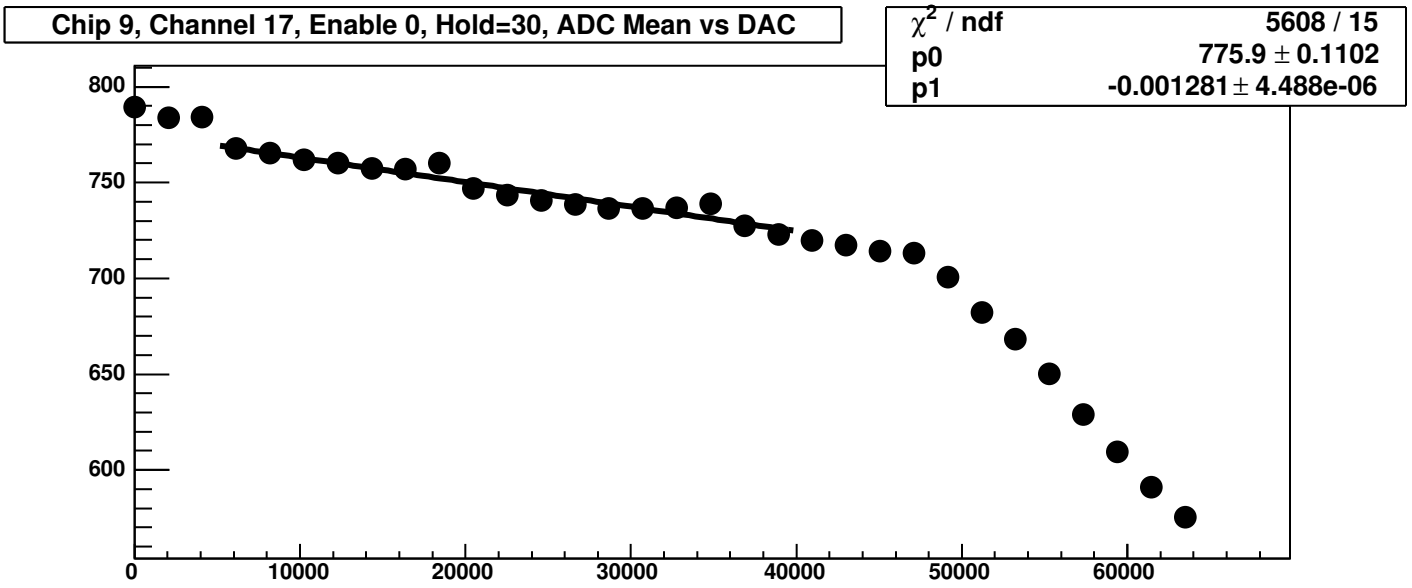
Chip 9, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC

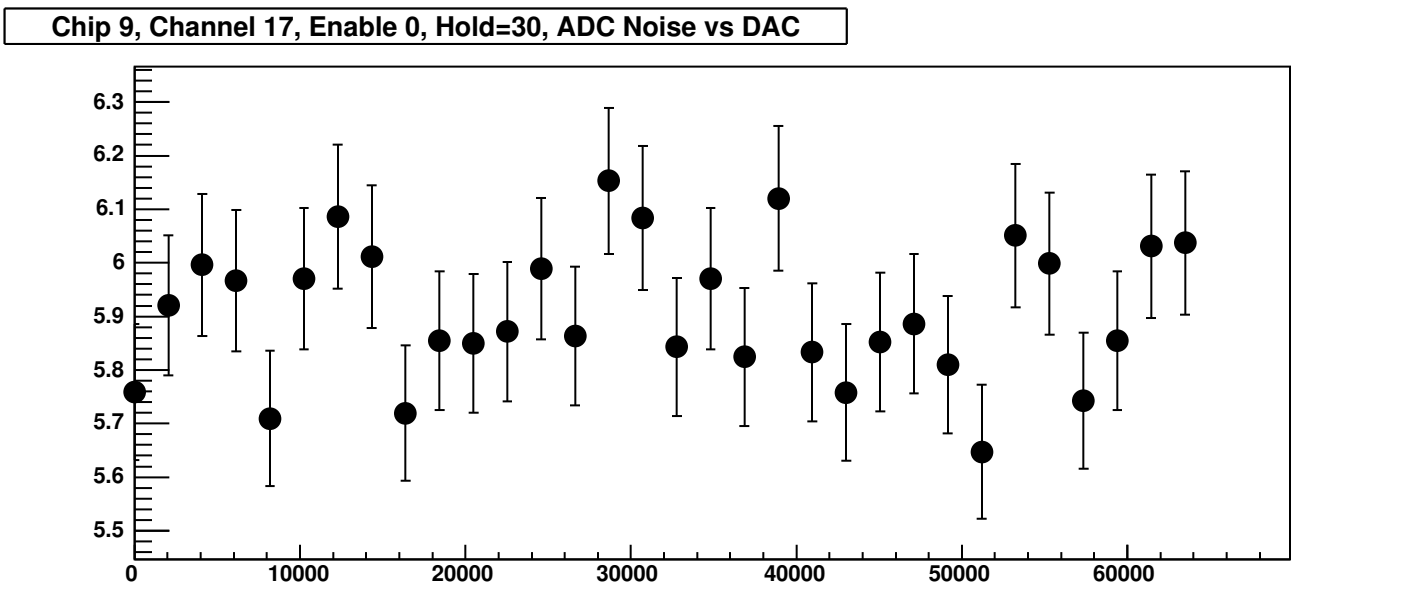


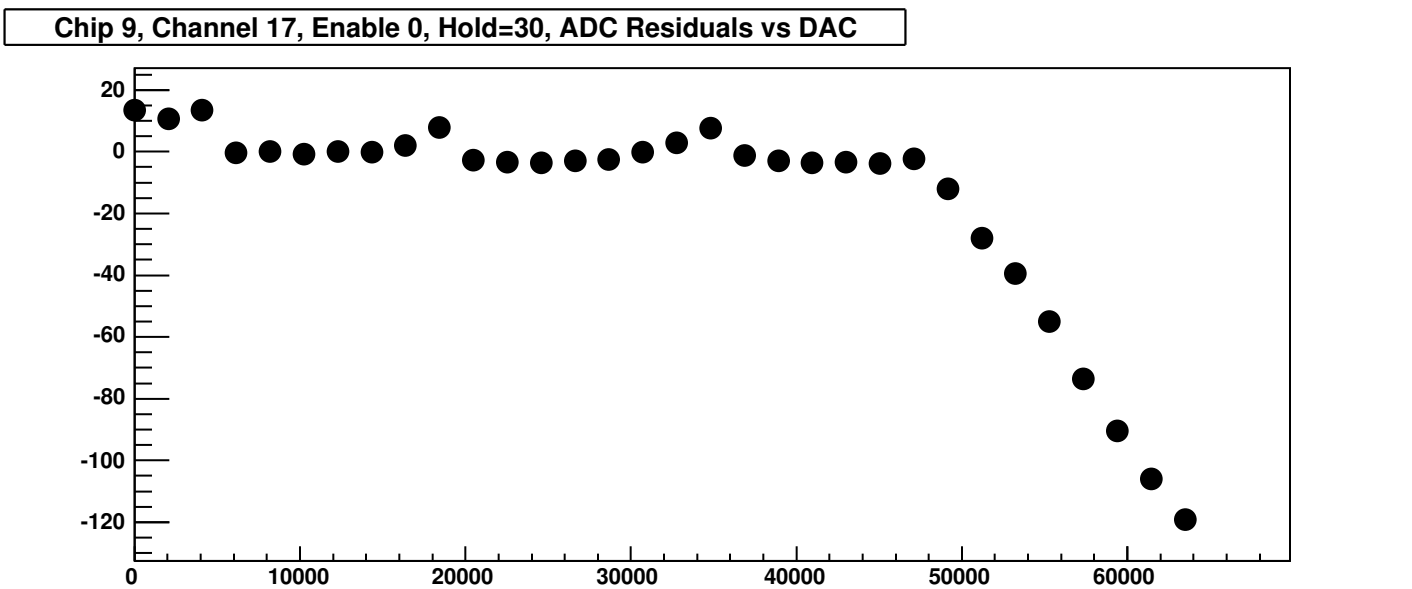

Chip 9, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8239 / 15
p0 972.9 ± 0.1167
p1 0.0001946 ± 4.851e-06
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

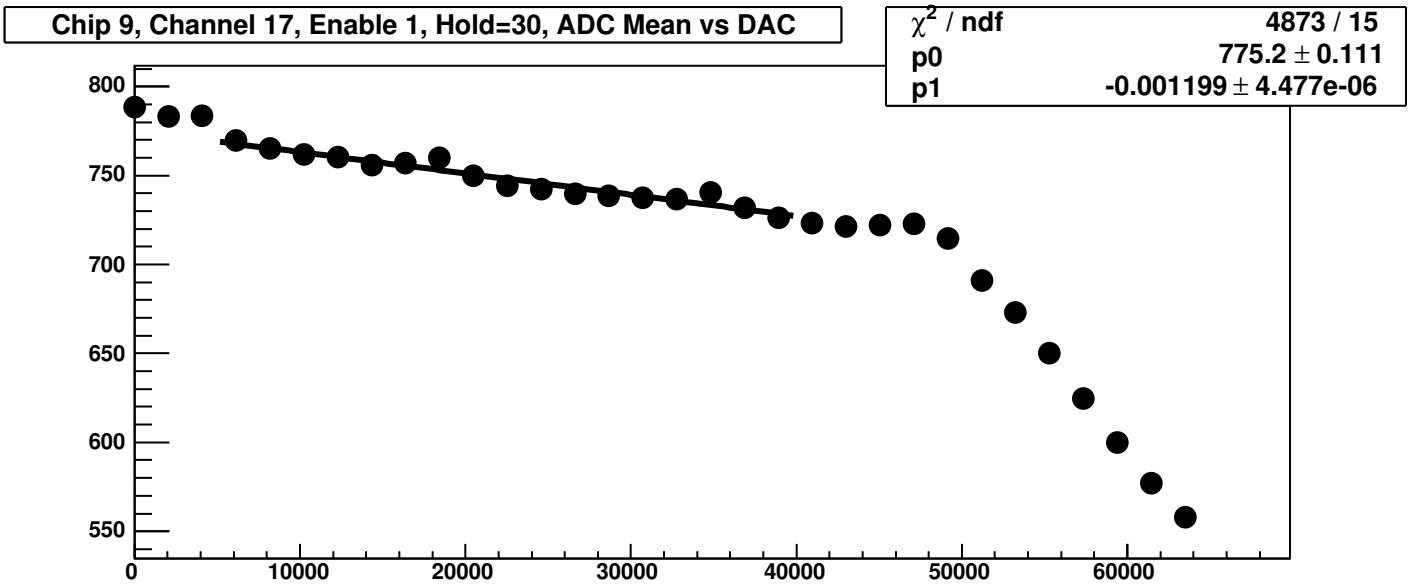

Chip 9, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC
7.8
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

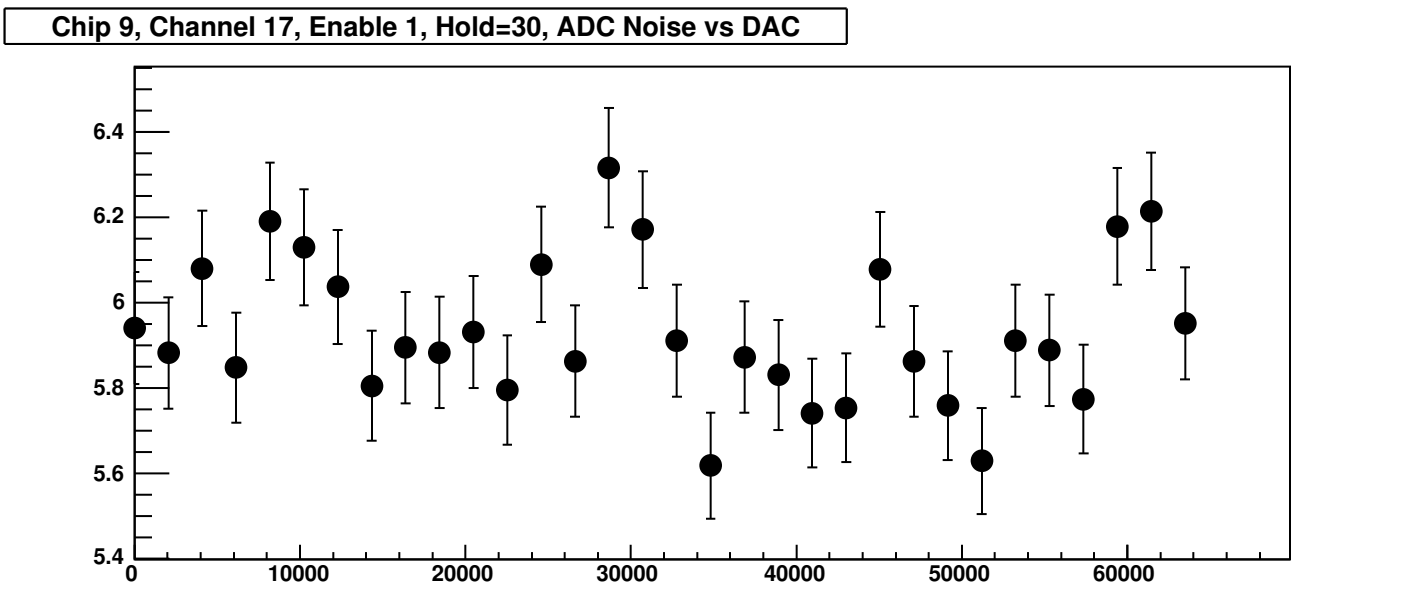

Chip 9, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

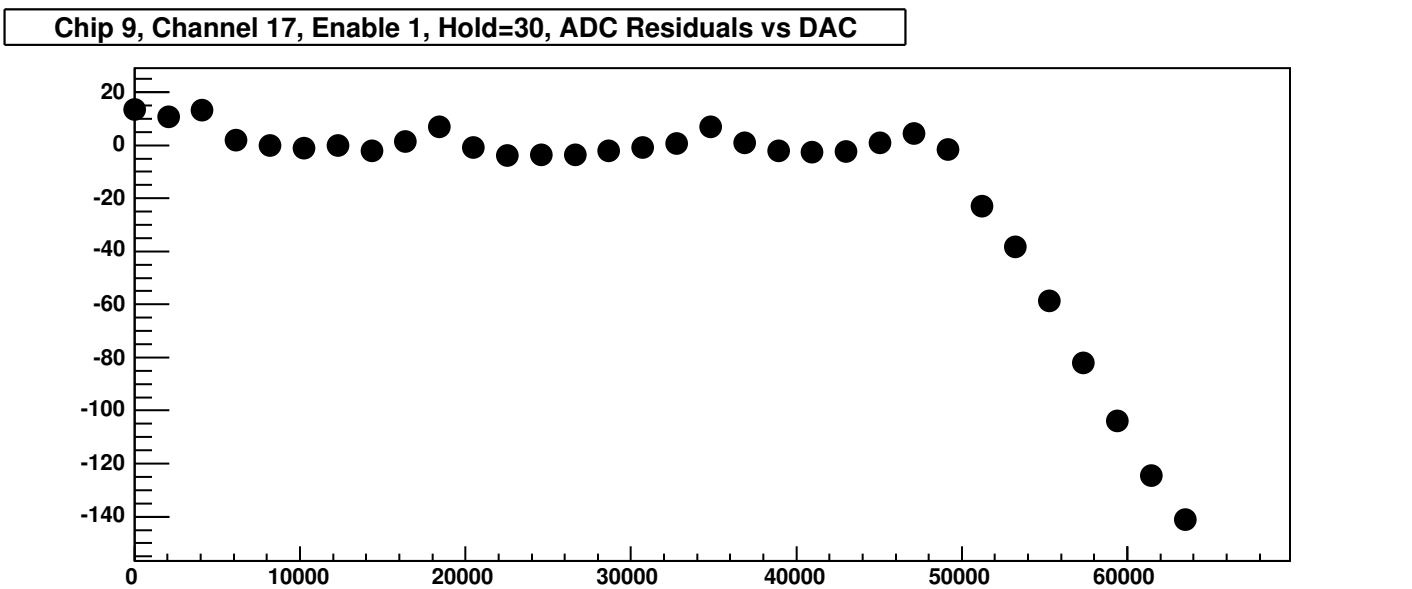

Chip 9, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5608 / 15
p0 775.9 ± 0.1102
p1 -0.001281 ± 4.488e-06
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

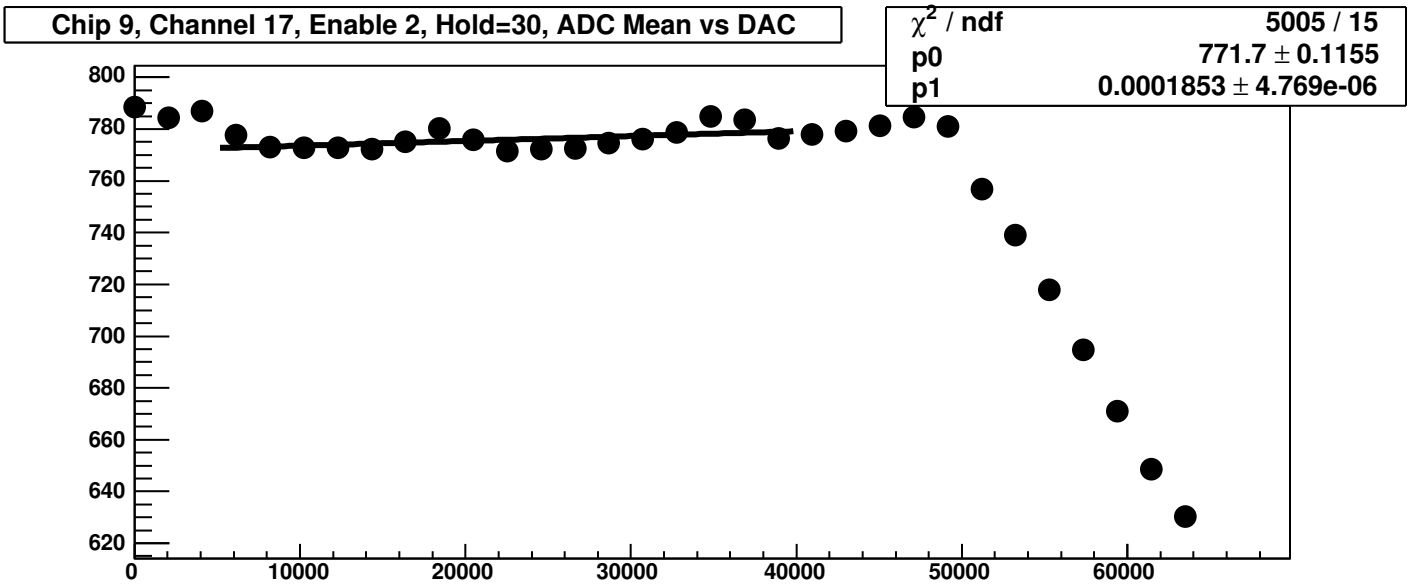

Chip 9, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

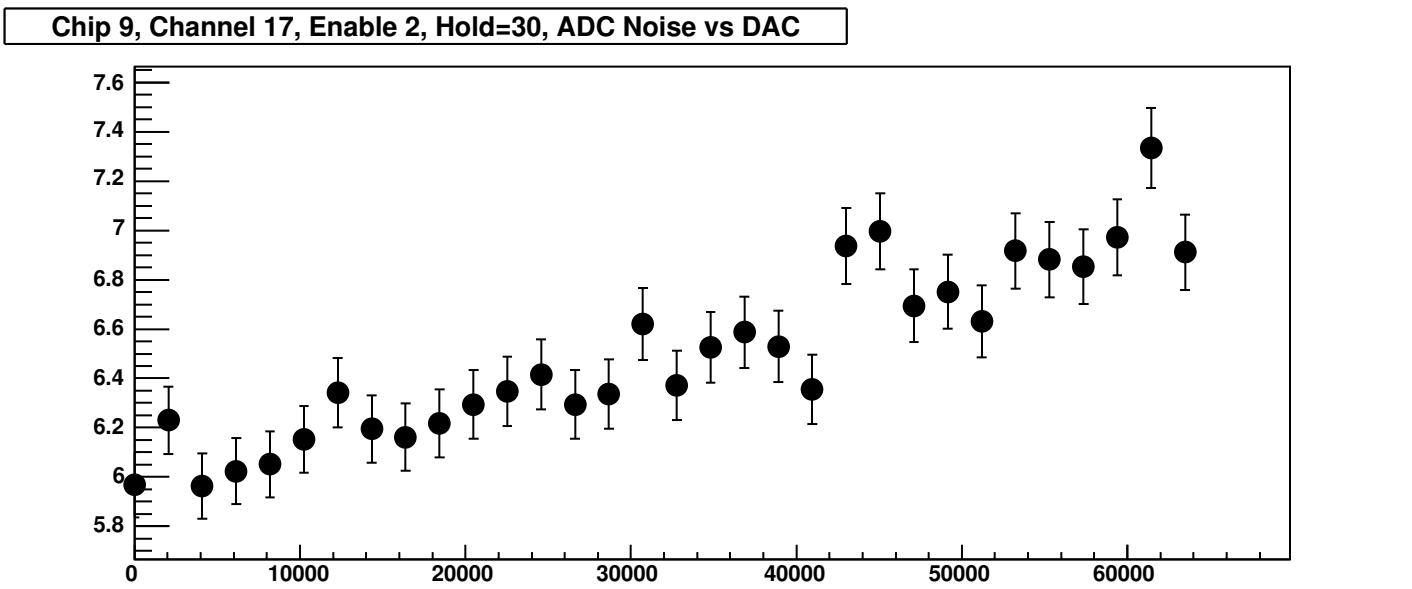

Chip 9, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

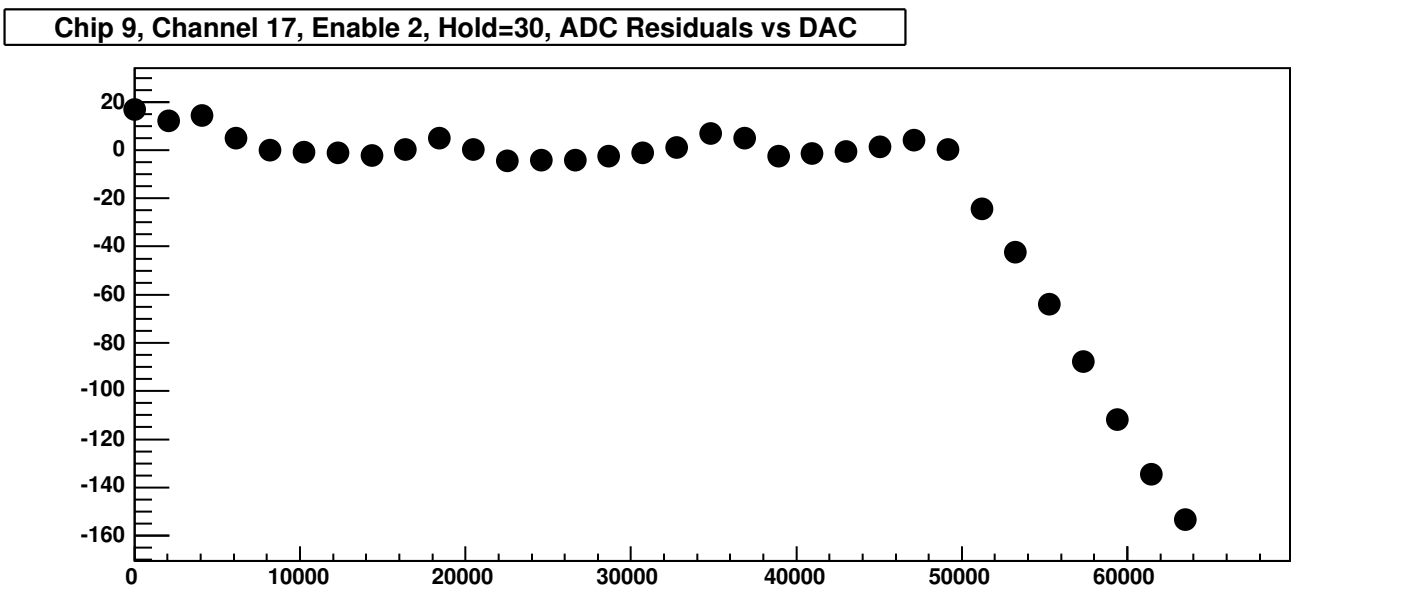

Chip 9, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4873 / 15
p0 775.2 ± 0.111
p1 -0.001199 ± 4.477e-06
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

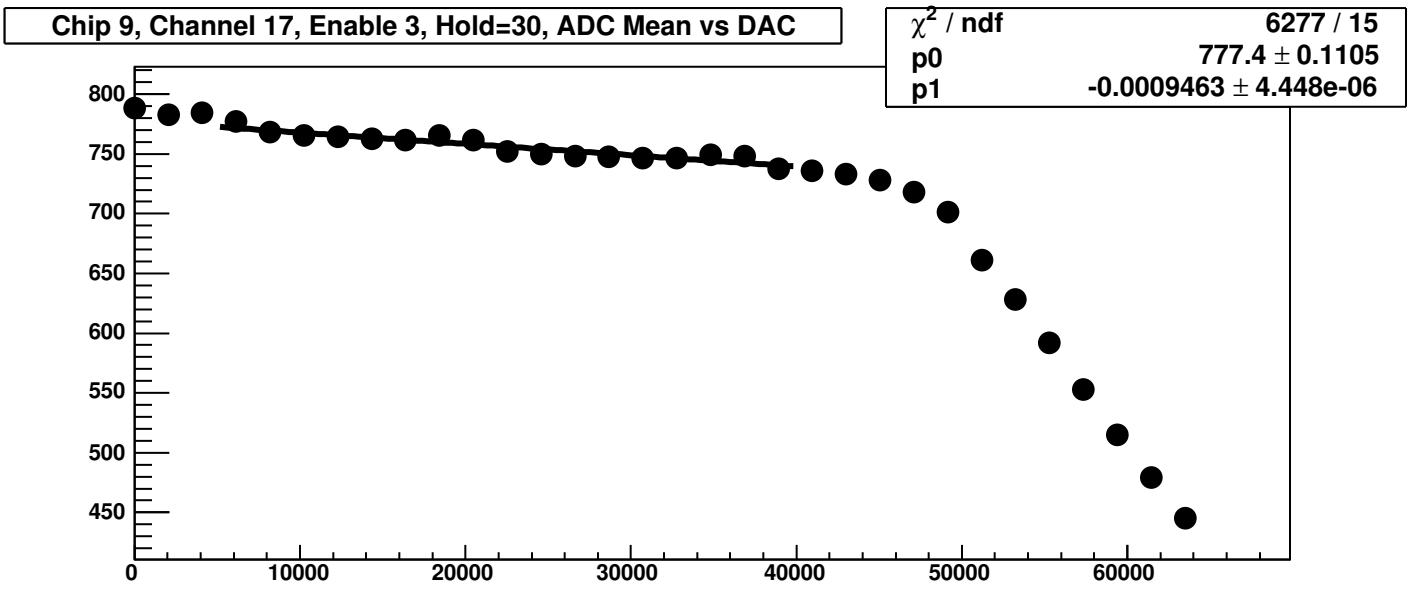

Chip 9, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

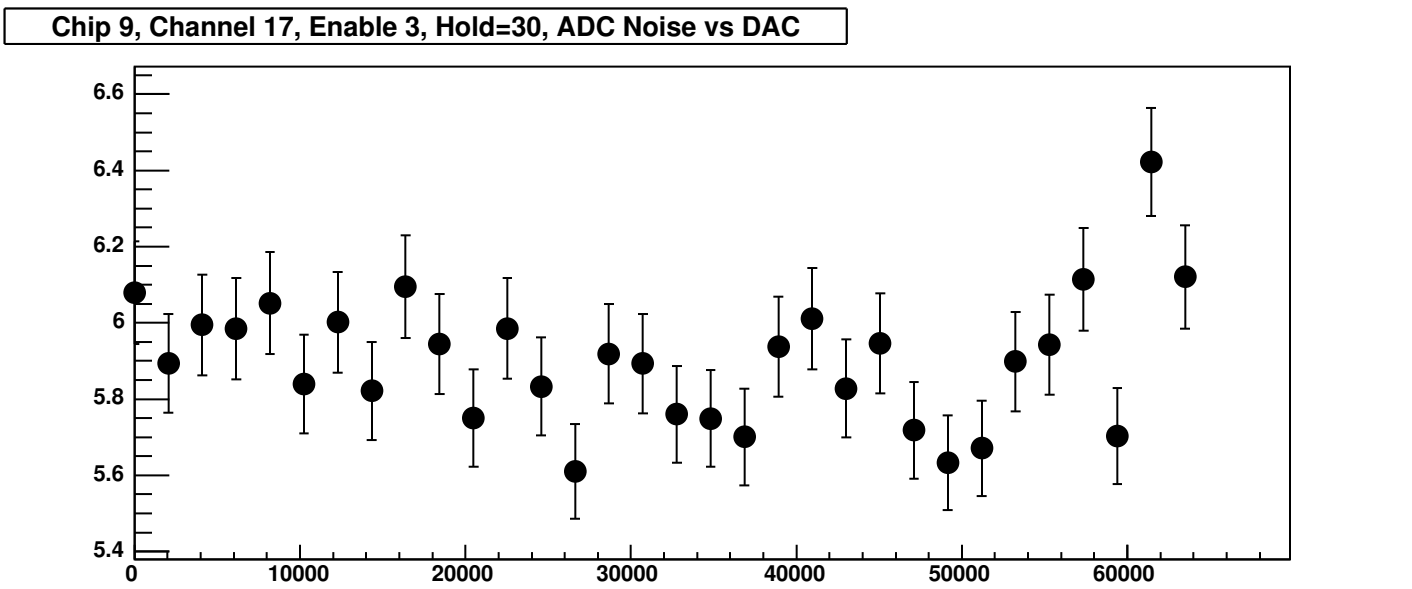

Chip 9, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

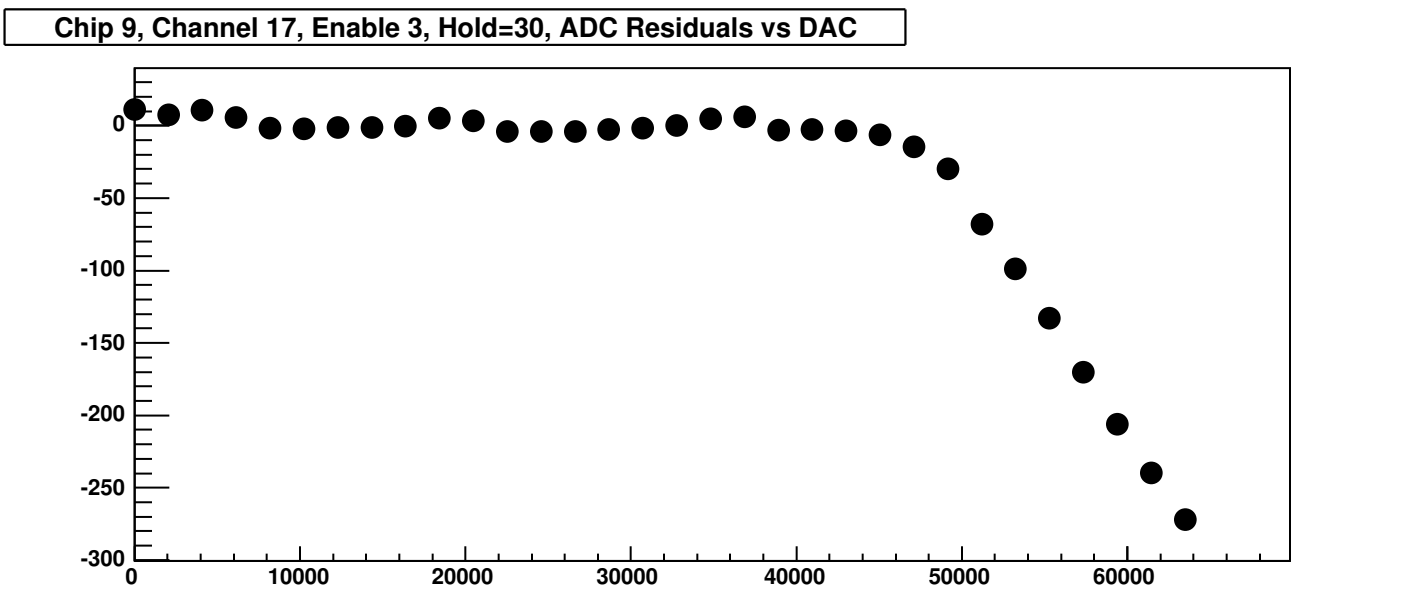

Chip 9, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5005 / 15
p0 771.7 ± 0.1155
p1 0.0001853 ± 4.769e-06
800
780
760
740
720
700
680
660
640
620
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

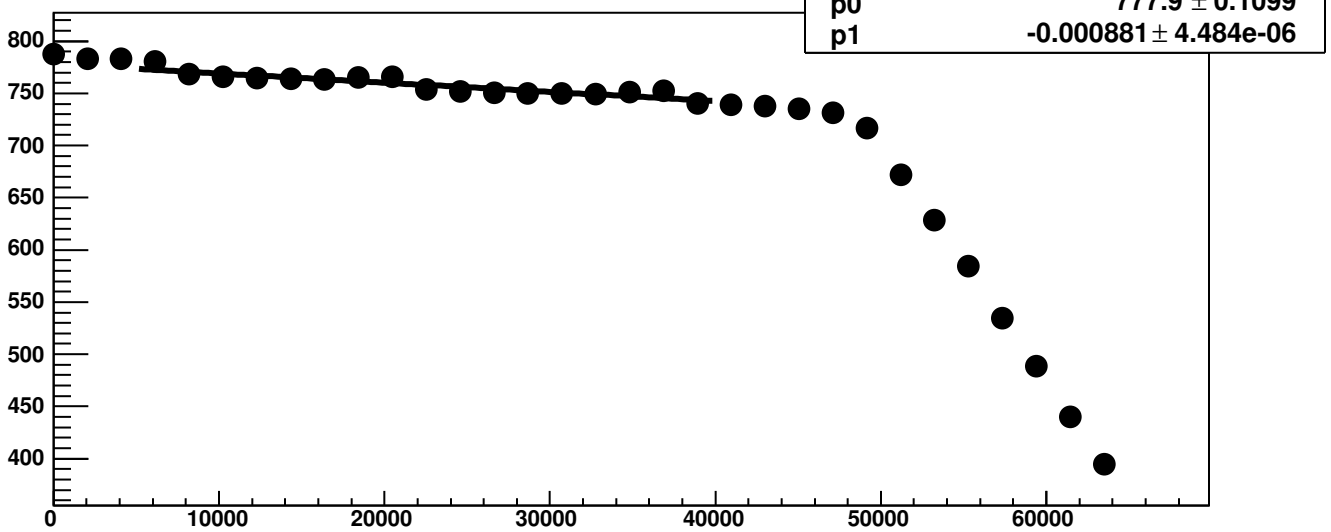

Chip 9, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 6277 / 15
p0 777.4 ± 0.1105
p1 -0.0009463 ± 4.448e-06
800
750
700
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000

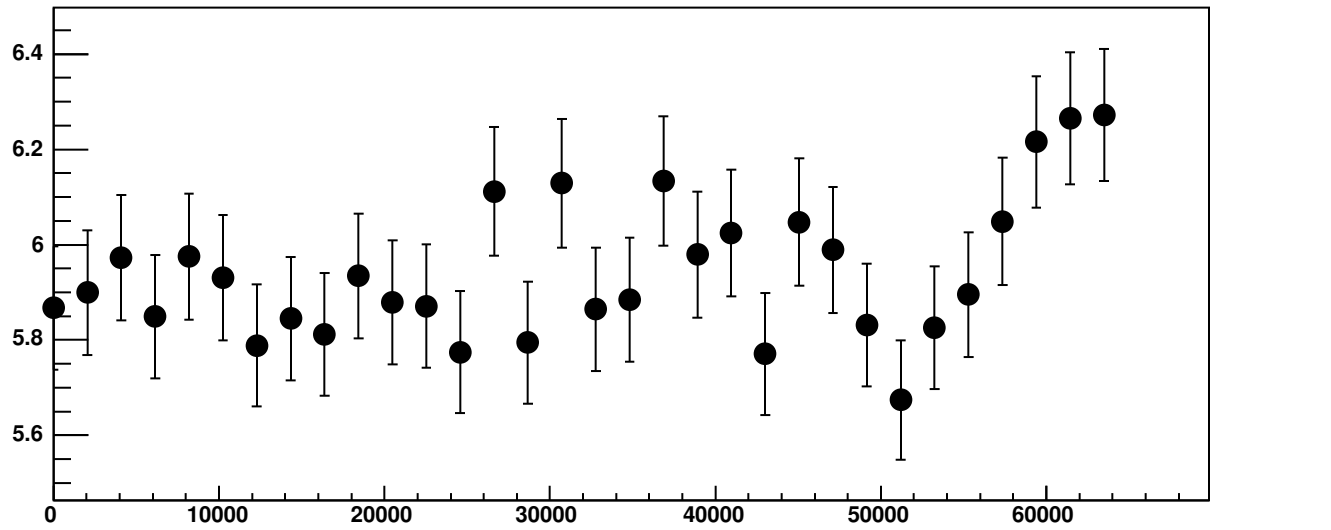

Chip 9, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 9, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

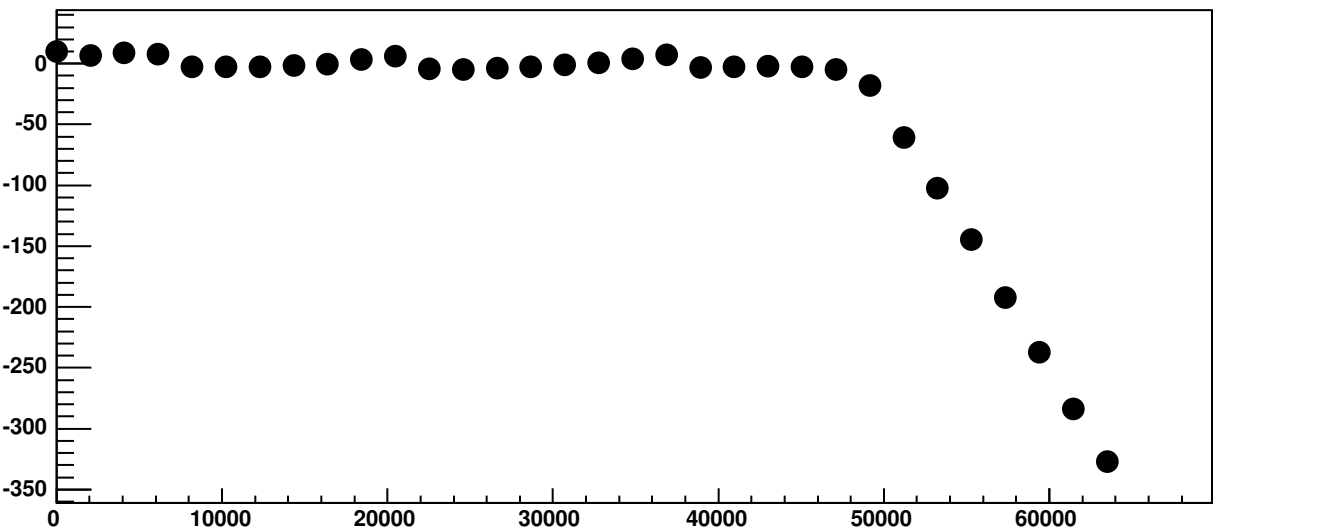
Chip 9, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

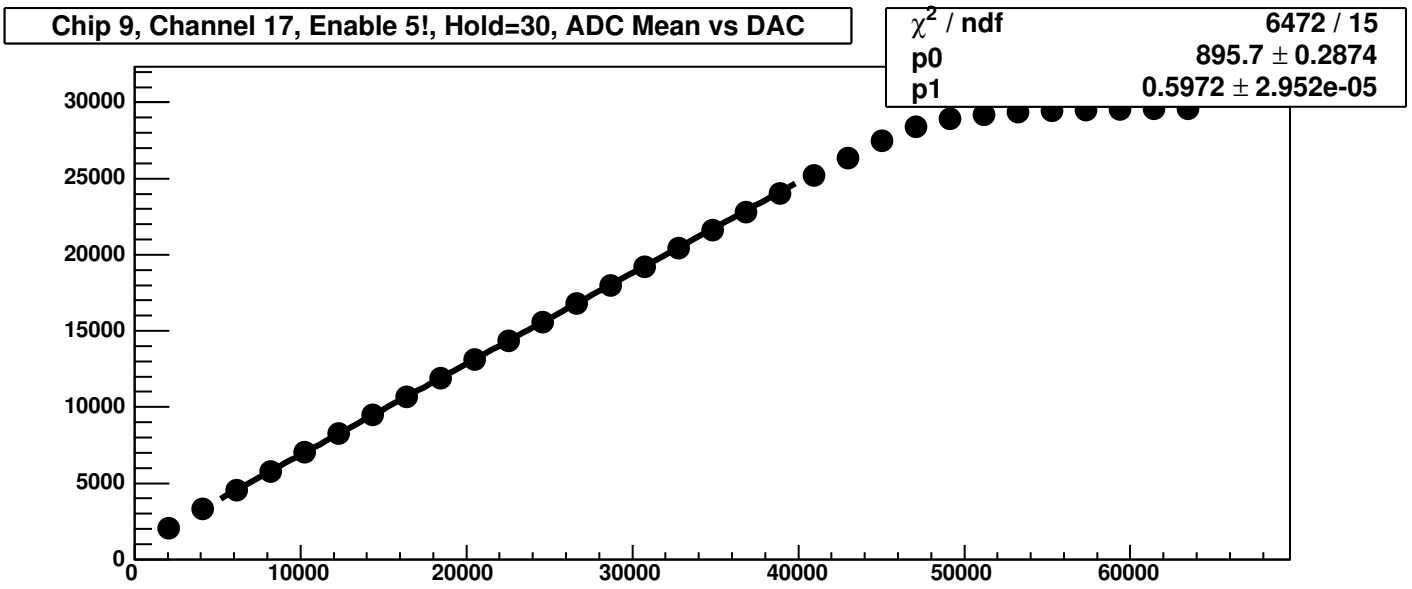


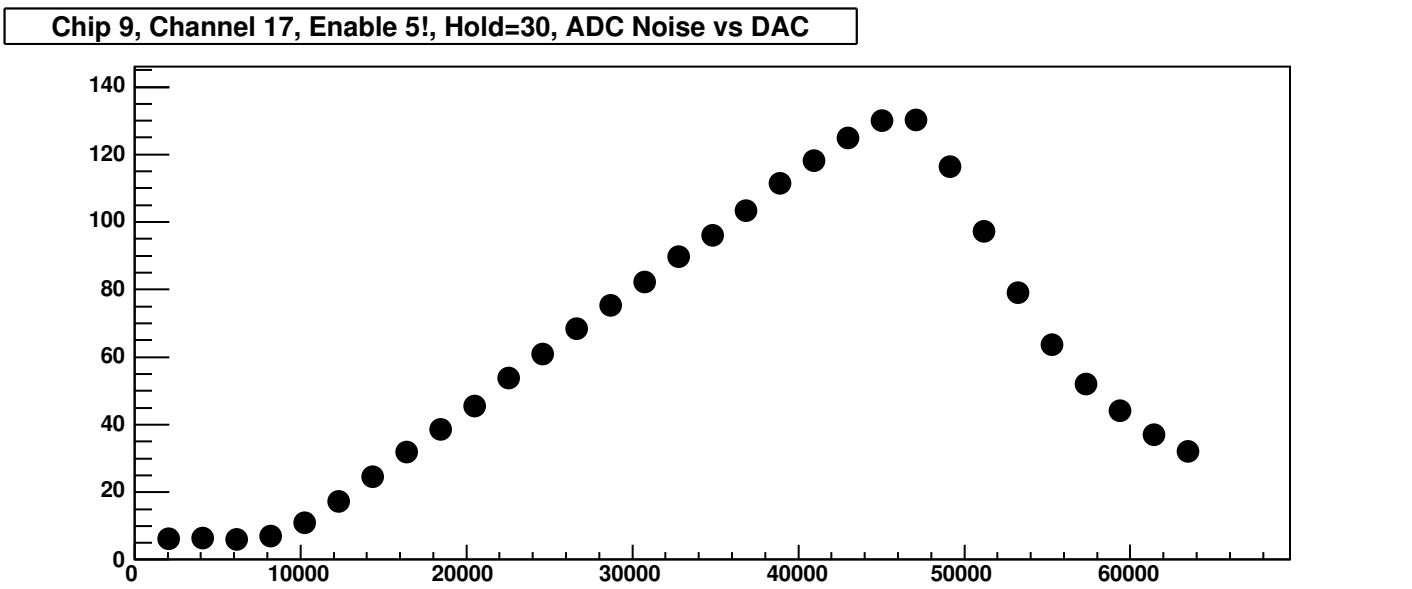
Chip 9, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC

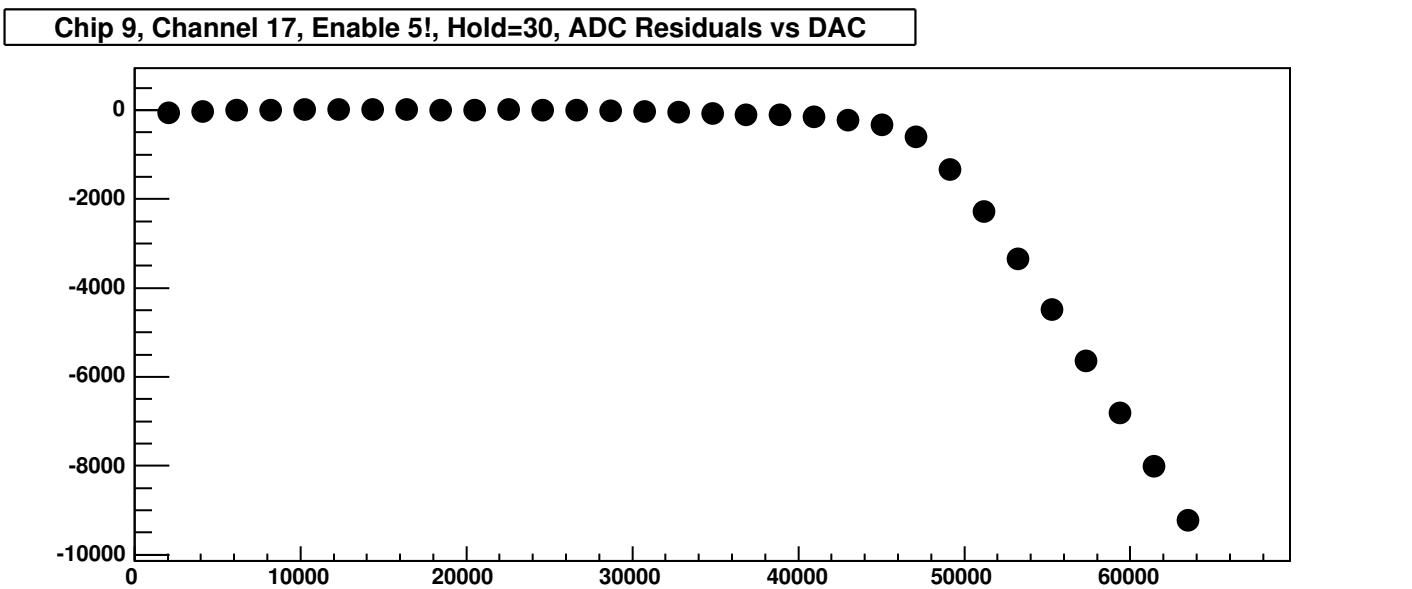


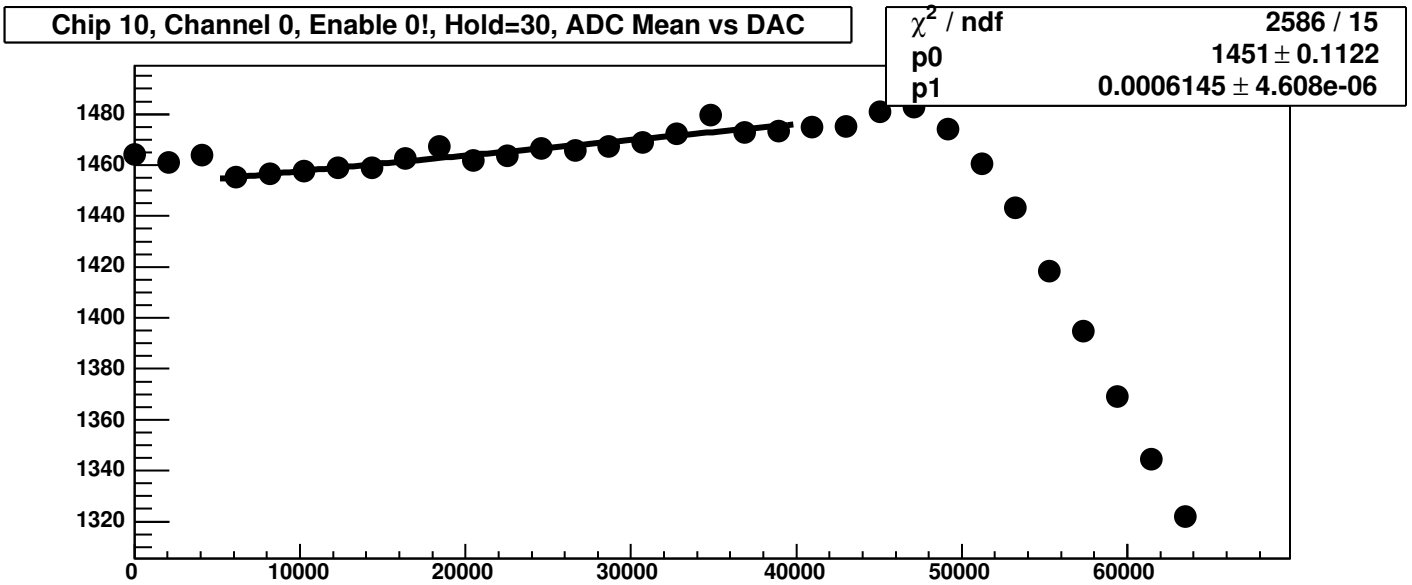
Chip 9, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC

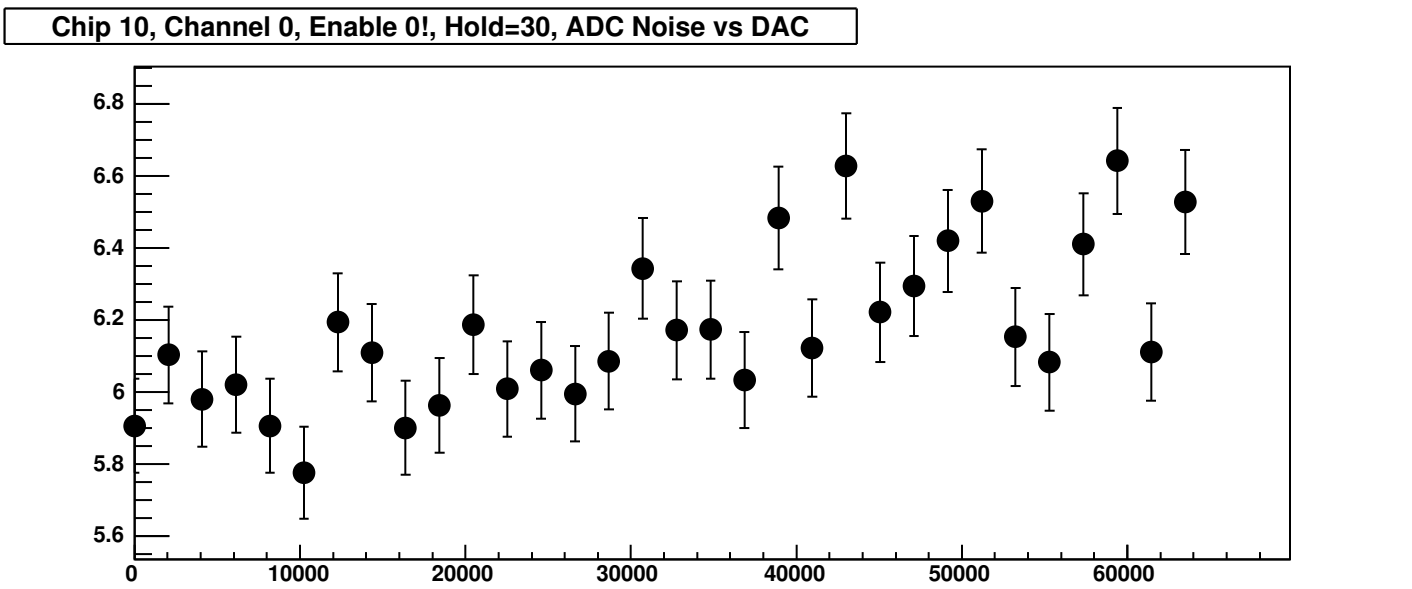


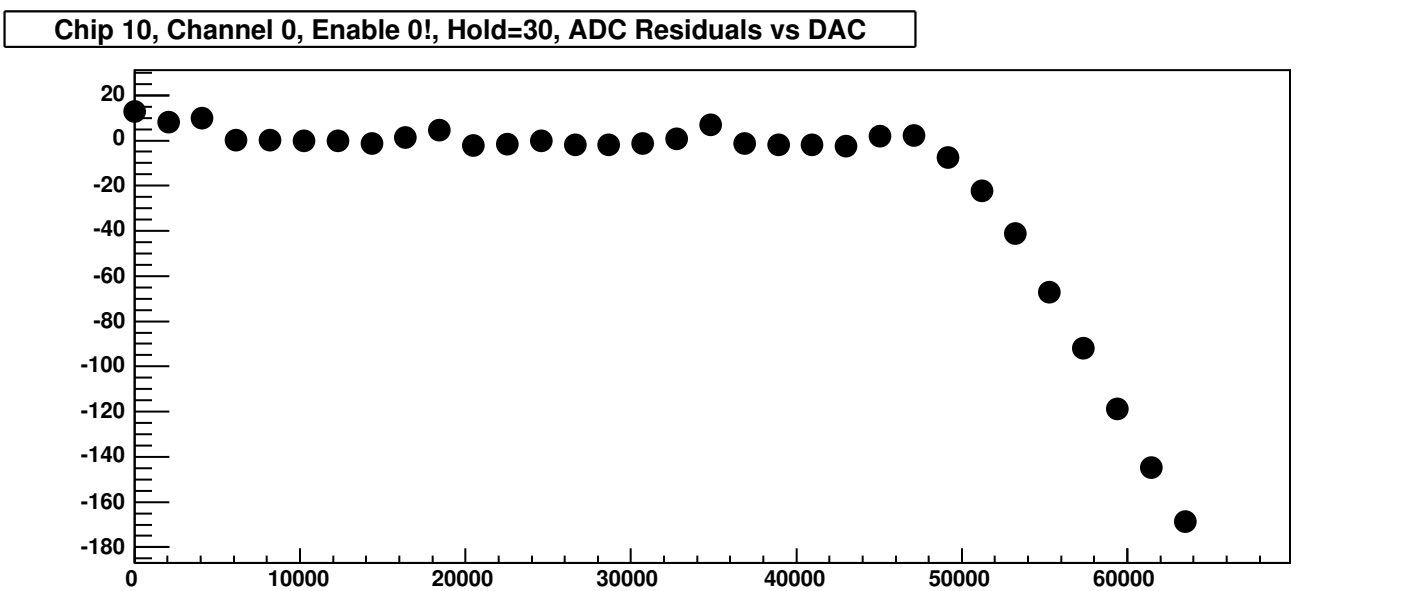

Chip 9, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 6472 / 15
p0 895.7 ± 0.2874
p1 0.5972 ± 2.952e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

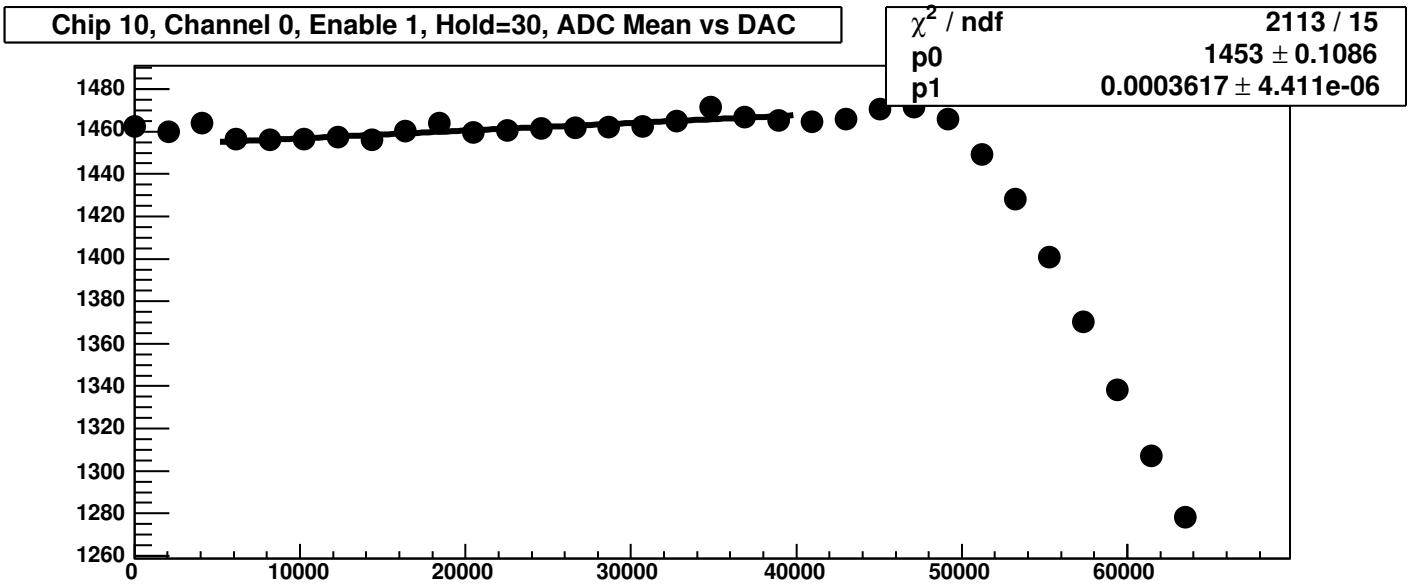

Chip 9, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

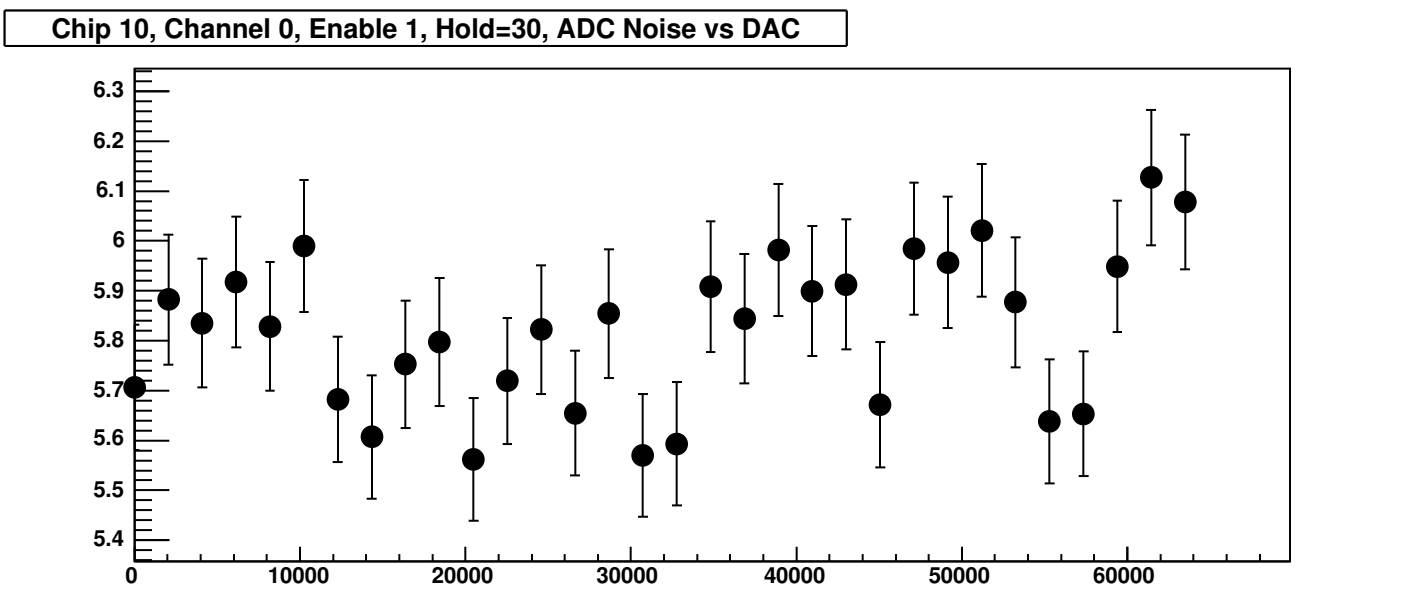

Chip 9, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

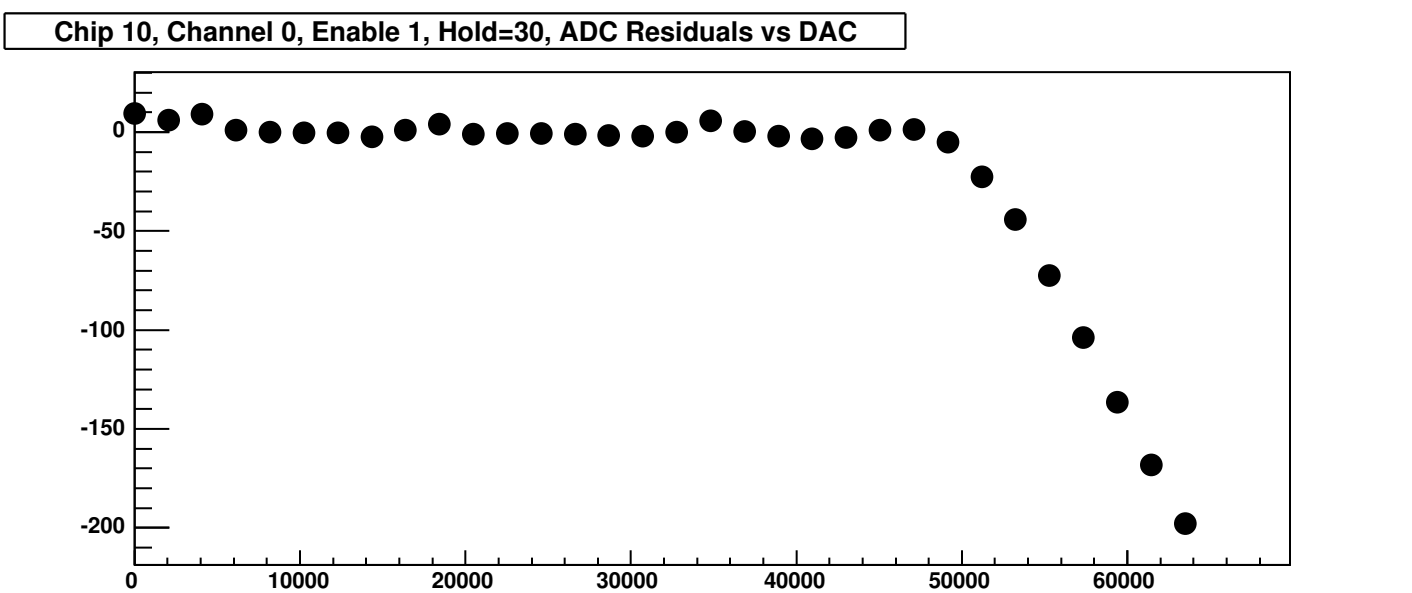

Chip 10, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 2586 / 15
p0 1451 ± 0.1122
p1 0.0006145 ± 4.608e-06
1480
1460
1440
1420
1400
1380
1360
1340
1320
0
10000
20000
30000
40000
50000
60000

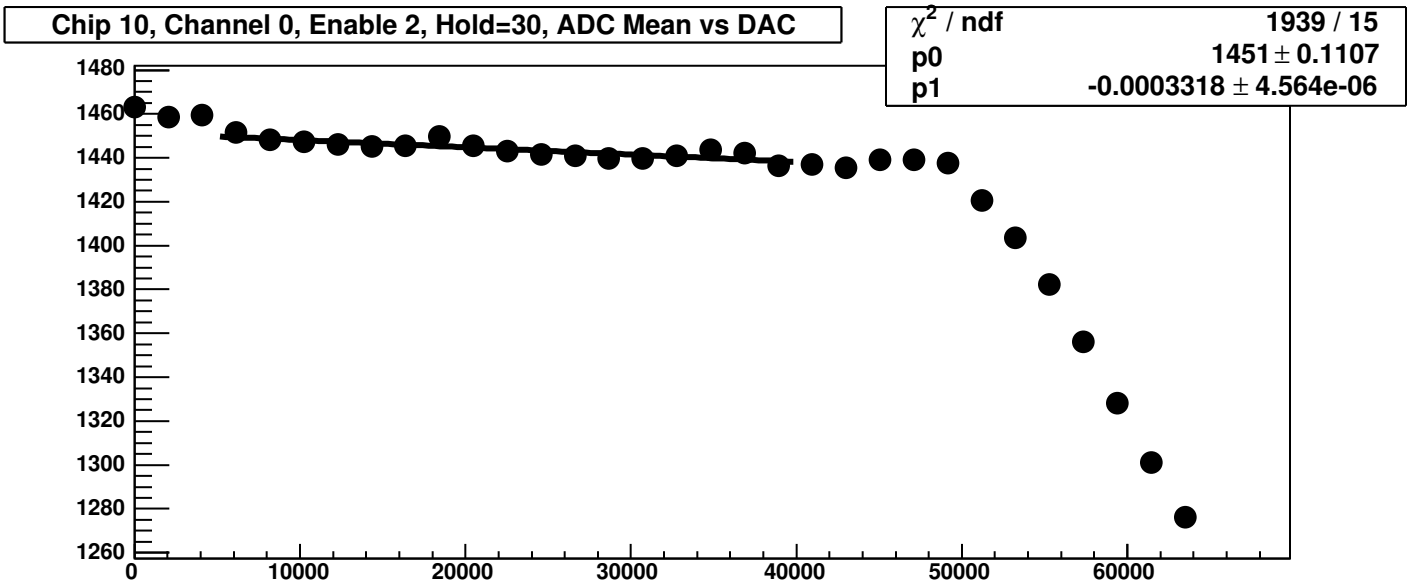

Chip 10, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

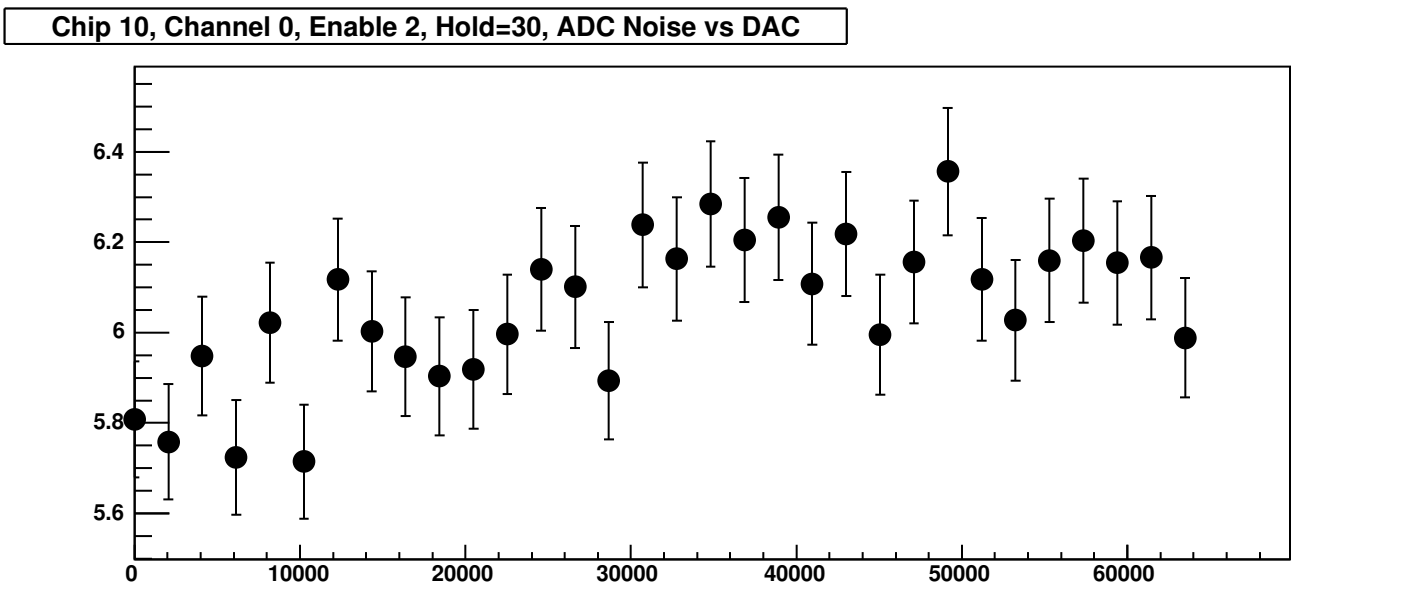

Chip 10, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

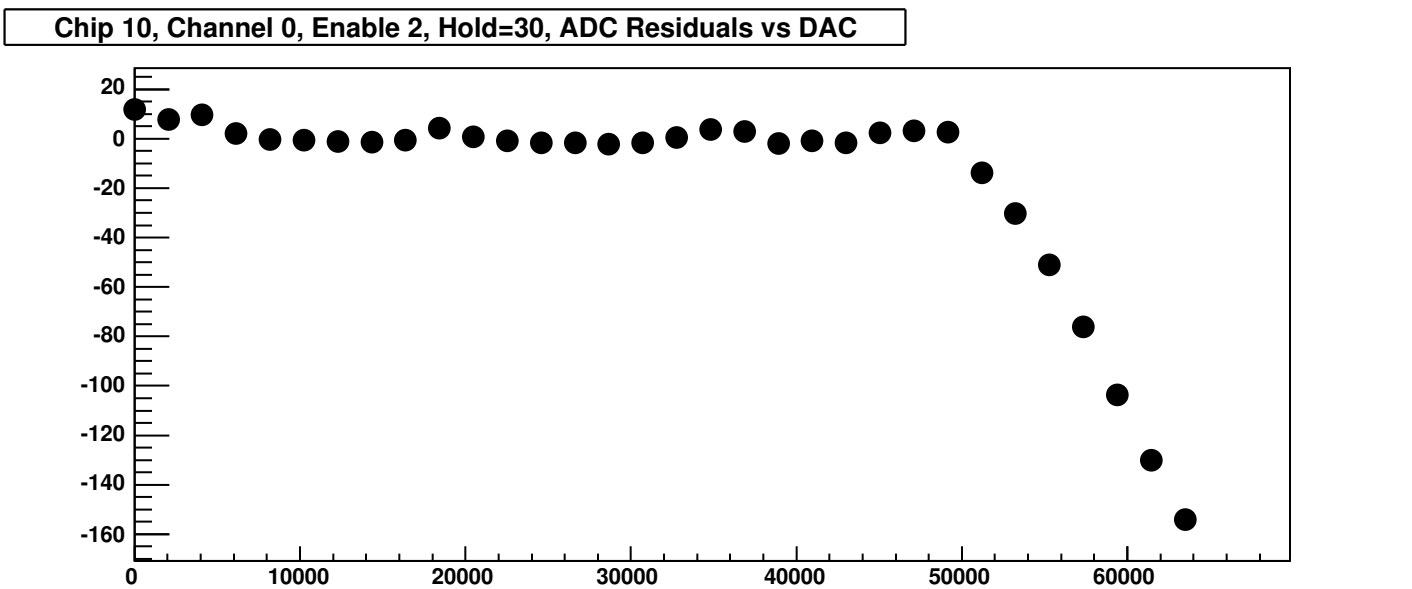

Chip 10, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2113 / 15
p0 1453 ± 0.1086
p1 0.0003617 ± 4.411e-06
1480
1460
1440
1420
1400
1380
1360
1340
1320
1300
1280
1260
0
10000
20000
30000
40000
50000
60000

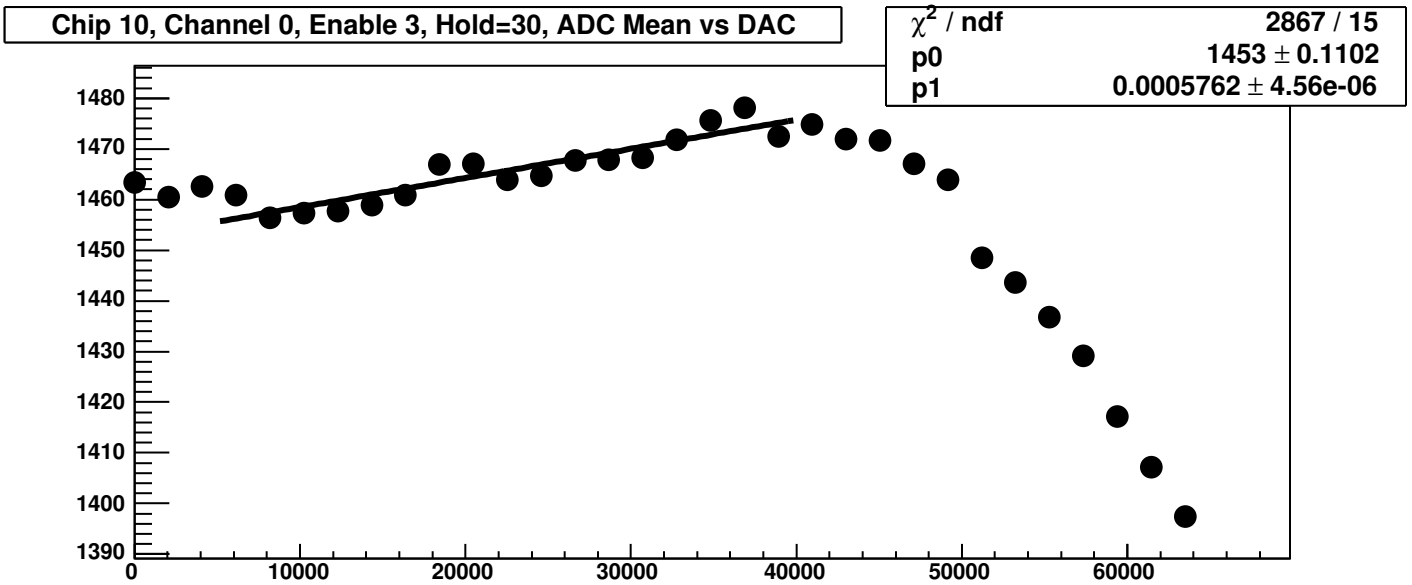

Chip 10, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

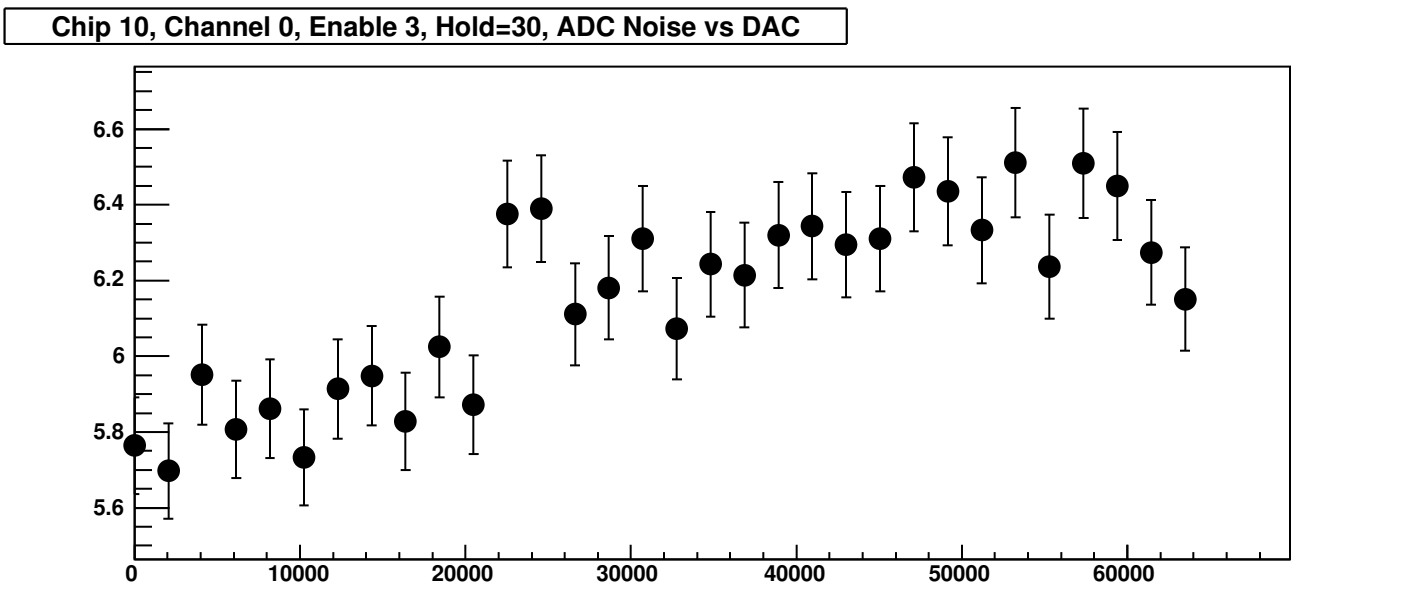

Chip 10, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

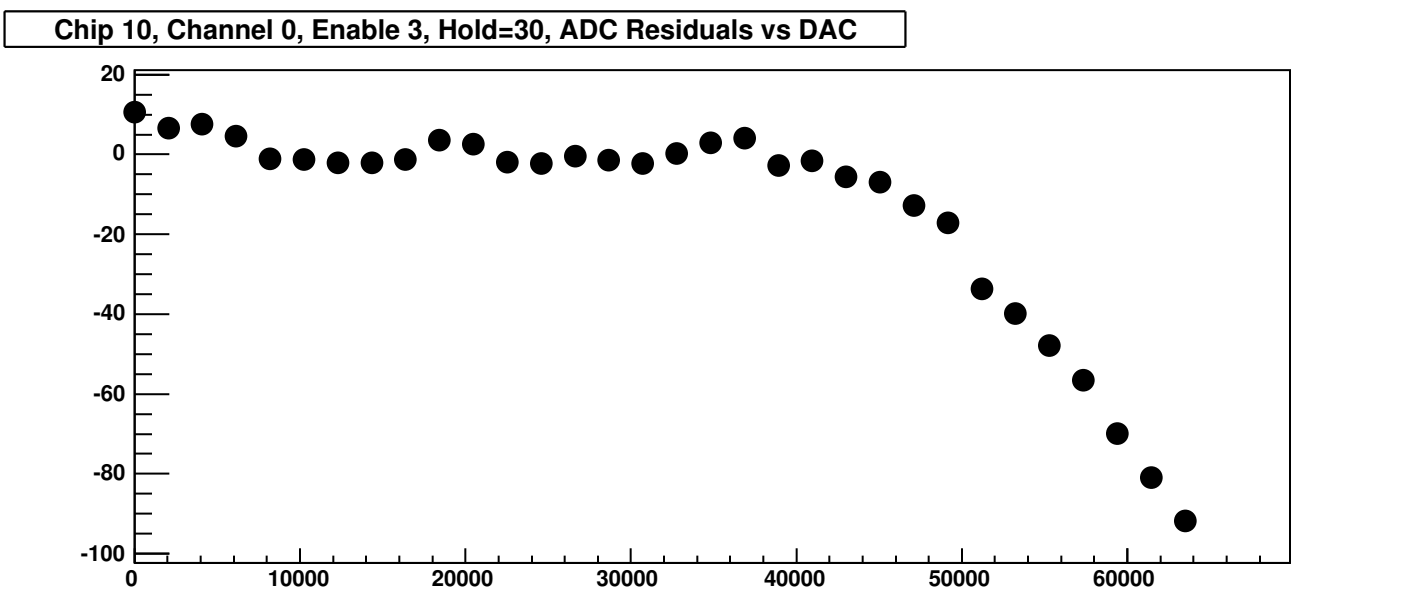

Chip 10, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1939 / 15
p0 1451 ± 0.1107
p1 -0.0003318 ± 4.564e-06
1480
1460
1440
1420
1400
1380
1360
1340
1320
1300
1280
1260
0
10000
20000
30000
40000
50000
60000

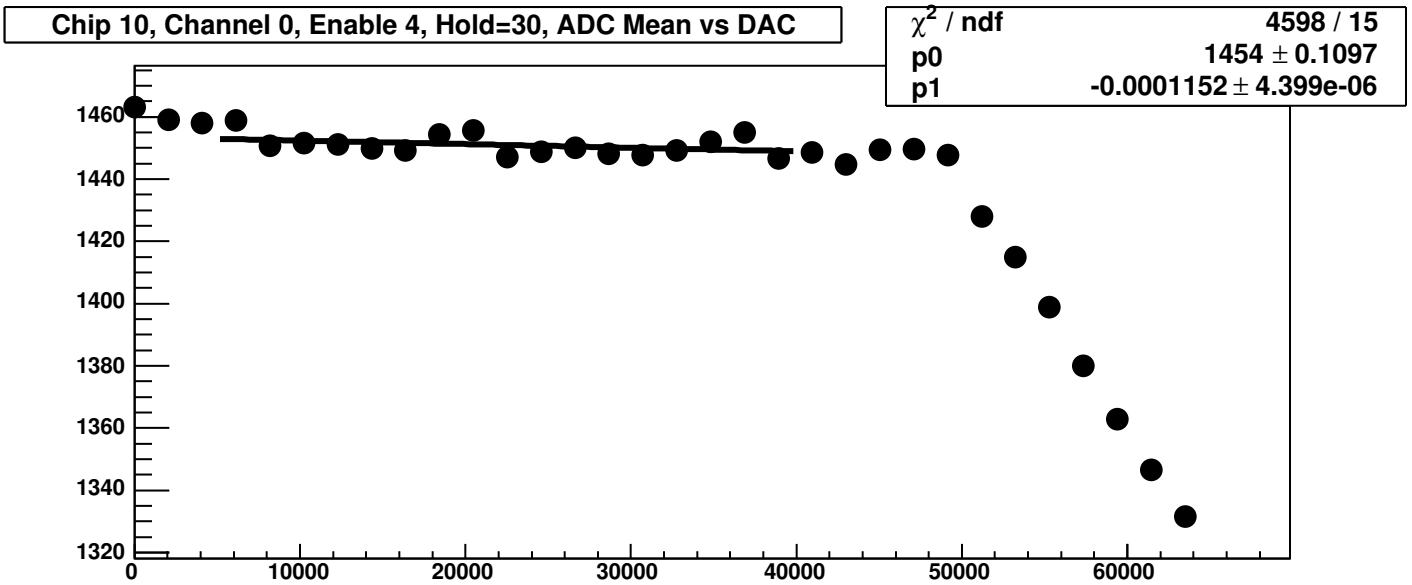

Chip 10, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

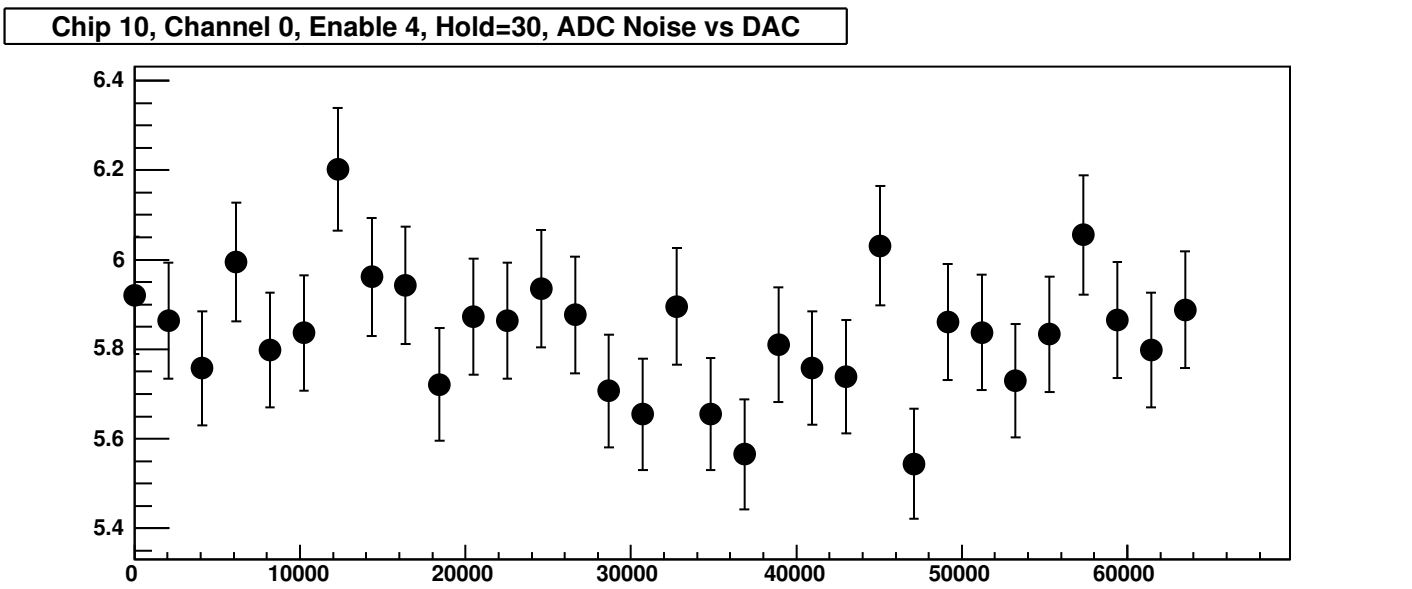

Chip 10, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

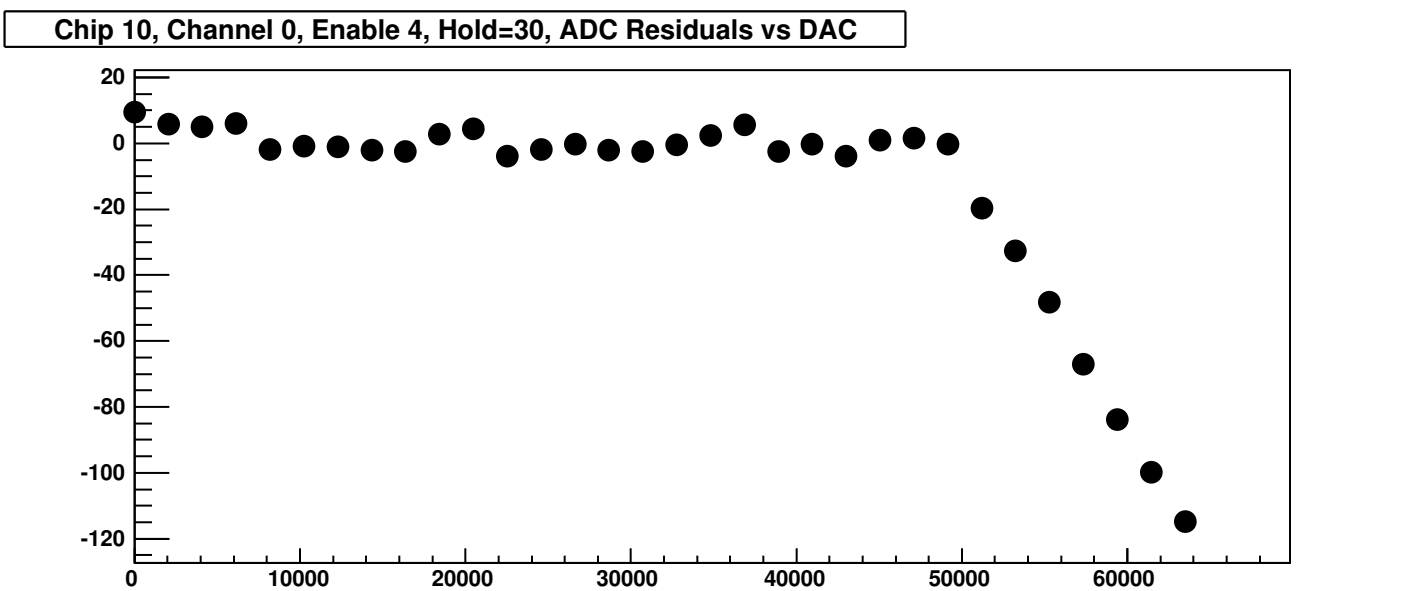

Chip 10, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2867 / 15
p0 1453 ± 0.1102
p1 0.0005762 ± 4.56e-06
1480
1470
1460
1450
1440
1430
1420
1410
1400
1390
0
10000
20000
30000
40000
50000
60000

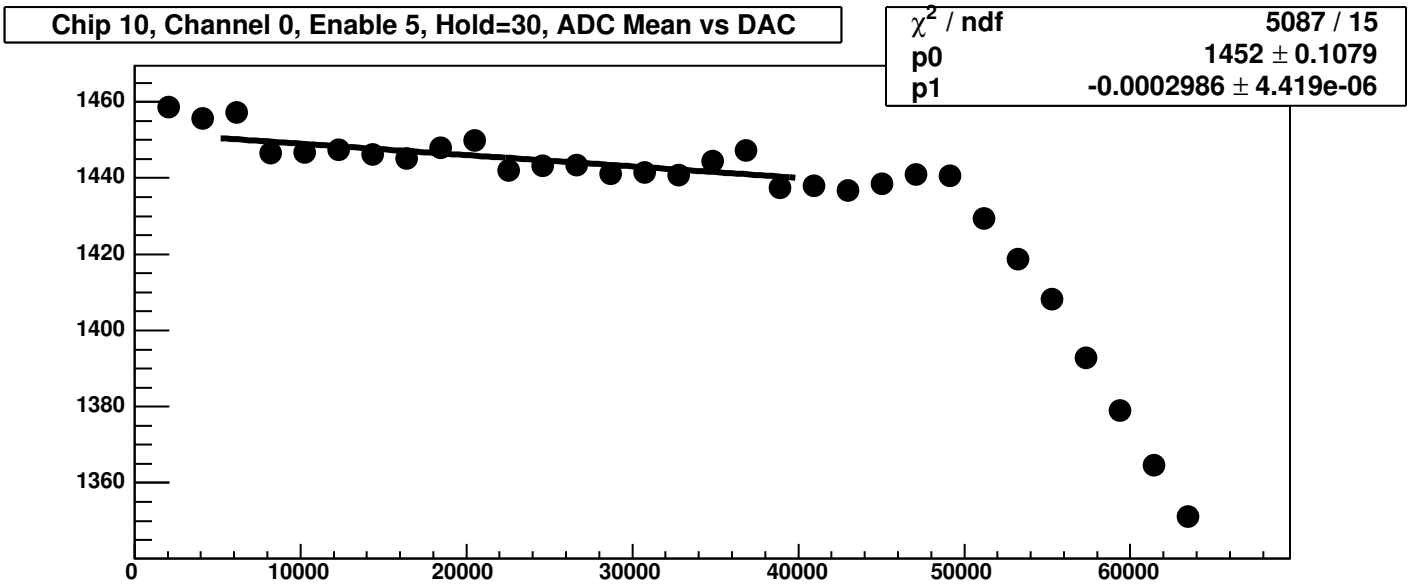

Chip 10, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

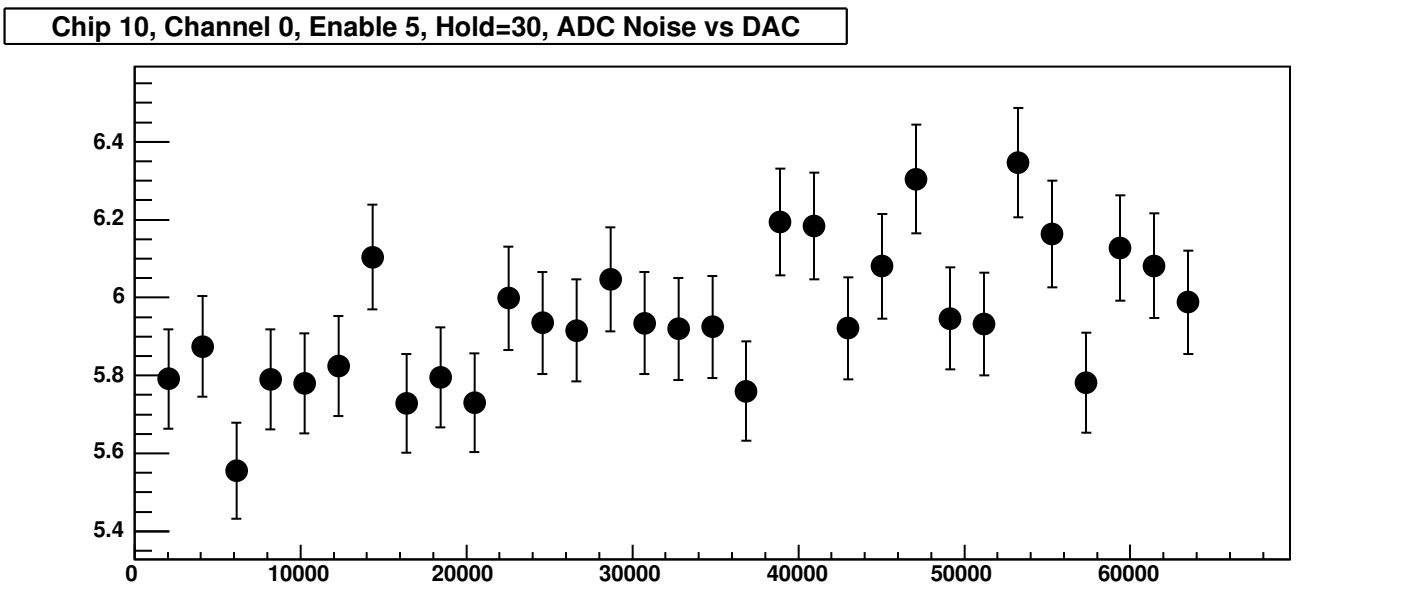

Chip 10, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

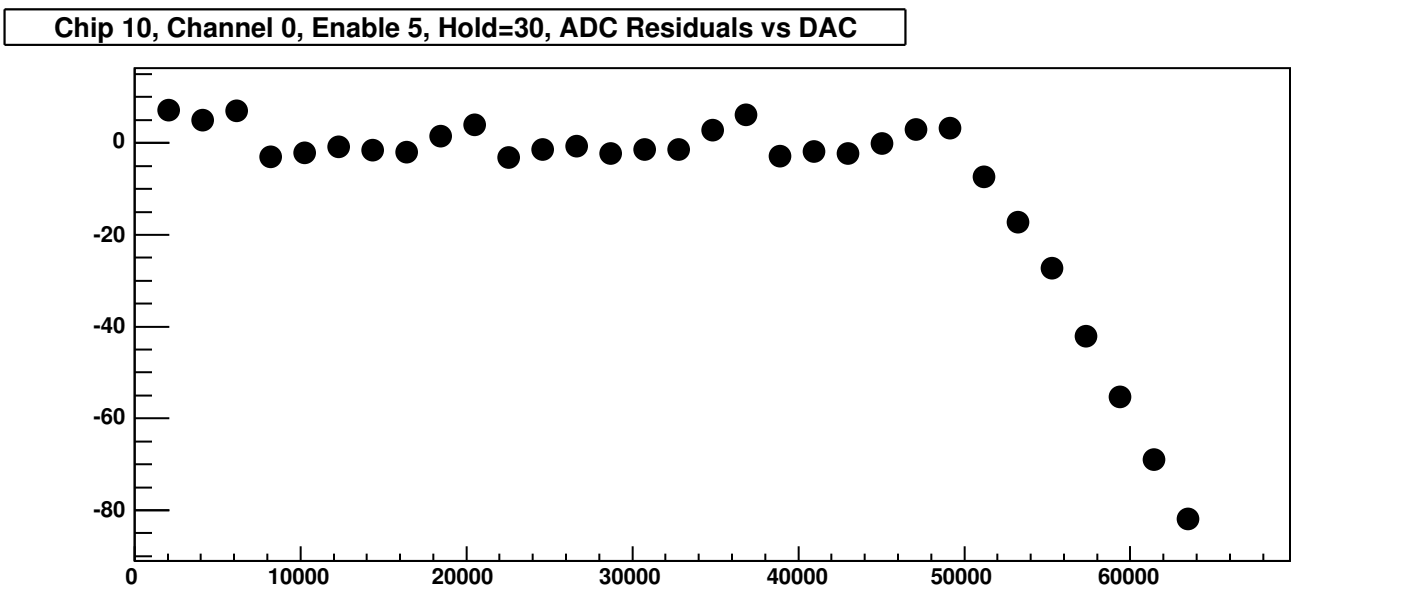

Chip 10, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4598 / 15
p0 1454 ± 0.1097
p1 -0.0001152 ± 4.399e-06
1460
1440
1420
1400
1380
1360
1340
1320
0
10000
20000
30000
40000
50000
60000

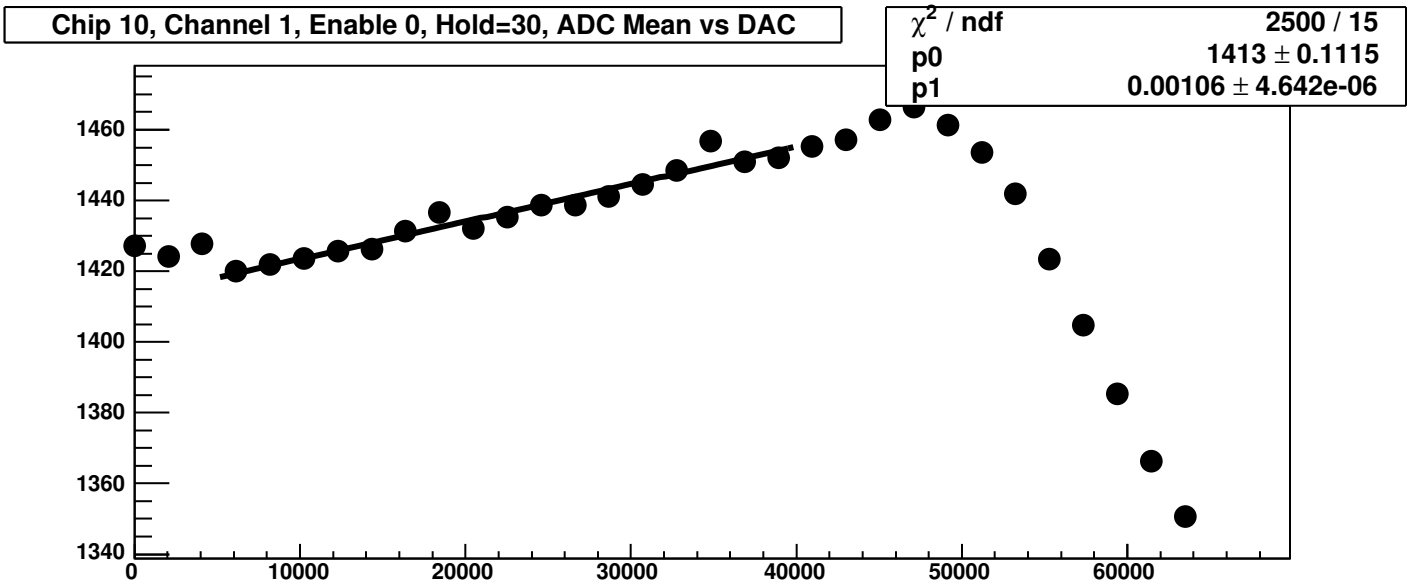

Chip 10, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

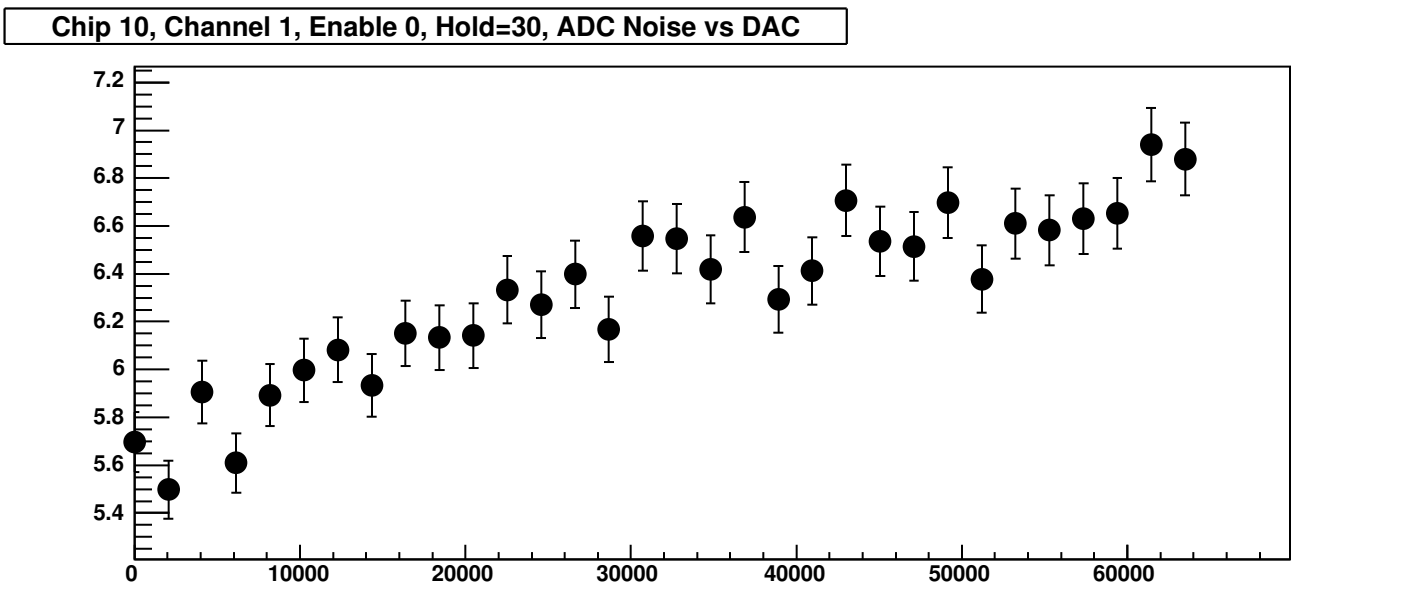

Chip 10, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

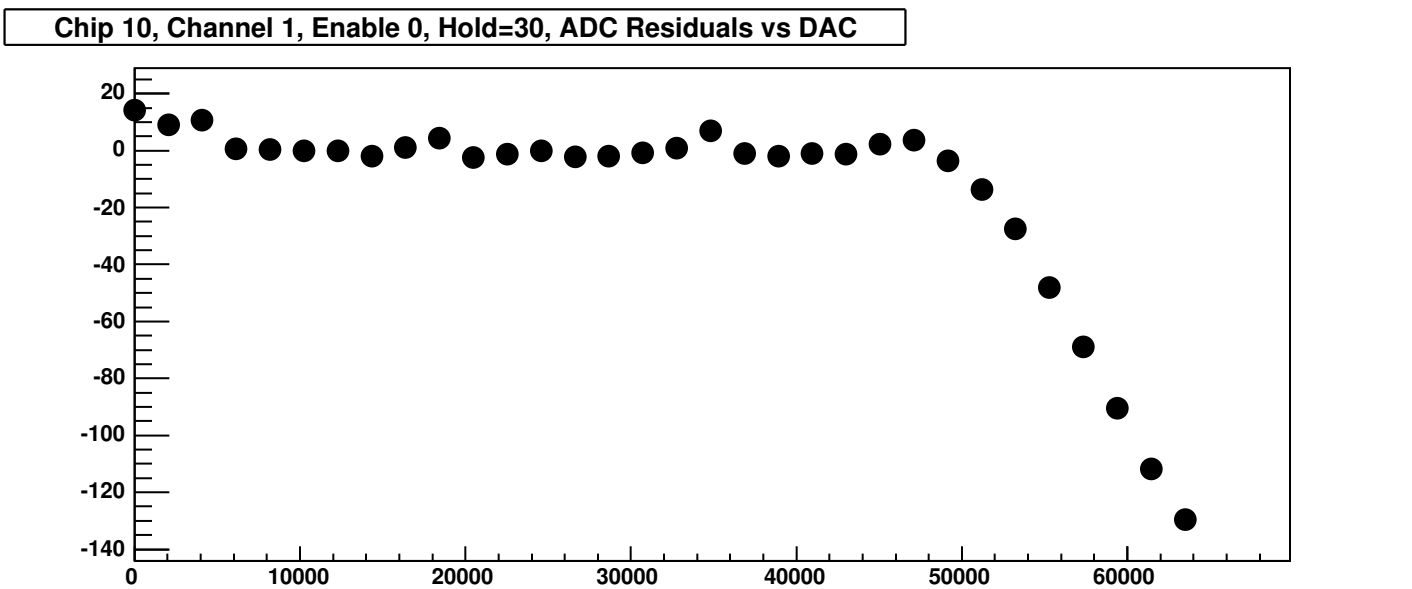

Chip 10, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5087 / 15
p0 1452 ± 0.1079
p1 -0.0002986 ± 4.419e-06
1460
1440
1420
1400
1380
1360
0
10000
20000
30000
40000
50000
60000

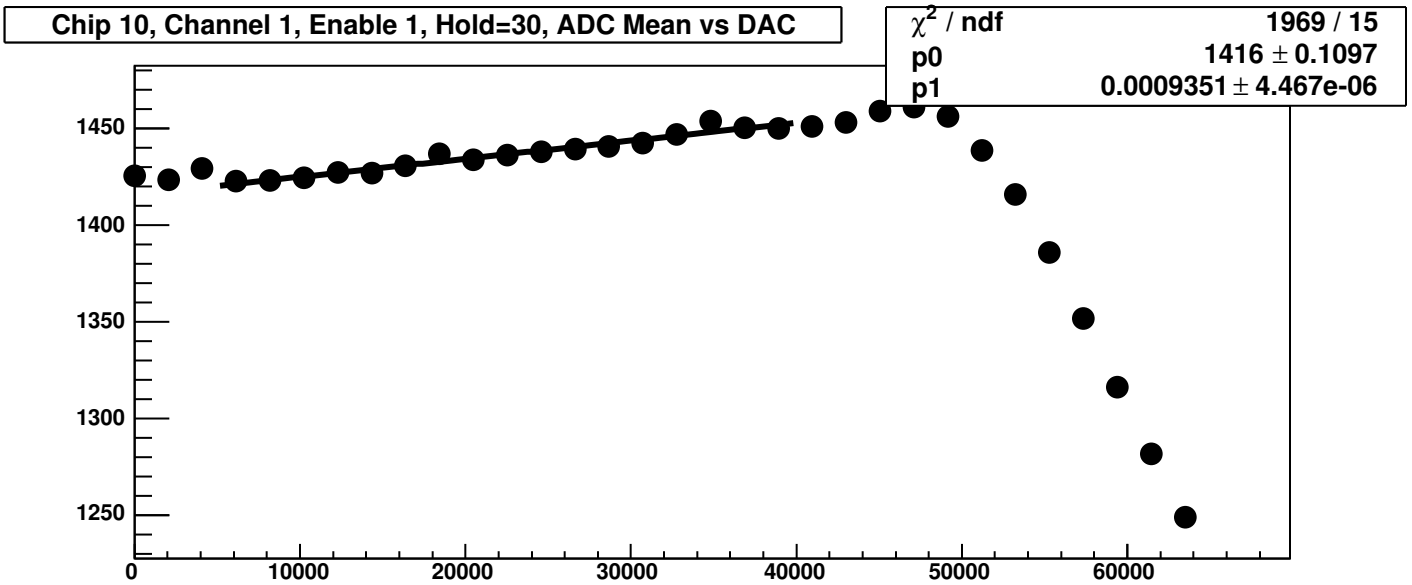

Chip 10, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

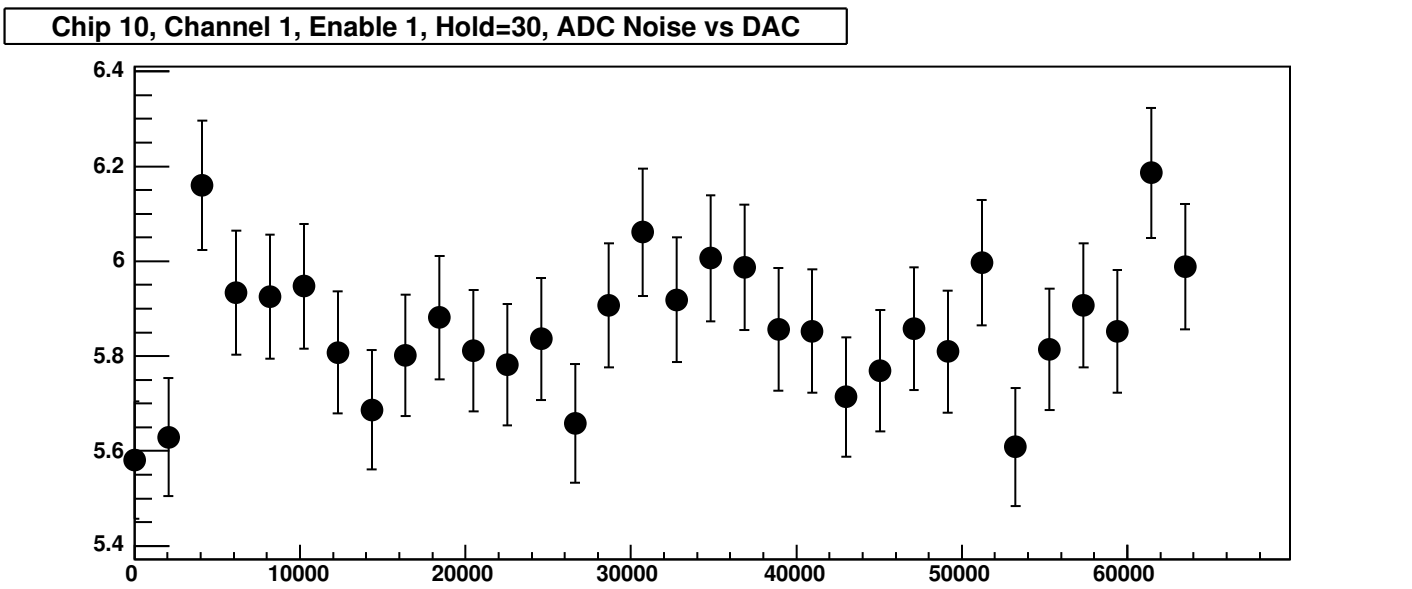

Chip 10, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

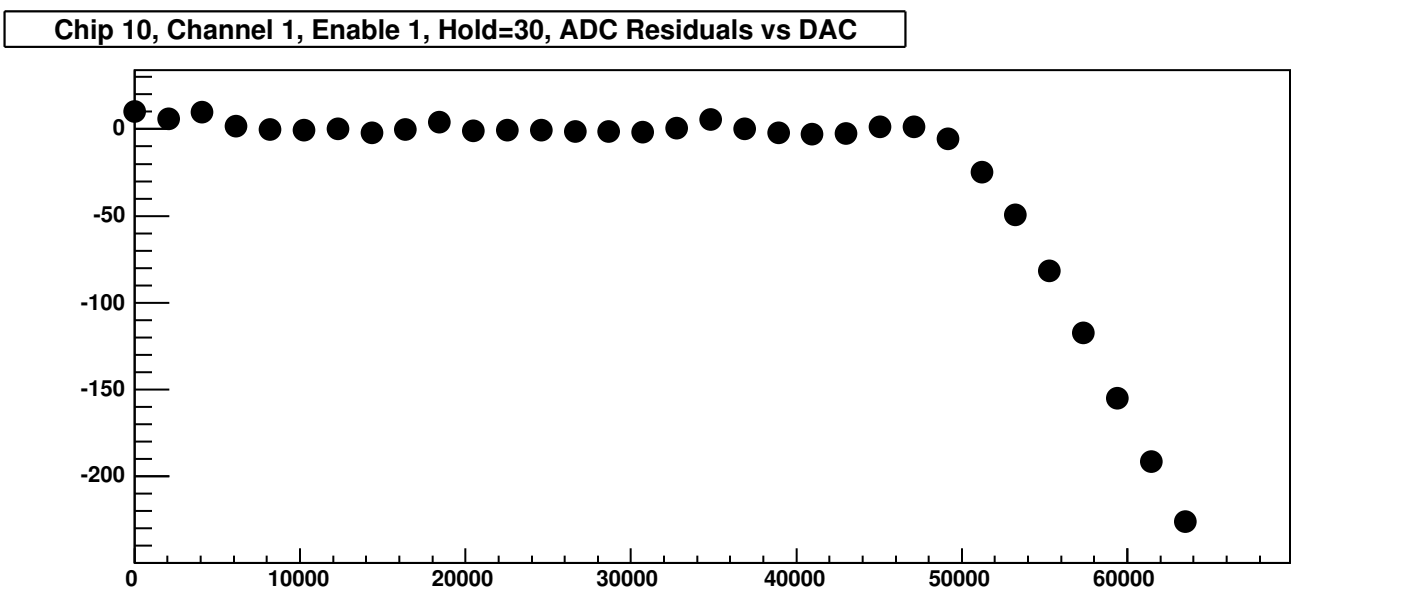

Chip 10, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2500 / 15
p0 1413 ± 0.1115
p1 0.00106 ± 4.642e-06
1460
1440
1420
1400
1380
1360
1340
0
10000
20000
30000
40000
50000
60000

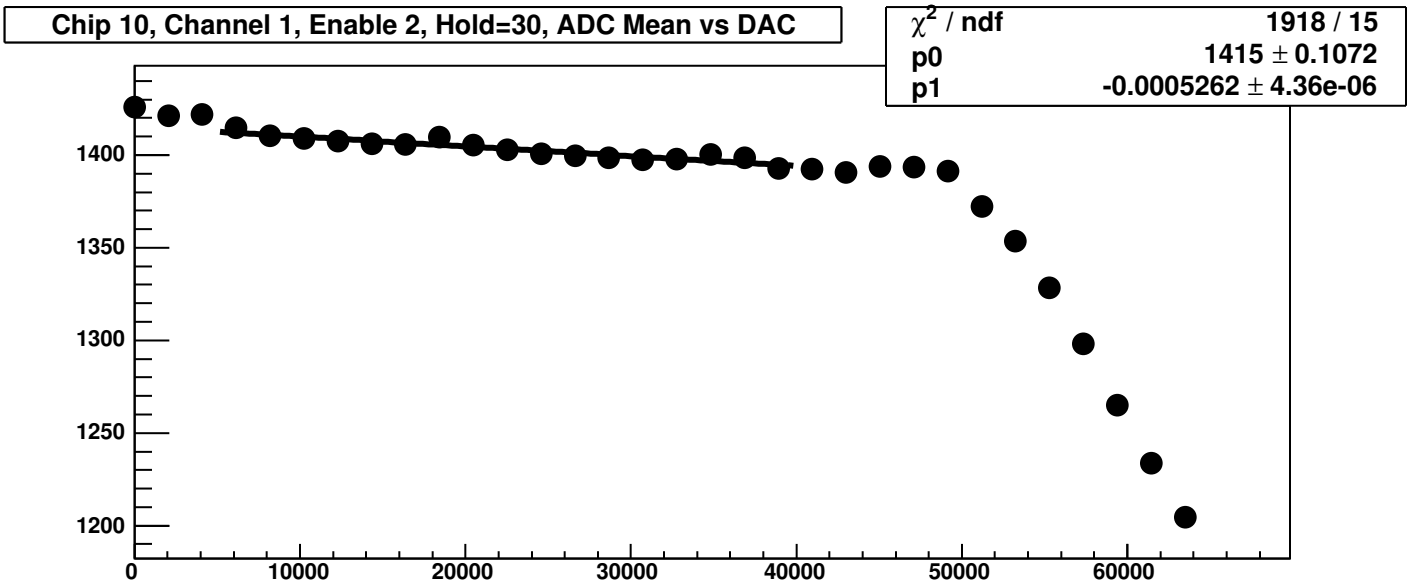

Chip 10, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

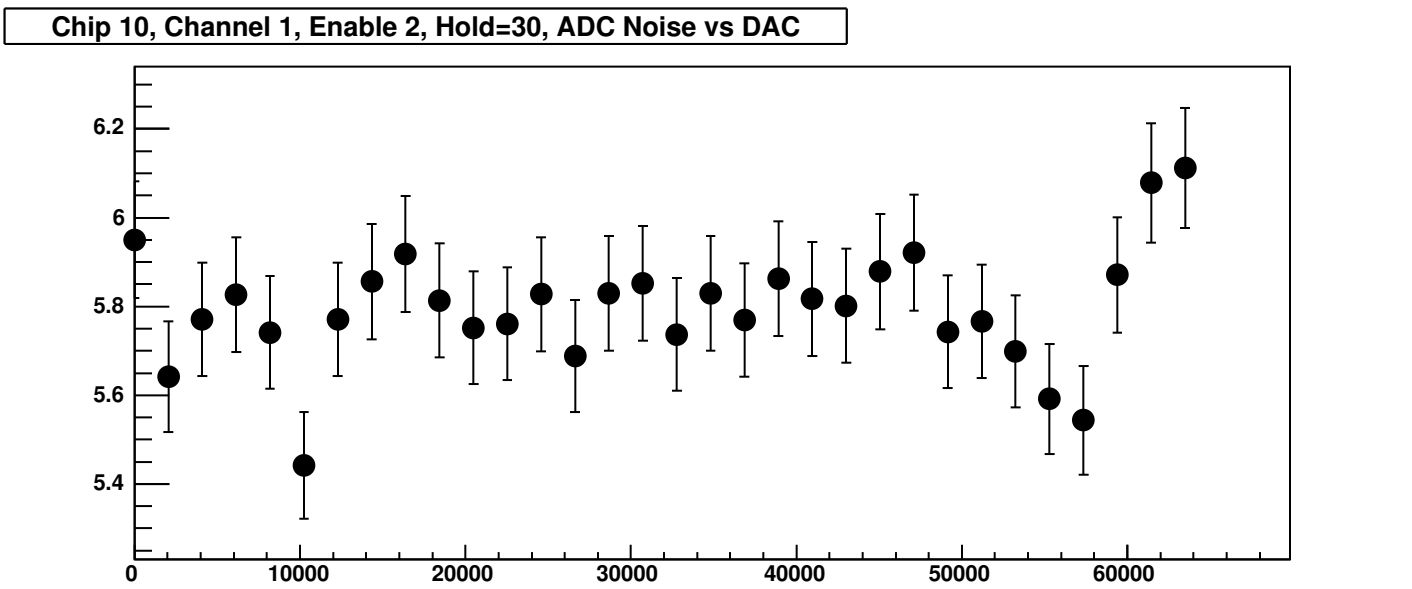

Chip 10, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

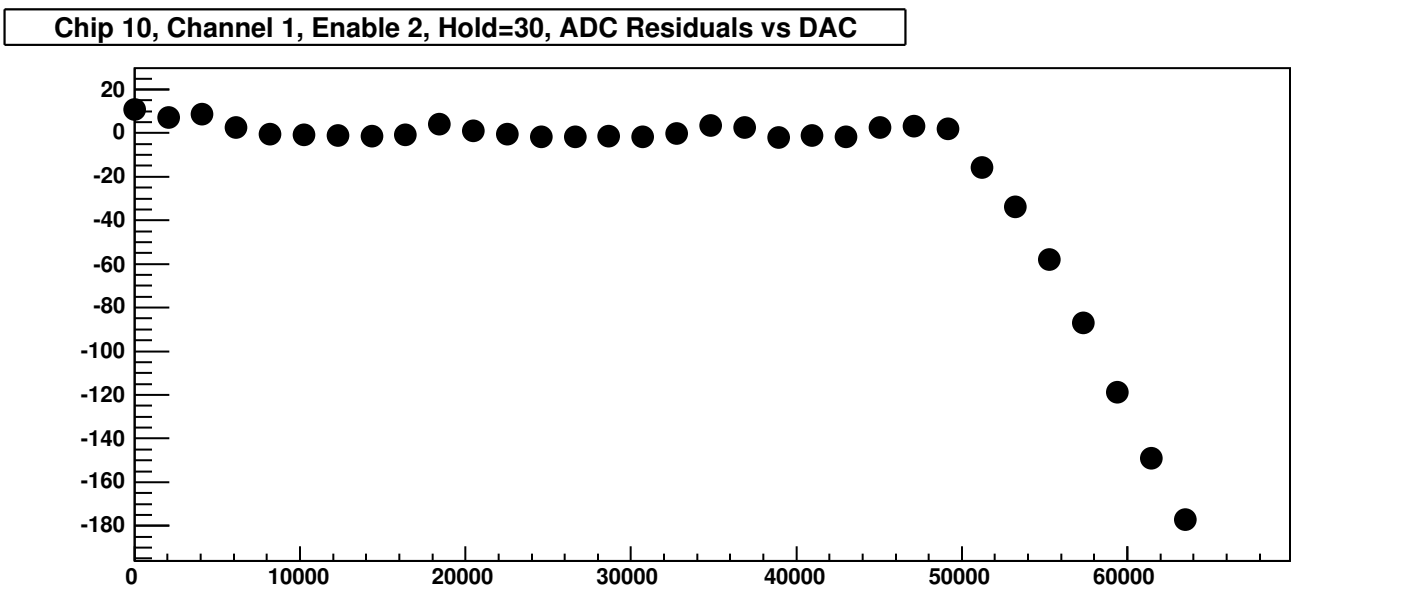

Chip 10, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1969 / 15
p0 1416 ± 0.1097
p1 0.0009351 ± 4.467e-06
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

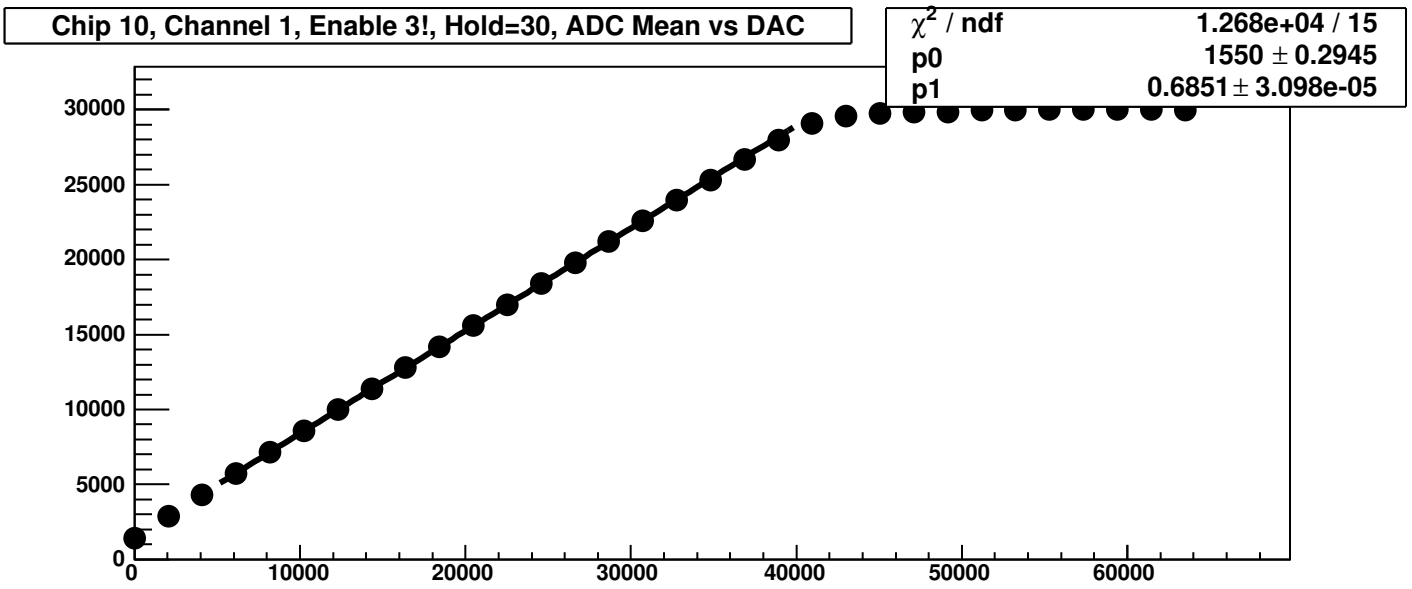

Chip 10, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

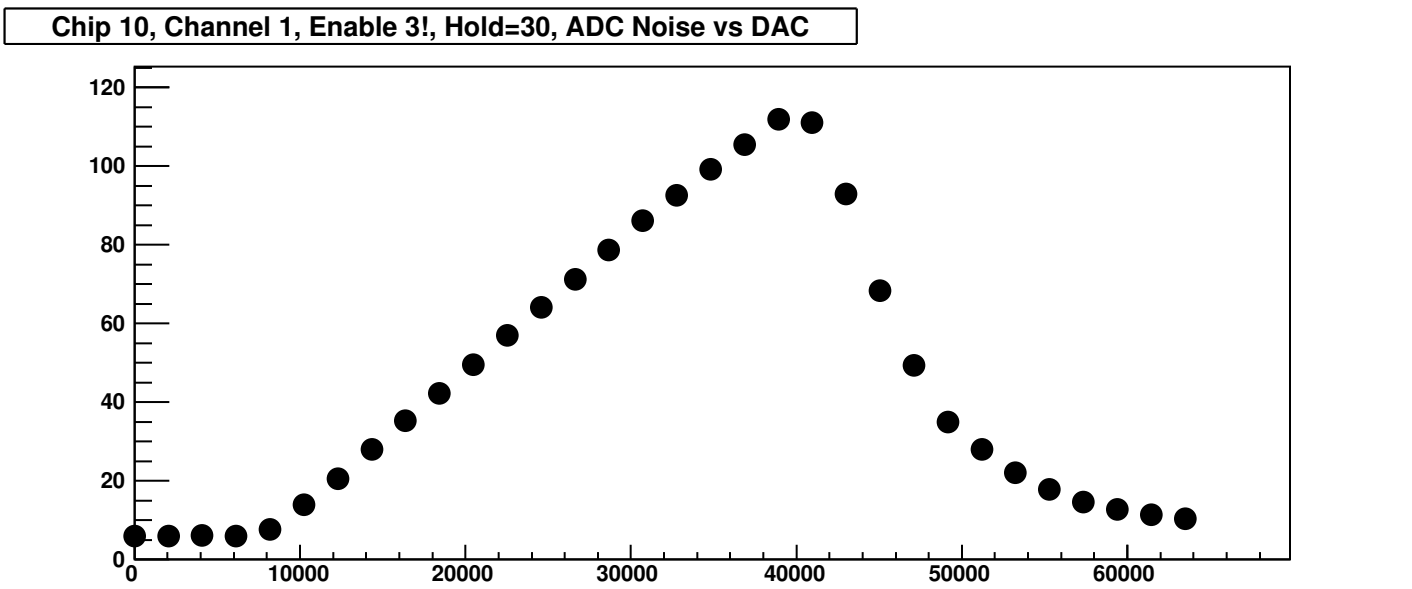

Chip 10, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

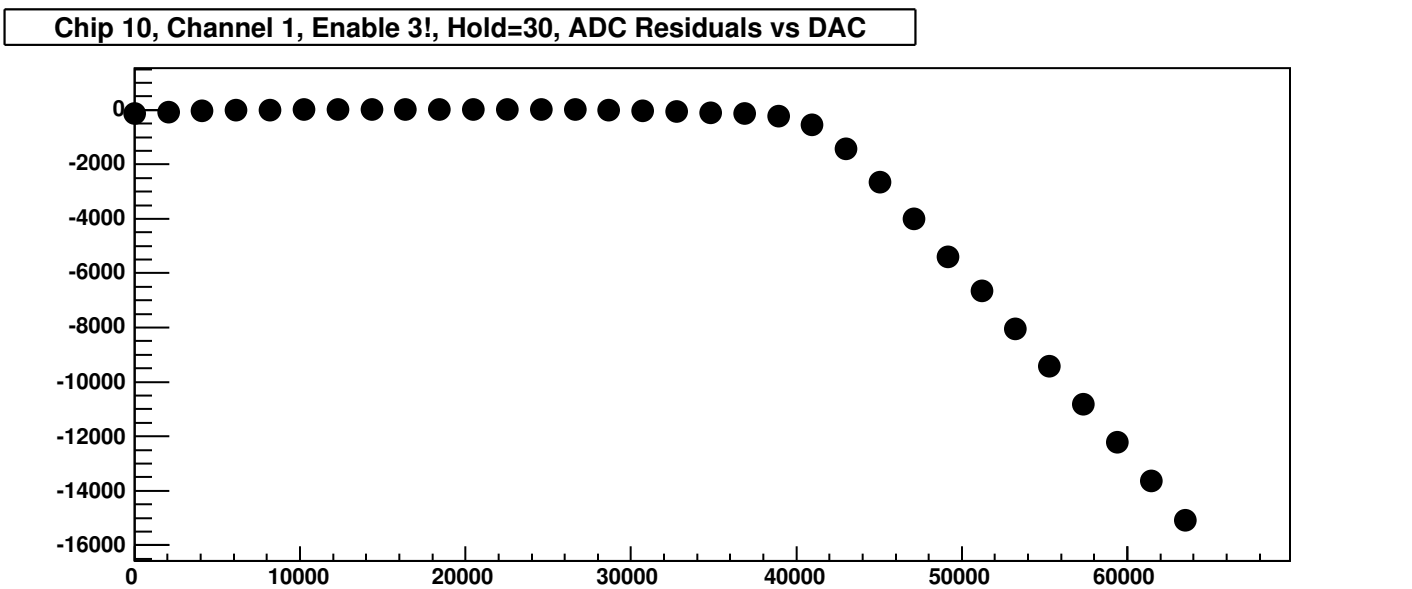

Chip 10, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1918 / 15
p0 1415 ± 0.1072
p1 -0.0005262 ± 4.36e-06
1400
1350
1300
1250
1200
0
10000
20000
30000
40000
50000
60000

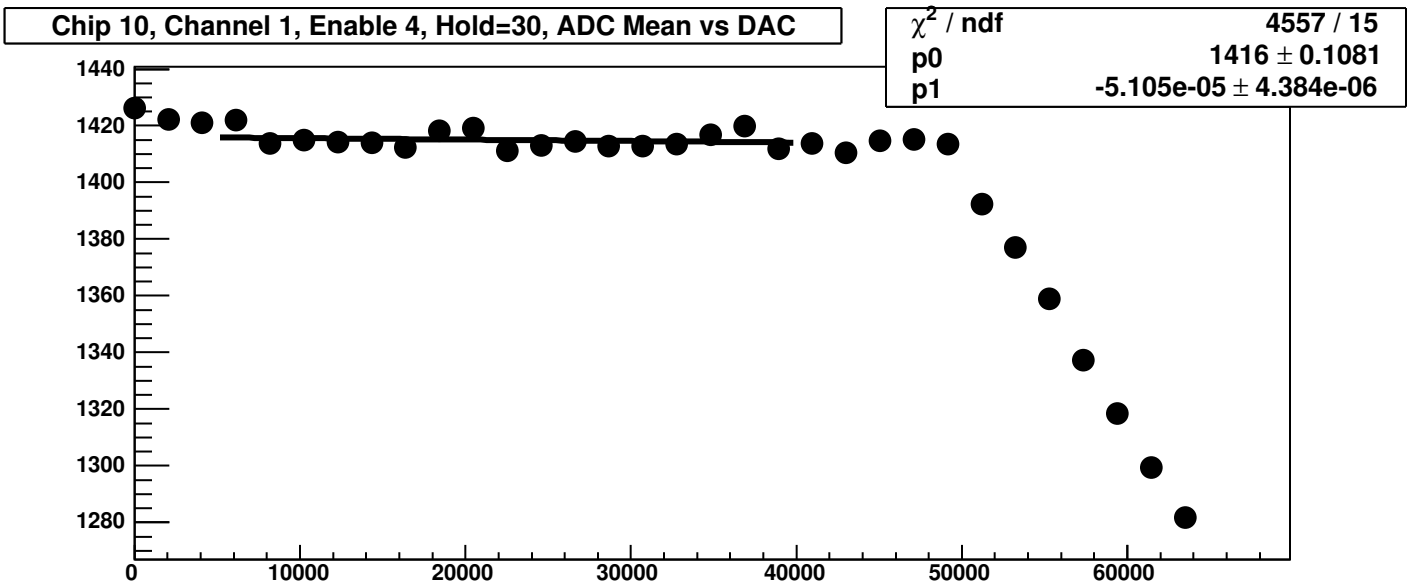

Chip 10, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

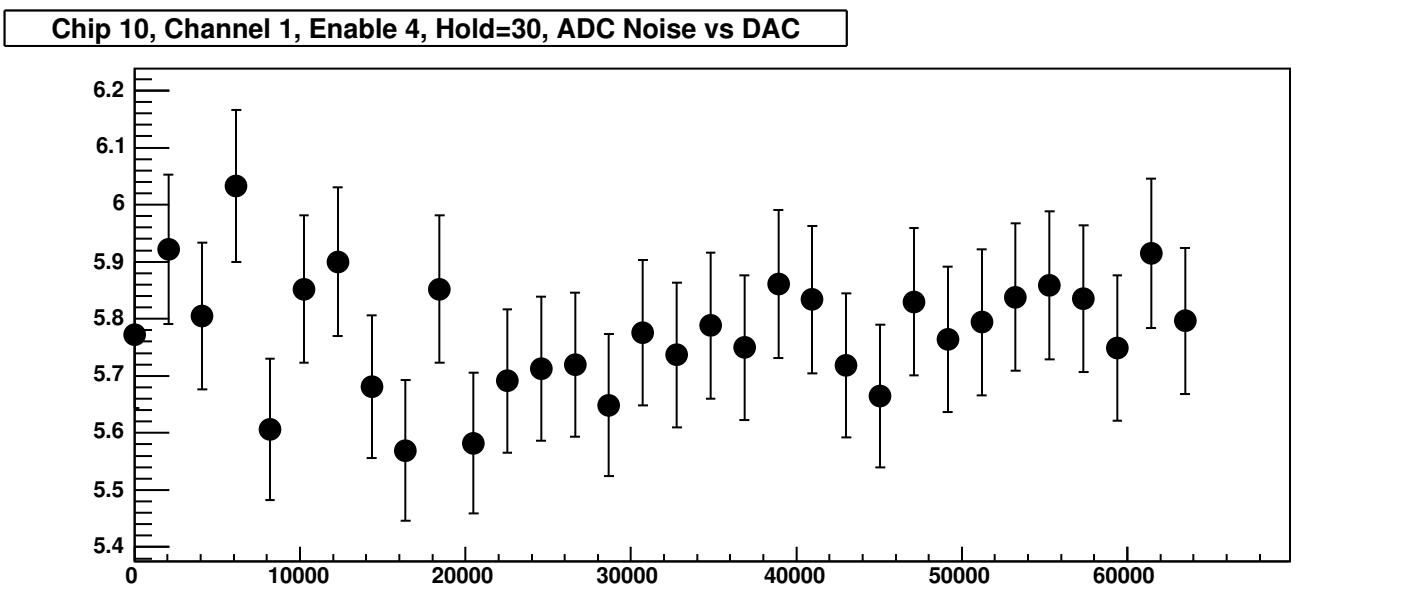

Chip 10, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

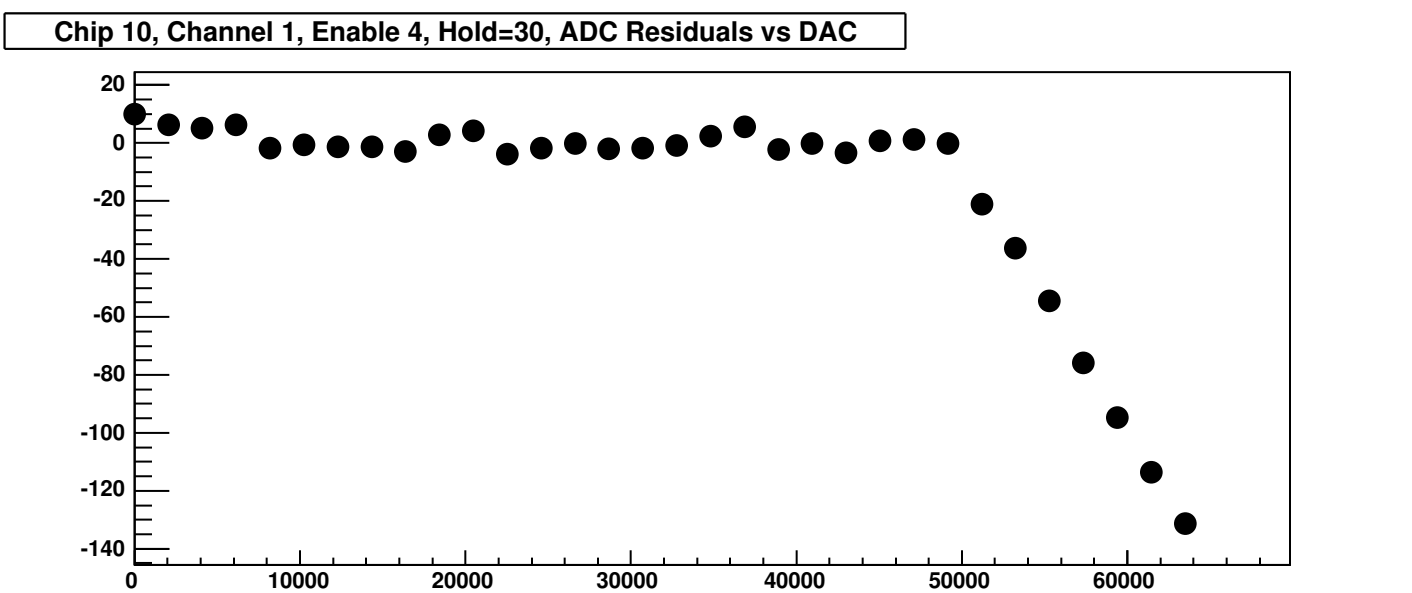

Chip 10, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.268e+04 / 15
p0 1550 ± 0.2945
p1 0.6851 ± 3.098e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

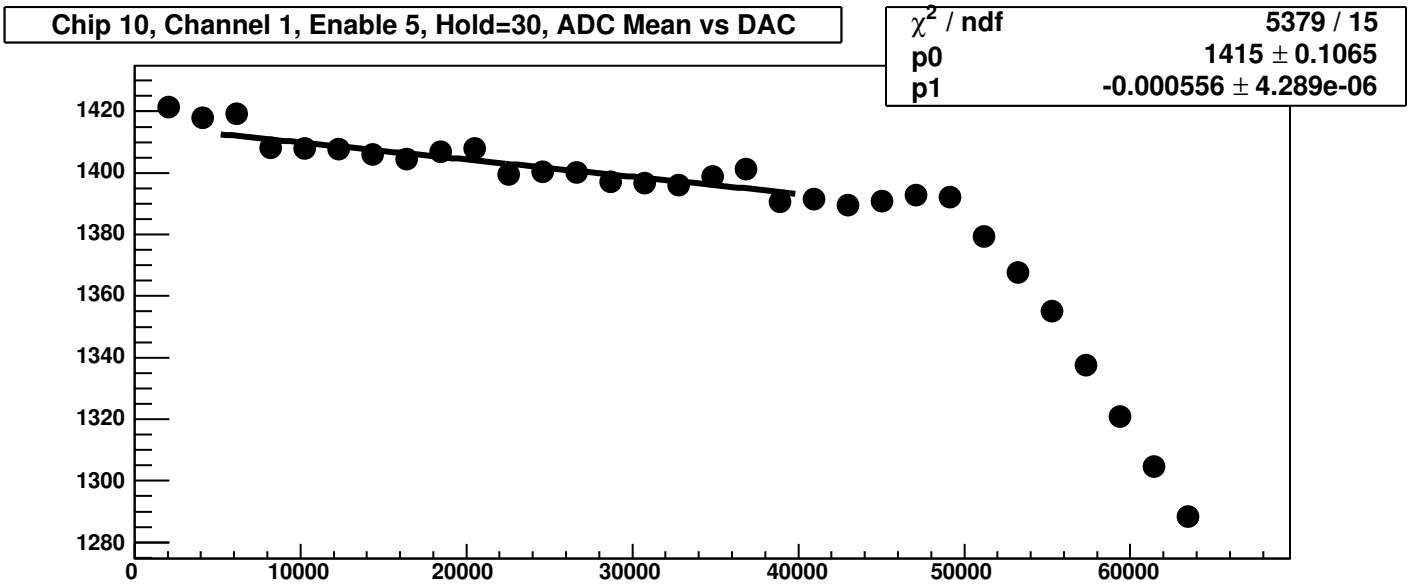

Chip 10, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

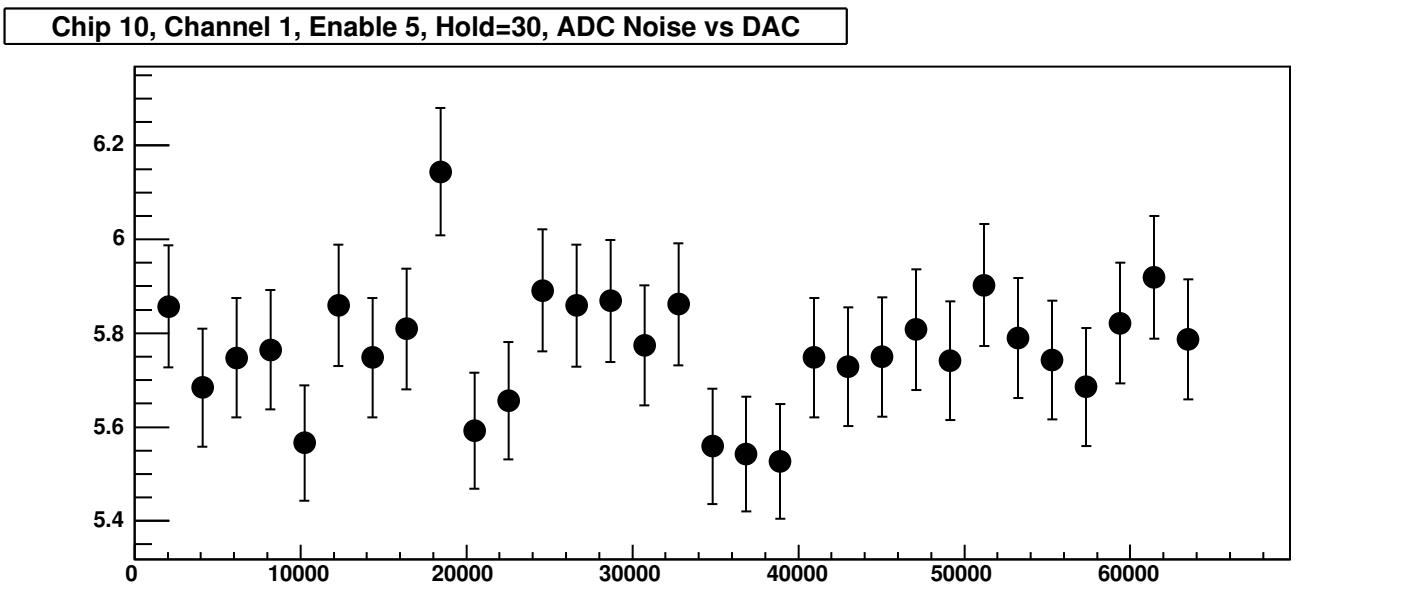

Chip 10, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
10000
20000
30000
40000
50000
60000

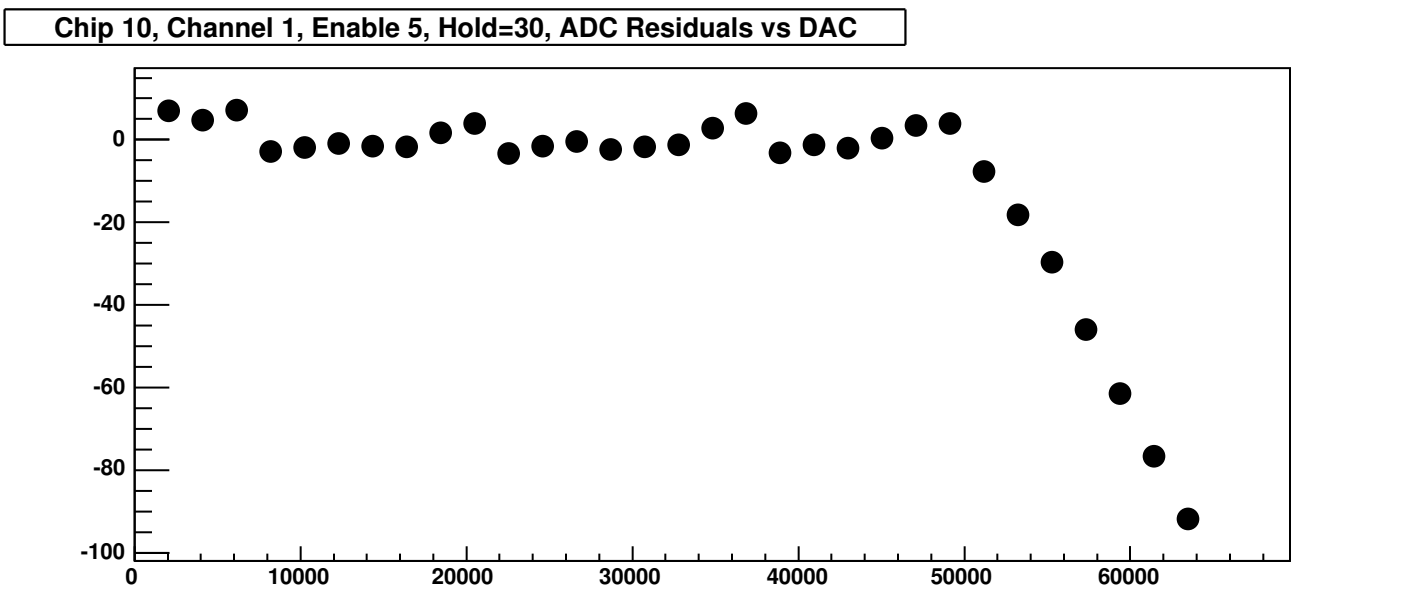

Chip 10, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4557 / 15
p0 1416 ± 0.1081
p1 -5.105e-05 ± 4.384e-06
1440
1420
1400
1380
1360
1340
1320
1300
1280
0
10000
20000
30000
40000
50000
60000

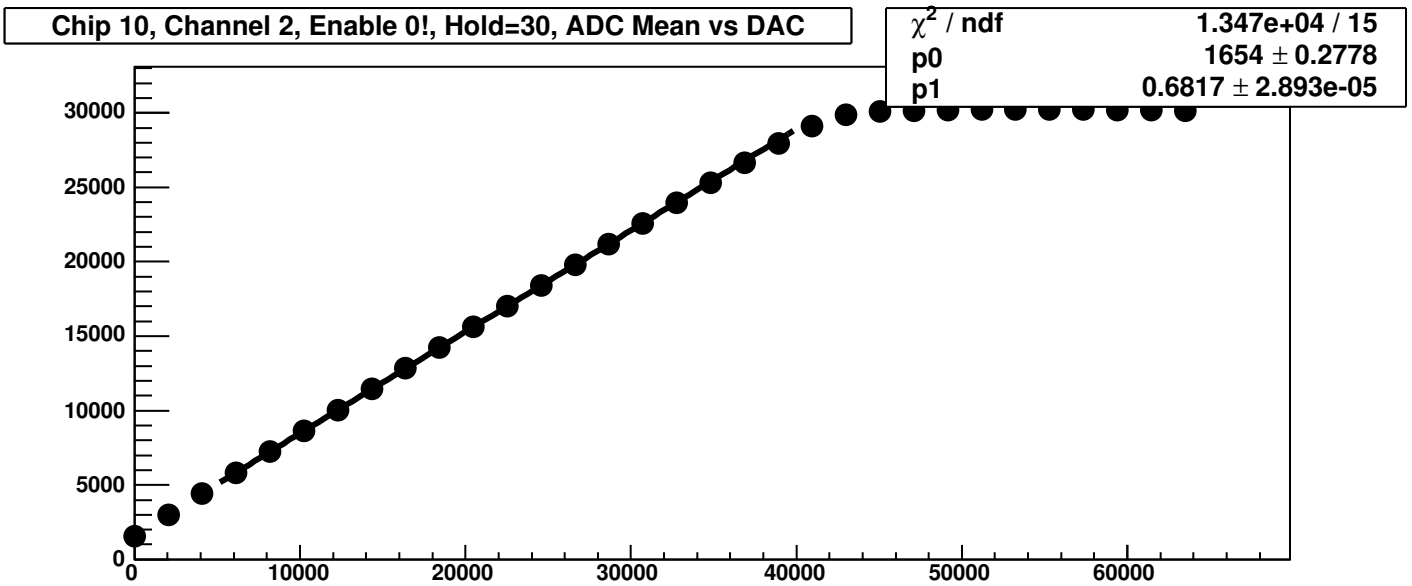

Chip 10, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

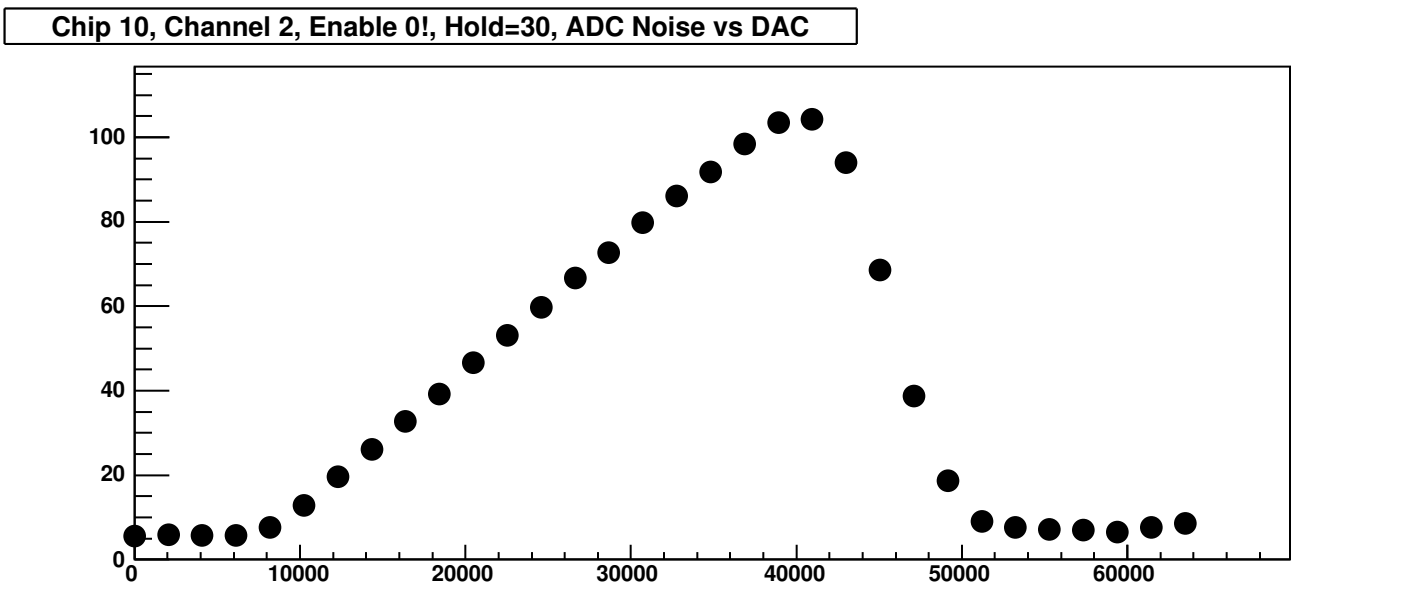

Chip 10, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

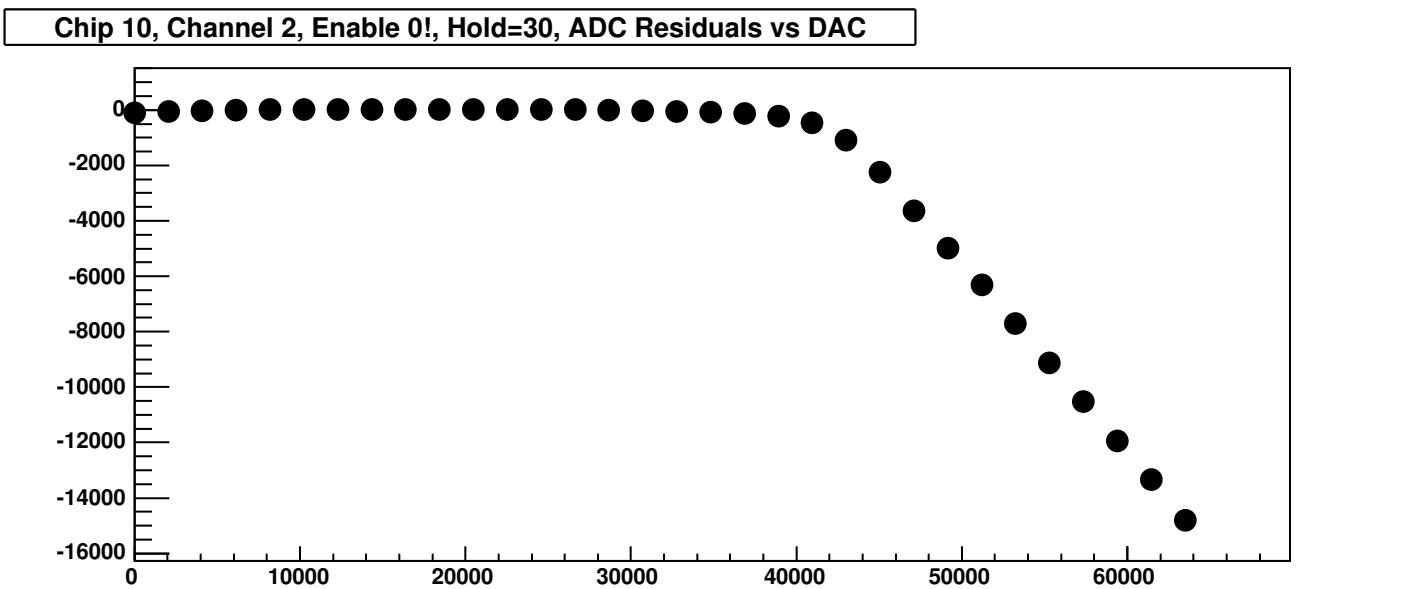

Chip 10, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5379 / 15
p0 1415 ± 0.1065
p1 -0.000556 ± 4.289e-06
1420
1400
1380
1360
1340
1320
1300
1280
0
10000
20000
30000
40000
50000
60000

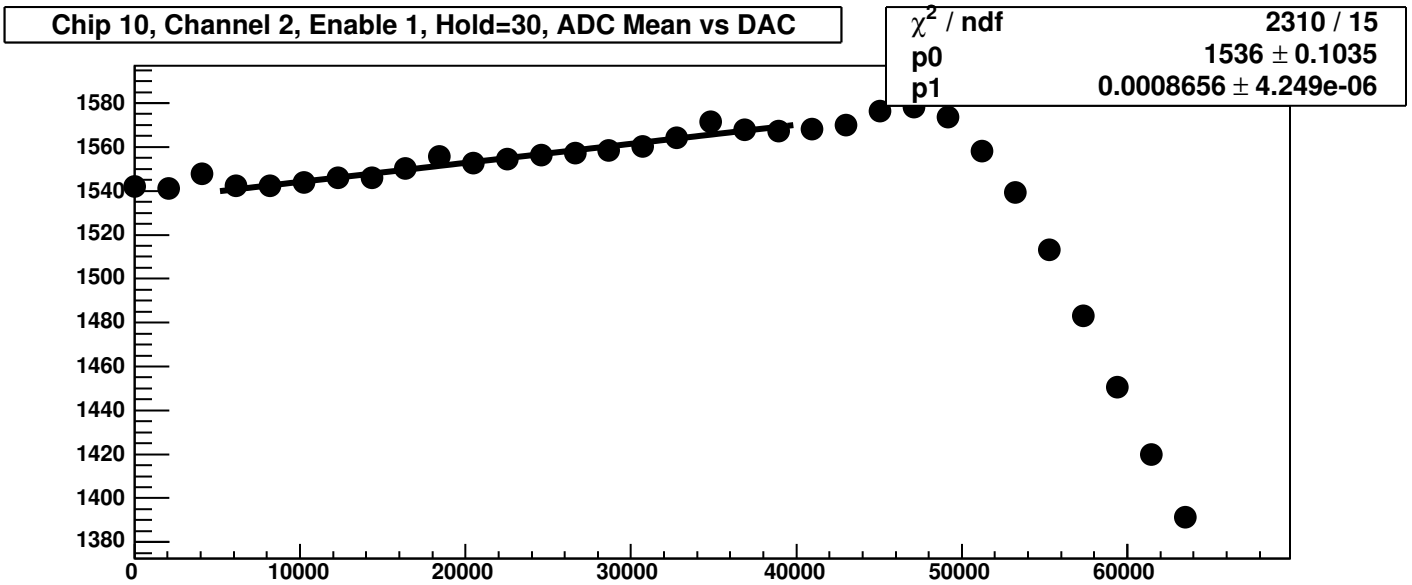

Chip 10, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

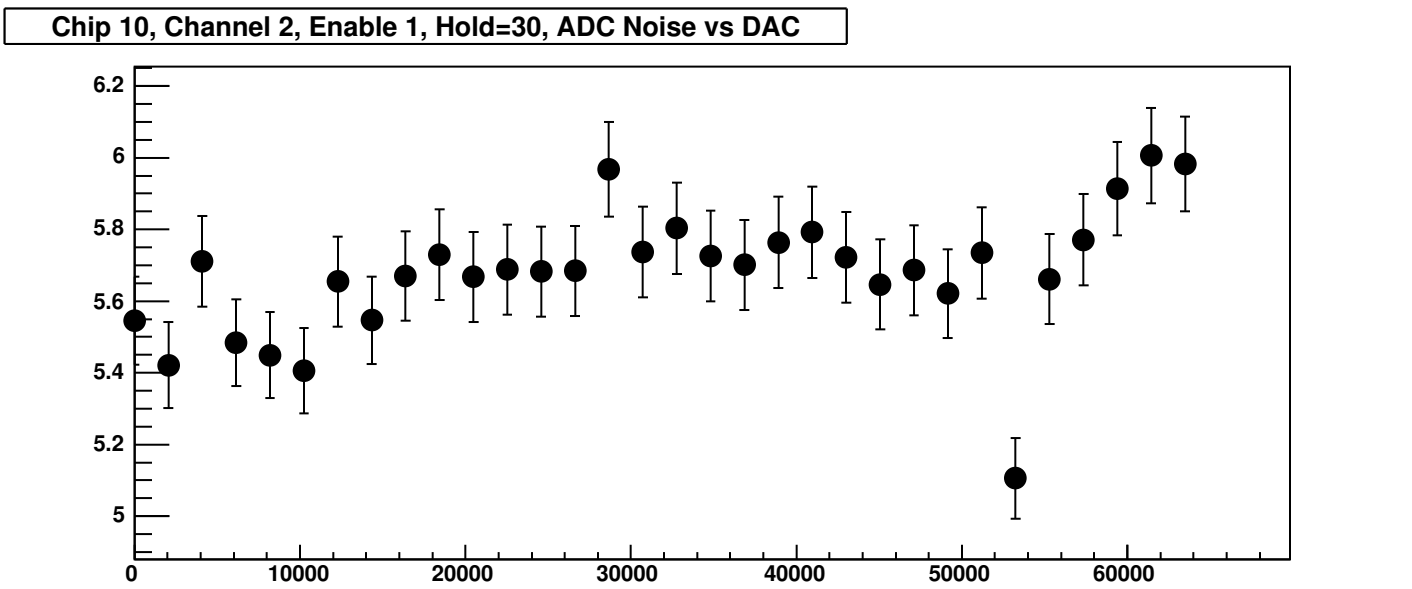

Chip 10, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

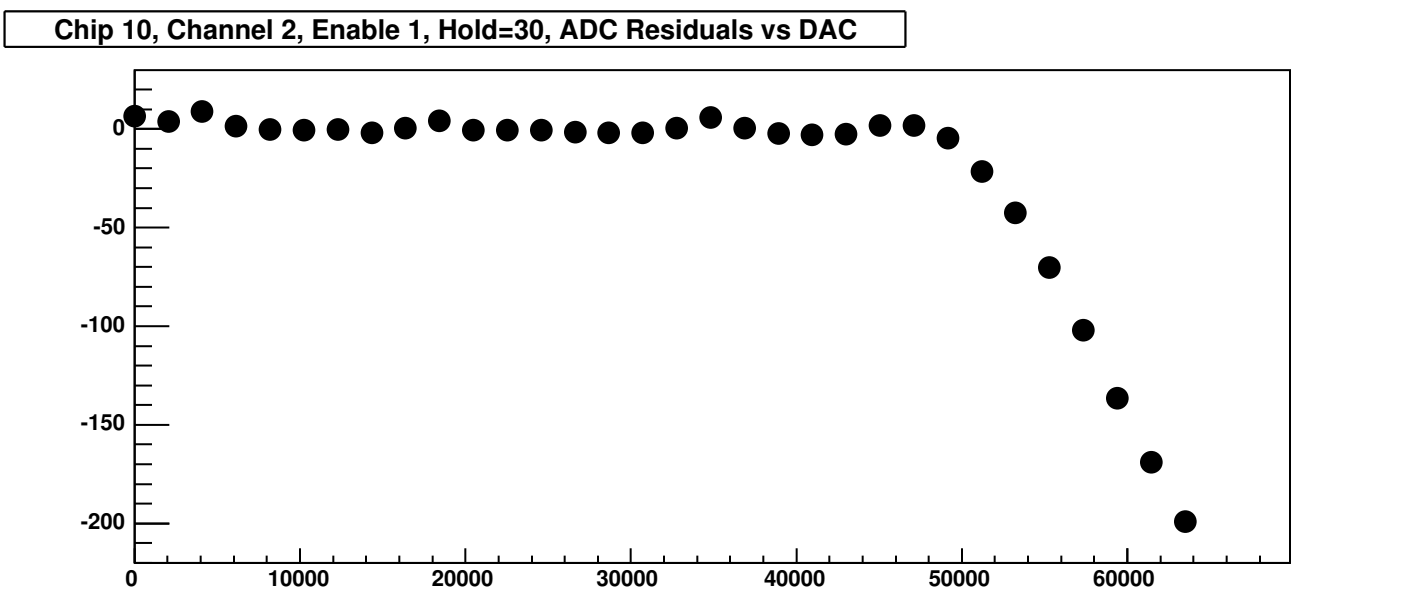

Chip 10, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.347e+04 / 15
p0 1654 ± 0.2778
p1 0.6817 ± 2.893e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
10000
20000
30000
40000
50000
60000

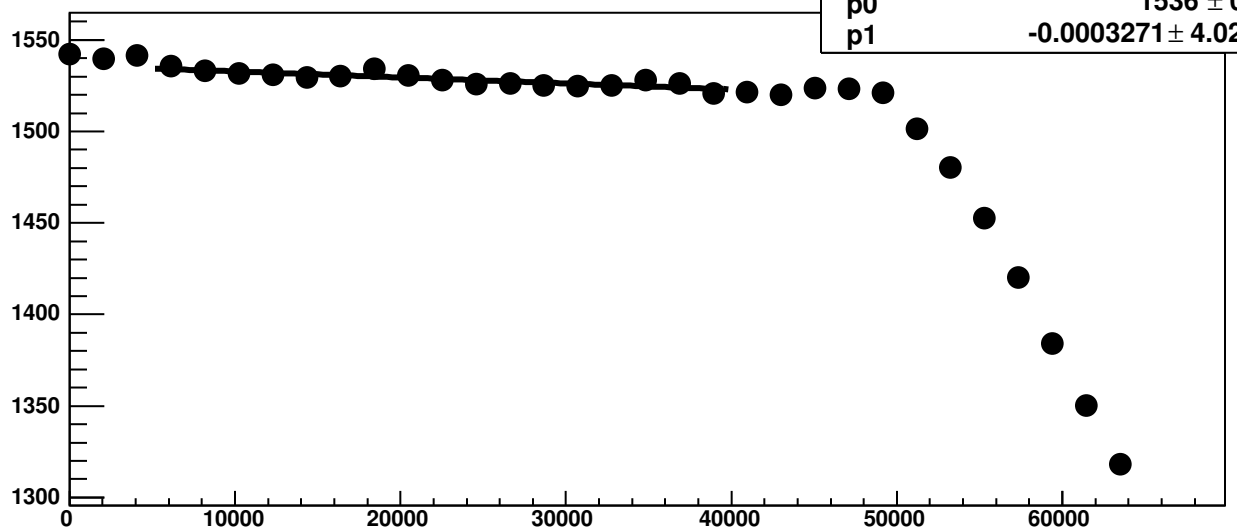

Chip 10, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2310 / 15
p0 1536 ± 0.1035
p1 0.0008656 ± 4.249e-06
1580
1560
1540
1520
1500
1480
1460
1440
1420
1400
1380
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

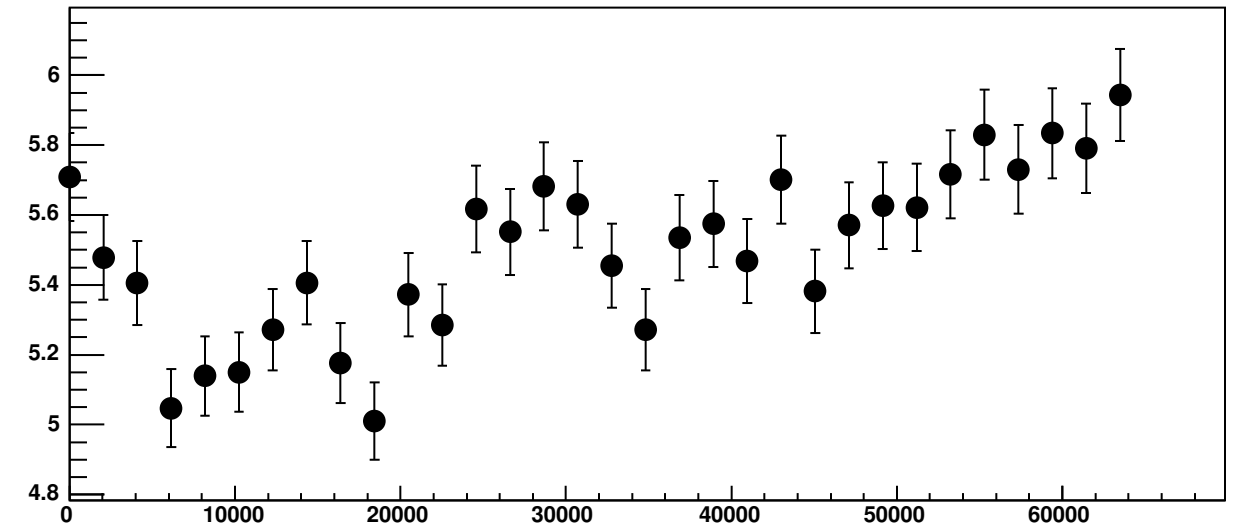

Chip 10, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

Chip 10, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC

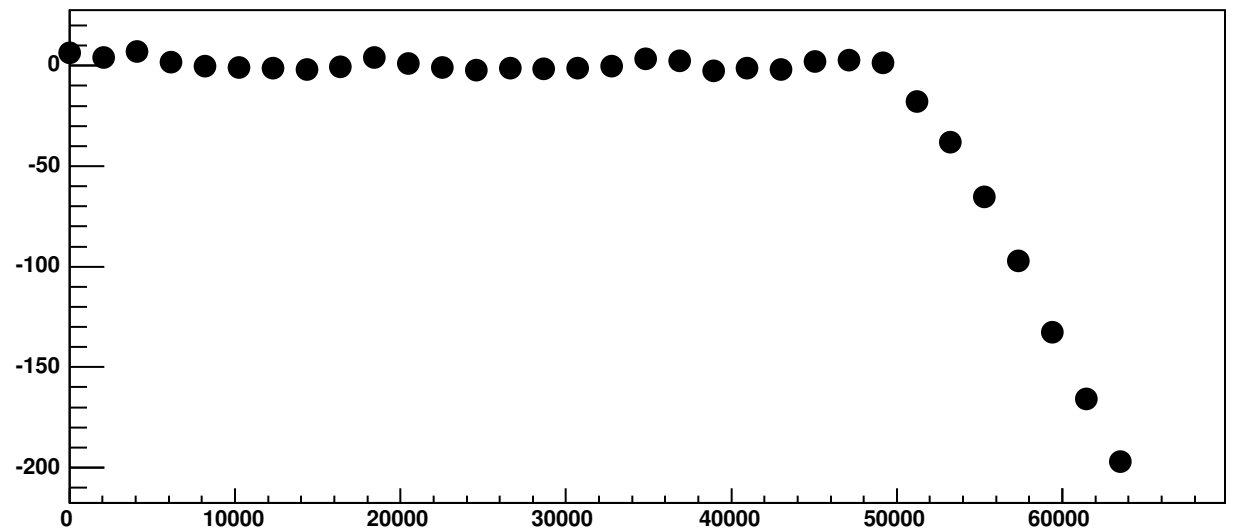
χ^2 / ndf	2288 / 15
p0	1536 ± 0.0972
p1	-0.0003271 ± 4.021e-06



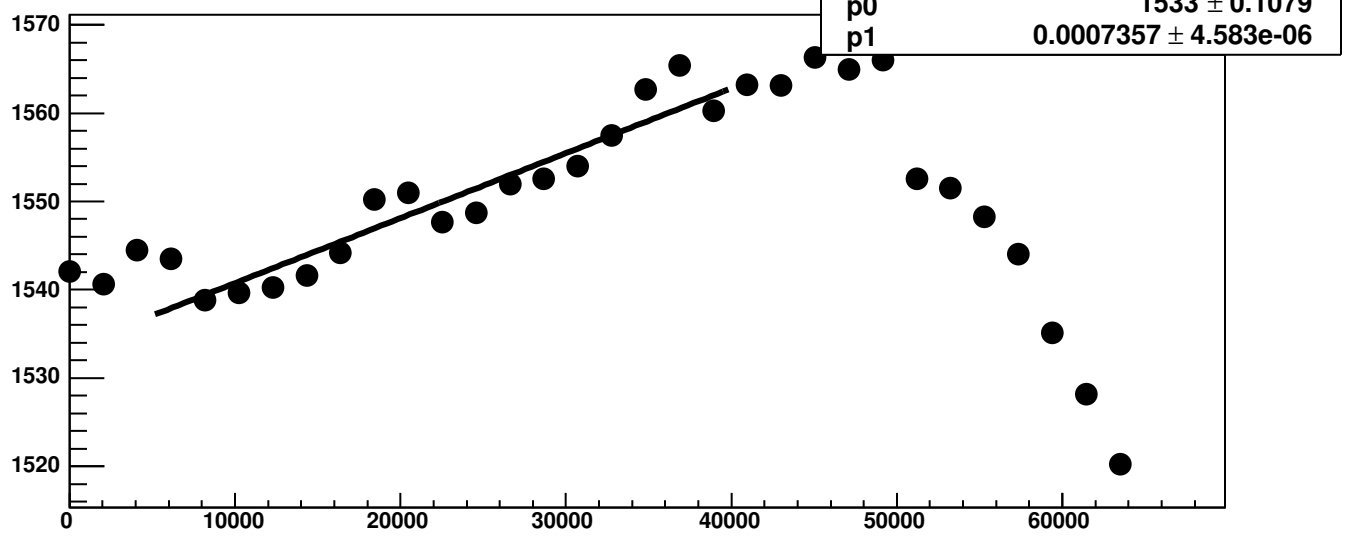
Chip 10, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



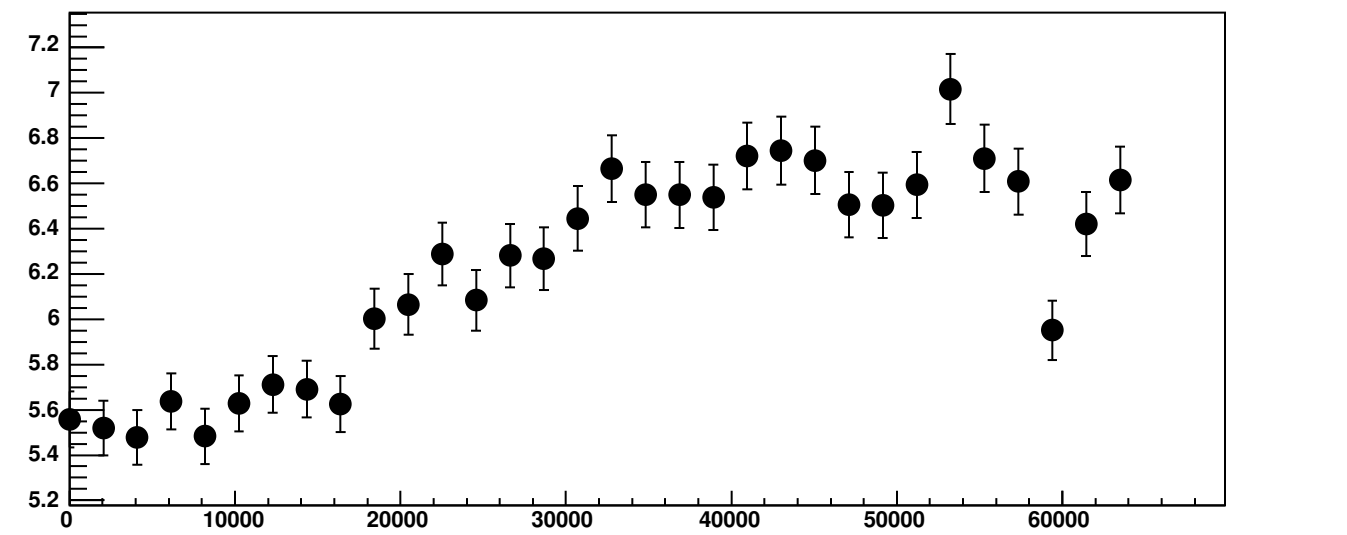
Chip 10, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC



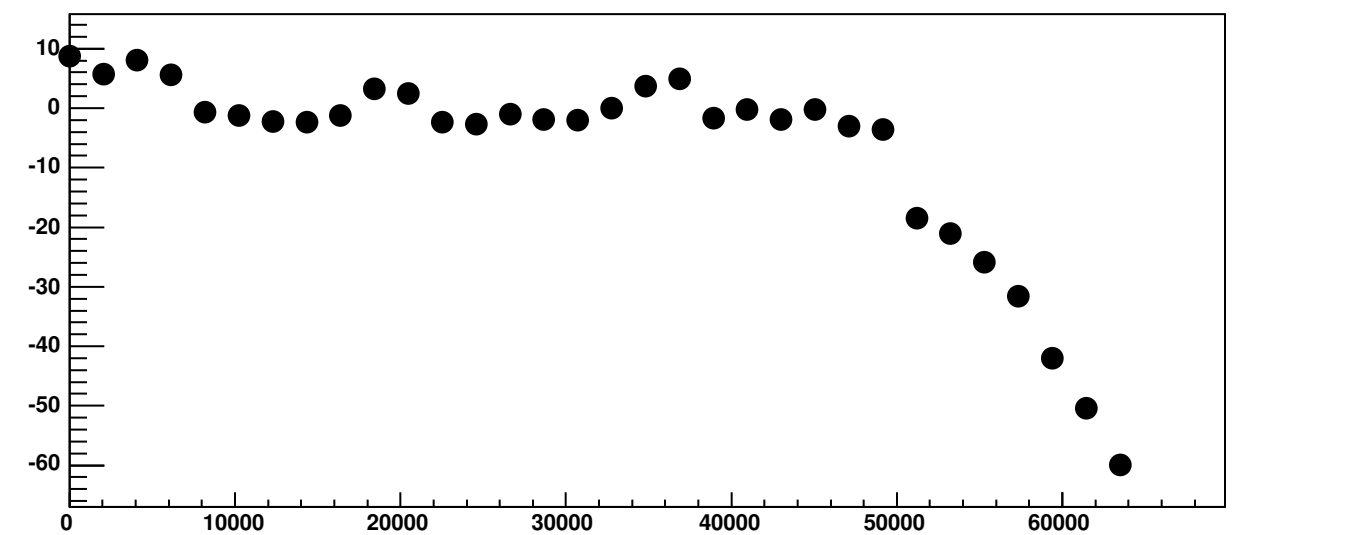
Chip 10, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC



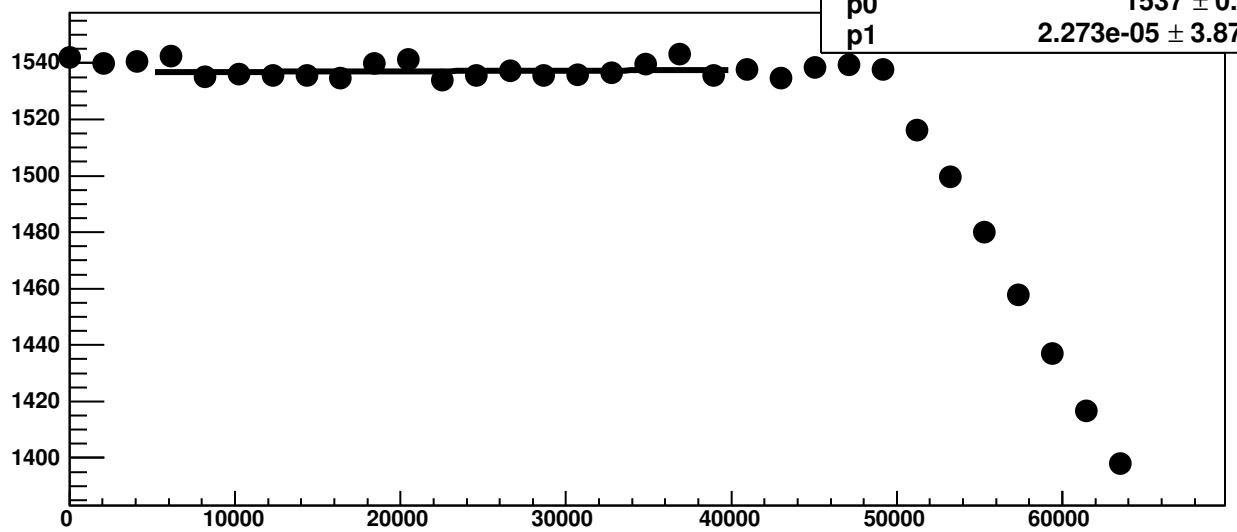
Chip 10, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC



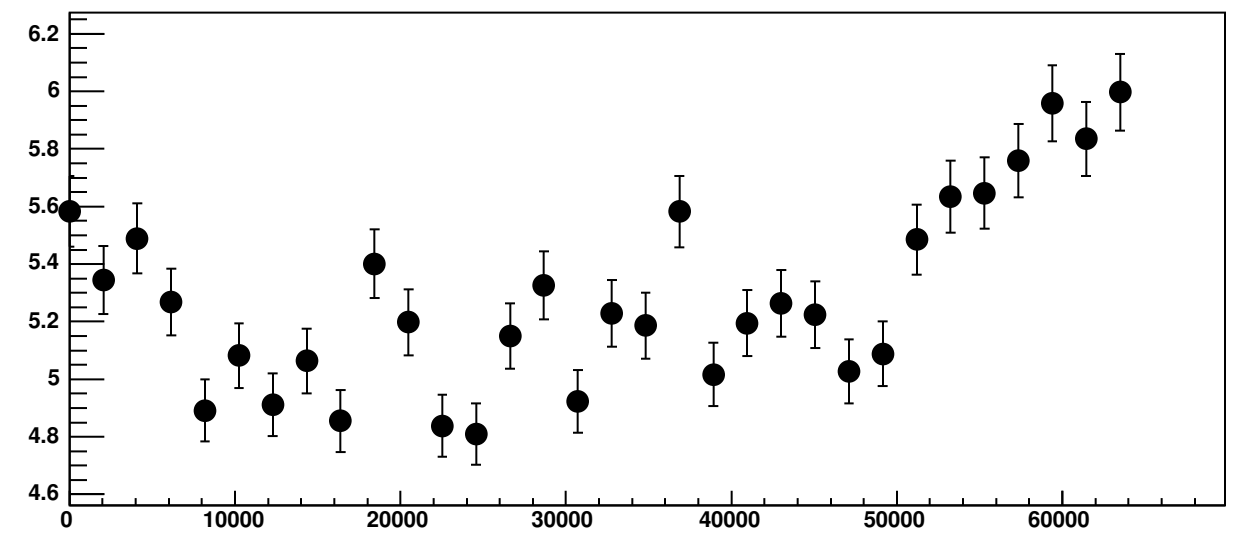
Chip 10, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC



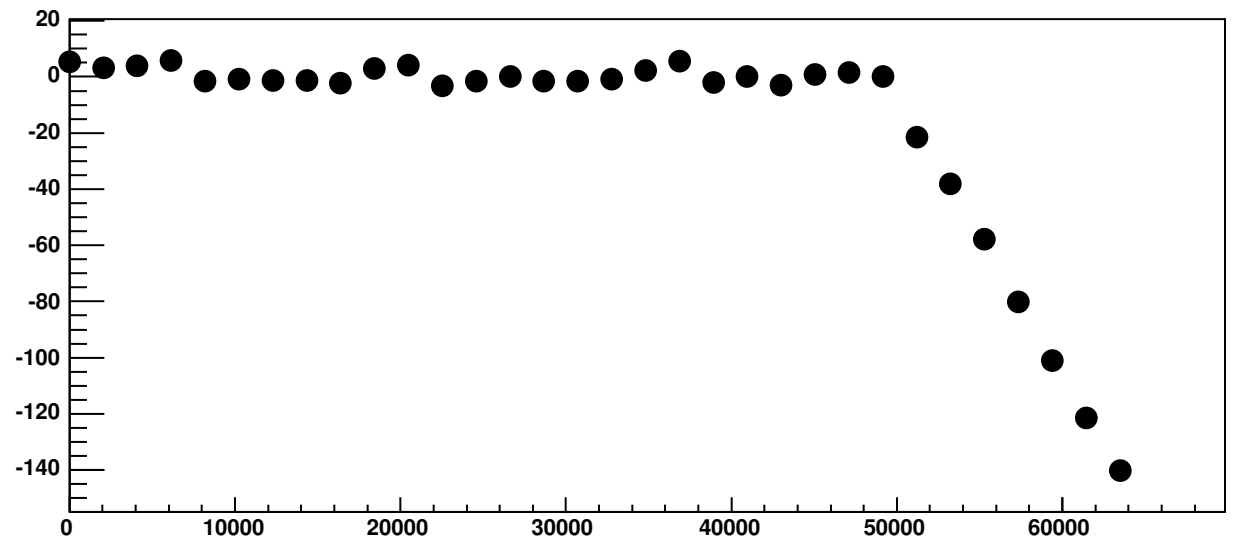
Chip 10, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

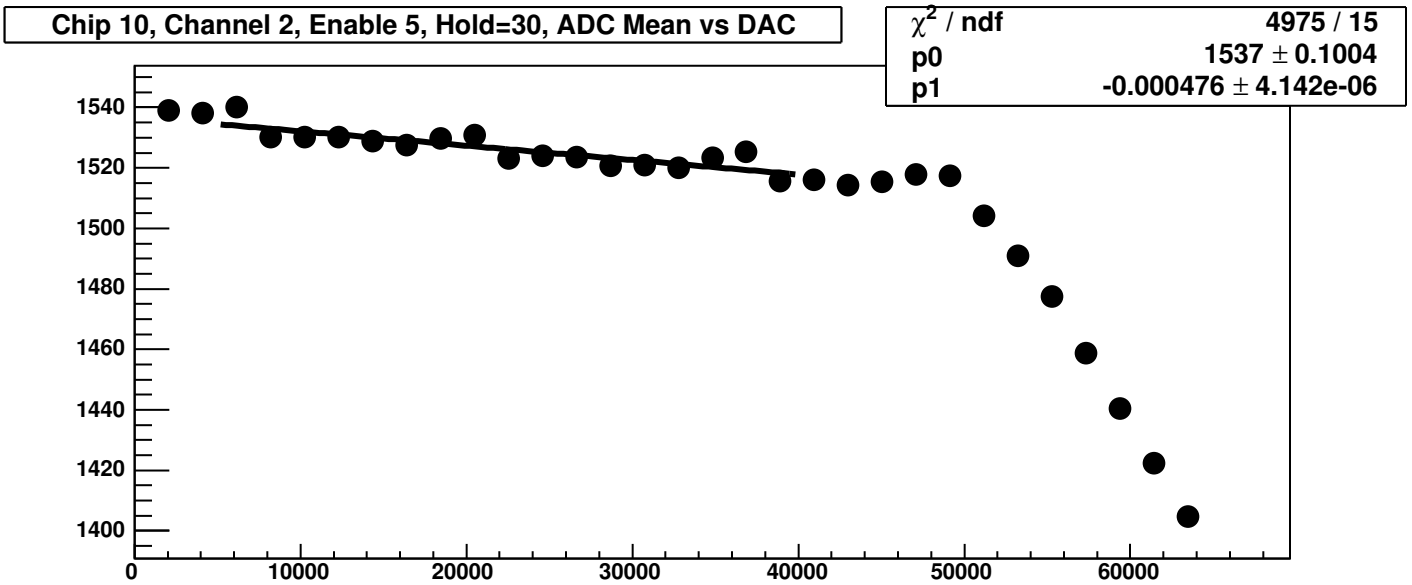


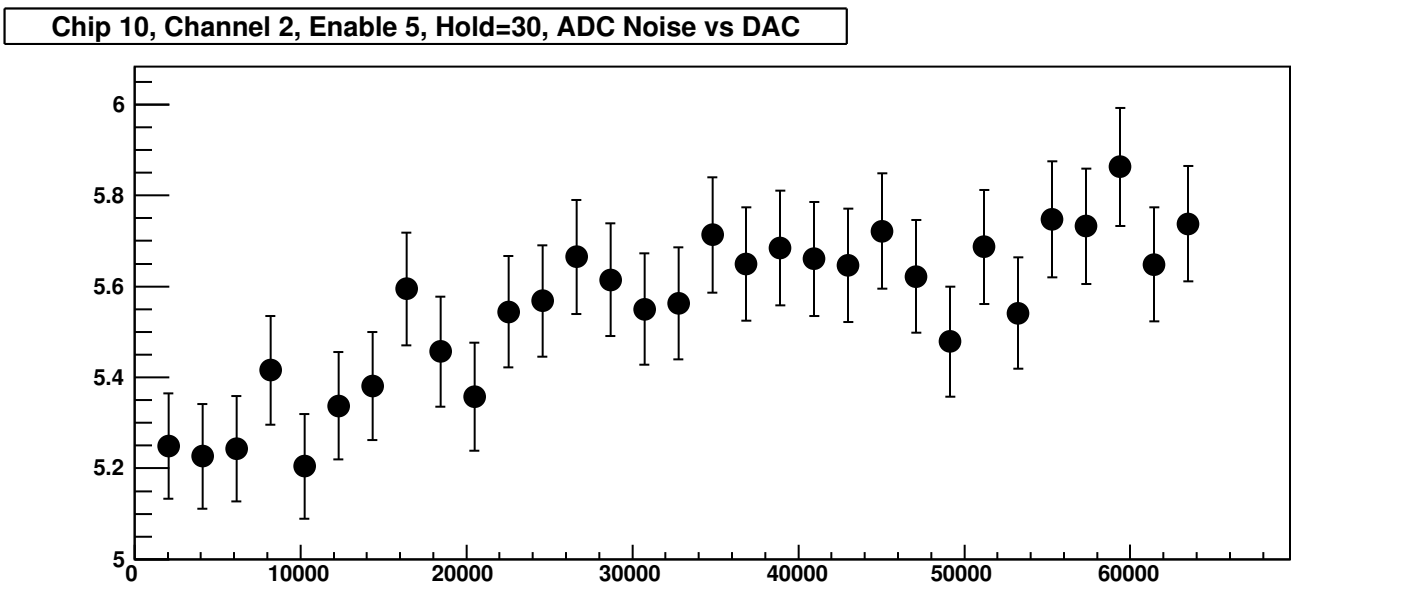
Chip 10, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC

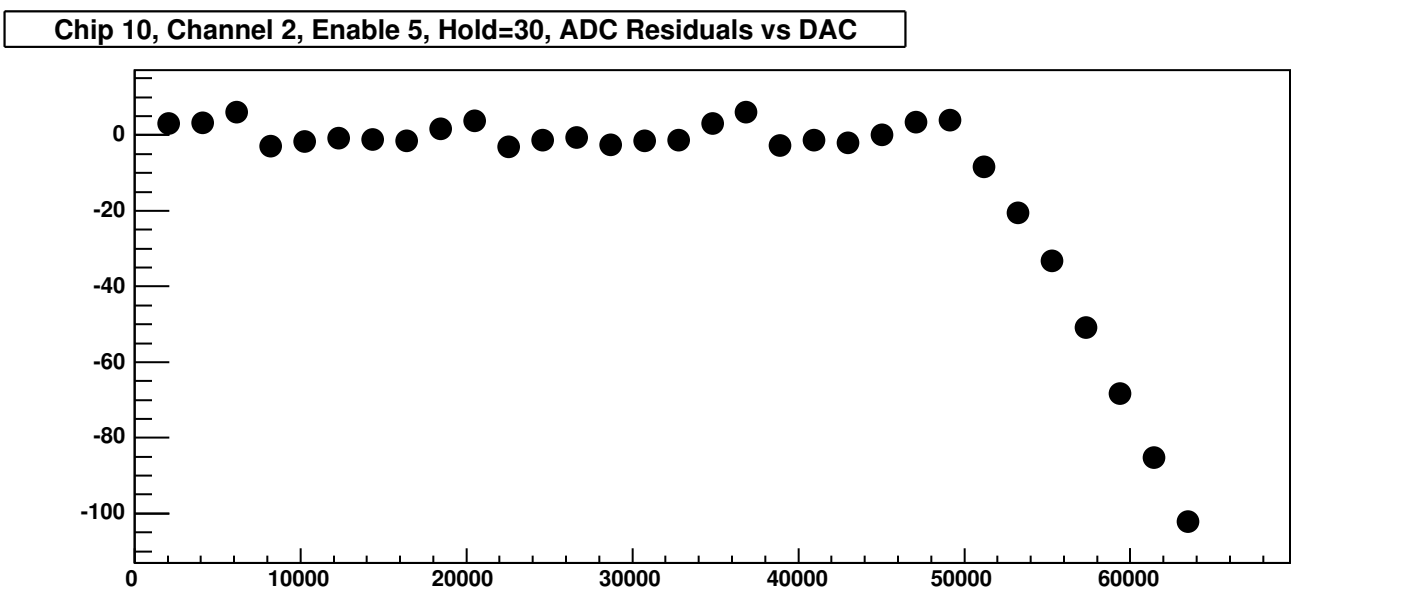


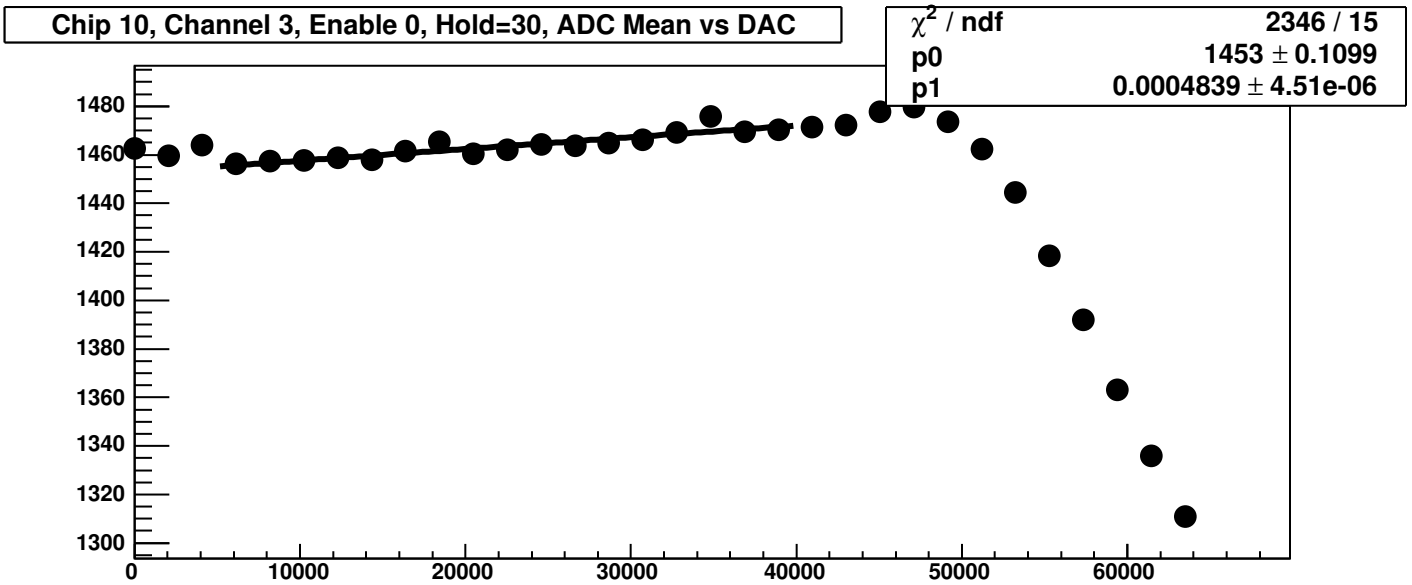
Chip 10, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

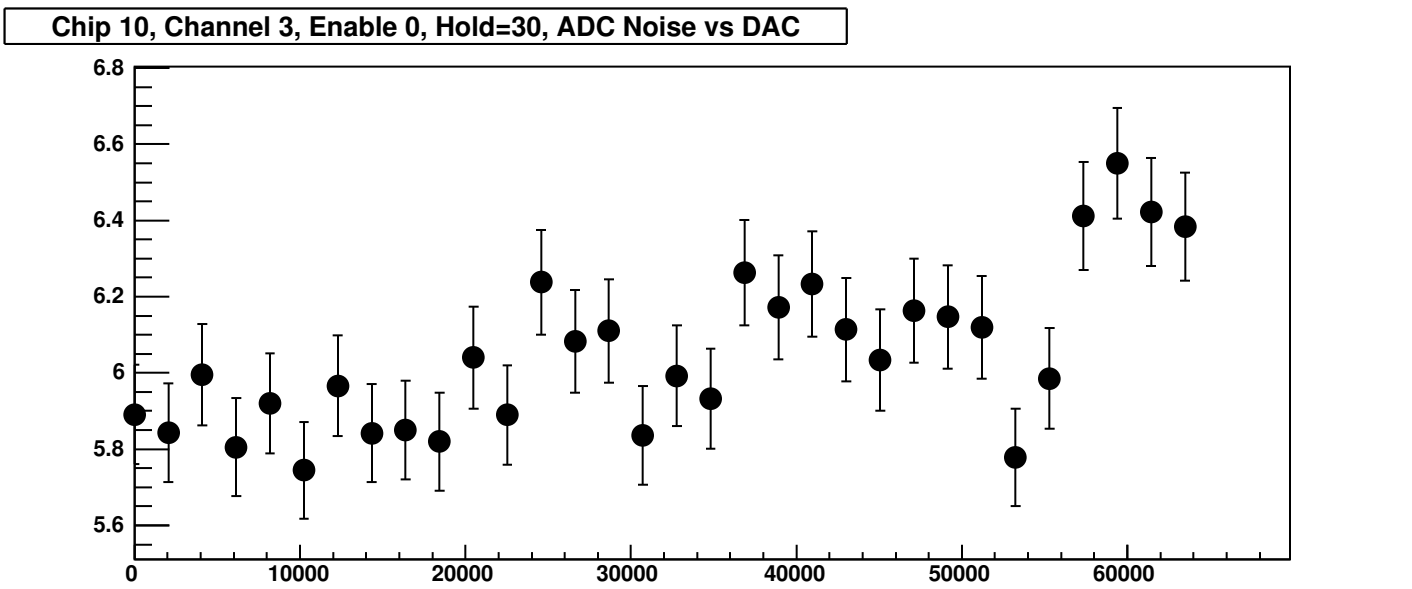


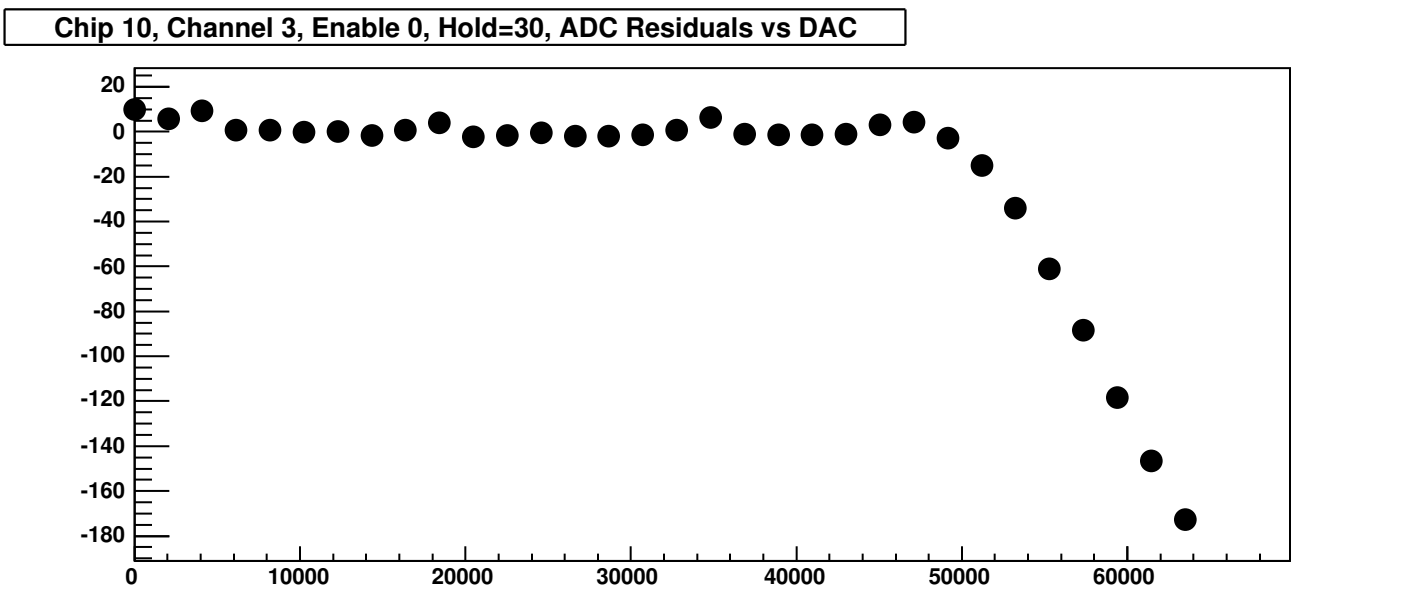

Chip 10, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4975 / 15
p0 1537 ± 0.1004
p1 -0.000476 ± 4.142e-06
1540
1520
1500
1480
1460
1440
1420
1400
0
10000
20000
30000
40000
50000
60000

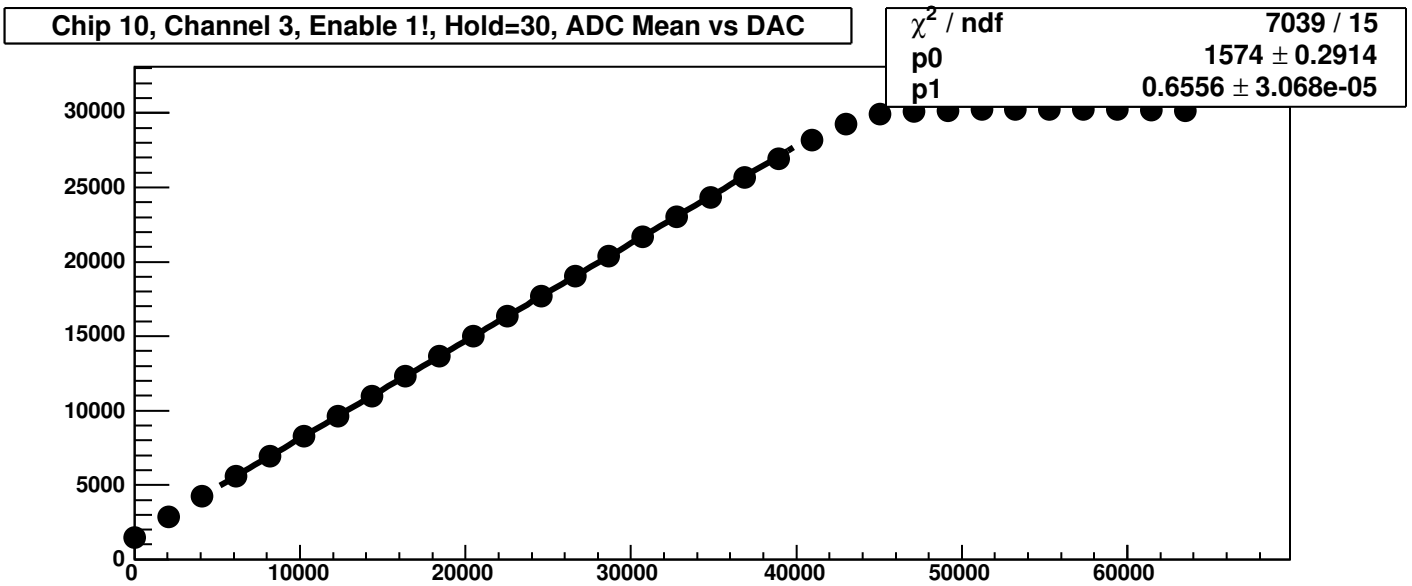

Chip 10, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

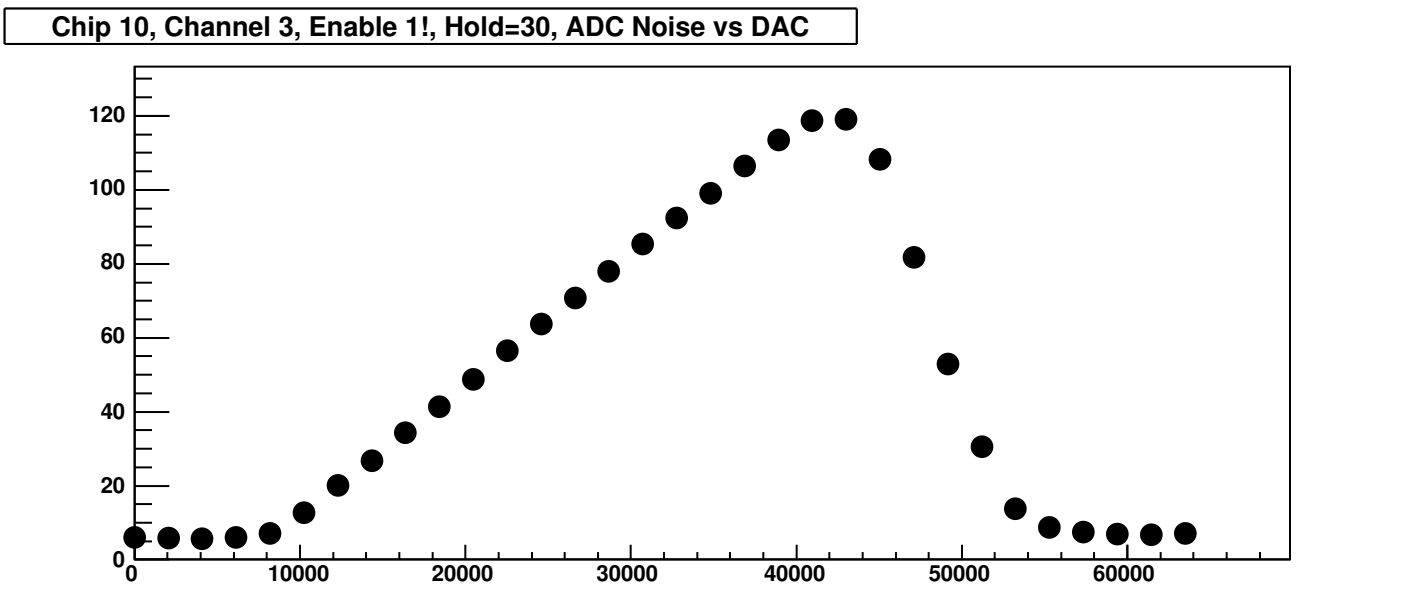

Chip 10, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

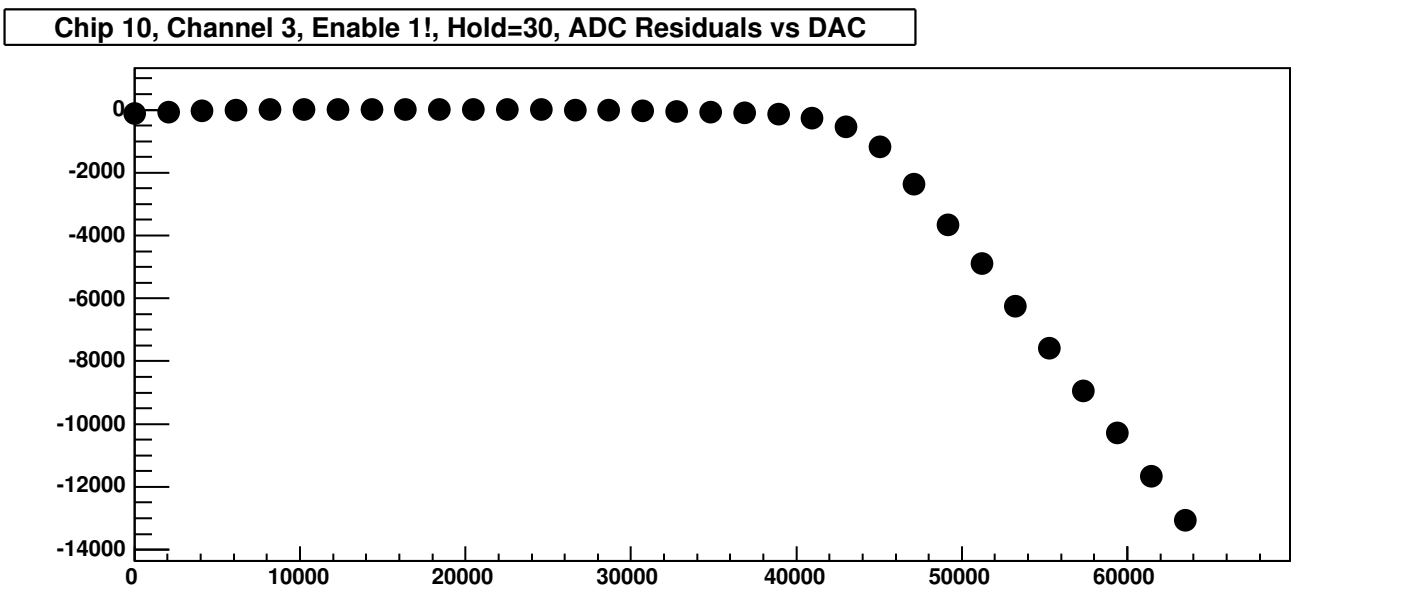

Chip 10, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2346 / 15
p0 1453 ± 0.1099
p1 0.0004839 ± 4.51e-06
1480
1460
1440
1420
1400
1380
1360
1340
1320
1300
0
10000
20000
30000
40000
50000
60000

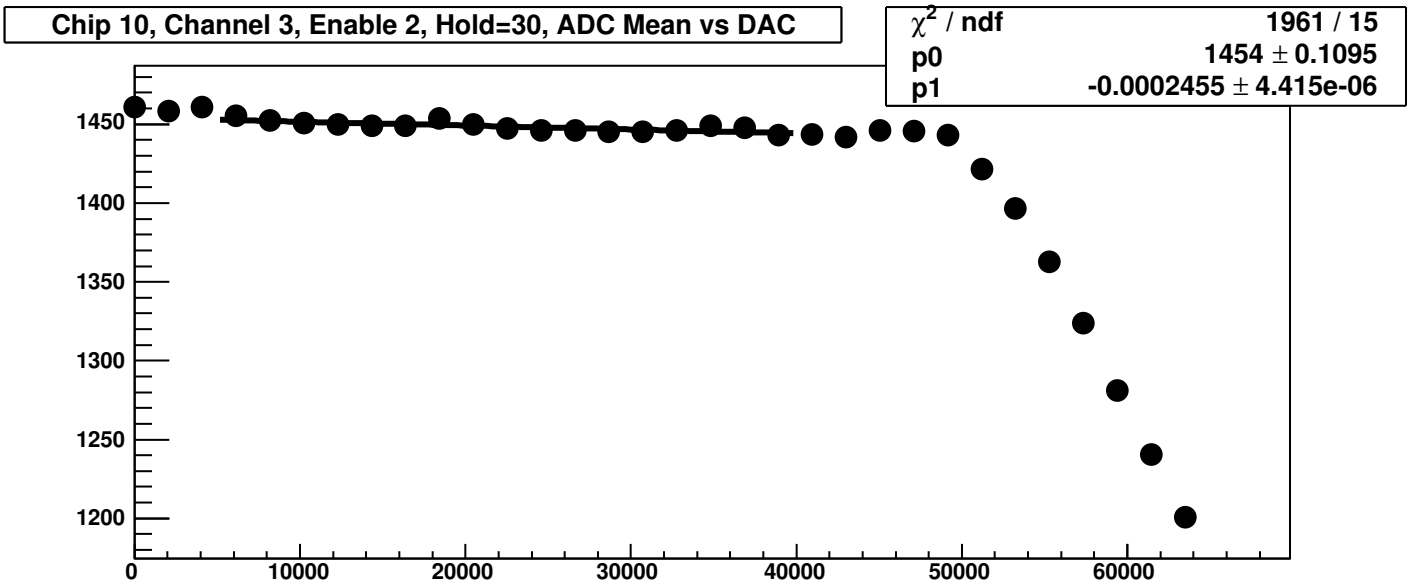

Chip 10, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

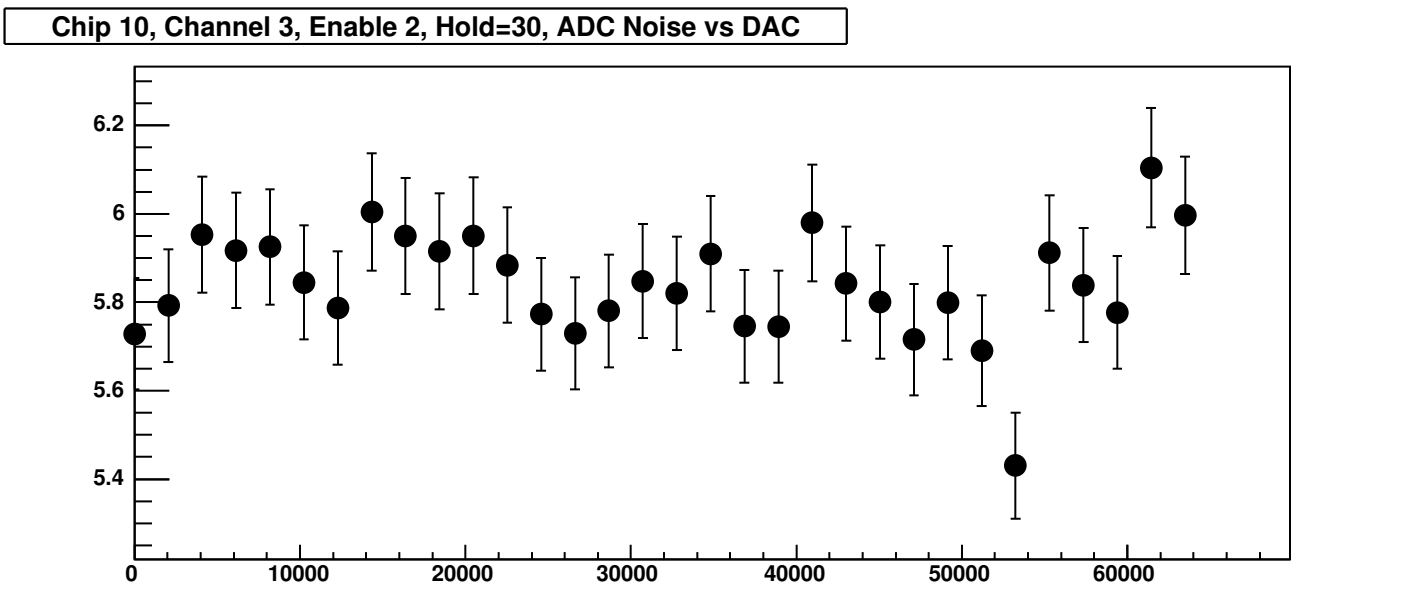

Chip 10, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

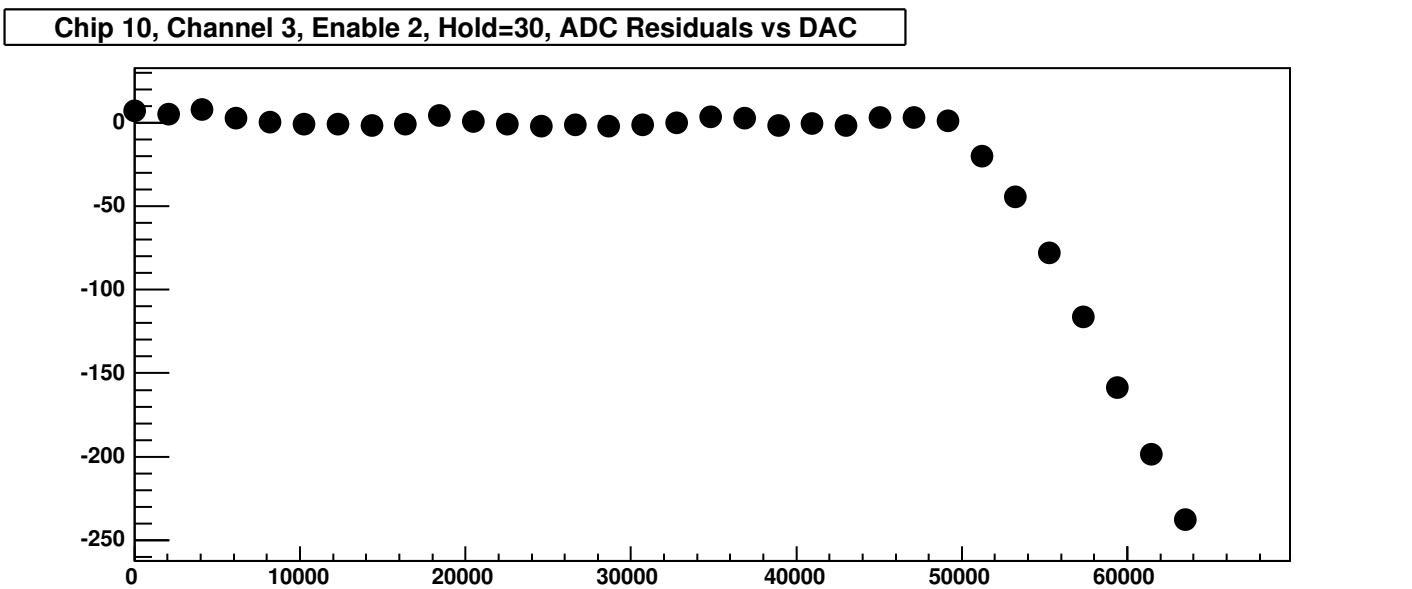

Chip 10, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 7039 / 15
p0 1574 ± 0.2914
p1 0.6556 ± 3.068e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

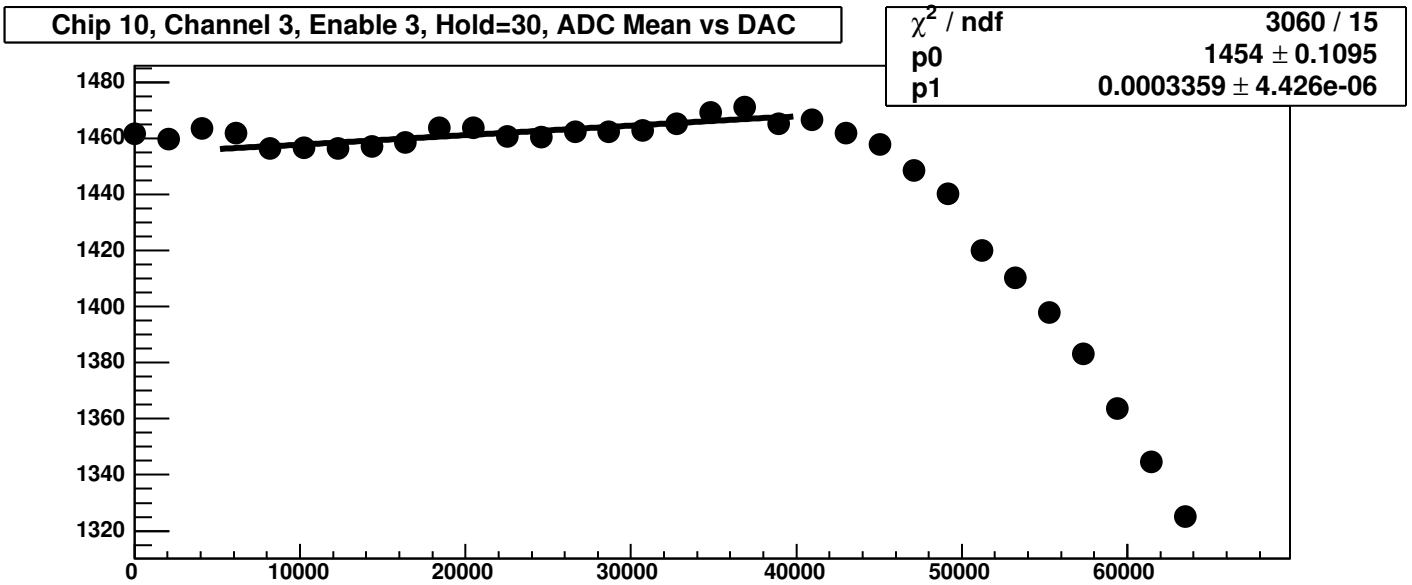

Chip 10, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

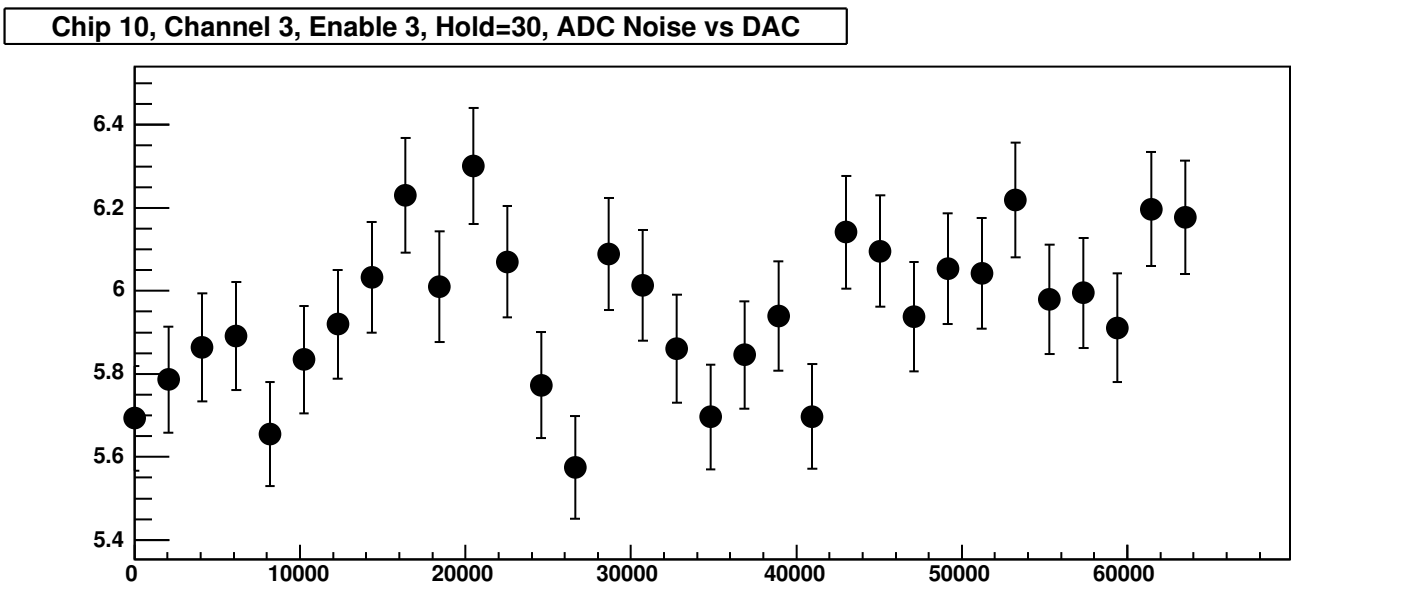

Chip 10, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

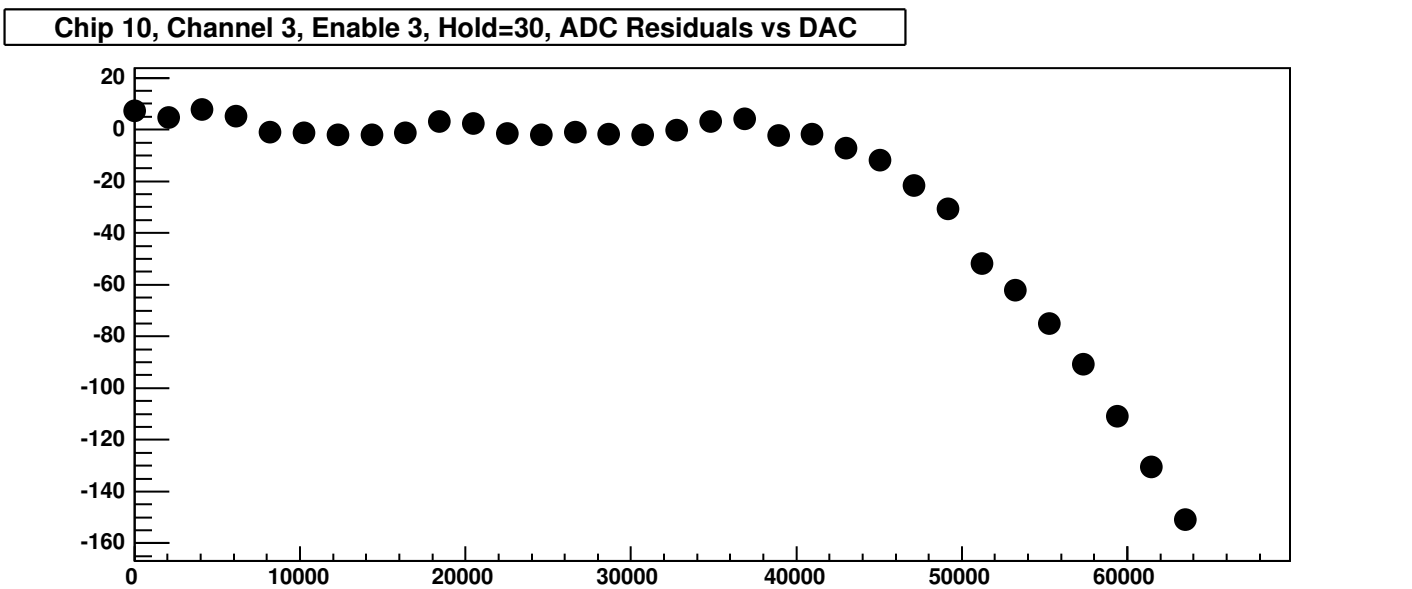

Chip 10, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1961 / 15
p0 1454 ± 0.1095
p1 -0.0002455 ± 4.415e-06
1450
1400
1350
1300
1250
1200
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

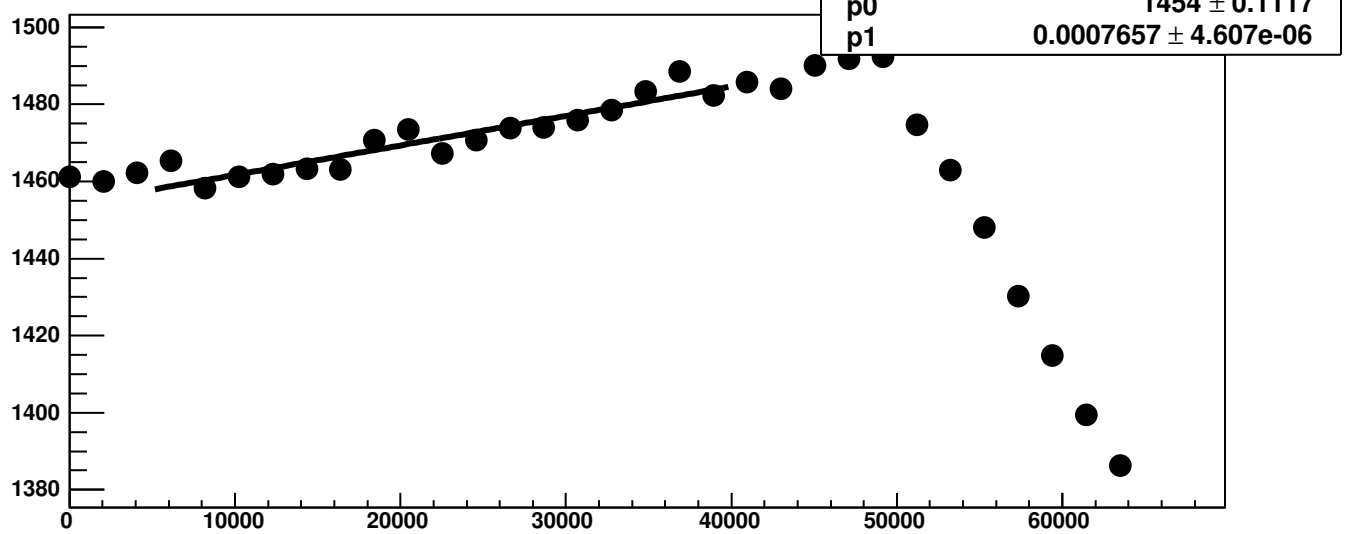

Chip 10, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3060 / 15
p0 1454 ± 0.1095
p1 0.0003359 ± 4.426e-06
1480
1460
1440
1420
1400
1380
1360
1340
1320
0
10000
20000
30000
40000
50000
60000

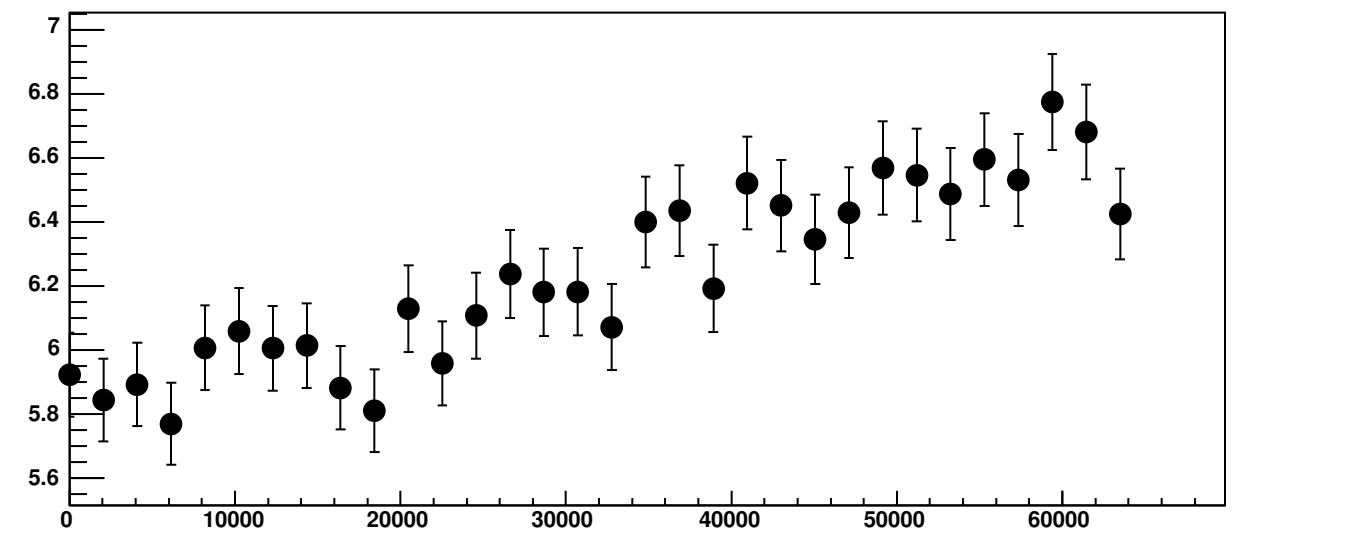

Chip 10, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

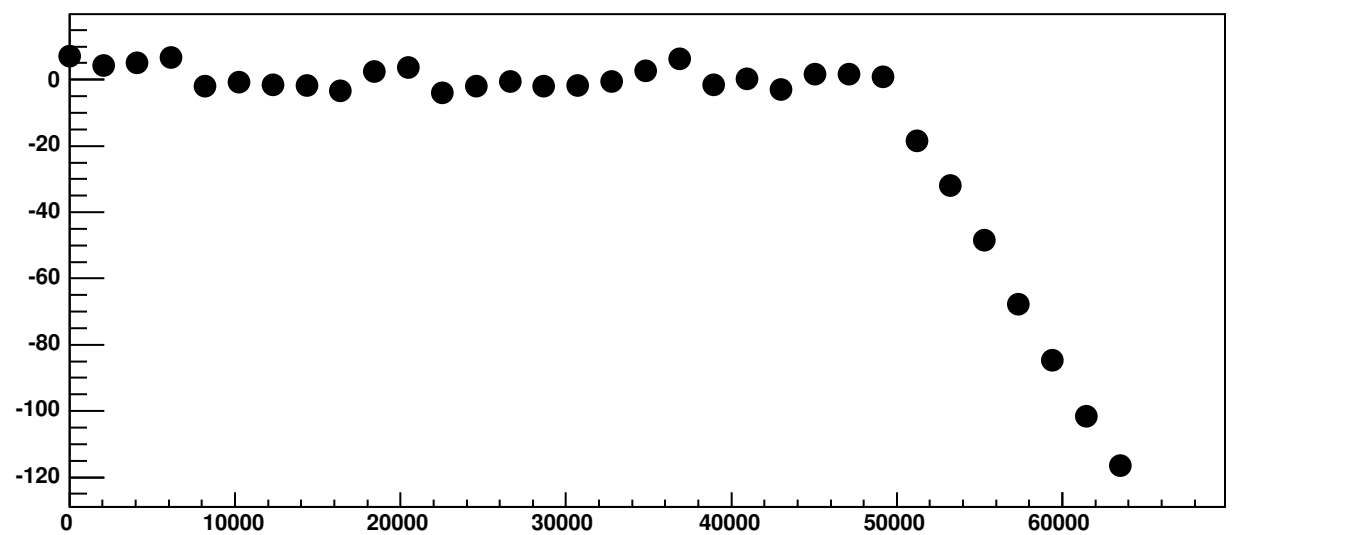
Chip 10, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC

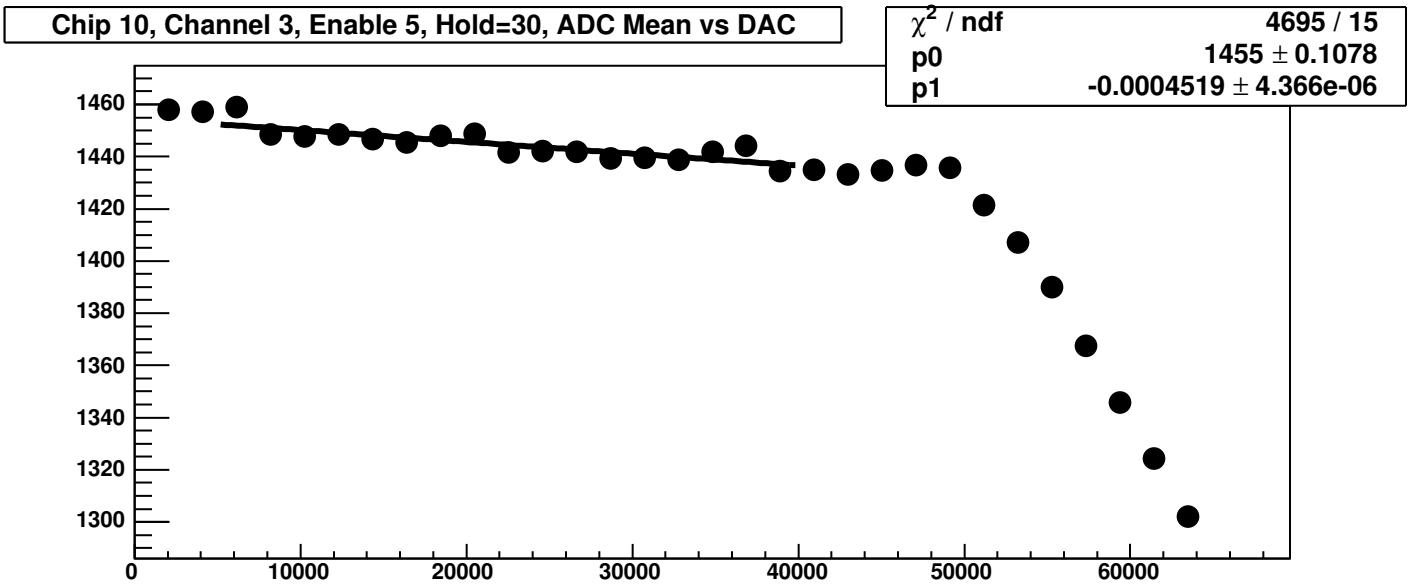


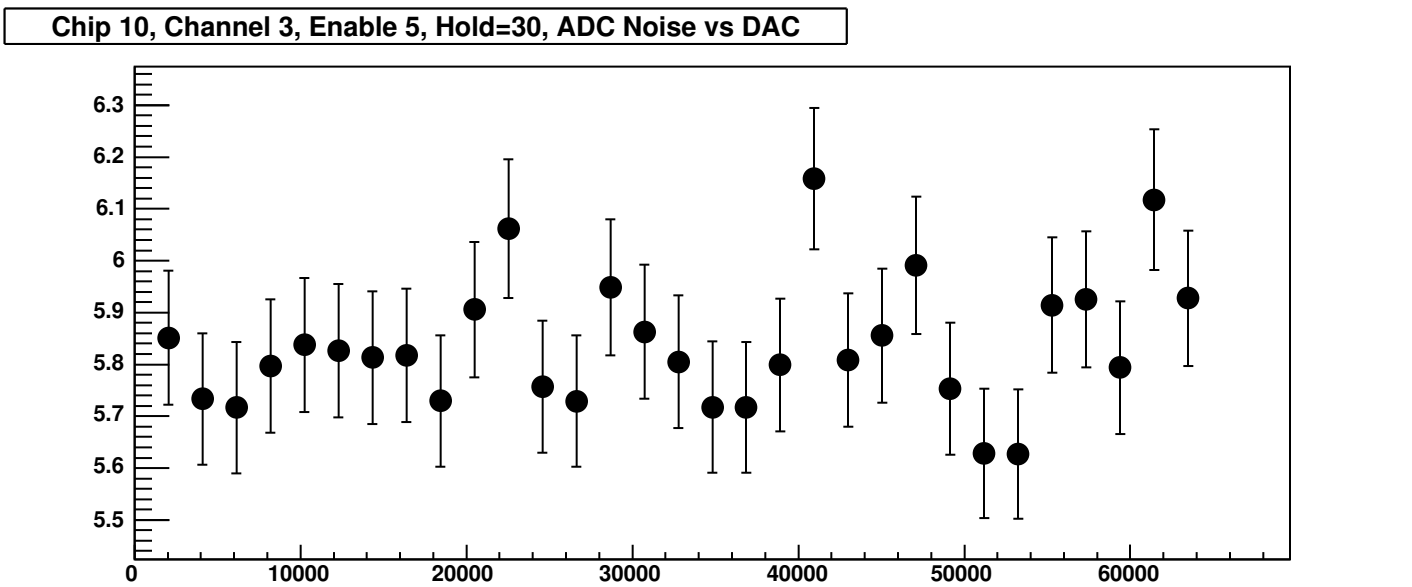
Chip 10, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC

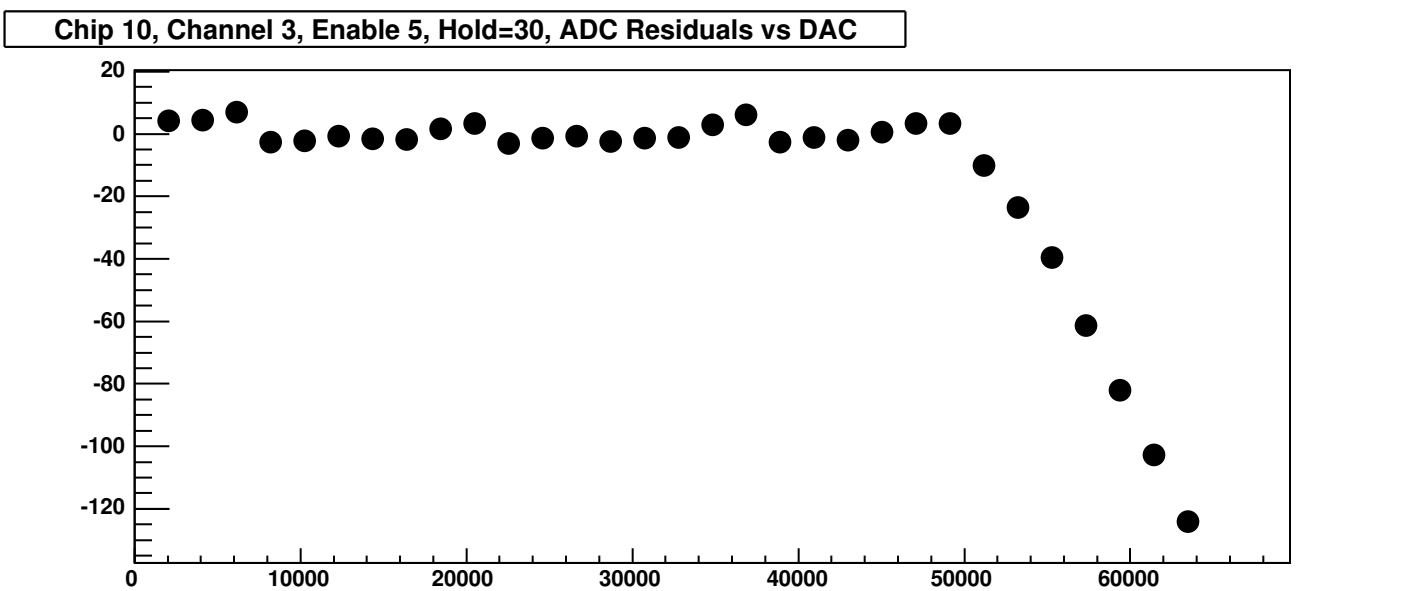


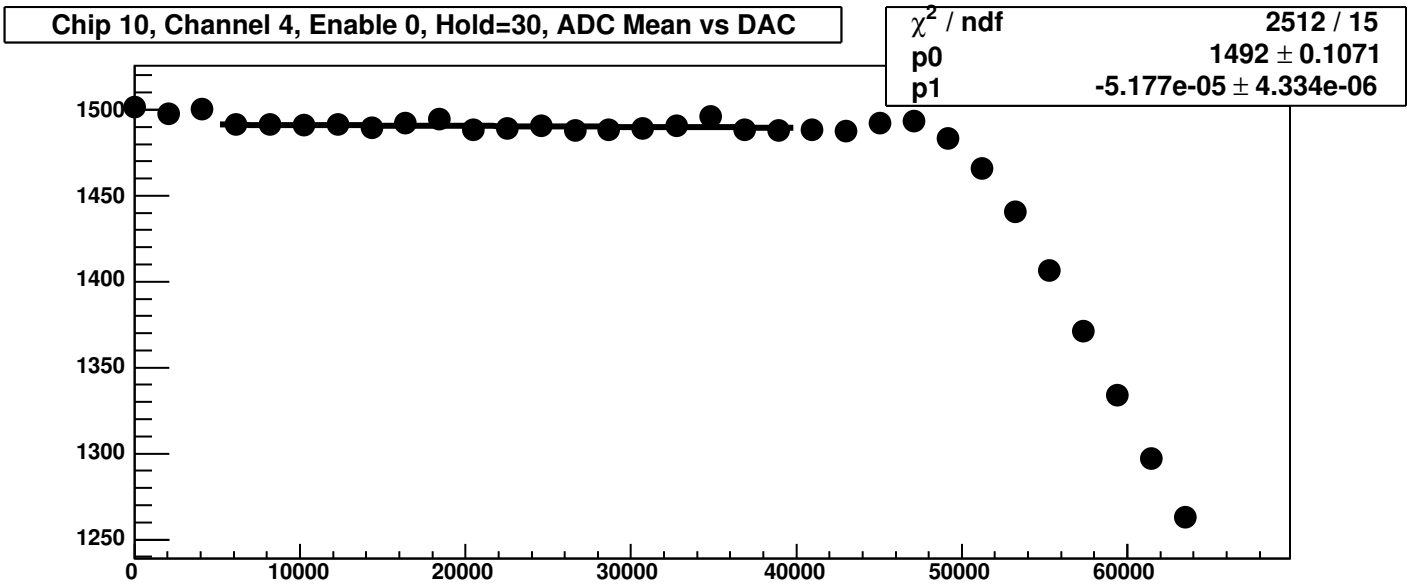
Chip 10, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC

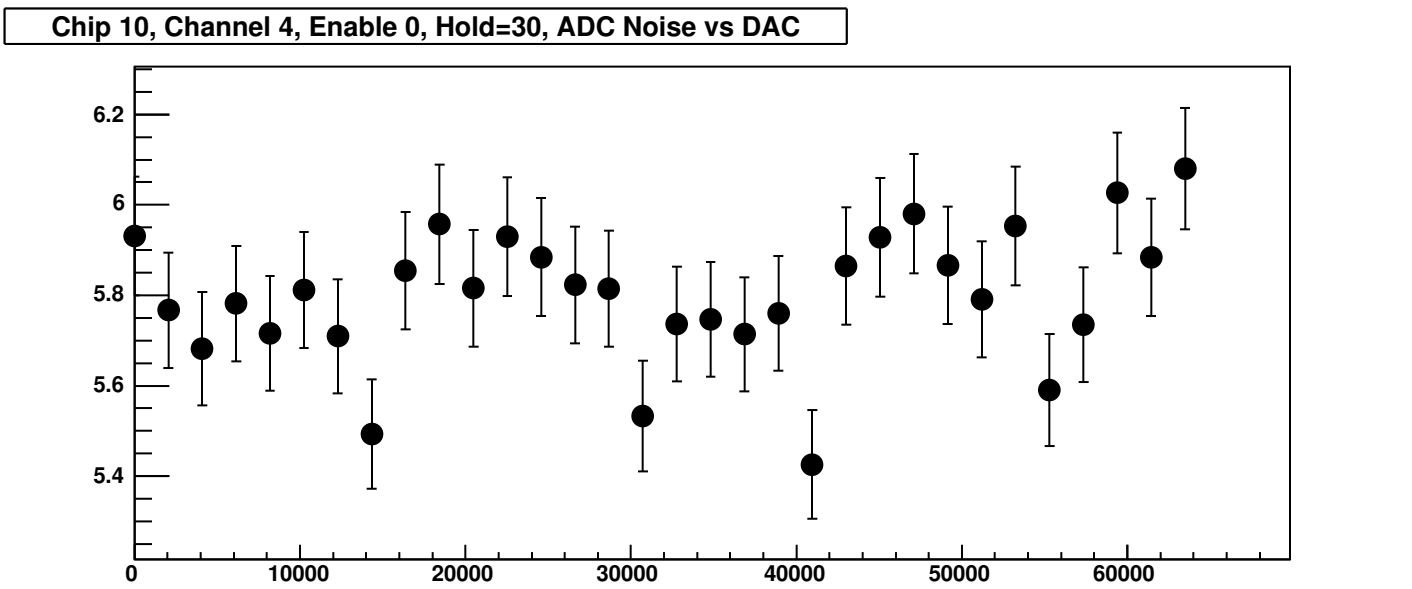


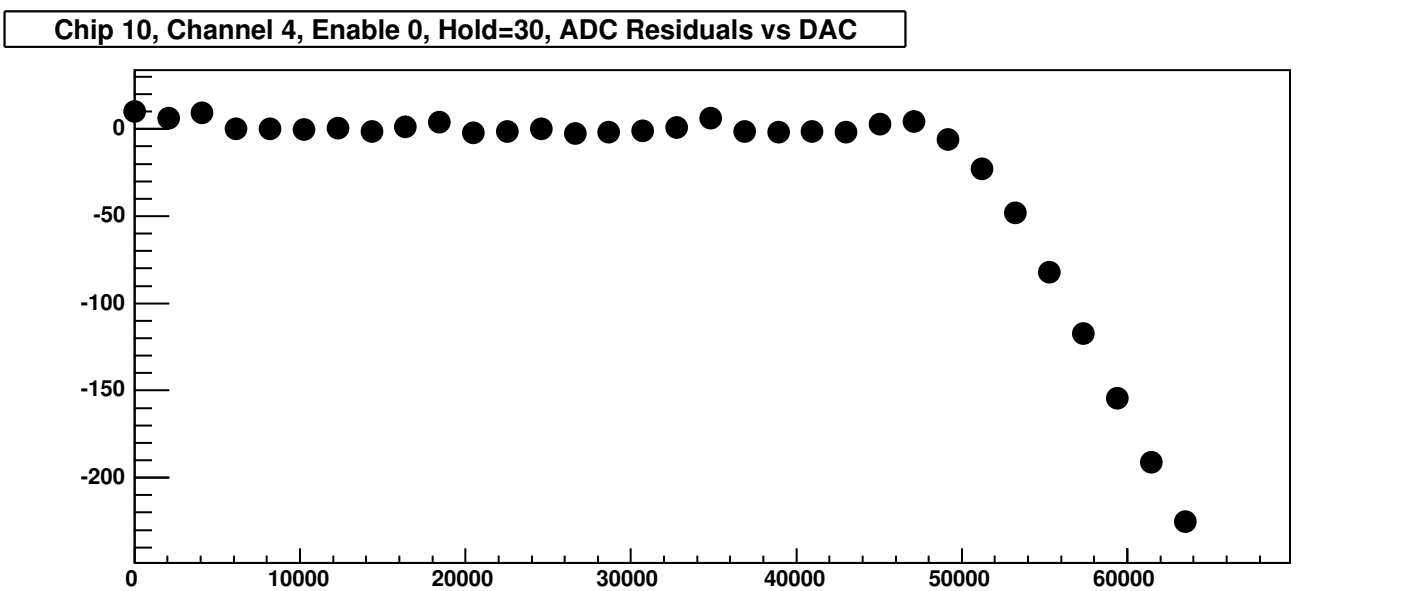

Chip 10, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4695 / 15
p0 1455 ± 0.1078
p1 -0.0004519 ± 4.366e-06
1460
1440
1420
1400
1380
1360
1340
1320
1300
0
10000
20000
30000
40000
50000
60000

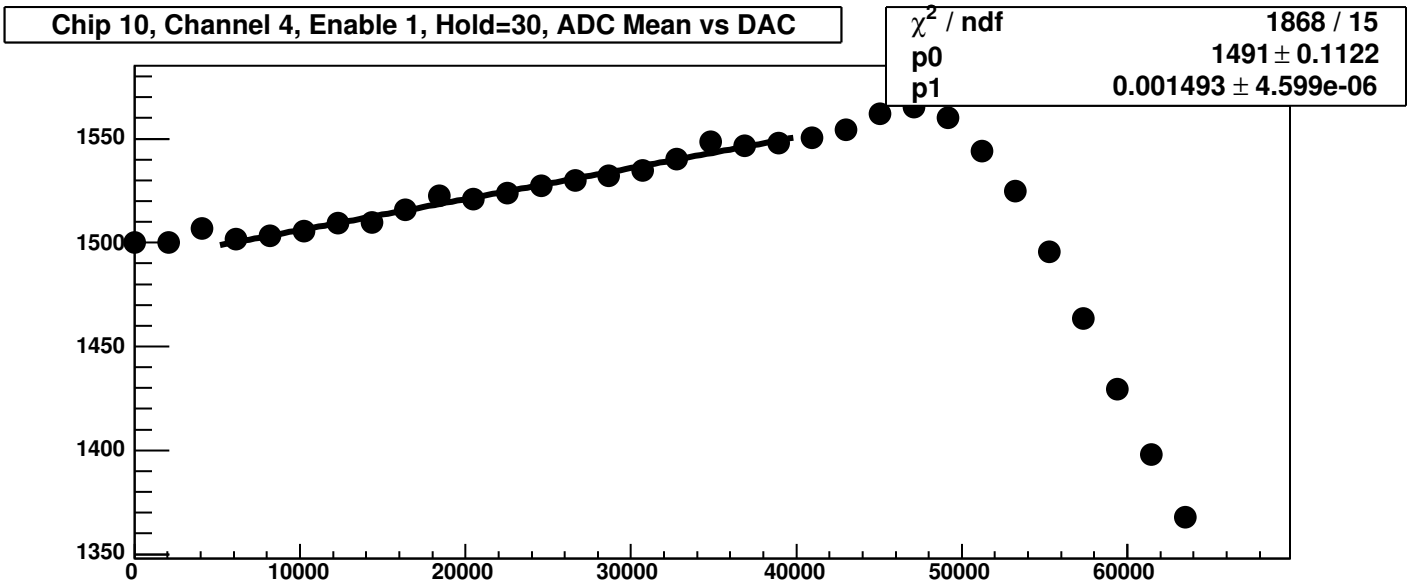

Chip 10, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

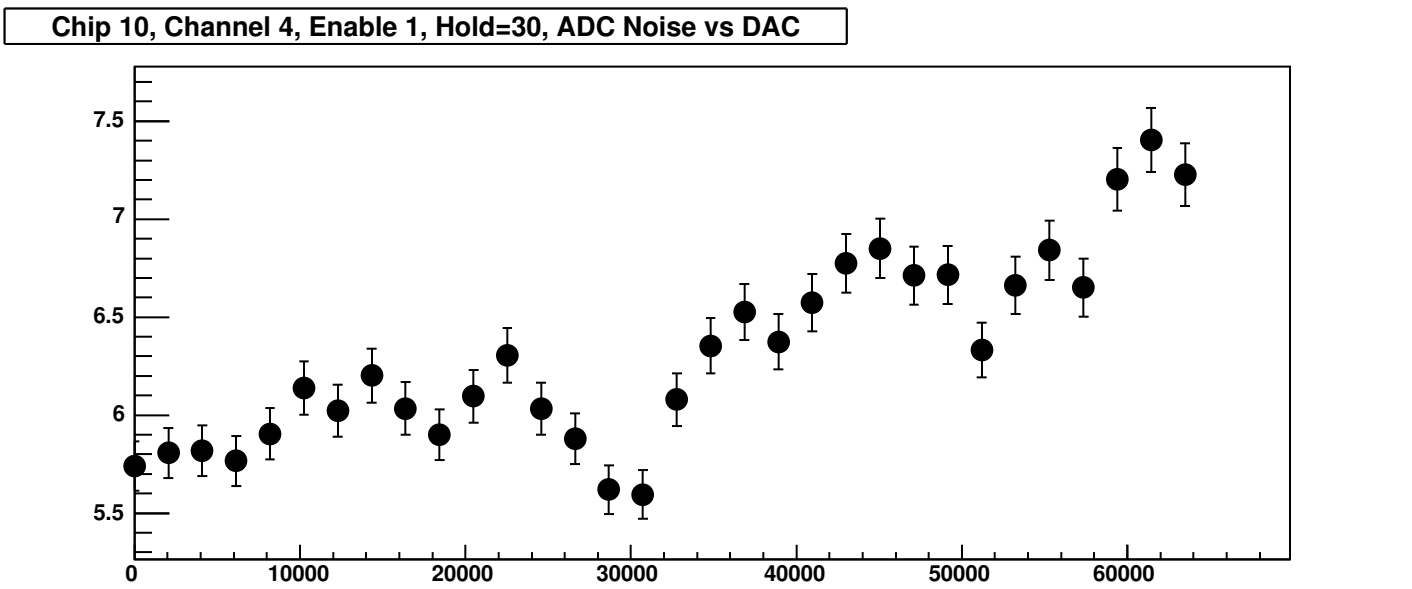

Chip 10, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

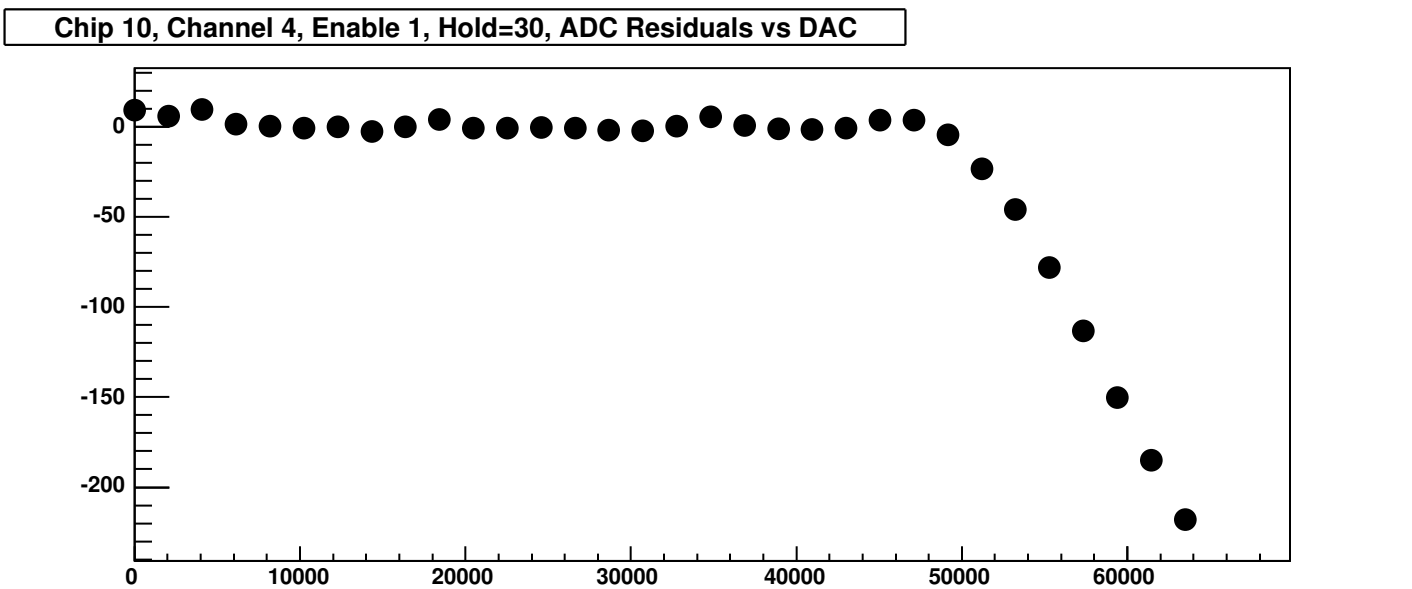

Chip 10, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2512 / 15
p0 1492 ± 0.1071
p1 -5.177e-05 ± 4.334e-06
1500
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

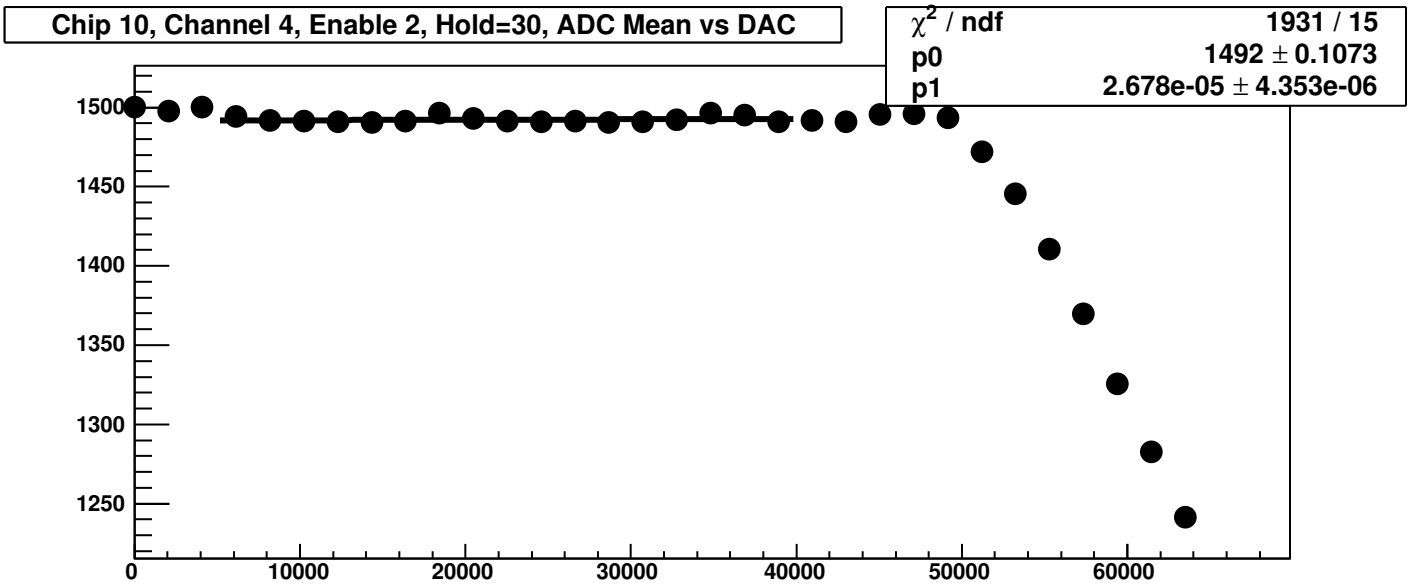

Chip 10, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

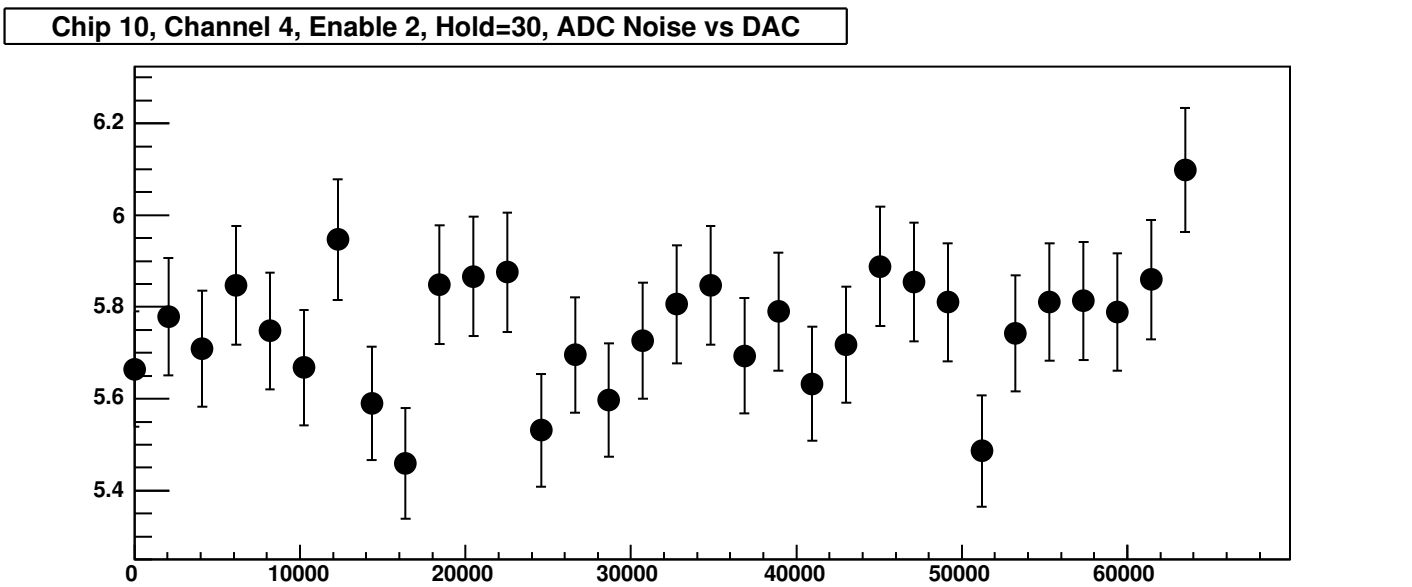

Chip 10, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

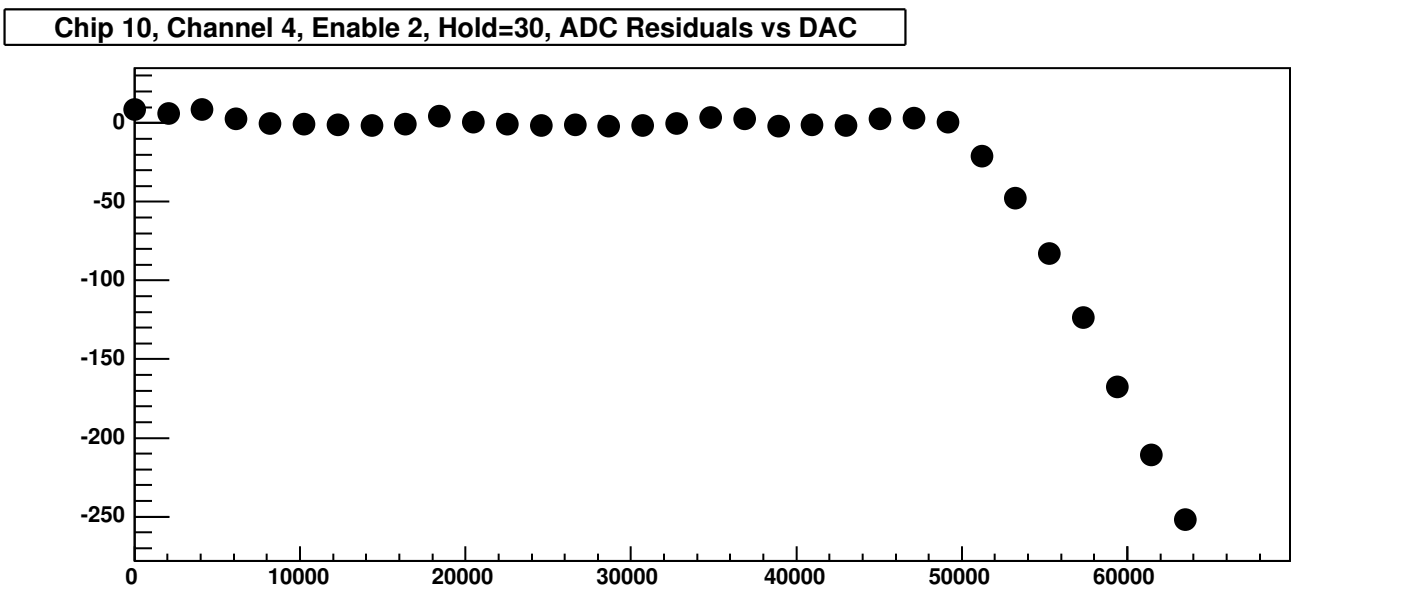

Chip 10, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1868 / 15
p0 1491 ± 0.1122
p1 0.001493 ± 4.599e-06
1550
1500
1450
1400
1350
0
10000
20000
30000
40000
50000
60000

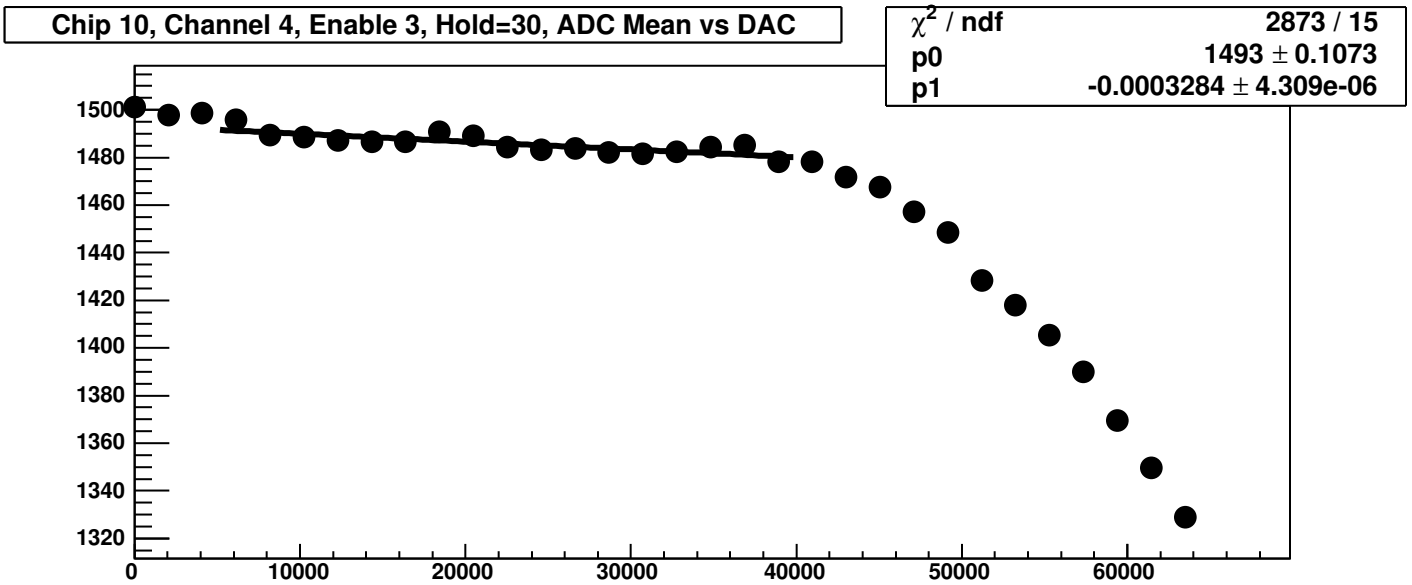

Chip 10, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000

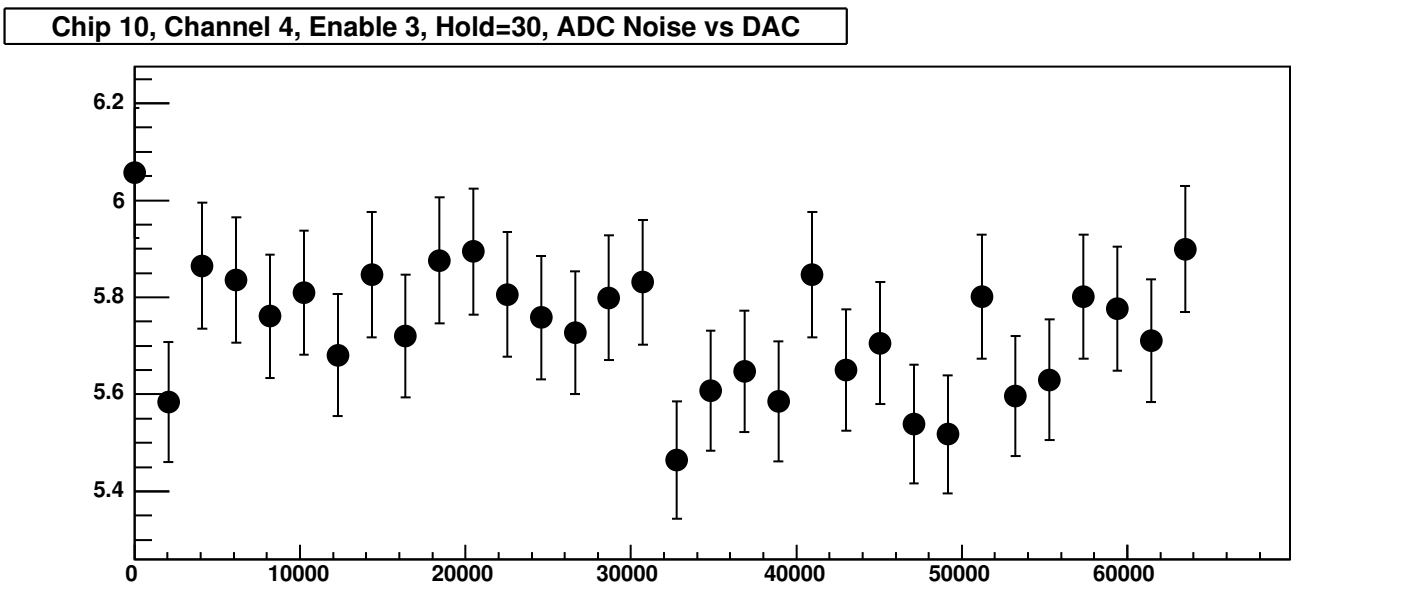

Chip 10, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

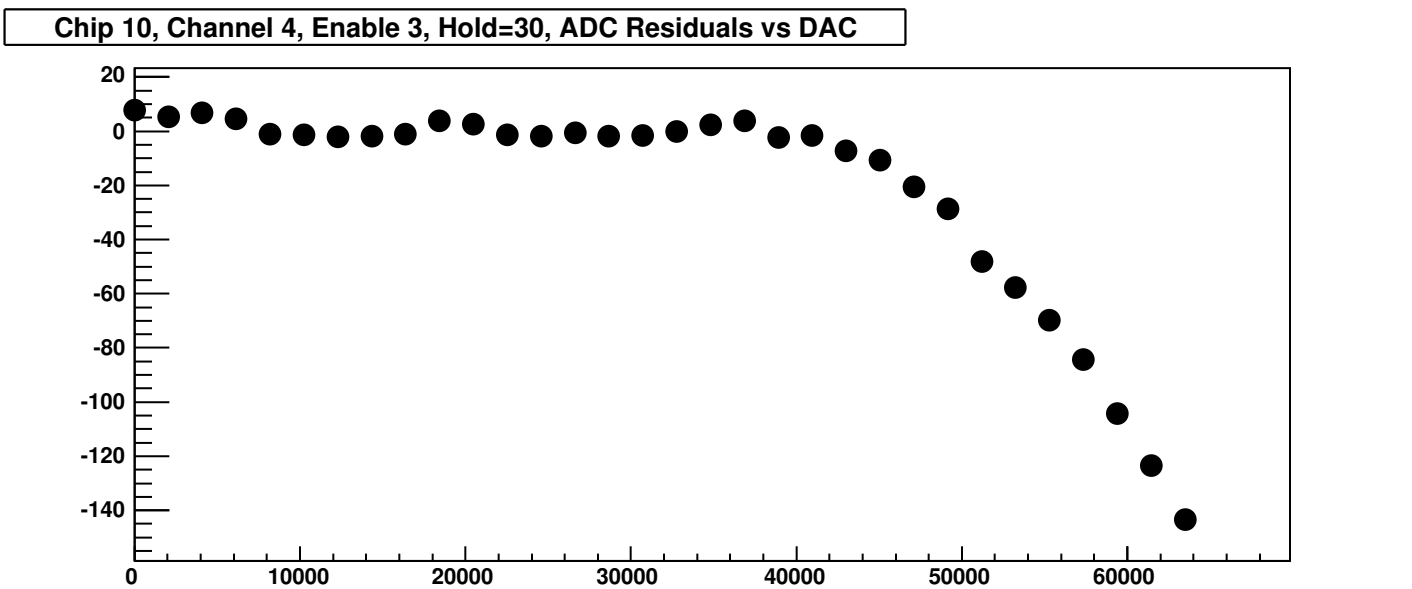

Chip 10, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1931 / 15
p0 1492 ± 0.1073
p1 2.678e-05 ± 4.353e-06
1500
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

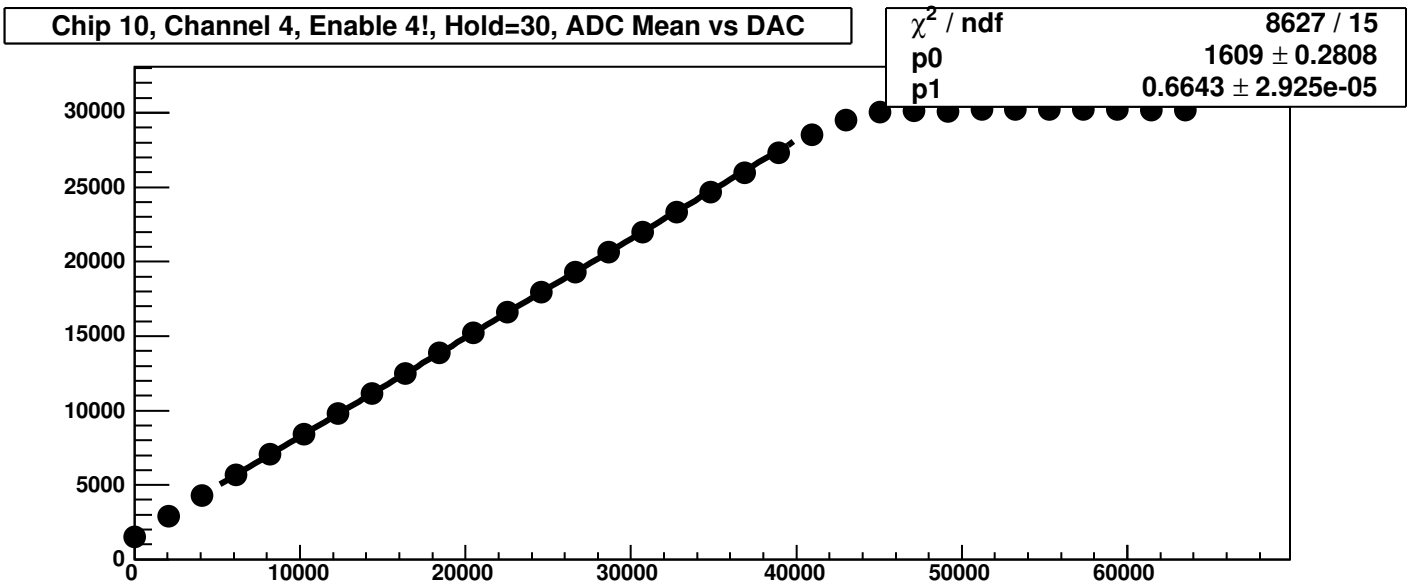

Chip 10, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

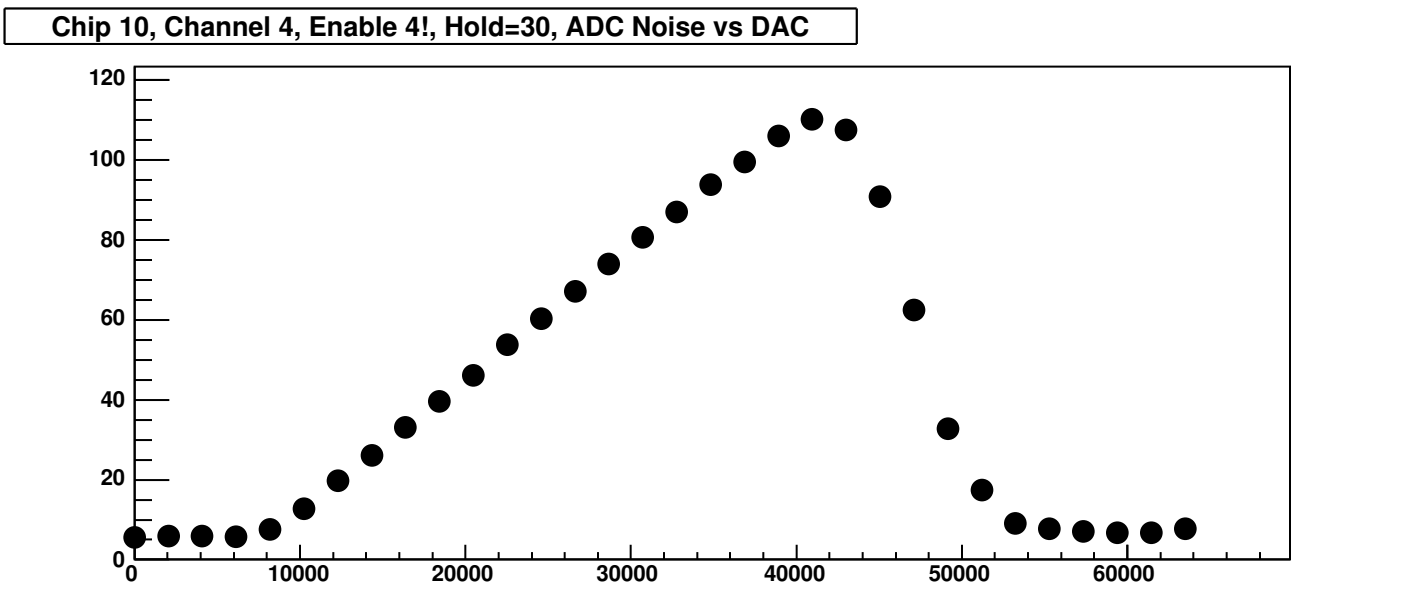

Chip 10, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

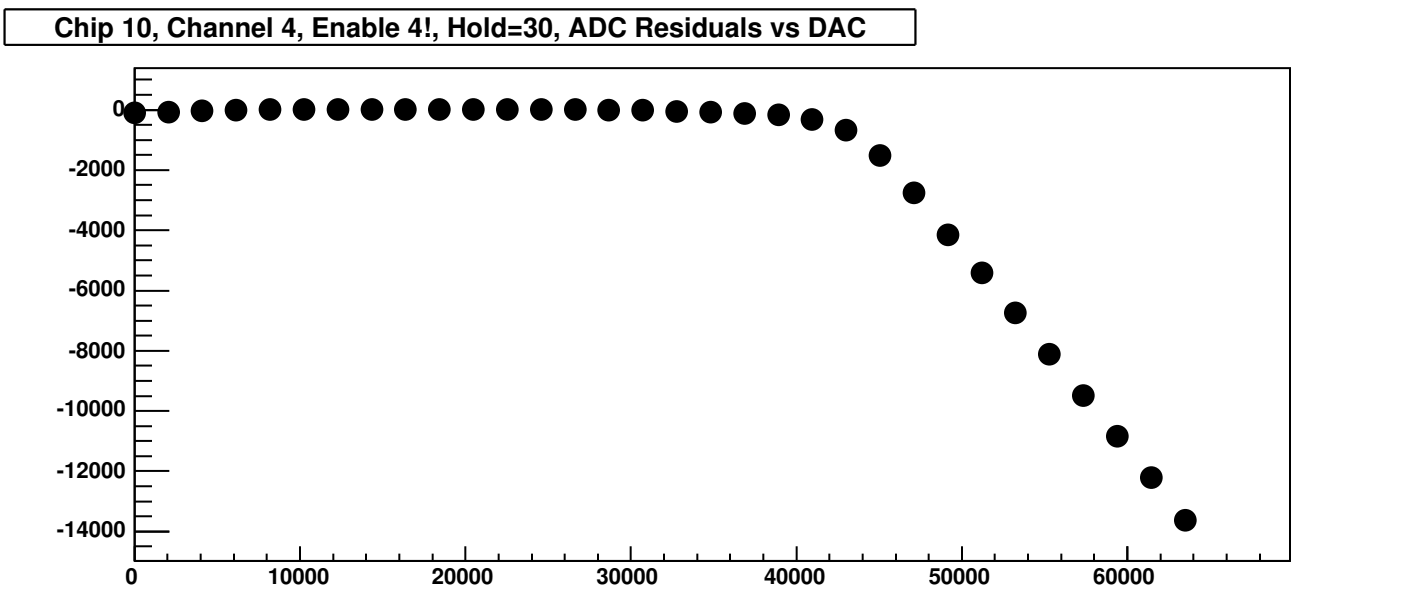

Chip 10, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2873 / 15
p0 1493 ± 0.1073
p1 -0.0003284 ± 4.309e-06
1500
1480
1460
1440
1420
1400
1380
1360
1340
1320
0
10000
20000
30000
40000
50000
60000

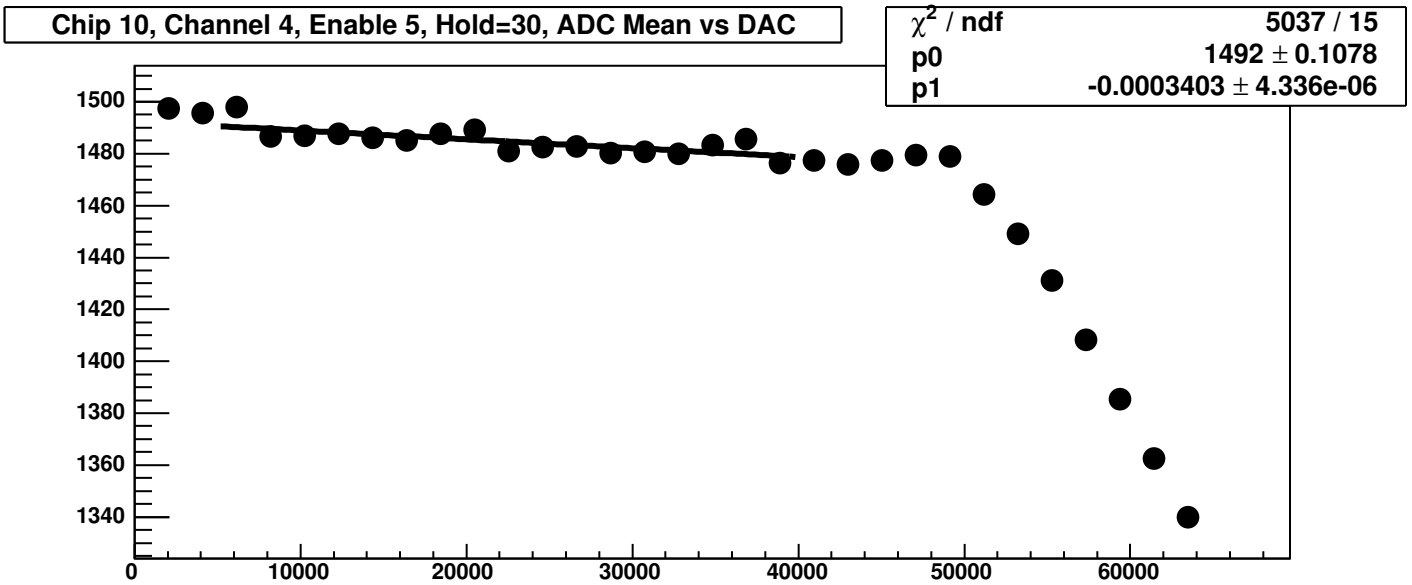

Chip 10, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

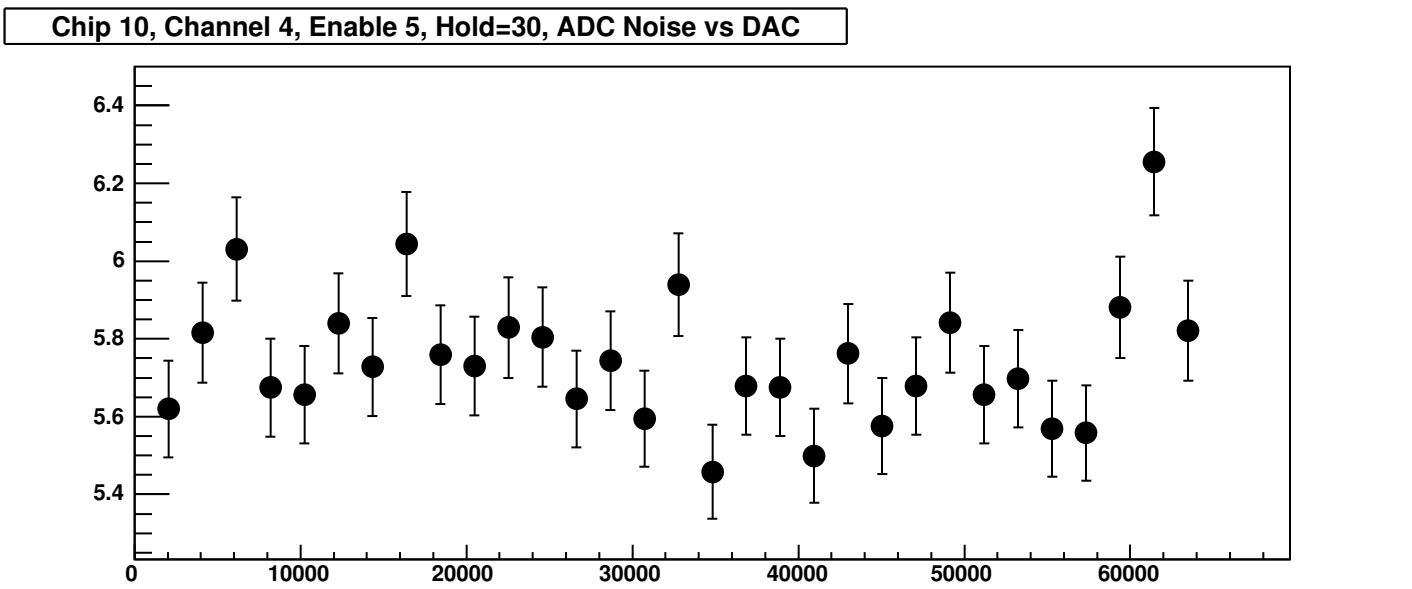

Chip 10, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

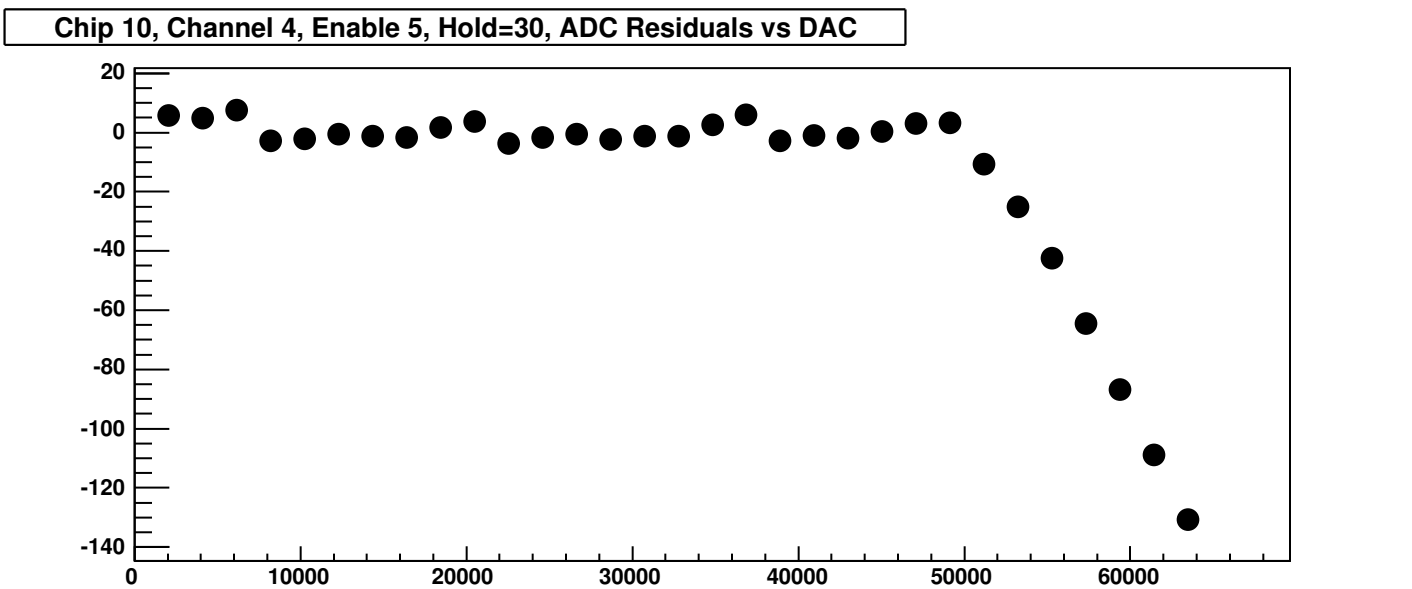

Chip 10, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 8627 / 15
p0 1609 ± 0.2808
p1 0.6643 ± 2.925e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

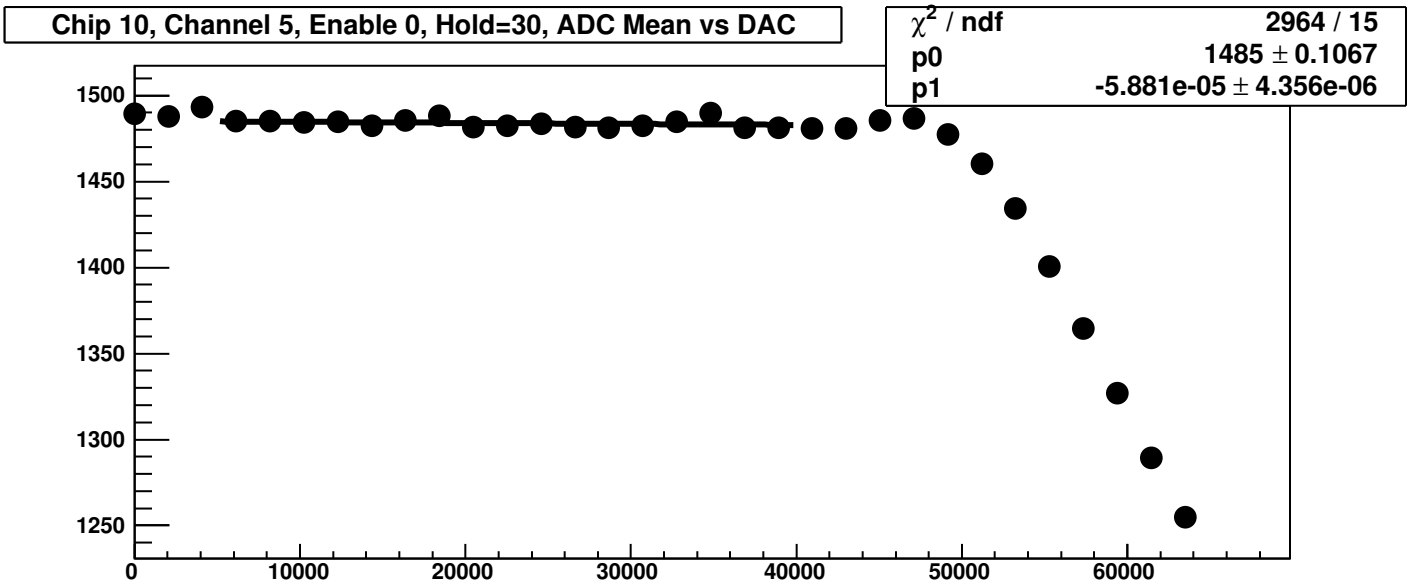

Chip 10, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

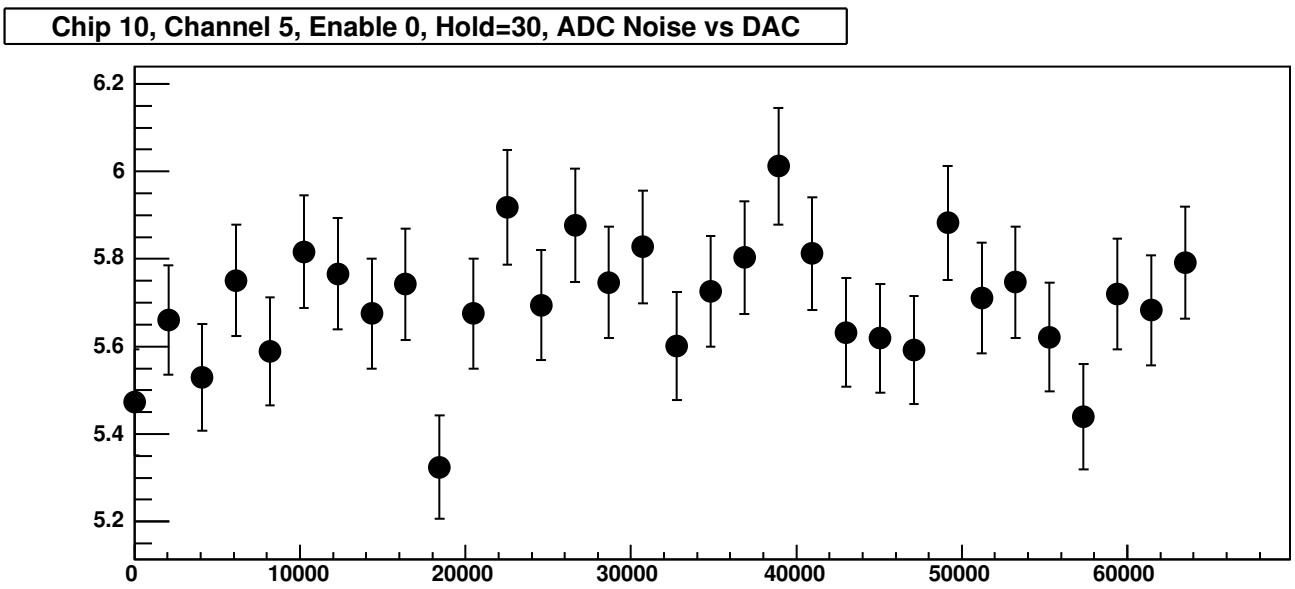

Chip 10, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

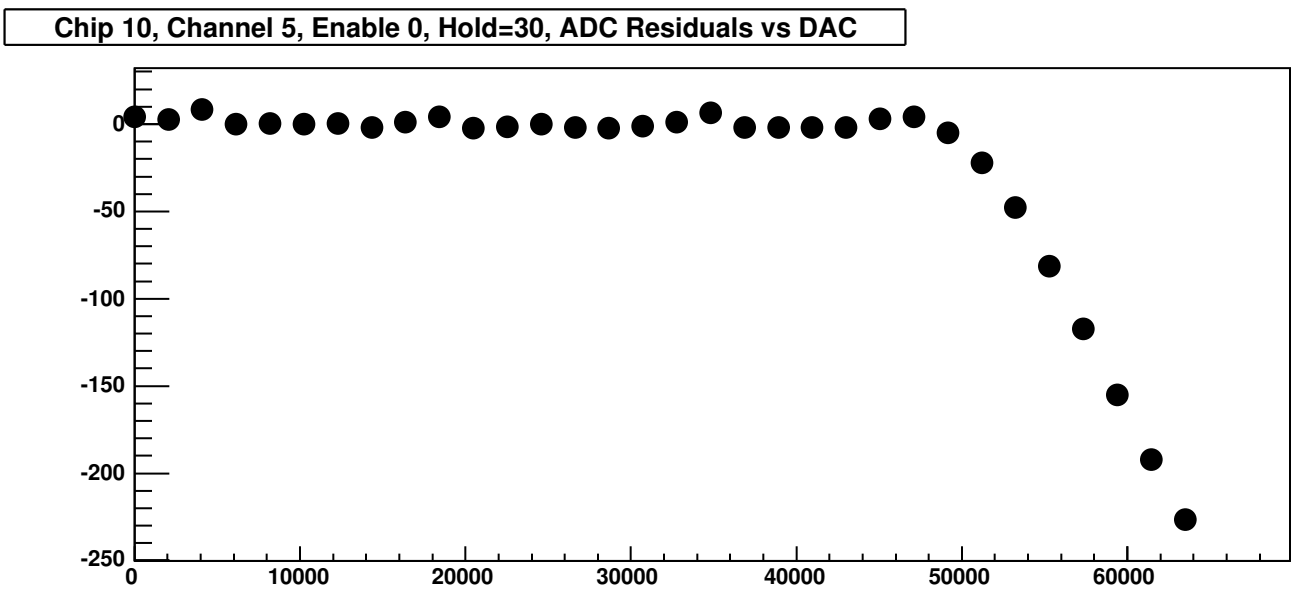

Chip 10, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5037 / 15
p0 1492 ± 0.1078
p1 -0.0003403 ± 4.336e-06
1500
1480
1460
1440
1420
1400
1380
1360
1340
0
10000
20000
30000
40000
50000
60000

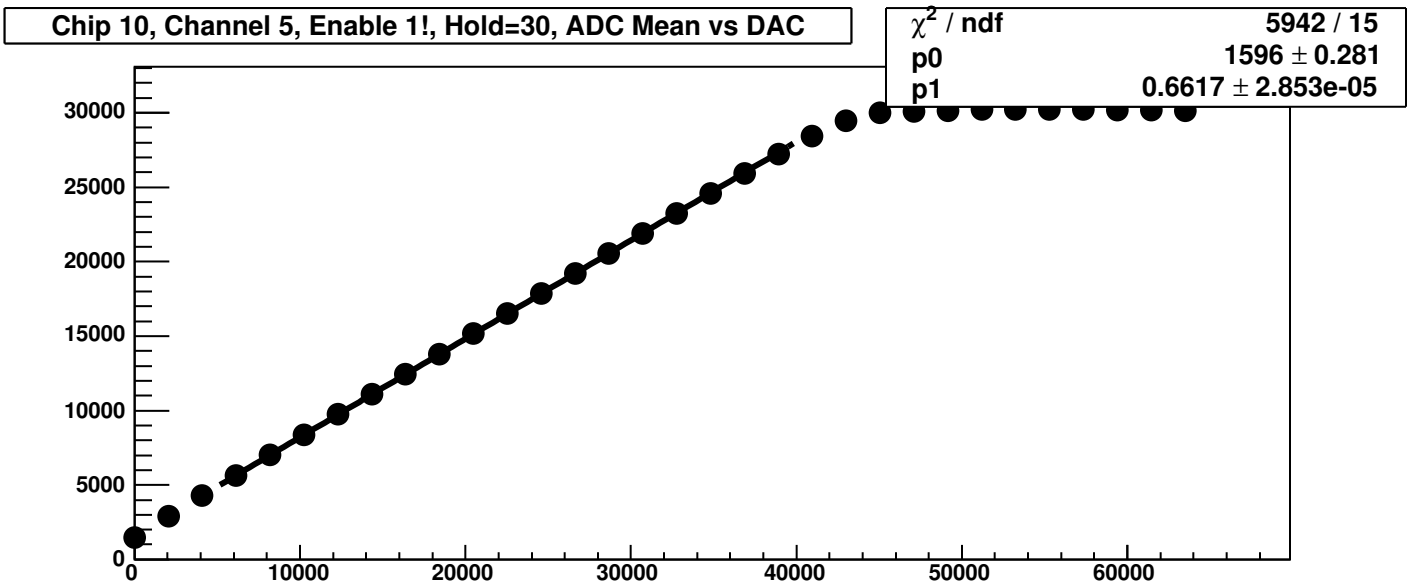

Chip 10, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

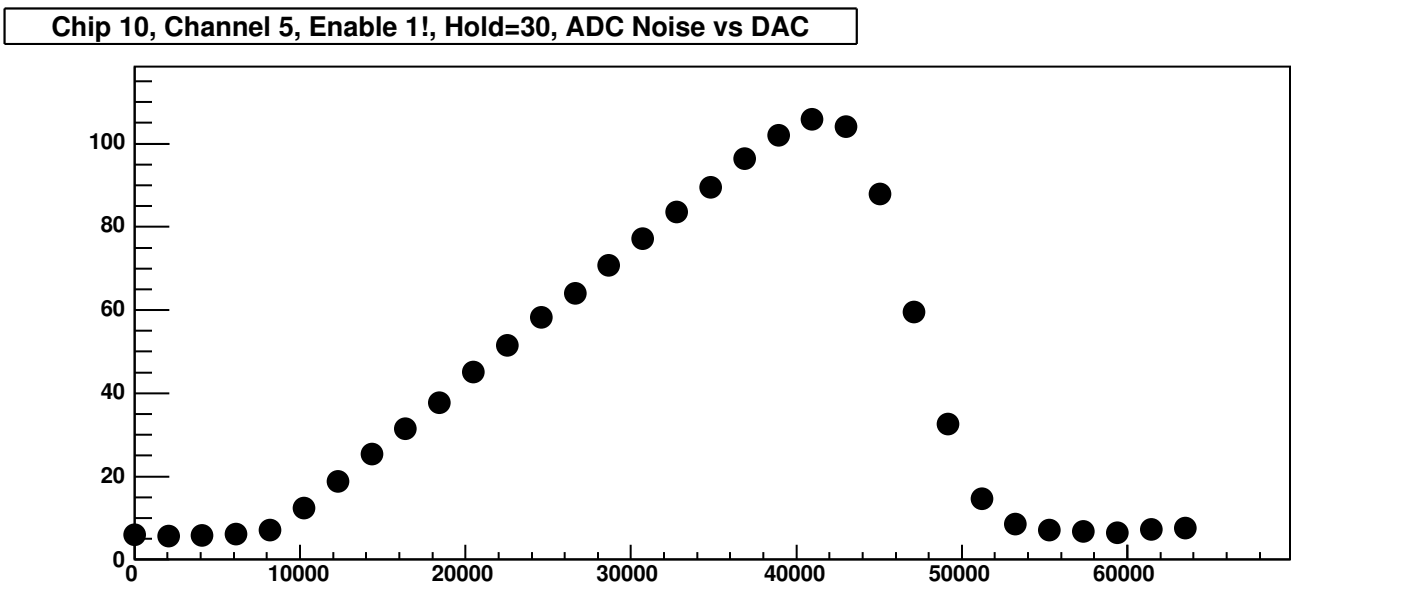

Chip 10, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

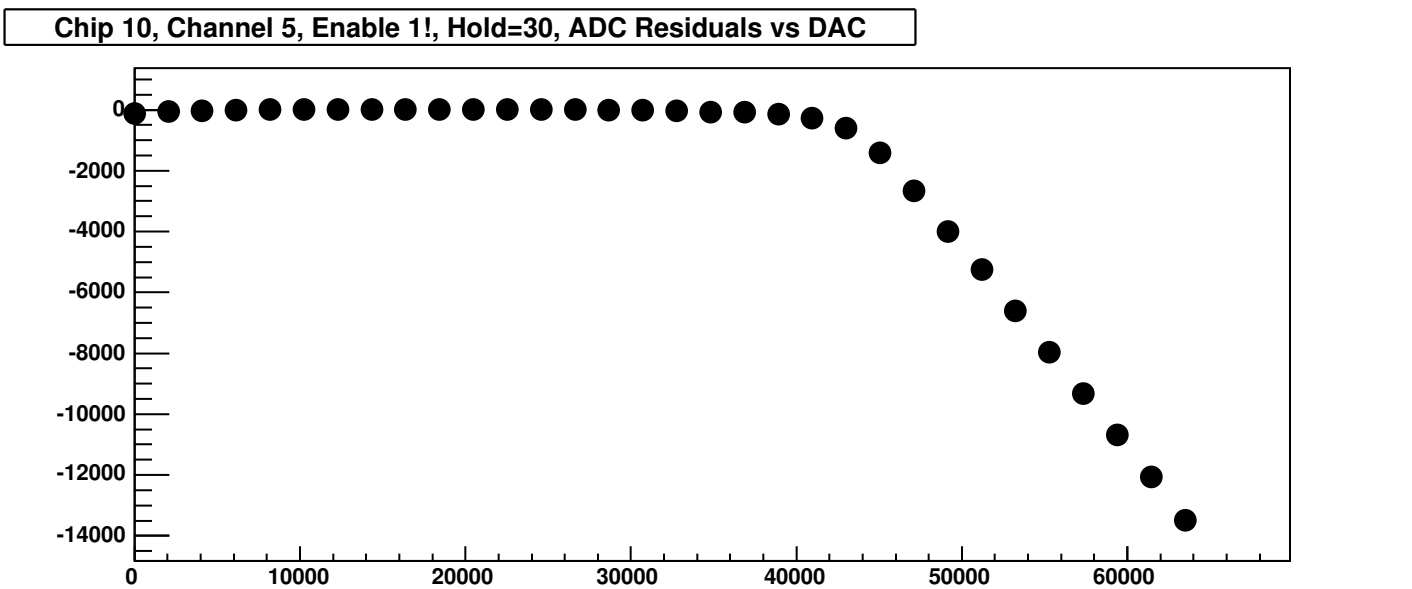

Chip 10, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2964 / 15
p0 1485 ± 0.1067
p1 -5.881e-05 ± 4.356e-06
1500
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

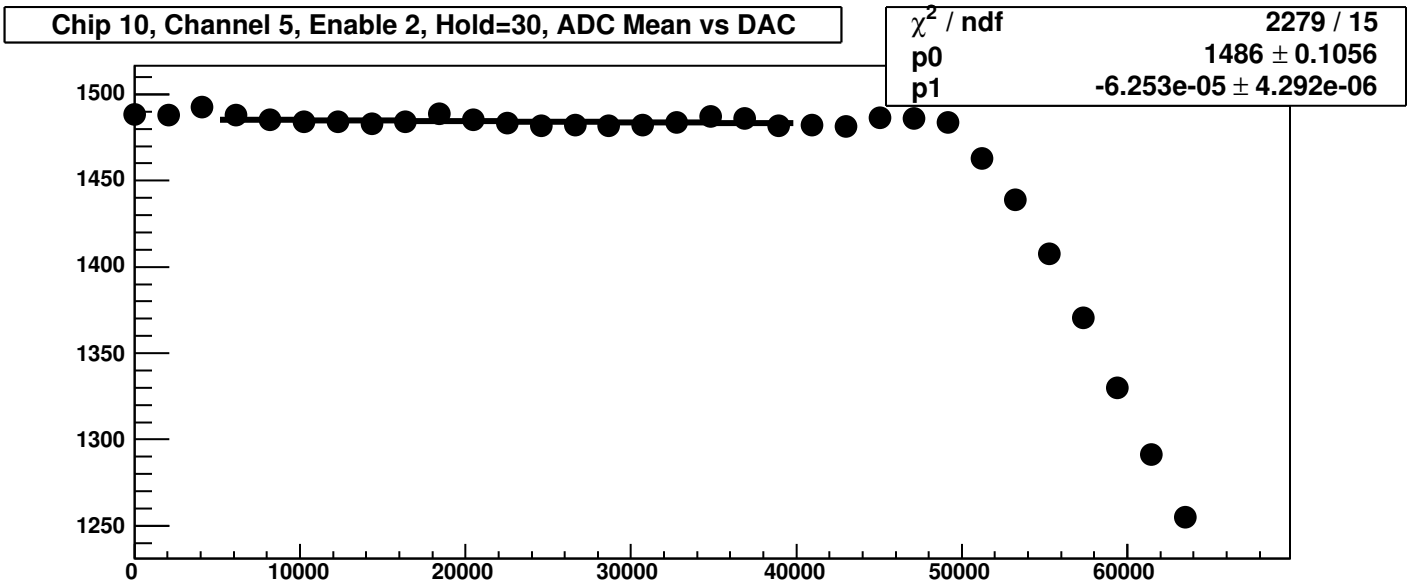

Chip 10, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

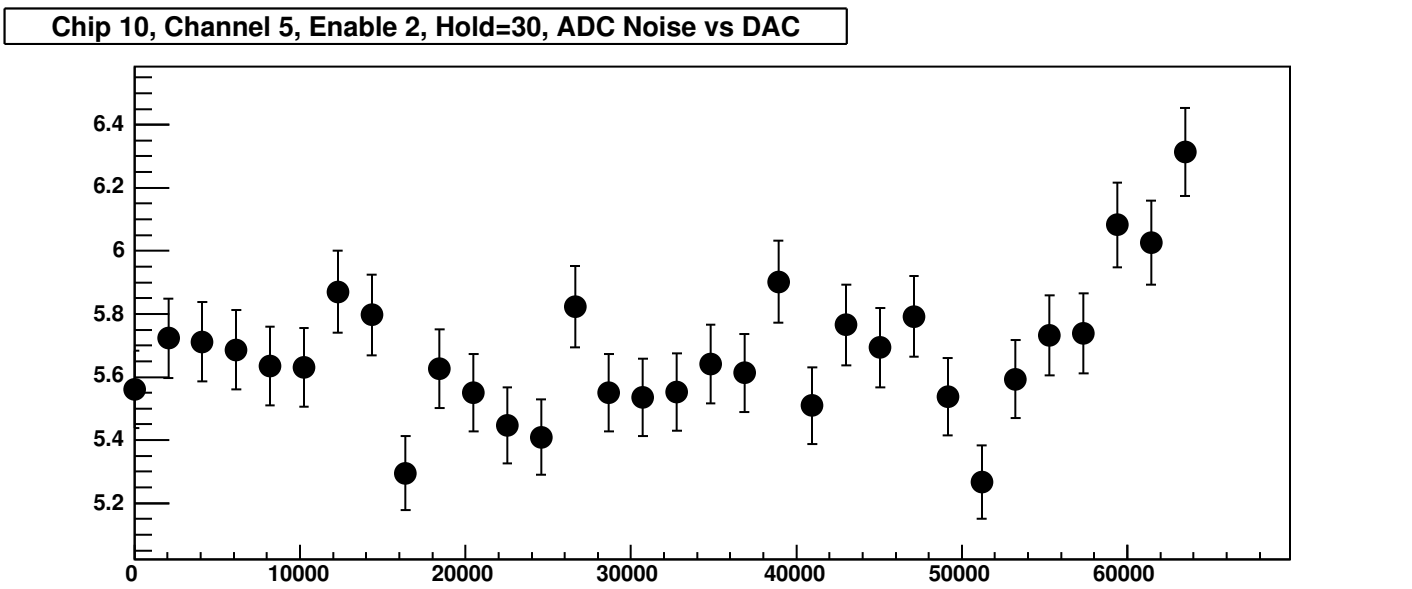

Chip 10, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

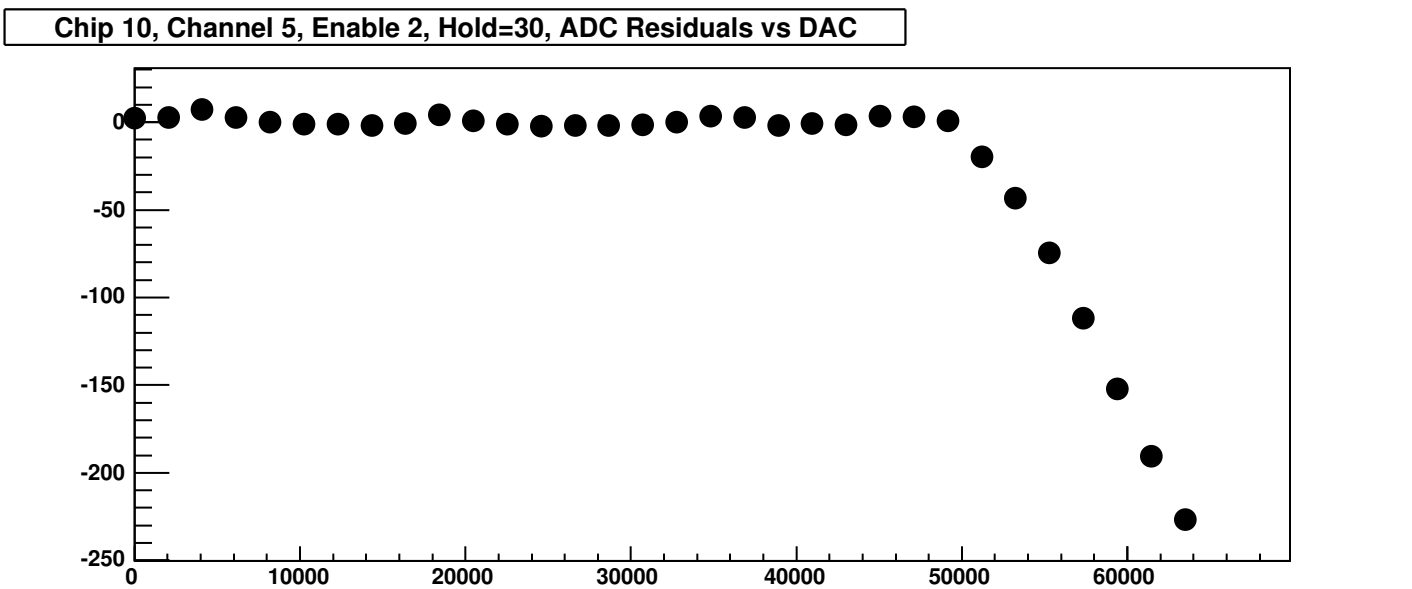

Chip 10, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 5942 / 15
p0 1596 ± 0.281
p1 0.6617 ± 2.853e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

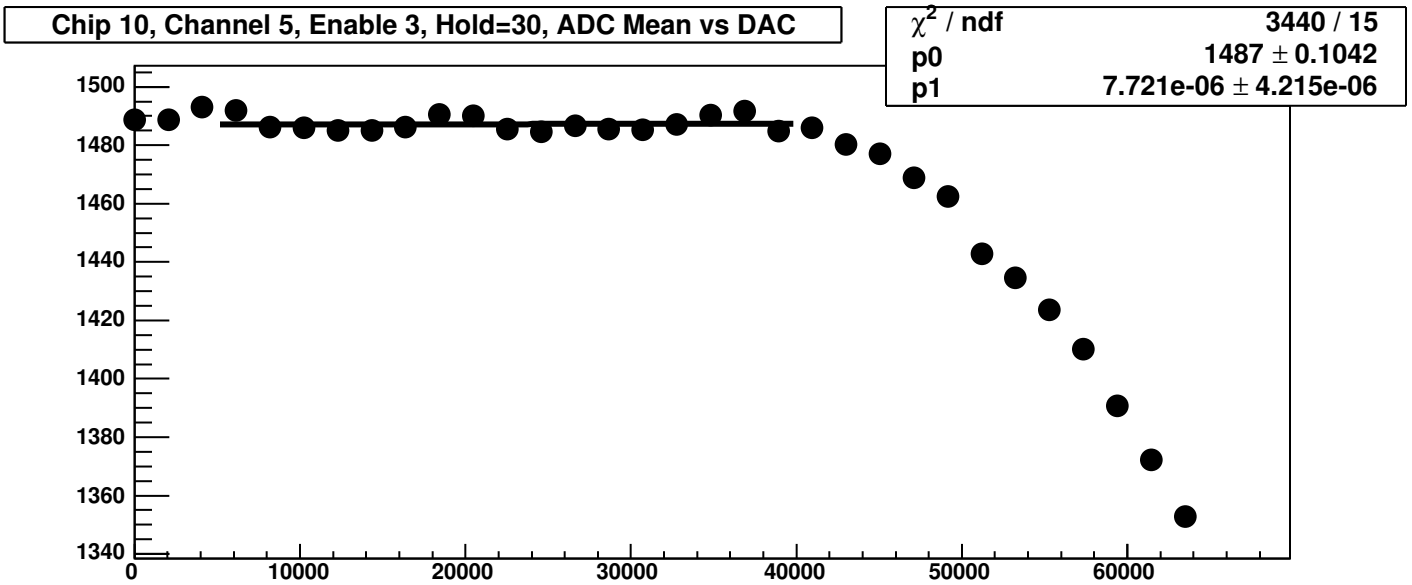

Chip 10, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

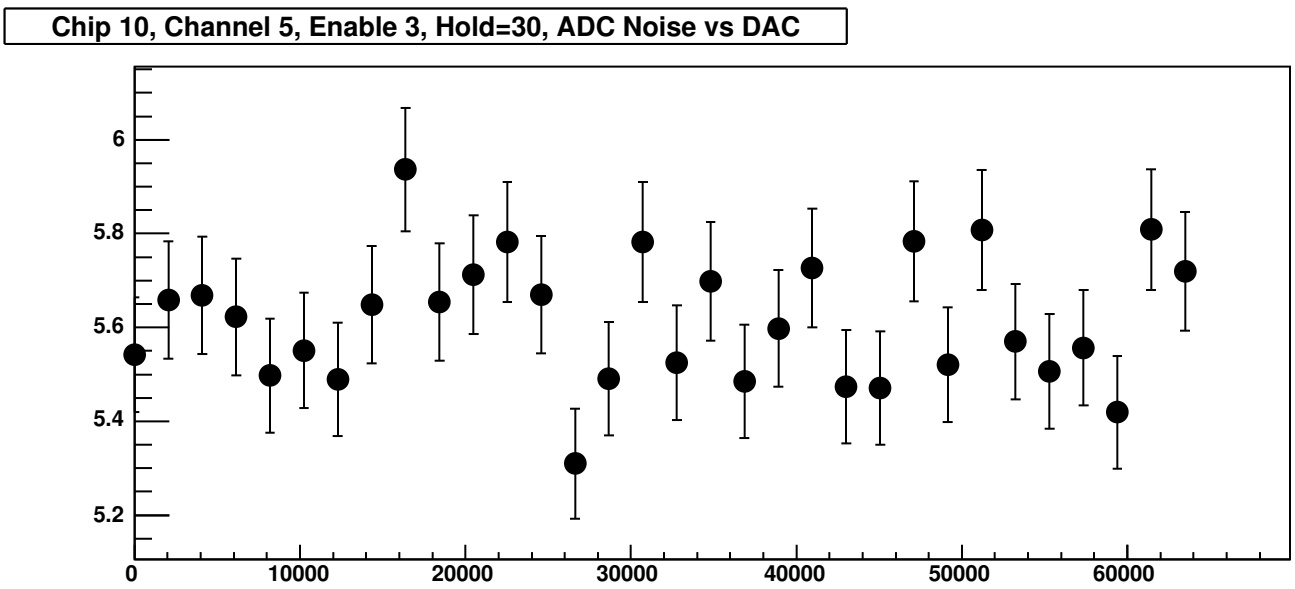

Chip 10, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

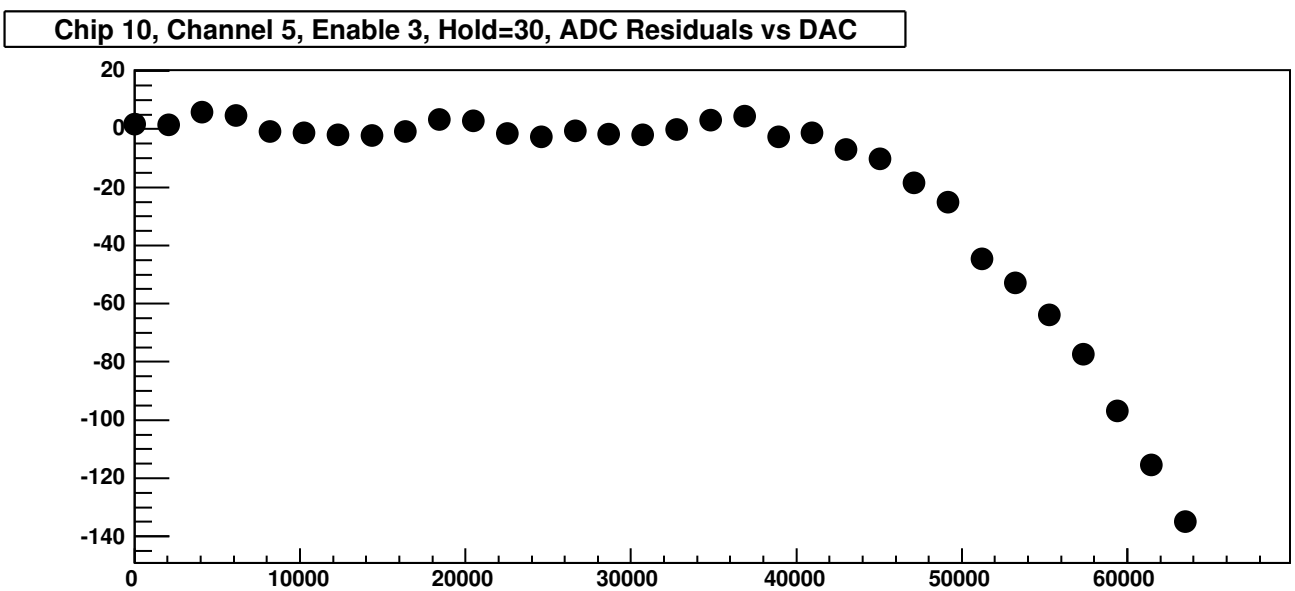

Chip 10, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2279 / 15
p0 1486 ± 0.1056
p1 -6.253e-05 ± 4.292e-06
1500
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

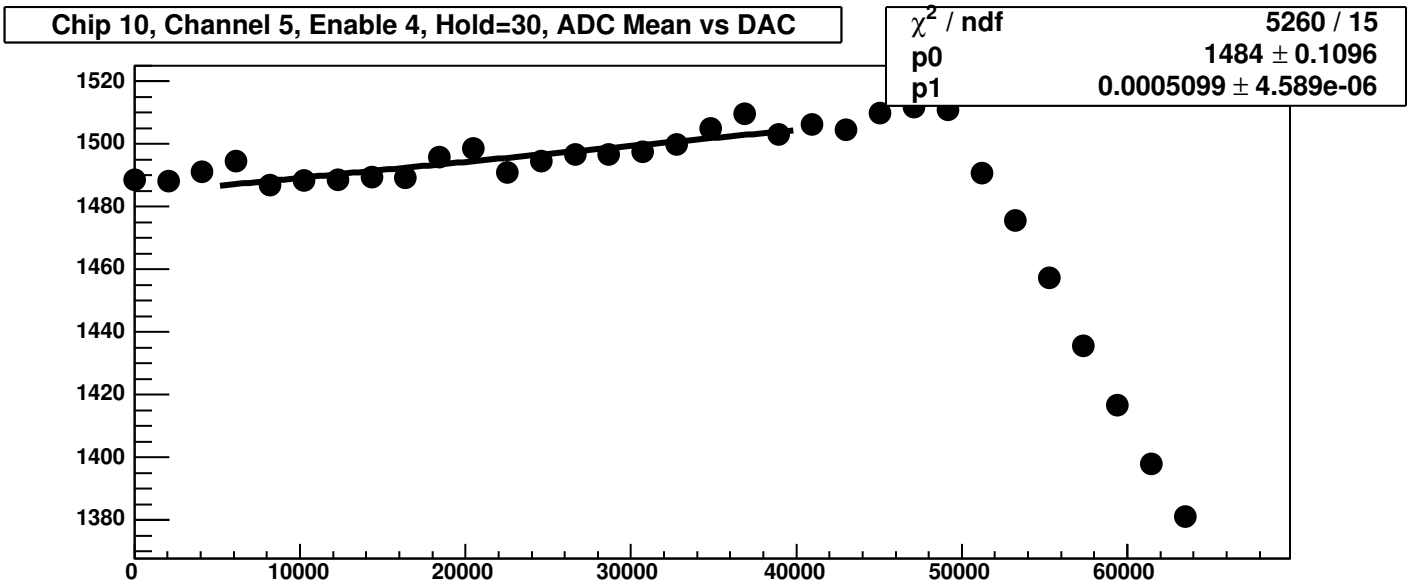

Chip 10, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

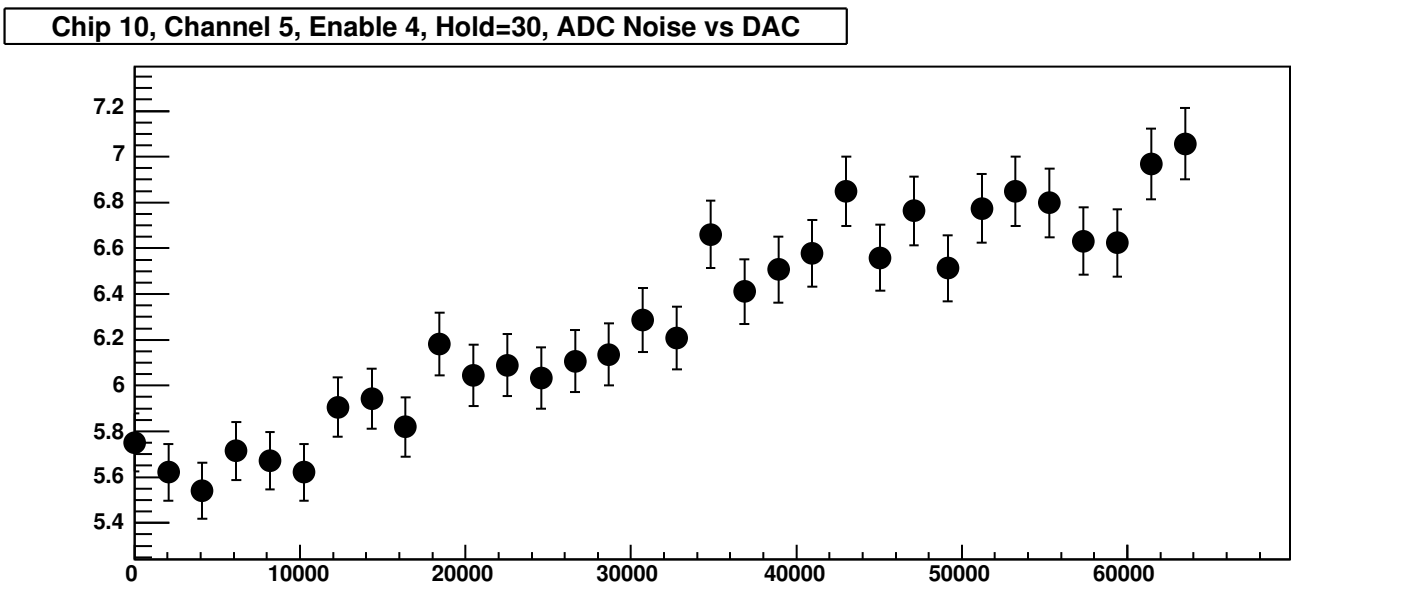

Chip 10, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

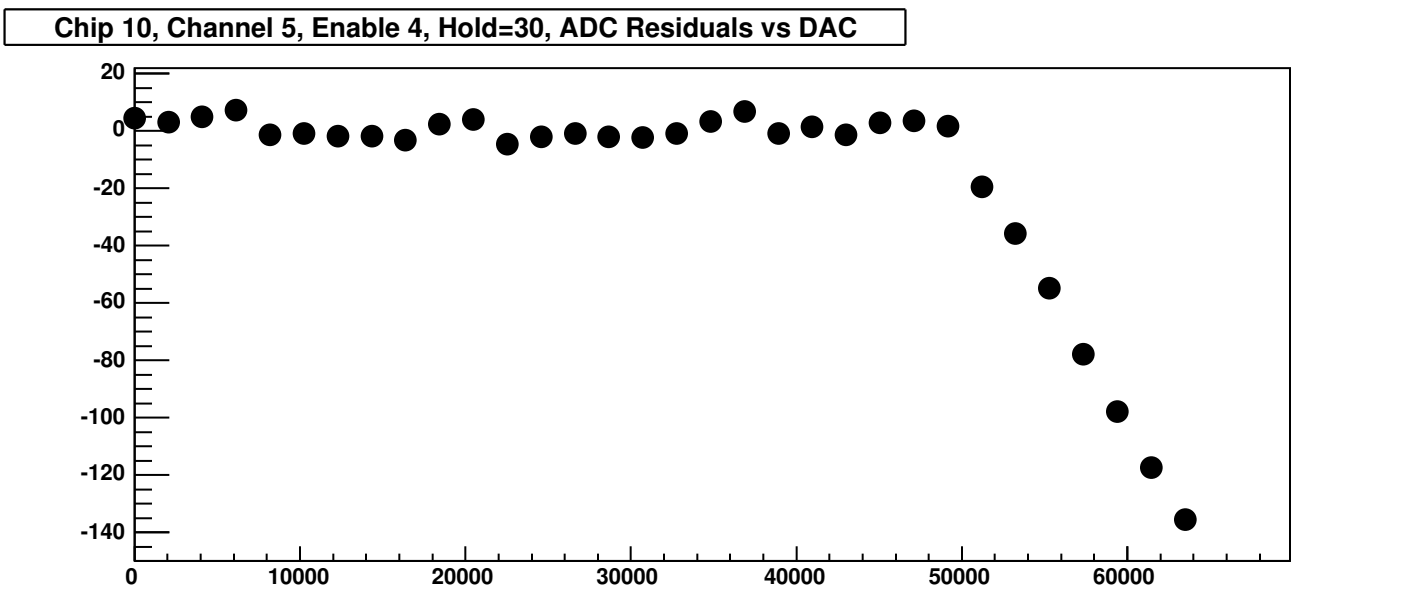

Chip 10, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3440 / 15
p0 1487 ± 0.1042
p1 7.721e-06 ± 4.215e-06
1500
1480
1460
1440
1420
1400
1380
1360
1340
0
10000
20000
30000
40000
50000
60000

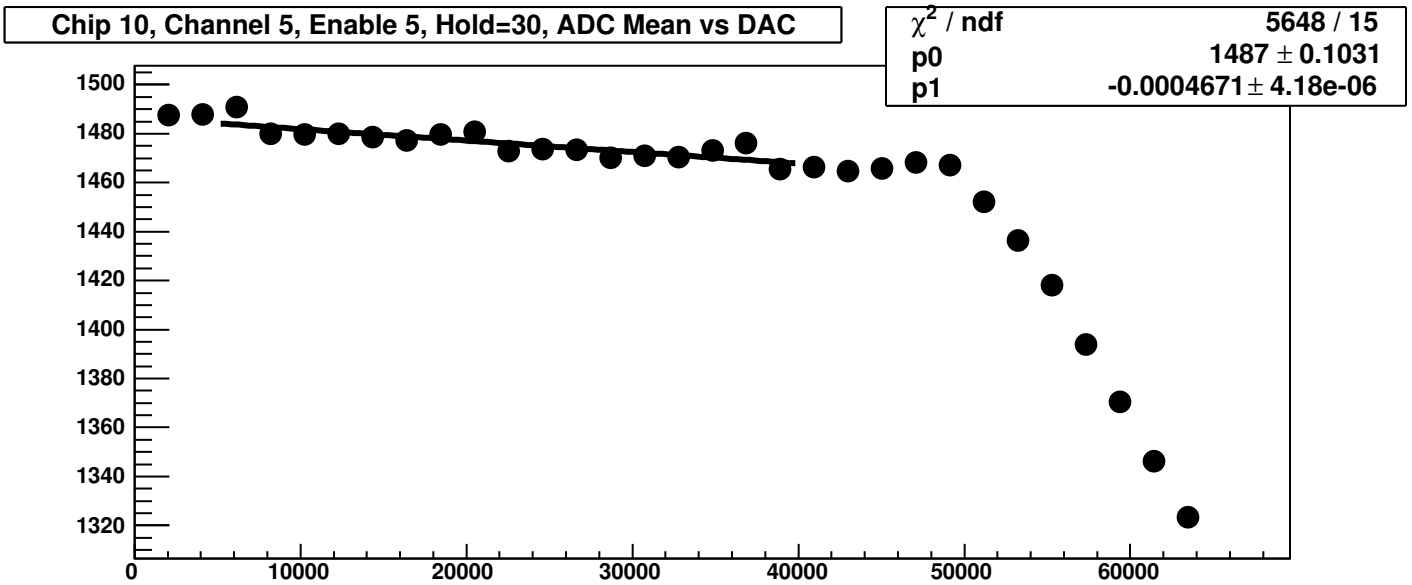

Chip 10, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

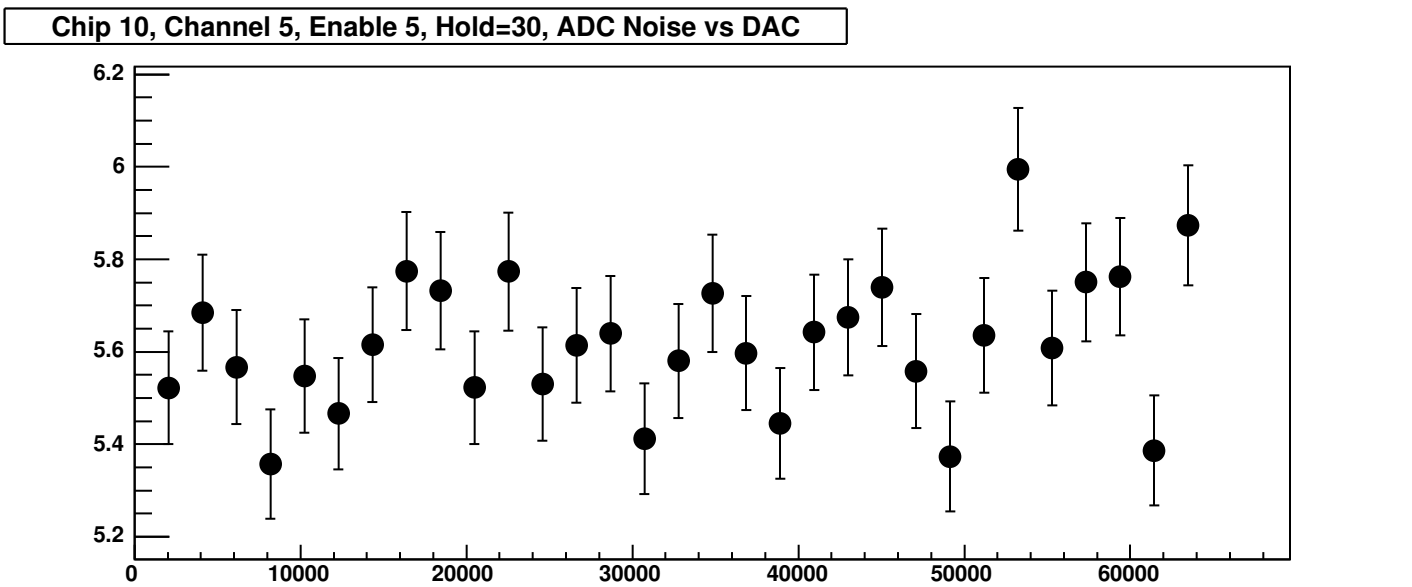

Chip 10, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

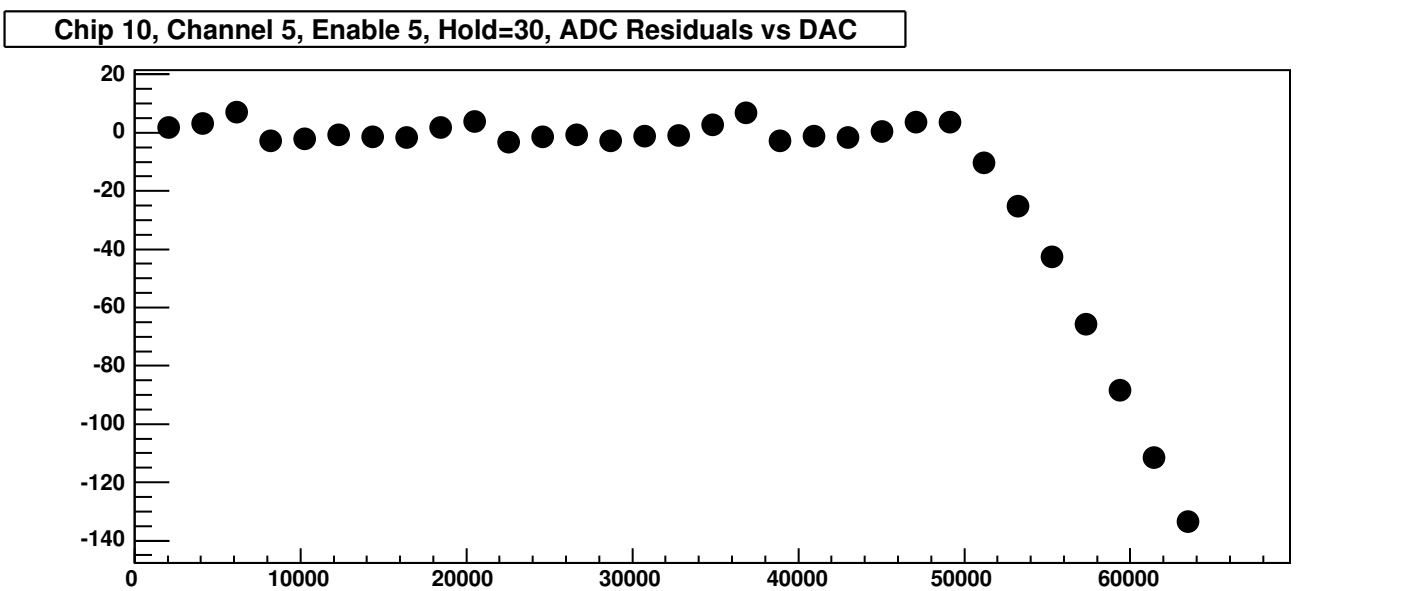

Chip 10, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5260 / 15
p0 1484 ± 0.1096
p1 0.0005099 ± 4.589e-06
1520
1500
1480
1460
1440
1420
1400
1380
0
10000
20000
30000
40000
50000
60000

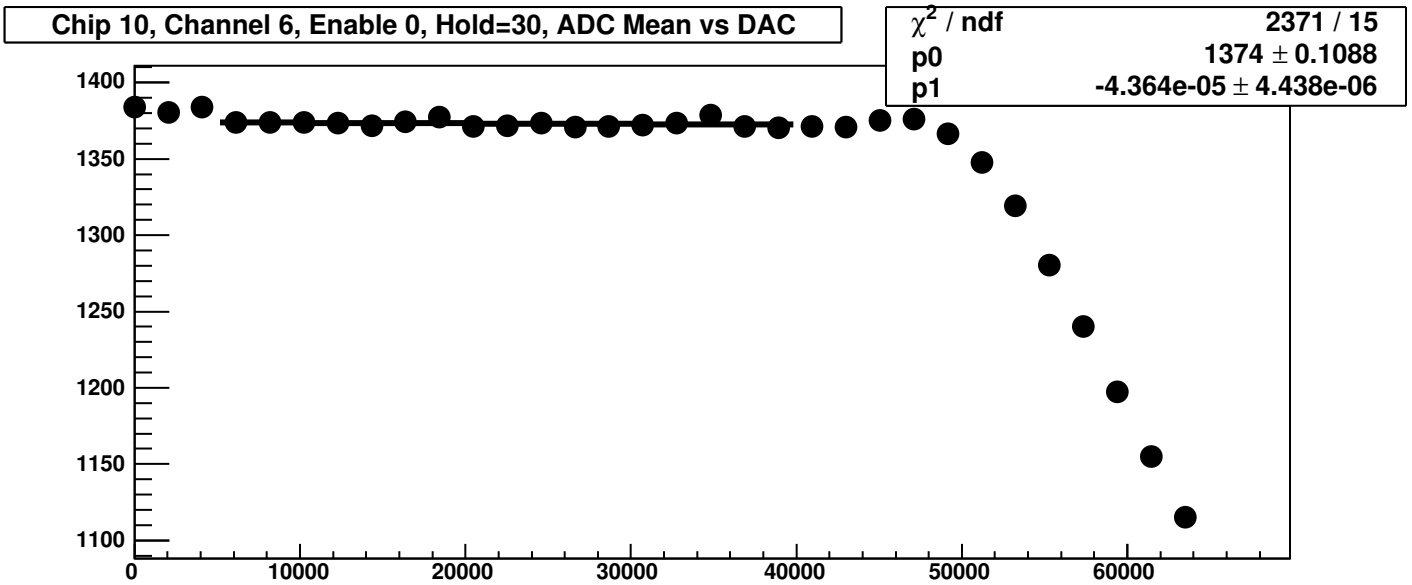

Chip 10, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

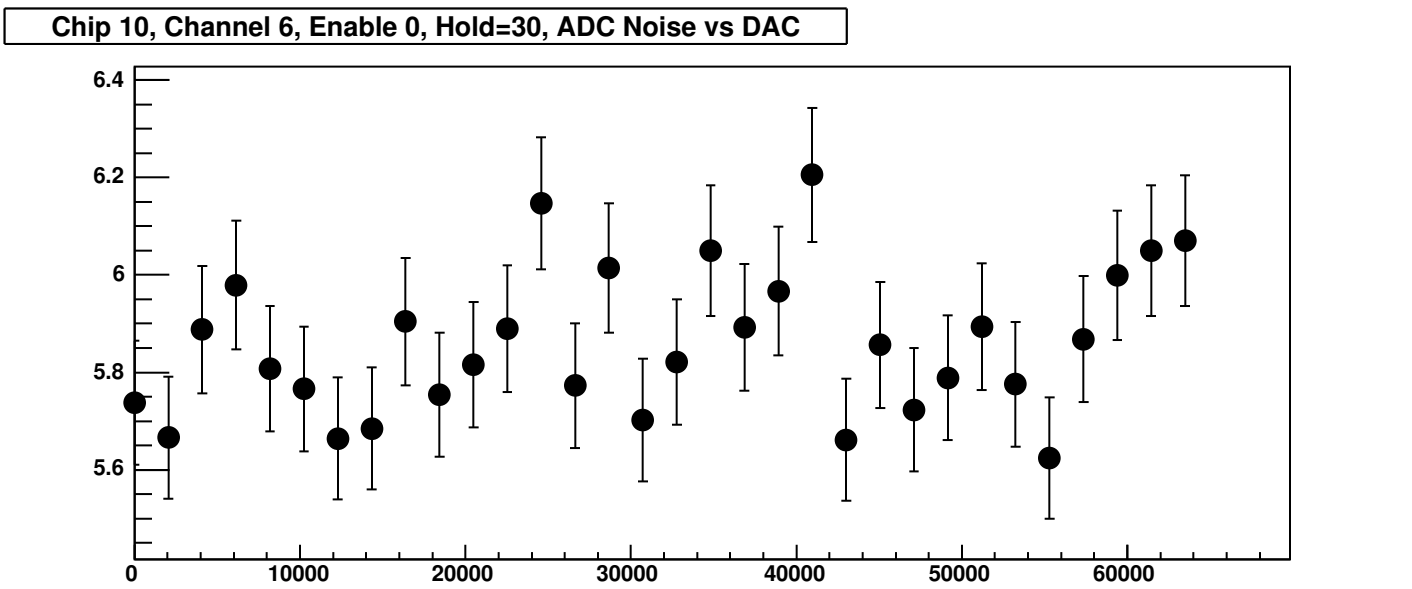

Chip 10, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

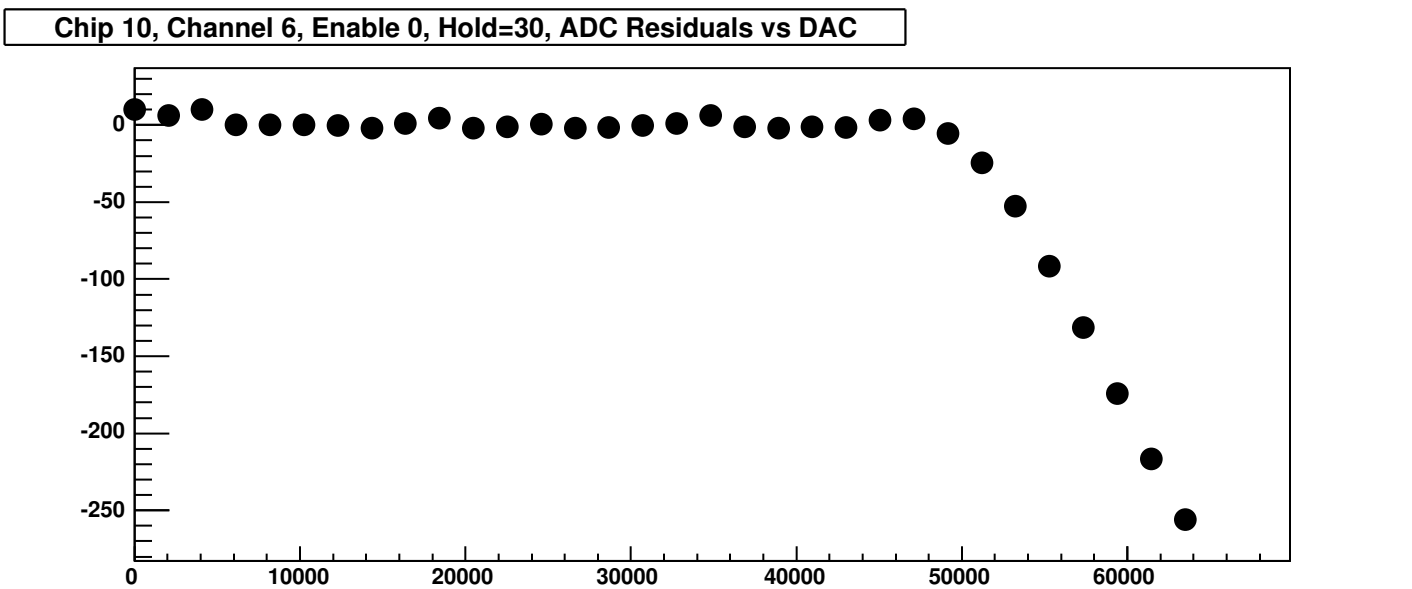

Chip 10, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5648 / 15
p0 1487 ± 0.1031
p1 -0.0004671 ± 4.18e-06
1500
1480
1460
1440
1420
1400
1380
1360
1340
1320
0
10000
20000
30000
40000
50000
60000

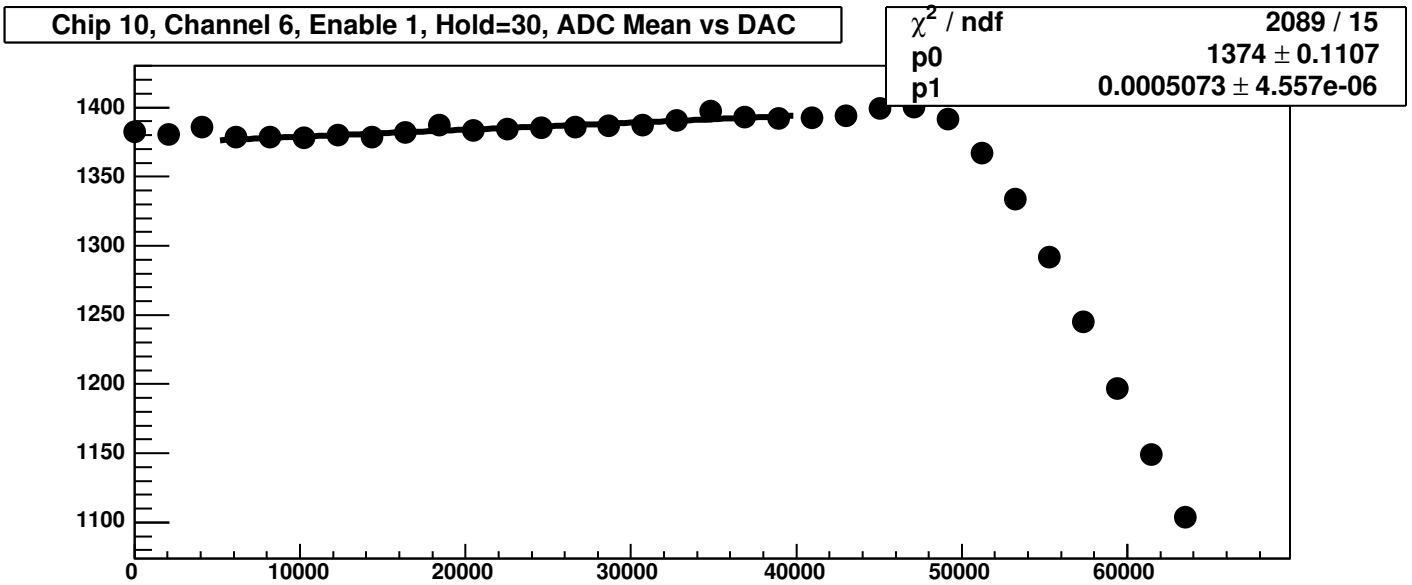

Chip 10, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

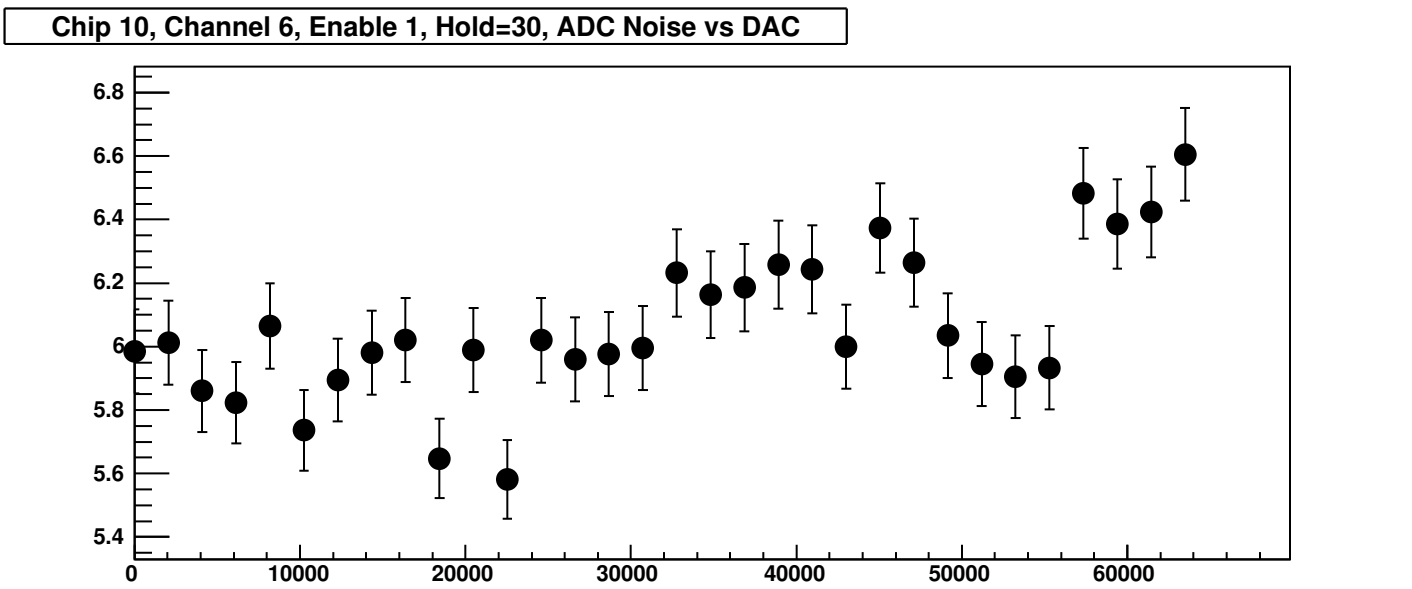

Chip 10, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

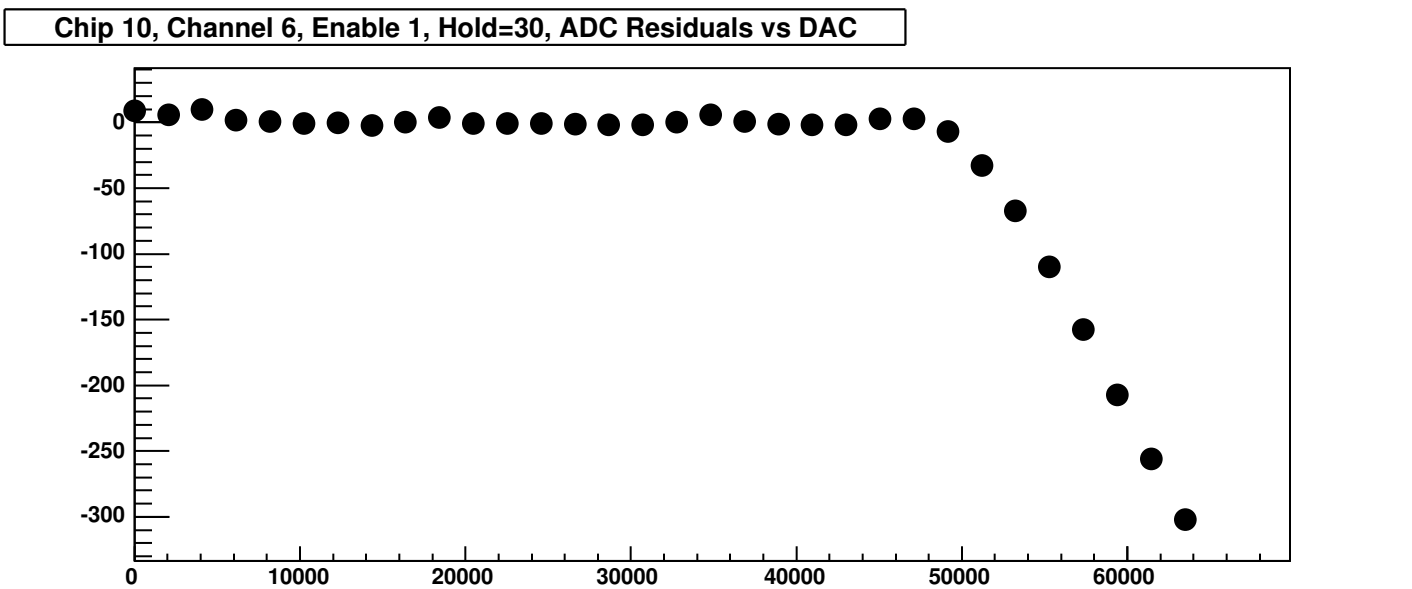

Chip 10, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2371 / 15
p0 1374 ± 0.1088
p1 -4.364e-05 ± 4.438e-06
1400
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

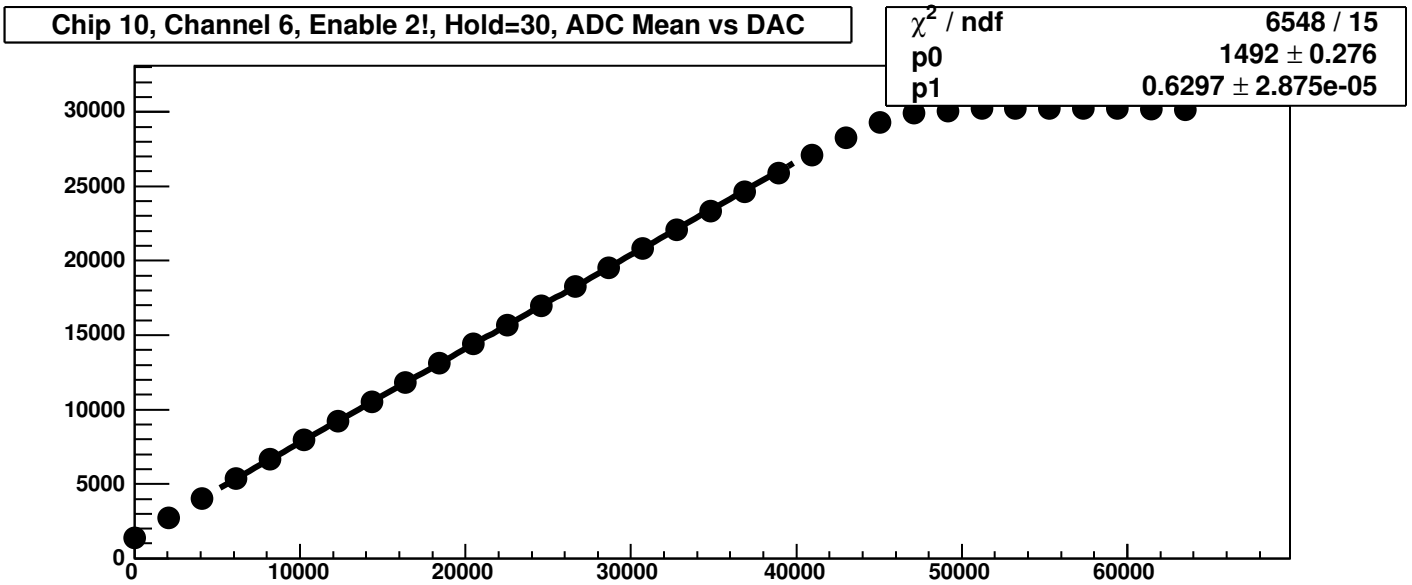

Chip 10, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

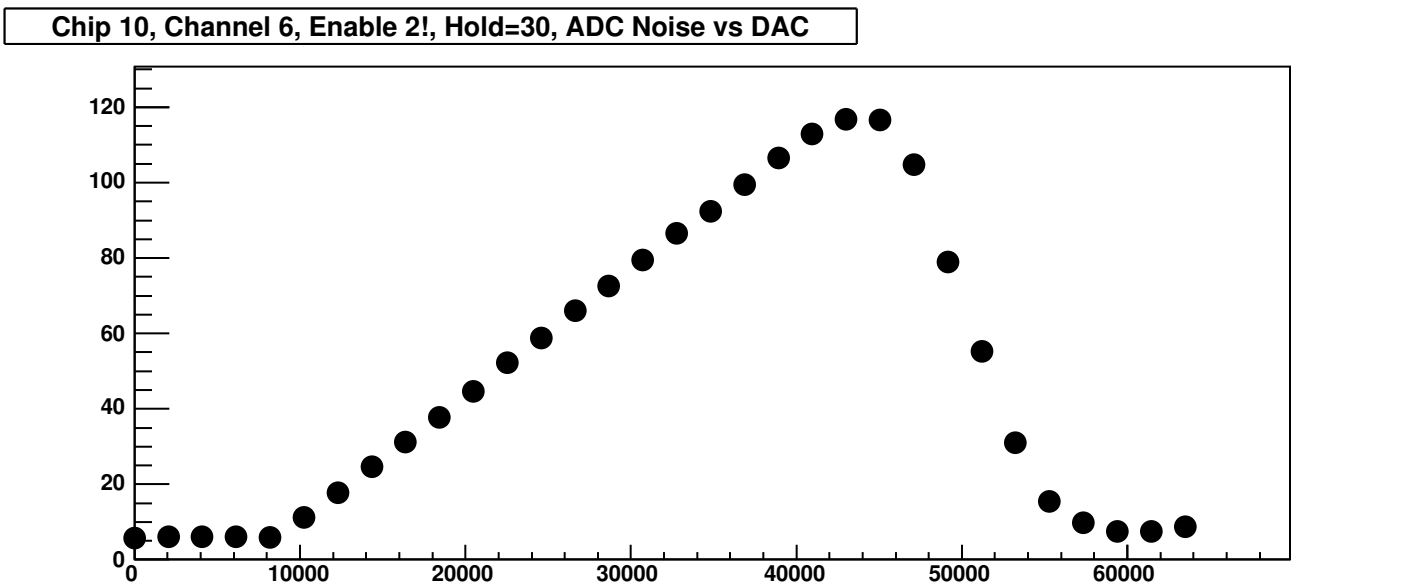

Chip 10, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

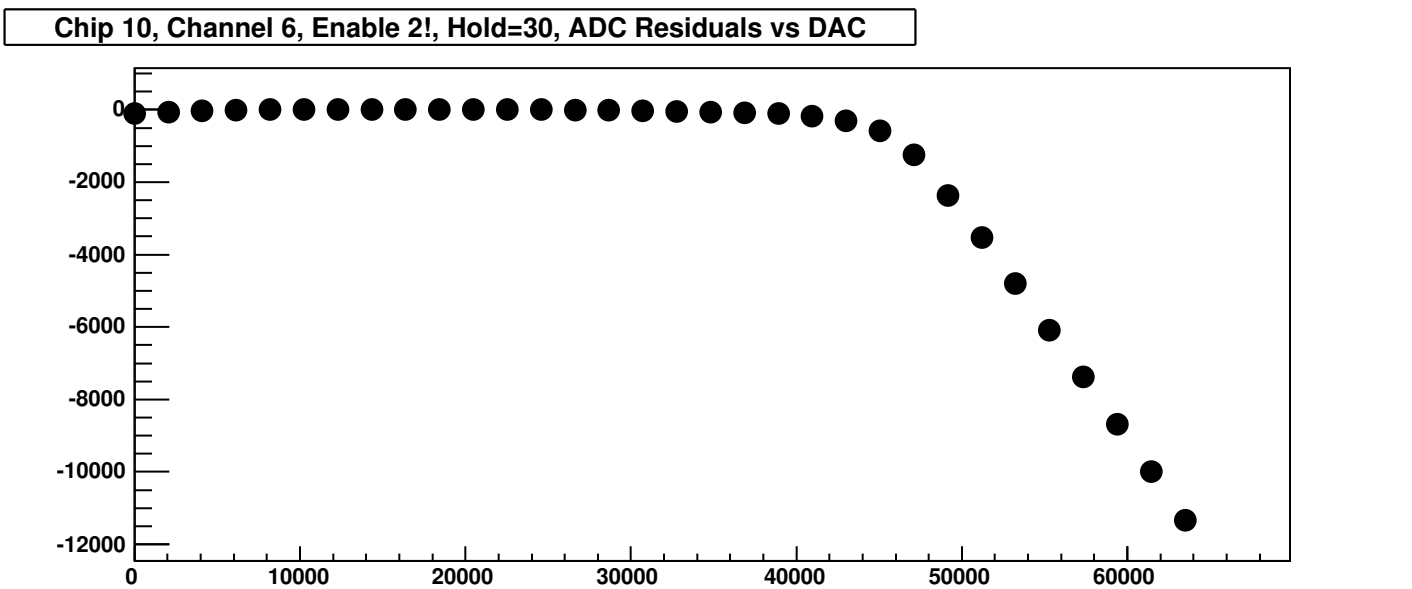

Chip 10, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2089 / 15
p0 1374 ± 0.1107
p1 0.0005073 ± 4.557e-06
1400
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

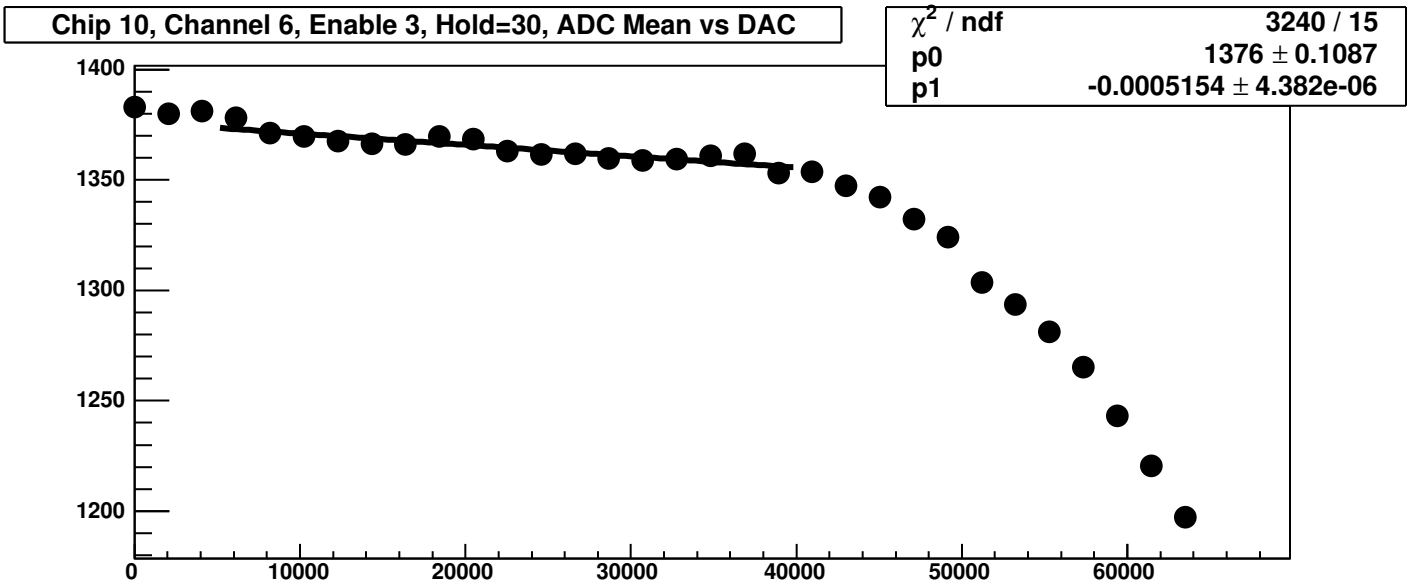

Chip 10, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

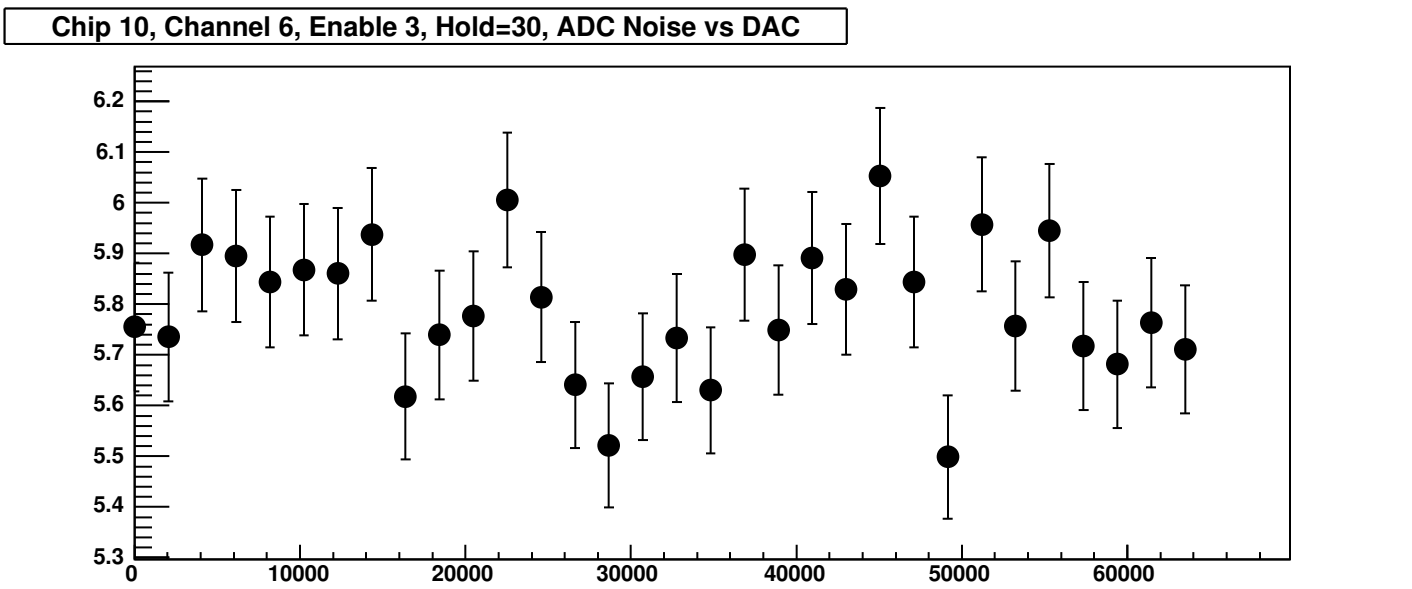

Chip 10, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

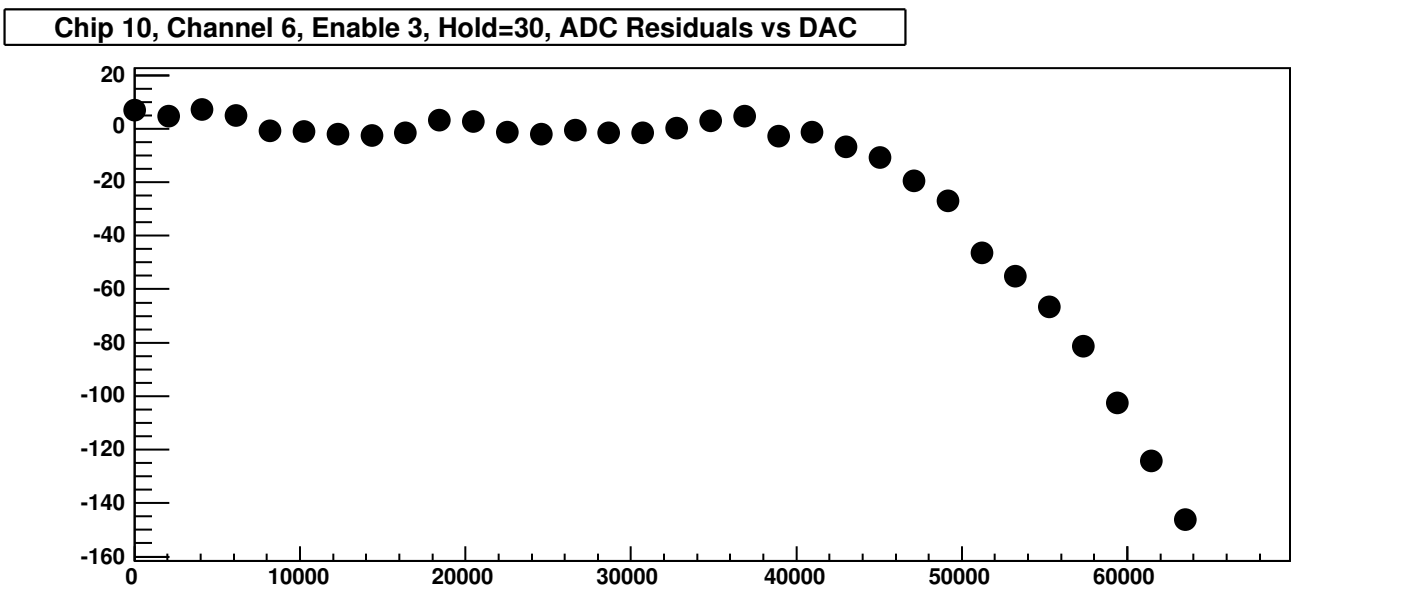

Chip 10, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 6548 / 15
p0 1492 ± 0.276
p1 0.6297 ± 2.875e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

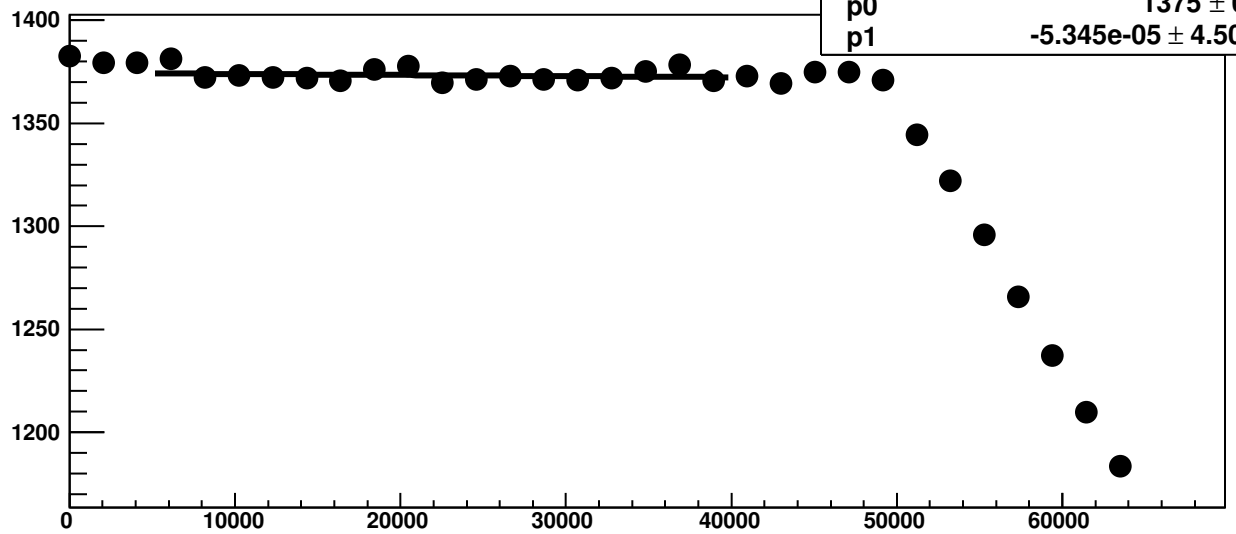

Chip 10, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3240 / 15
p0 1376 ± 0.1087
p1 -0.0005154 ± 4.382e-06
1400
1350
1300
1250
1200
0
10000
20000
30000
40000
50000
60000

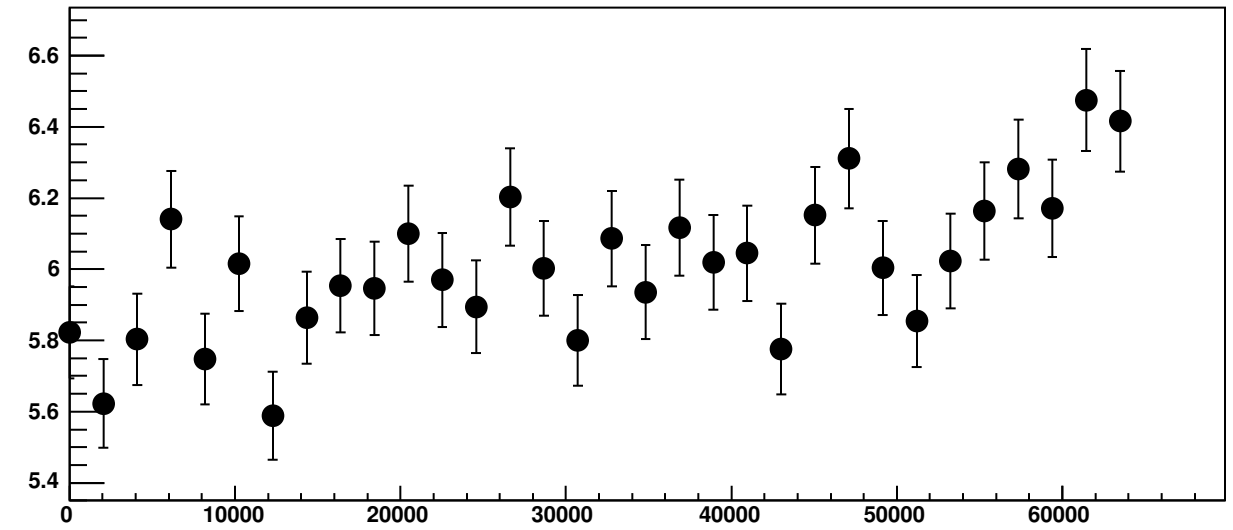

Chip 10, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

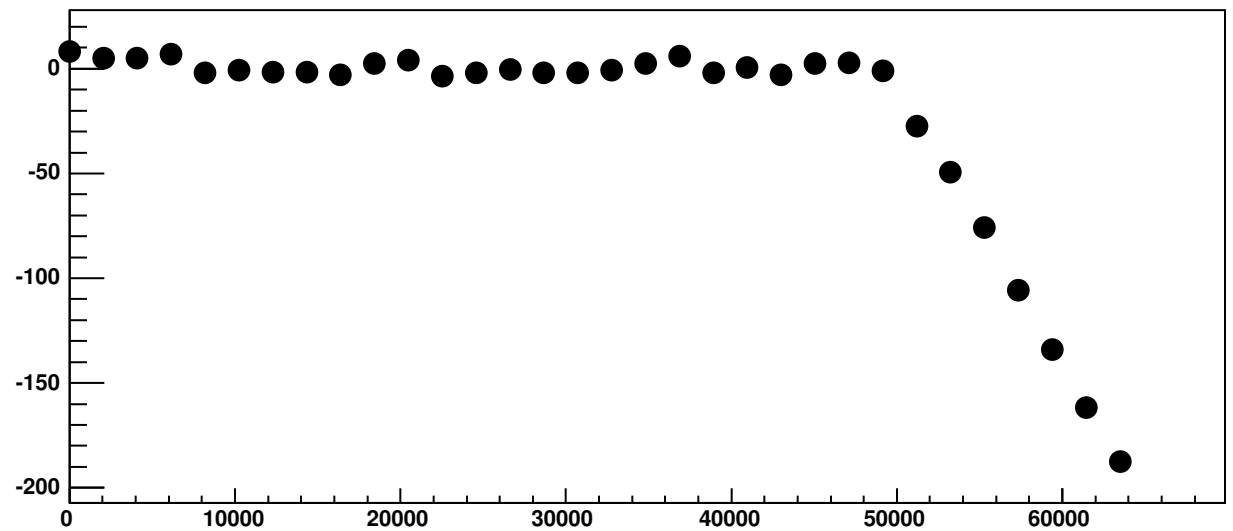
Chip 10, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

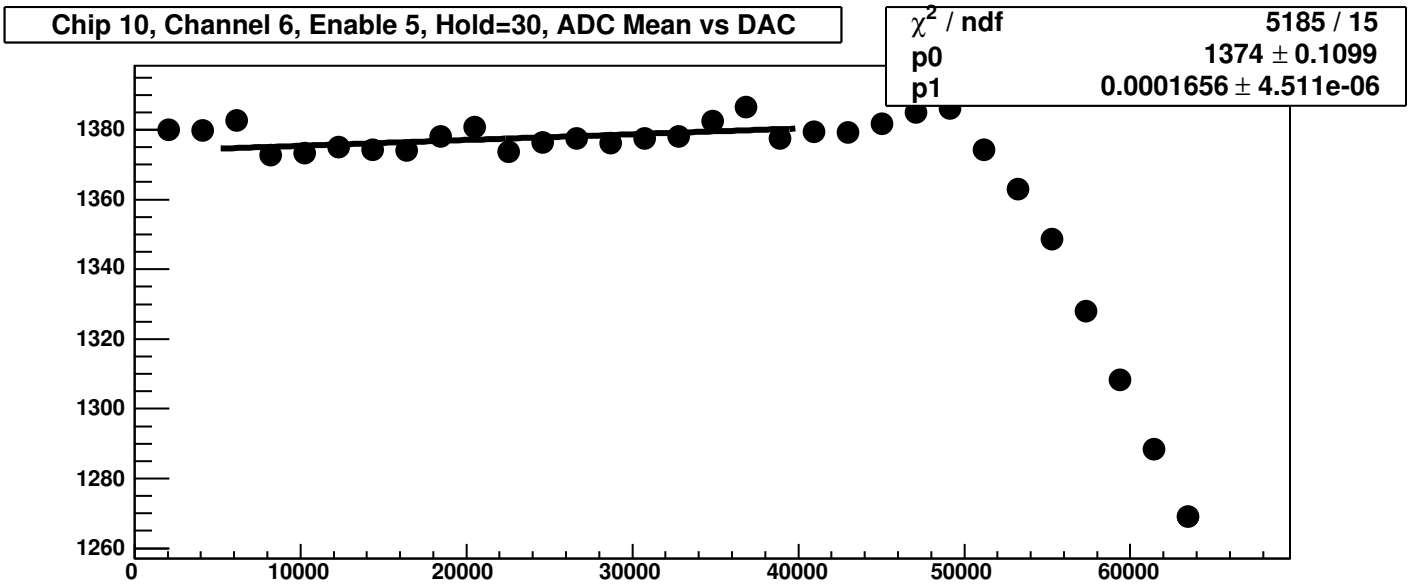


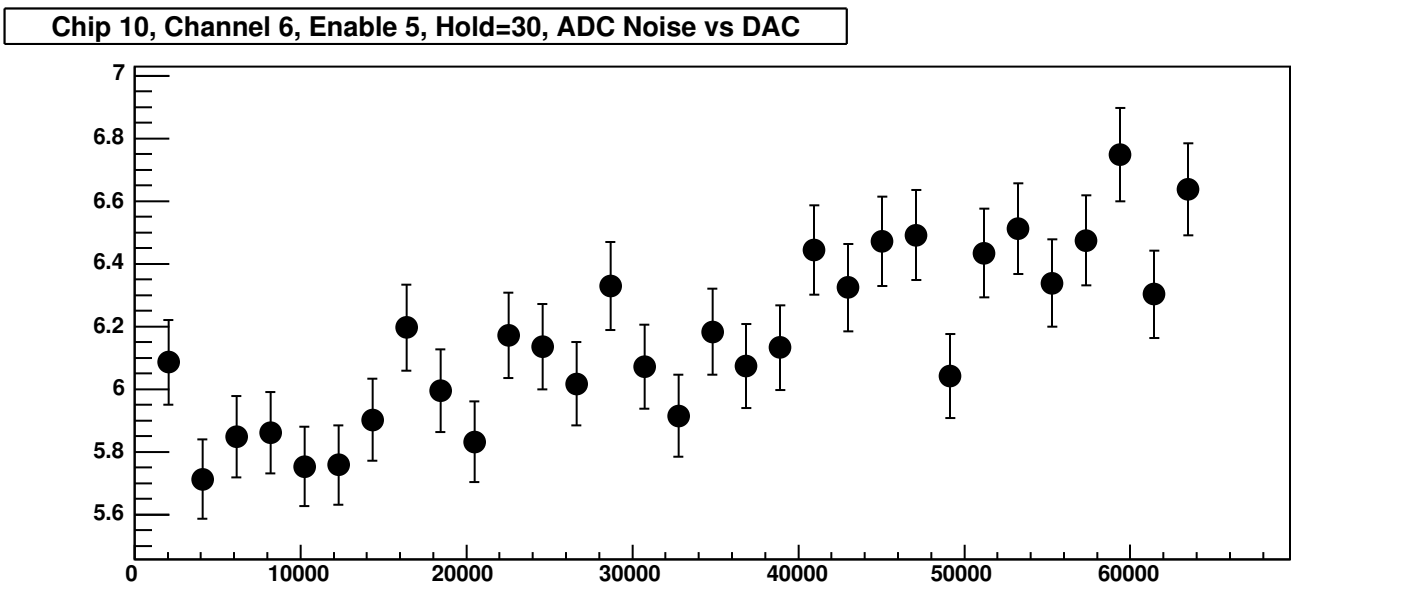
Chip 10, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC

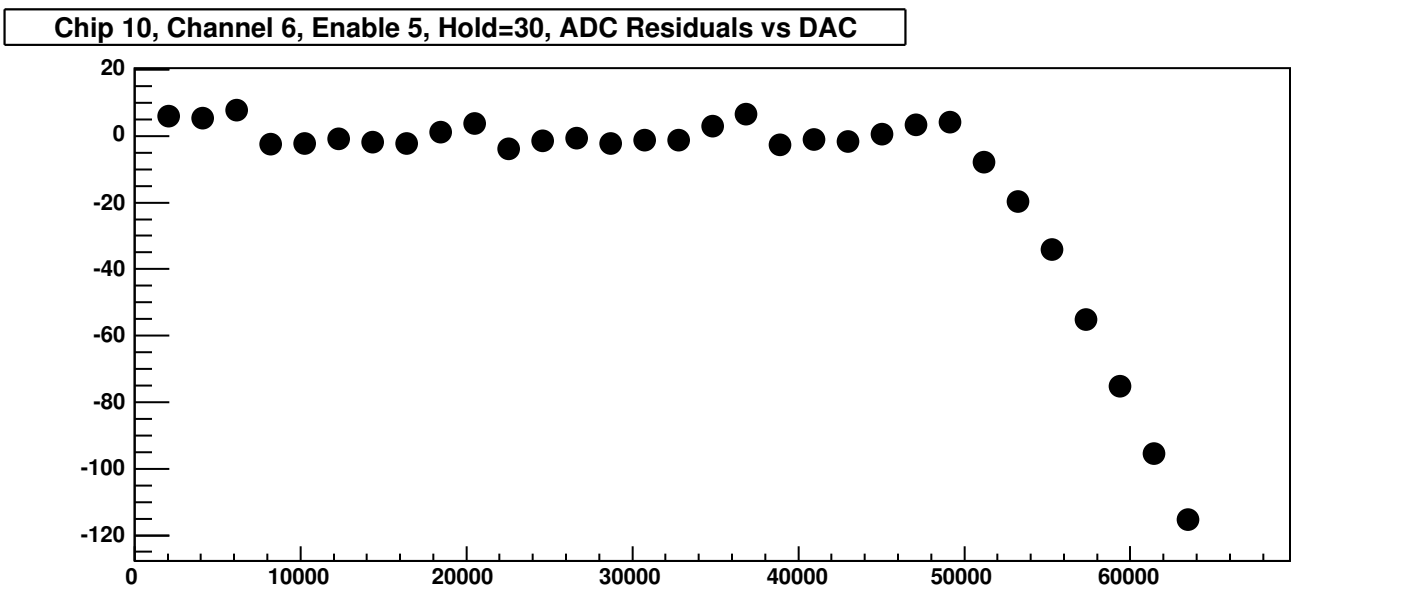


Chip 10, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC



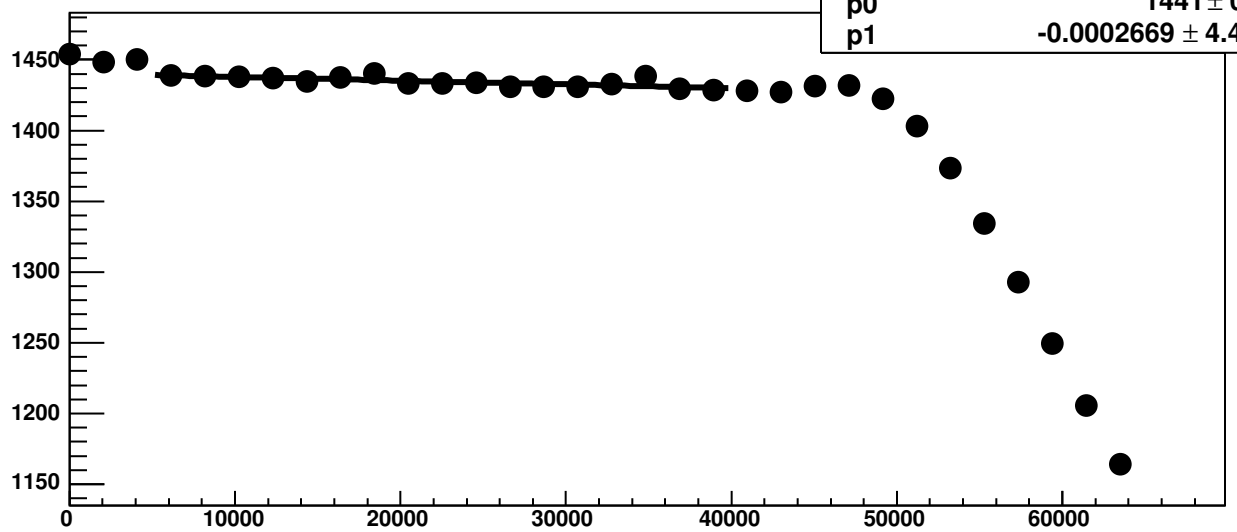

Chip 10, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5185 / 15
p0 1374 ± 0.1099
p1 0.0001656 ± 4.511e-06
1380
1360
1340
1320
1300
1280
1260
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

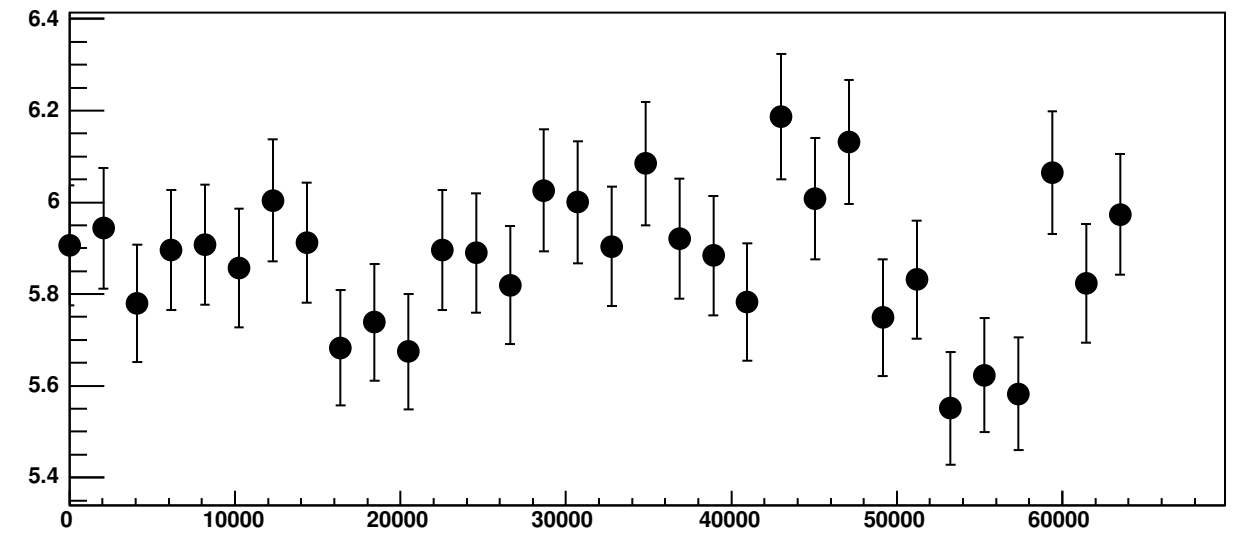

Chip 10, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

Chip 10, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC

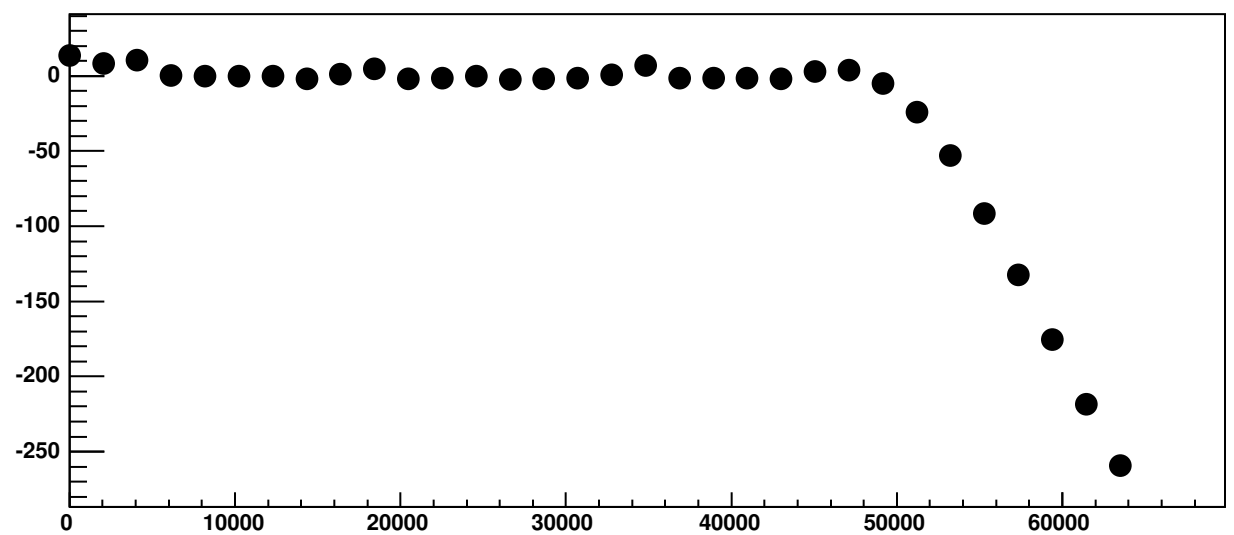
χ^2 / ndf	2860 / 15
p0	1441 ± 0.1097
p1	-0.0002669 ± 4.47e-06

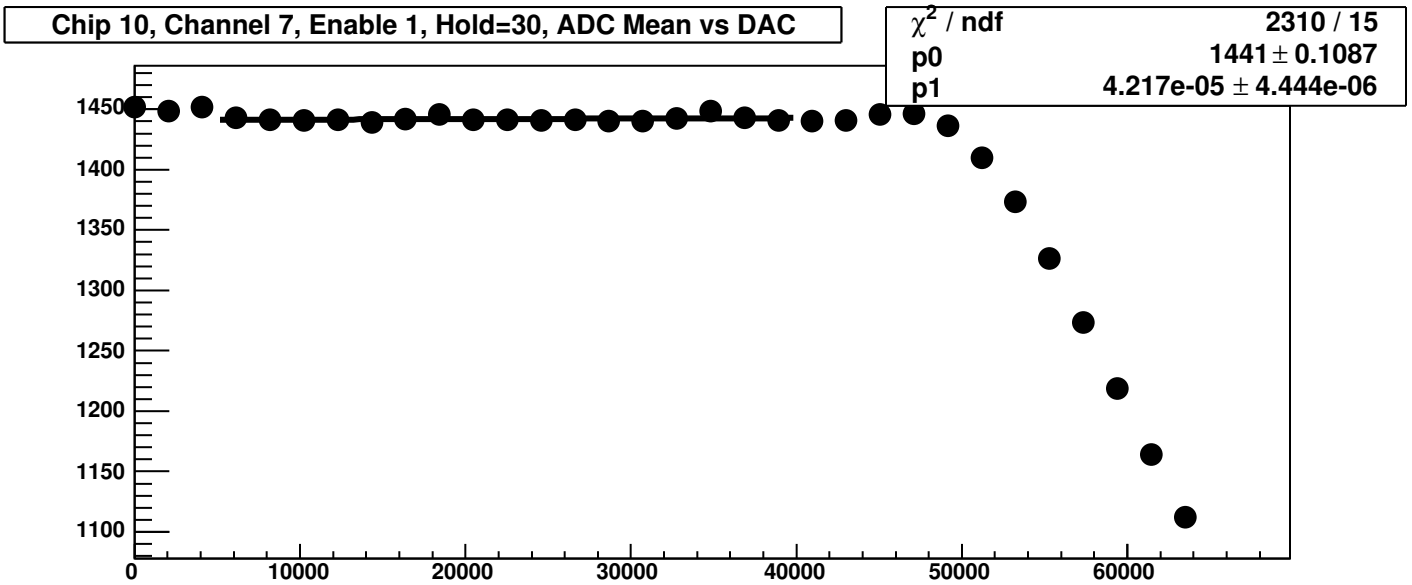


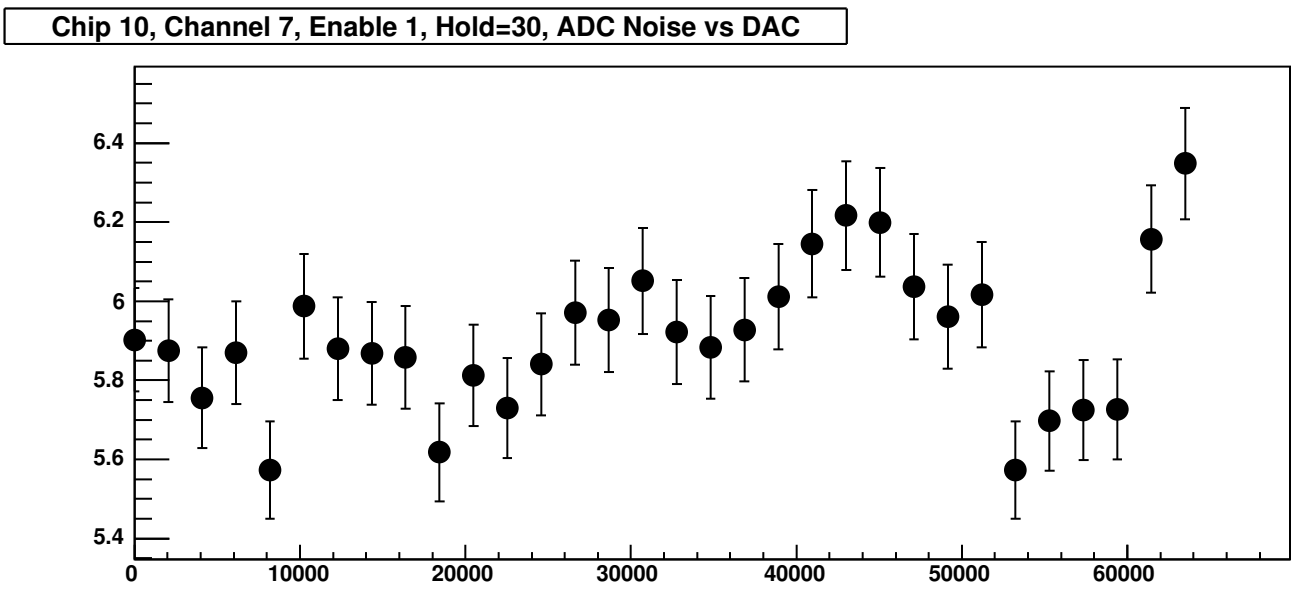
Chip 10, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC

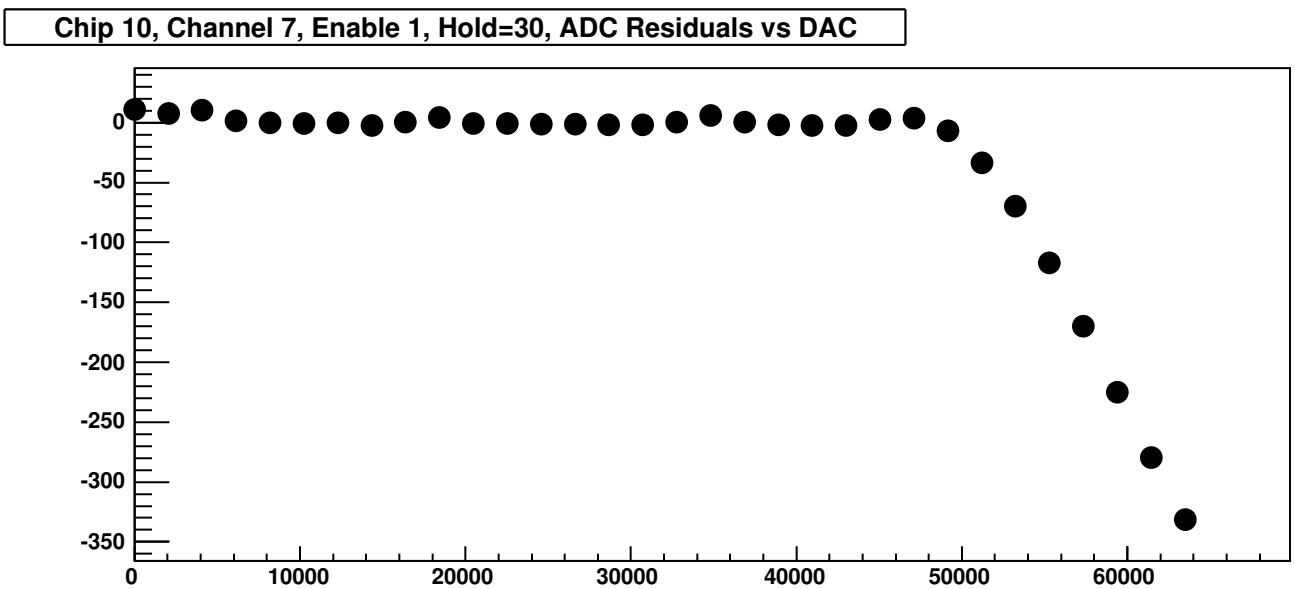


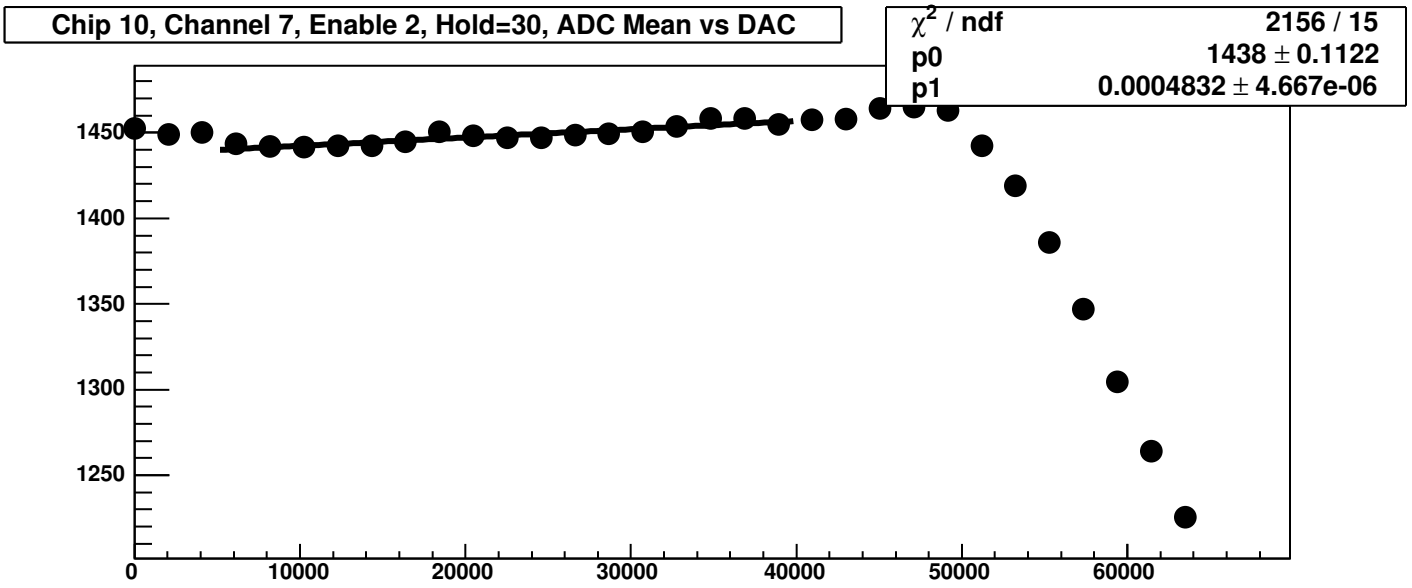
Chip 10, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

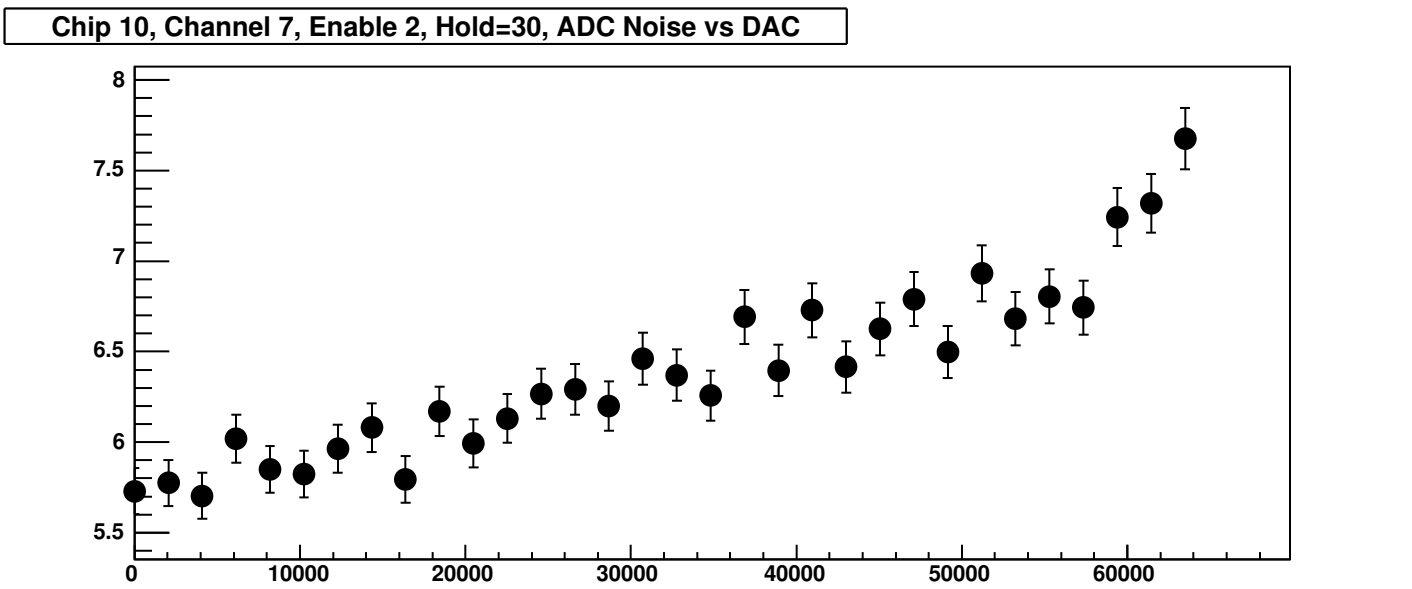


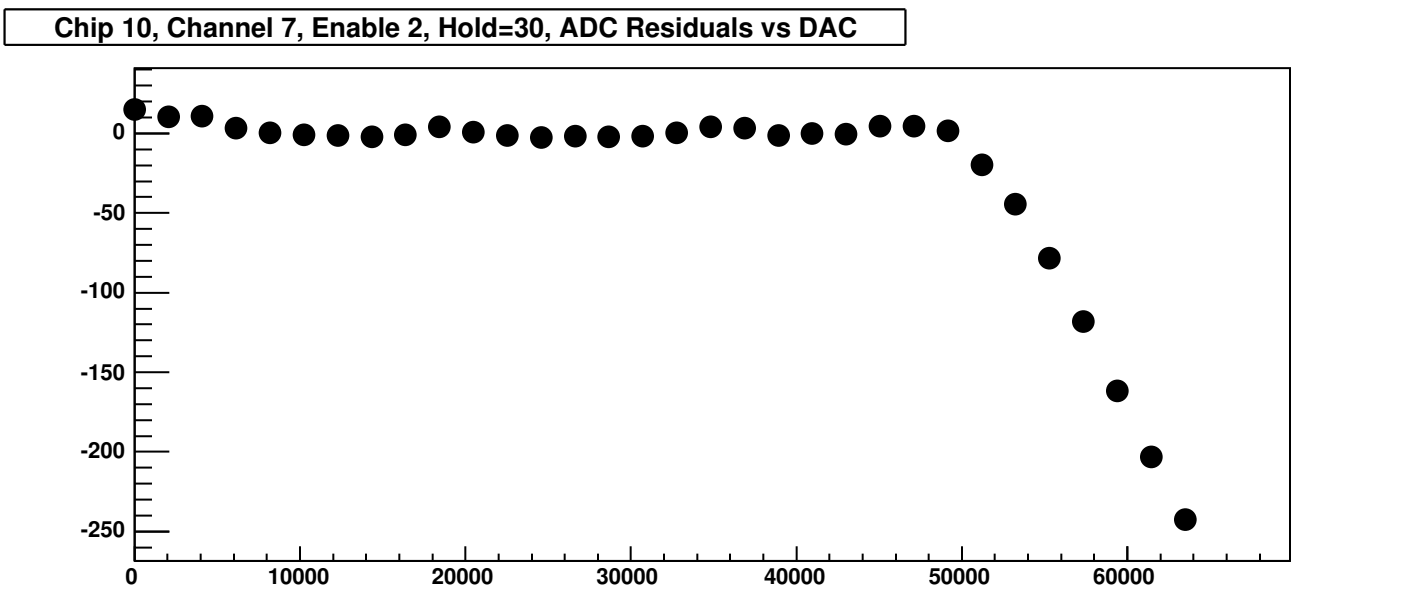

Chip 10, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2310 / 15
p0 1441 ± 0.1087
p1 4.217e-05 ± 4.444e-06
1450
1400
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

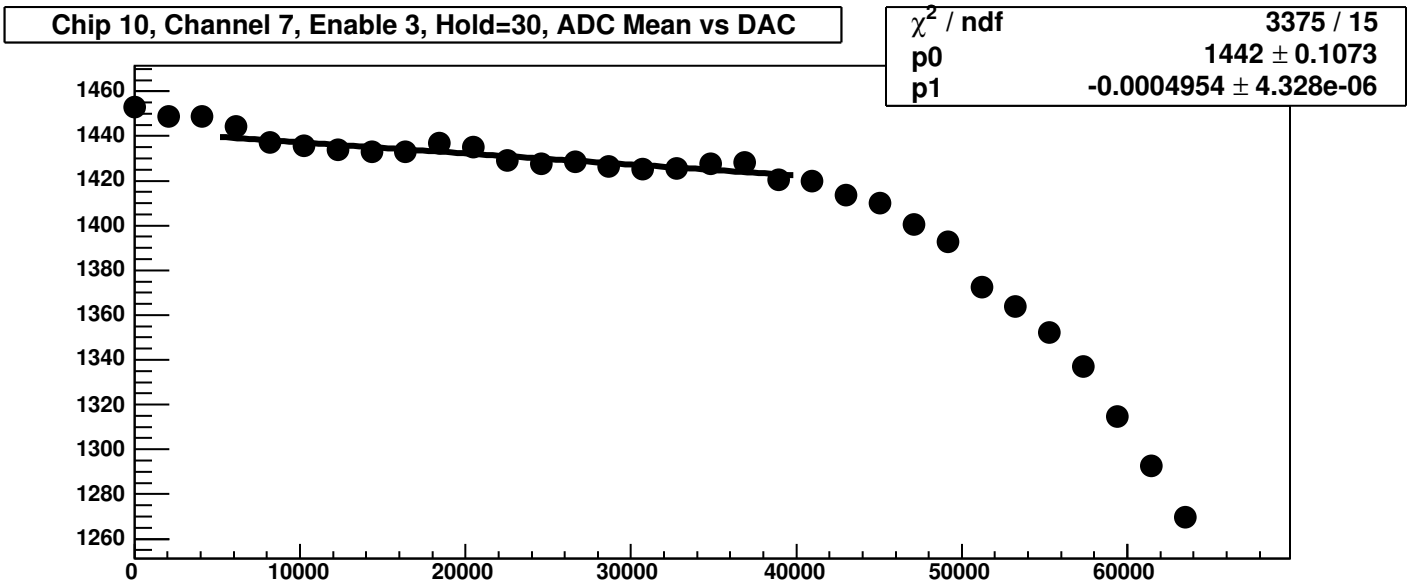

Chip 10, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

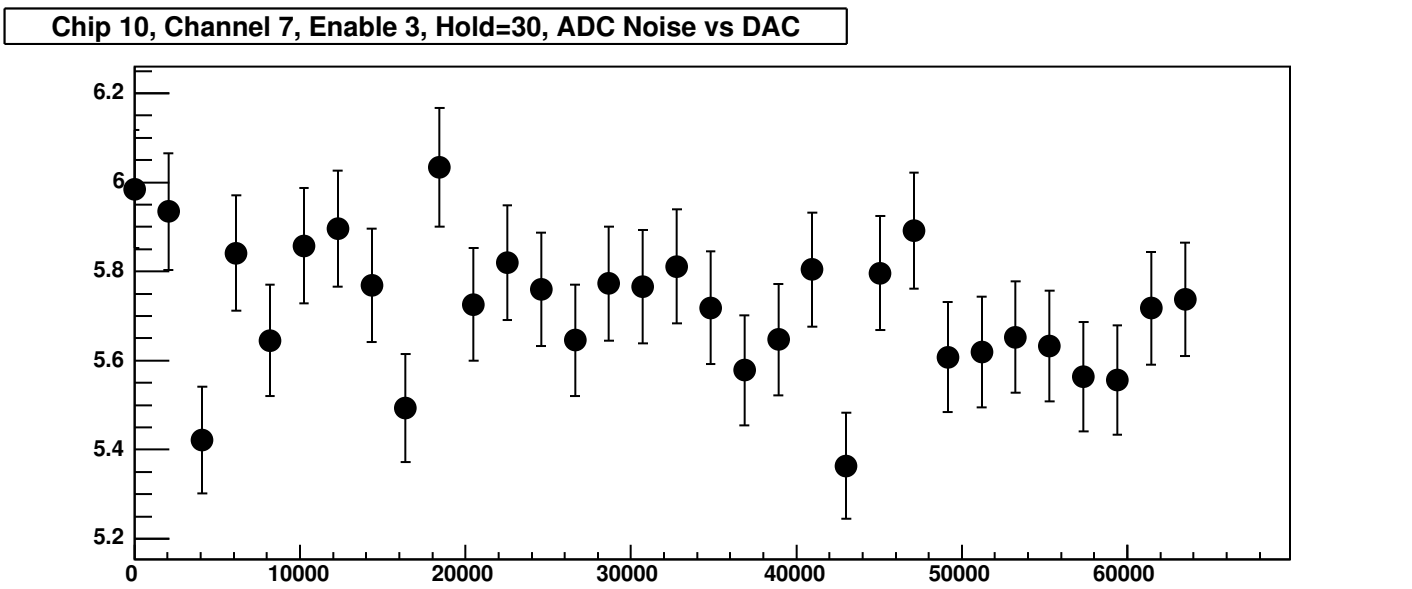

Chip 10, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

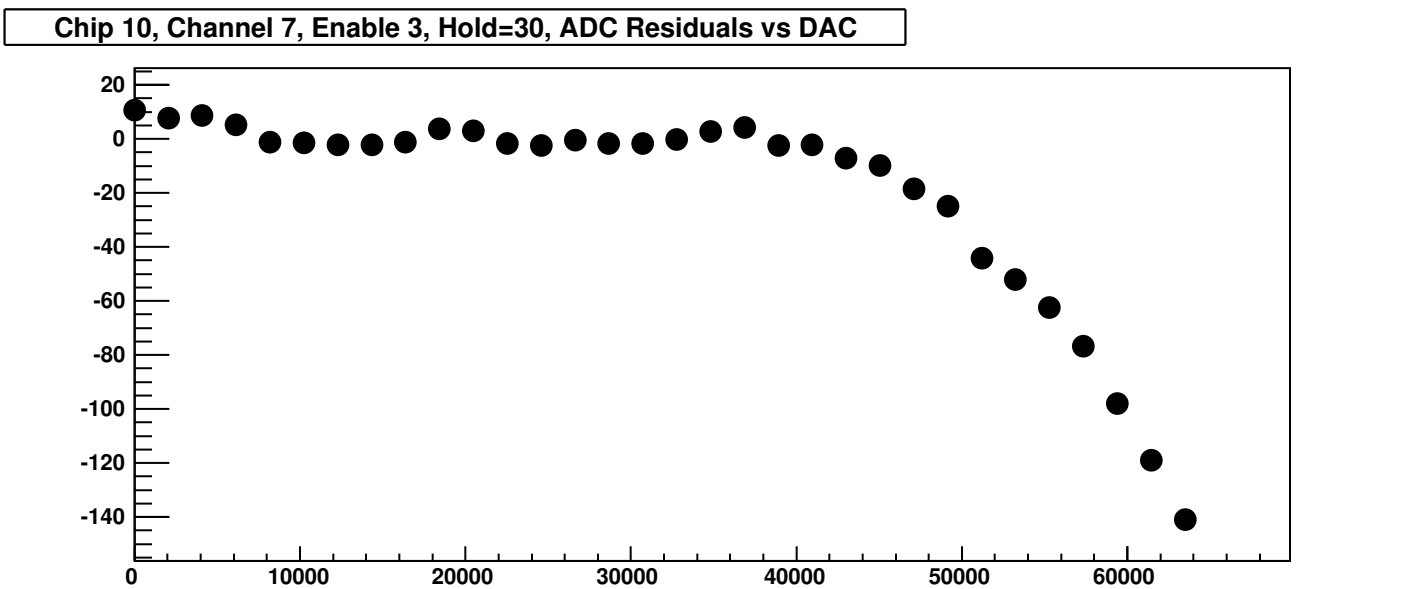

Chip 10, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2156 / 15
p0 1438 ± 0.1122
p1 0.0004832 ± 4.667e-06
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

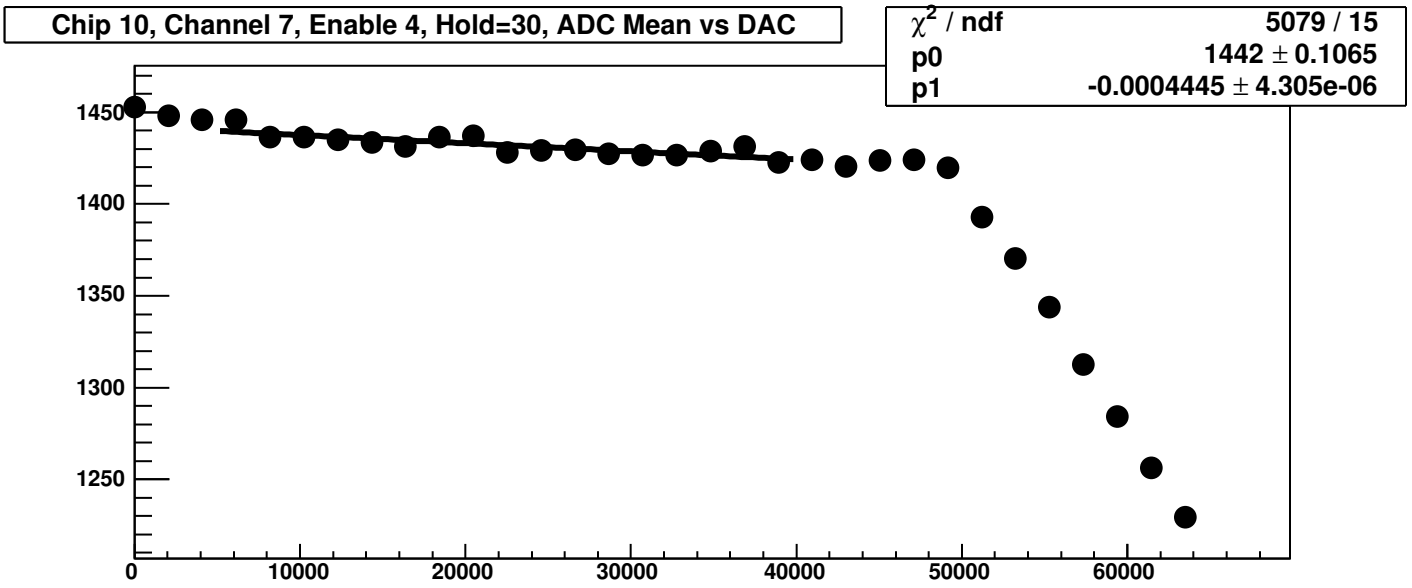

Chip 10, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC
8
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000

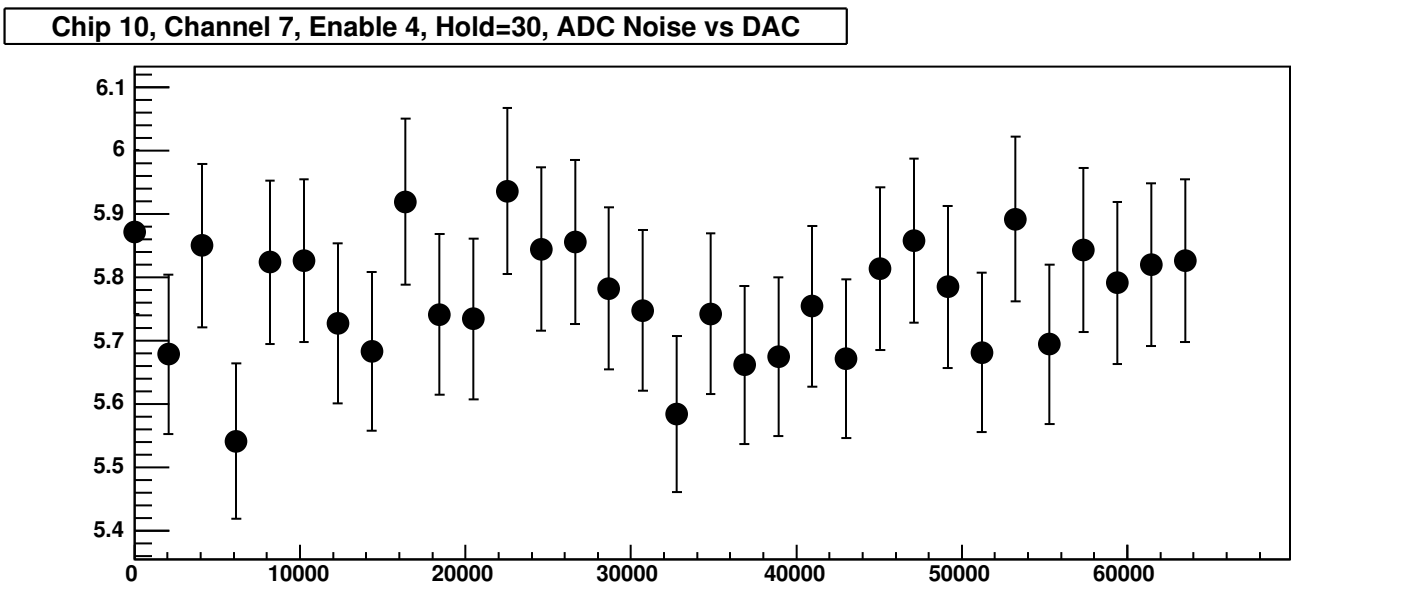

Chip 10, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

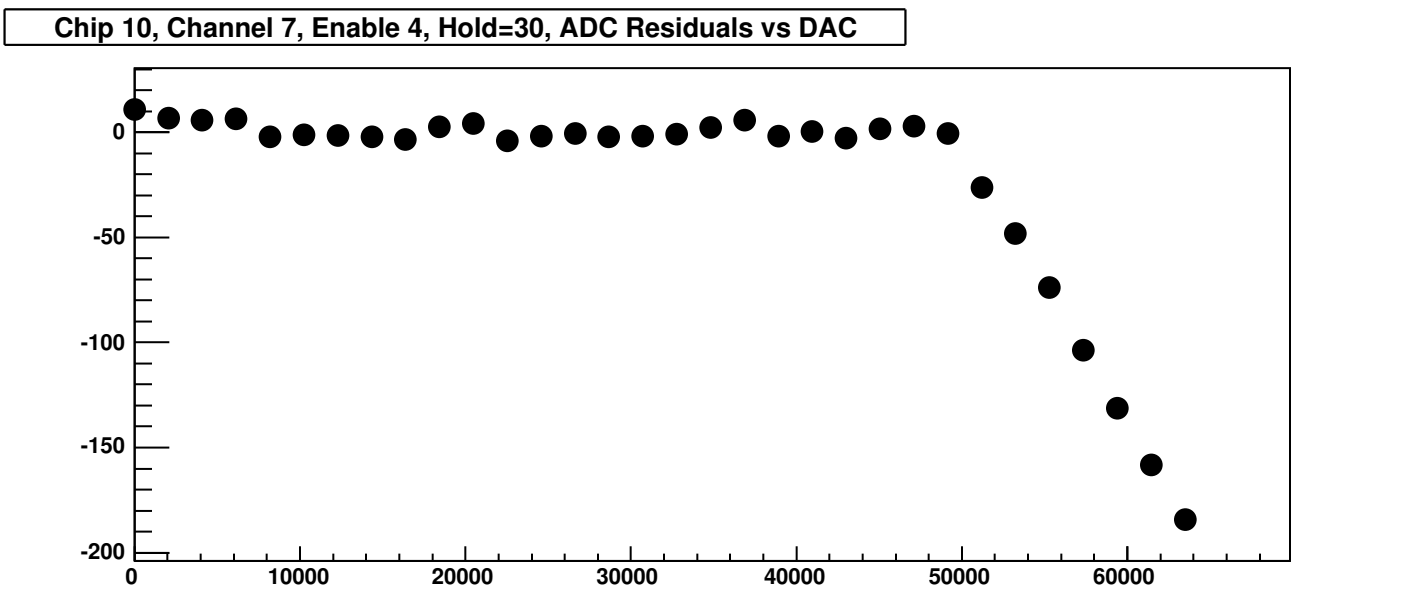

Chip 10, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3375 / 15
p0 1442 ± 0.1073
p1 -0.0004954 ± 4.328e-06
1460
1440
1420
1400
1380
1360
1340
1320
1300
1280
1260
0
10000
20000
30000
40000
50000
60000

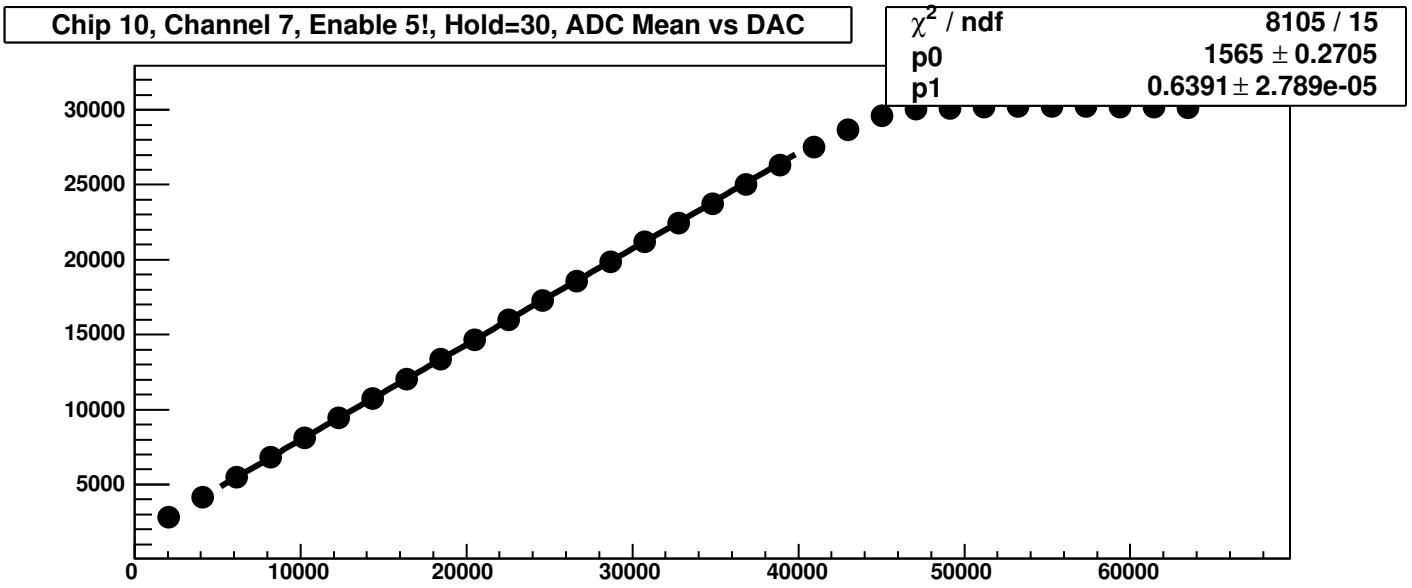

Chip 10, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

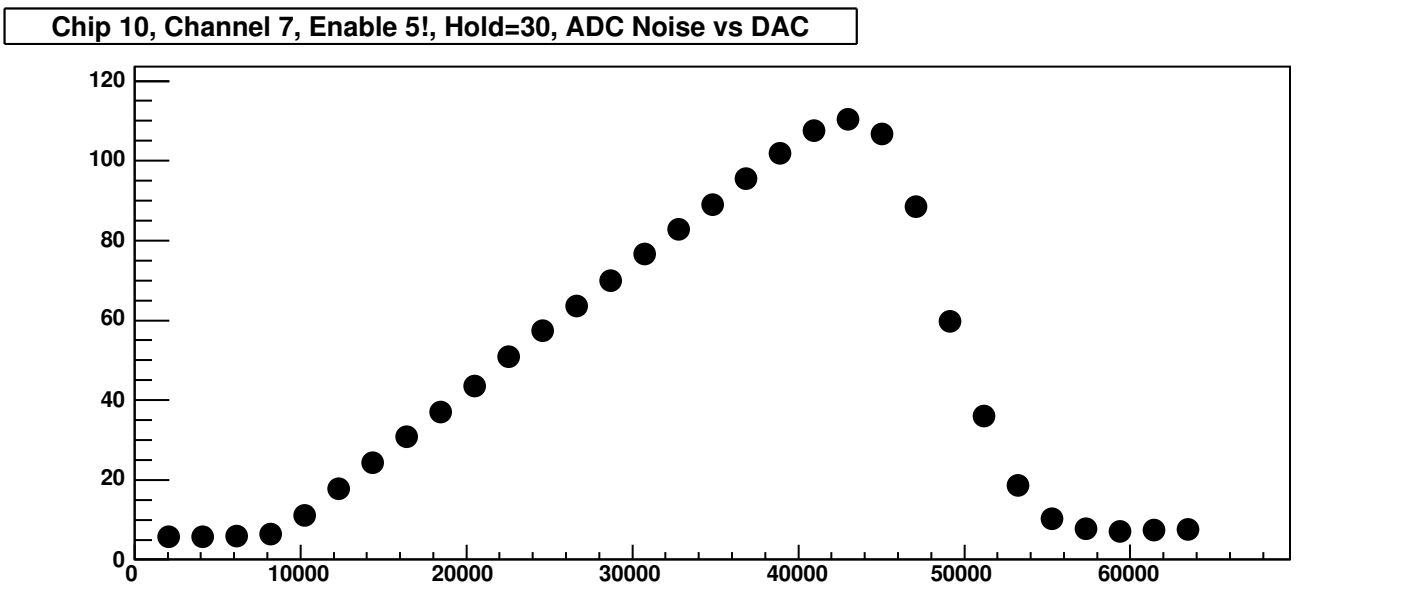

Chip 10, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

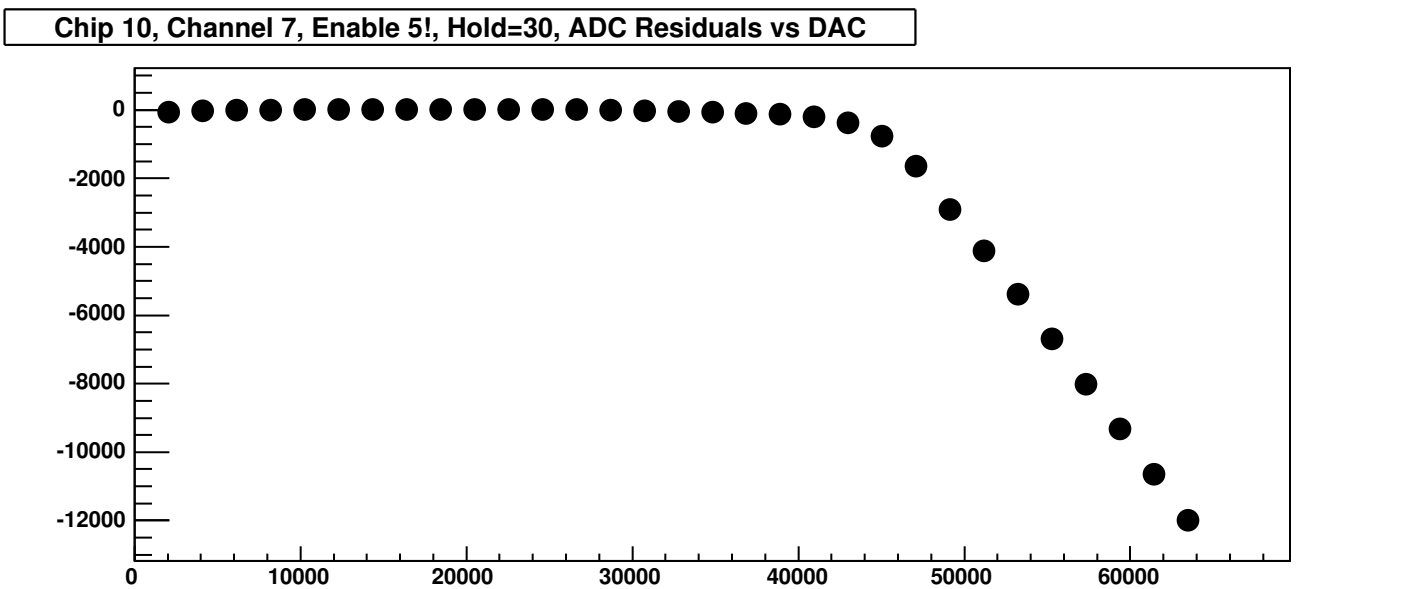

Chip 10, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5079 / 15
p0 1442 ± 0.1065
p1 -0.0004445 ± 4.305e-06
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

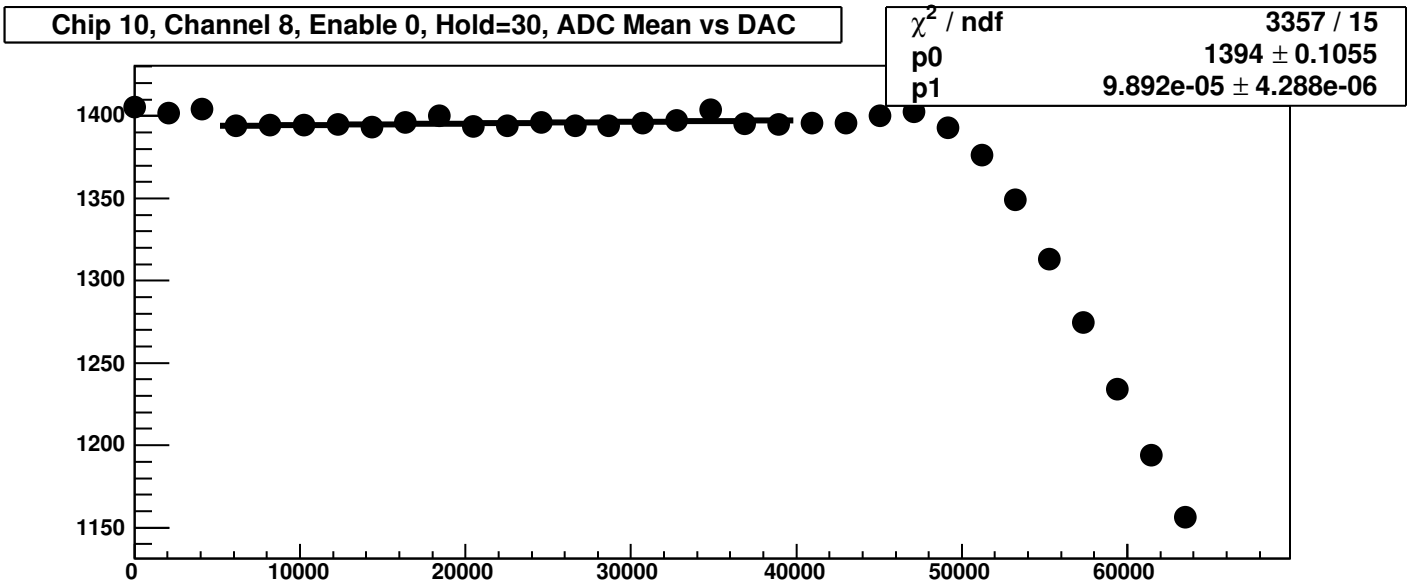

Chip 10, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

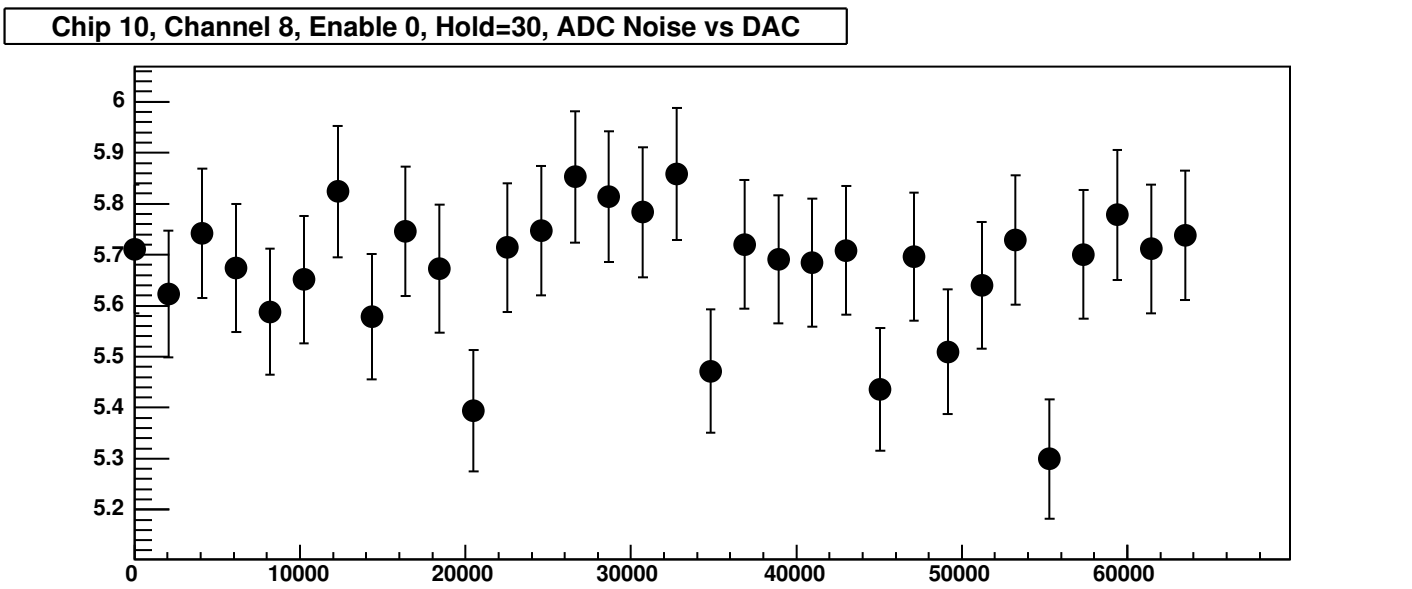

Chip 10, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

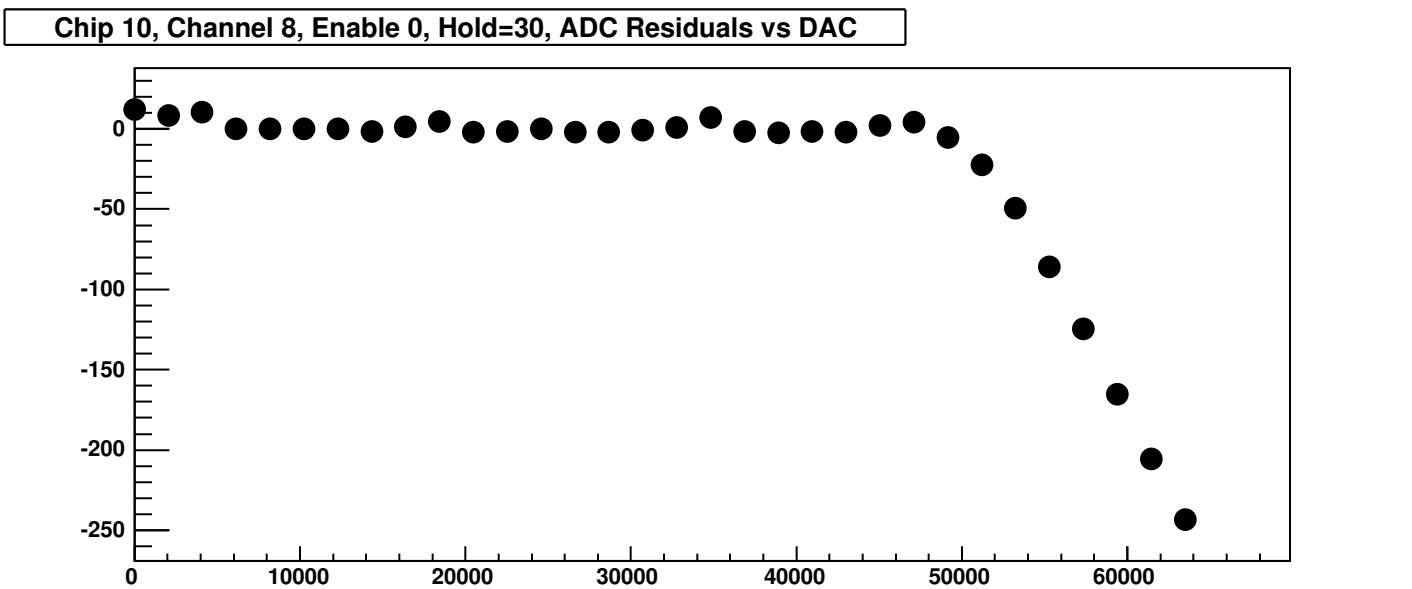

Chip 10, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 8105 / 15
p0 1565 ± 0.2705
p1 0.6391 ± 2.789e-05
30000
25000
20000
15000
10000
5000
0
10000
20000
30000
40000
50000
60000

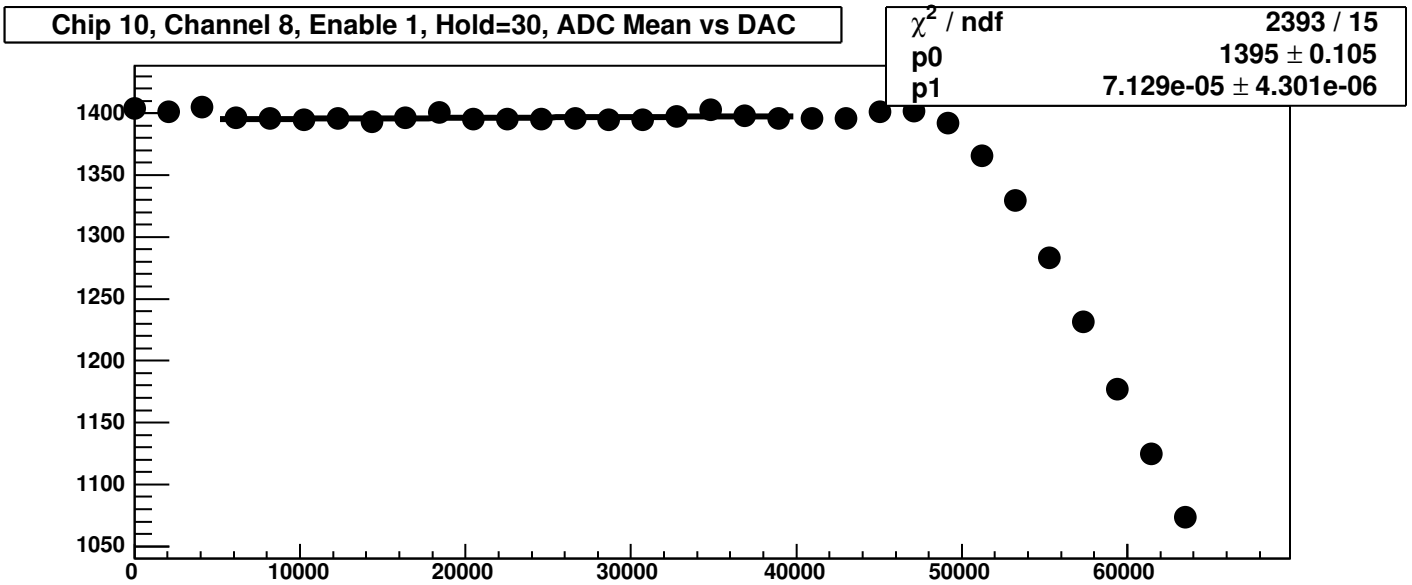

Chip 10, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

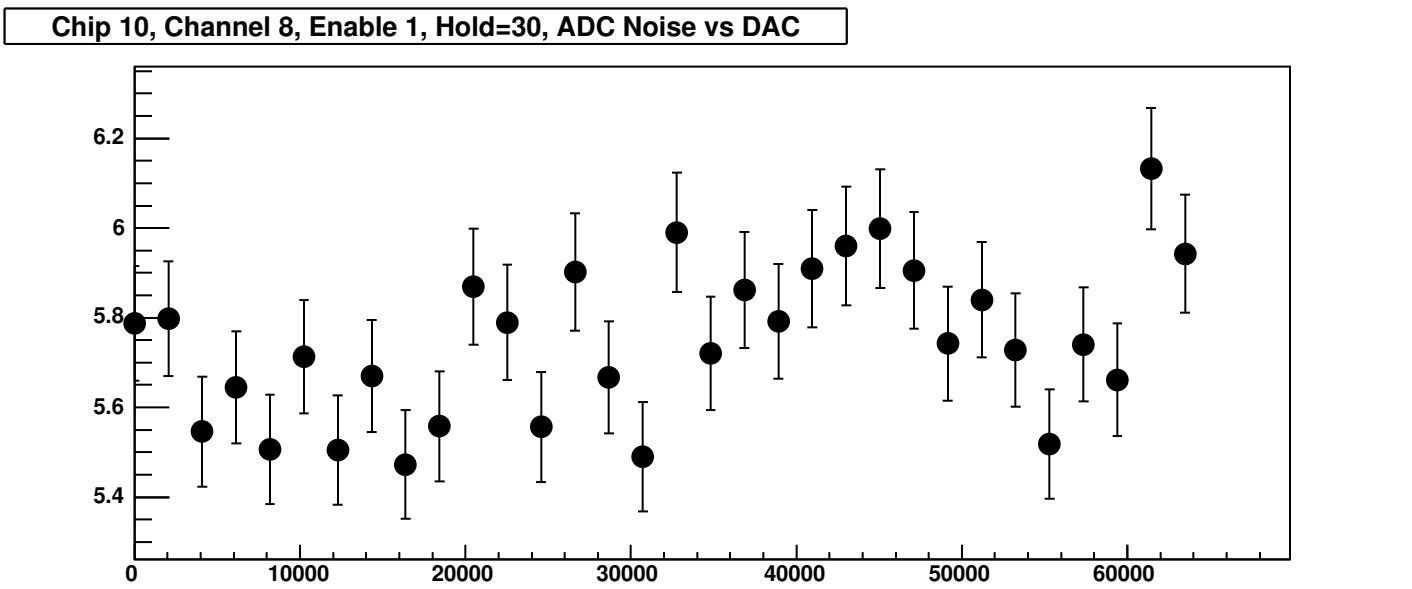

Chip 10, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

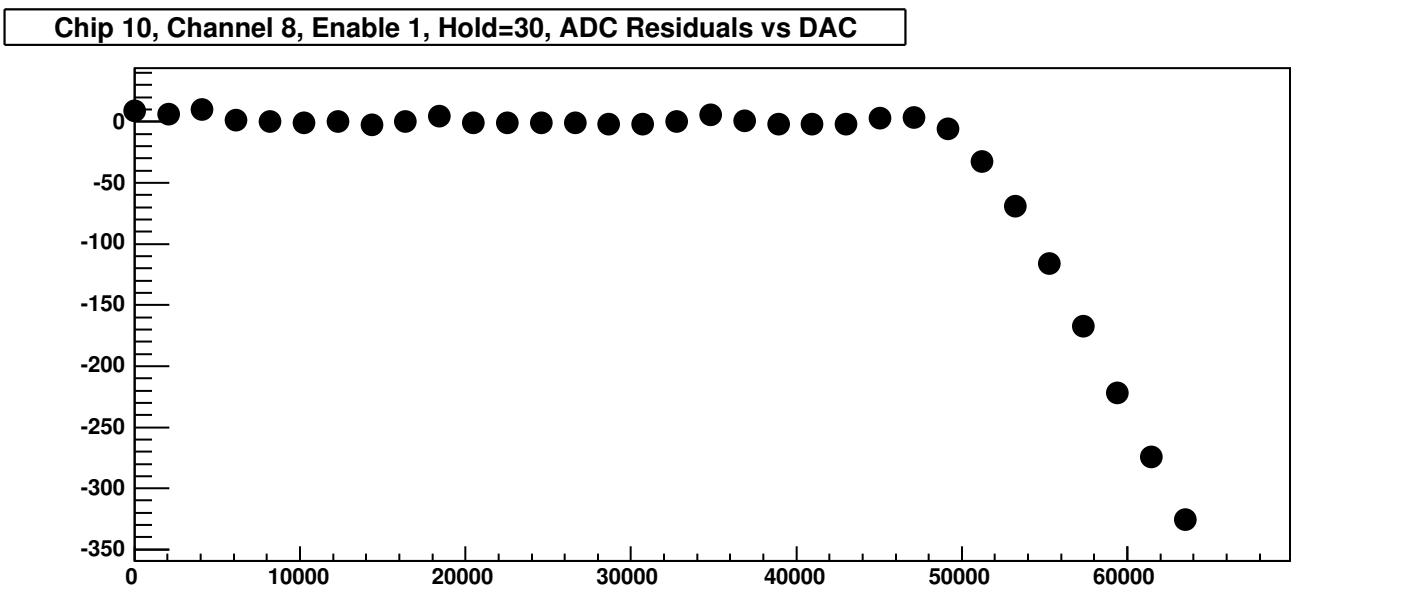

Chip 10, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3357 / 15
p0 1394 ± 0.1055
p1 9.892e-05 ± 4.288e-06
1400
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

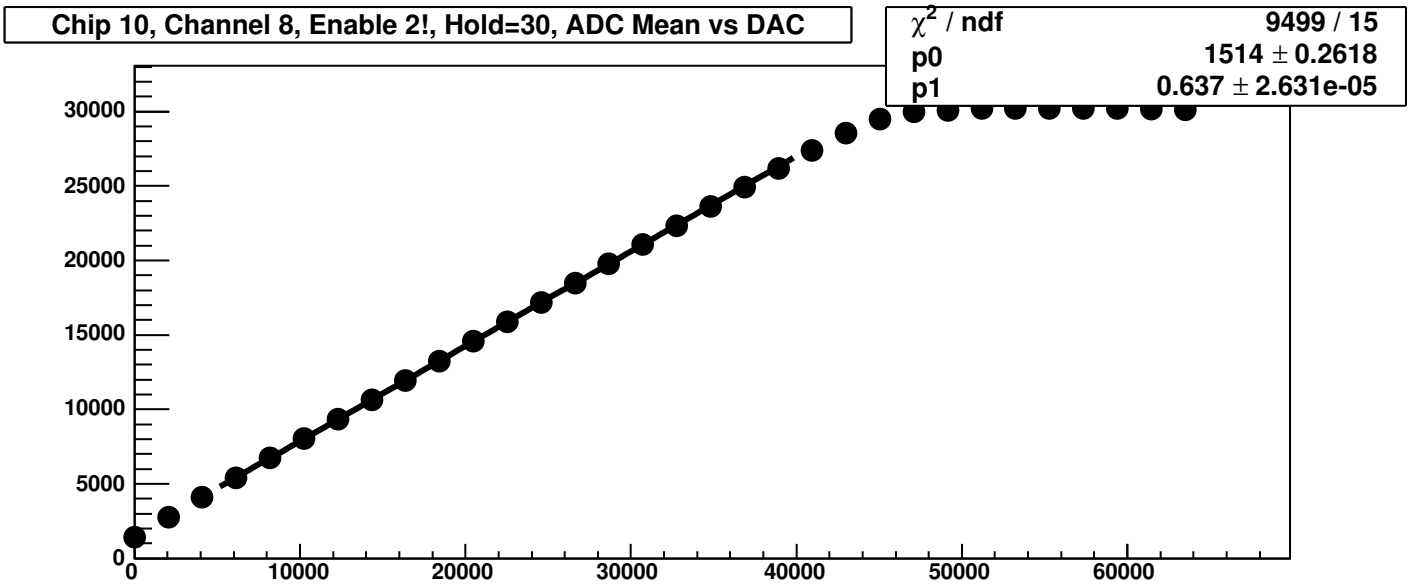

Chip 10, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
0
10000
20000
30000
40000
50000
60000

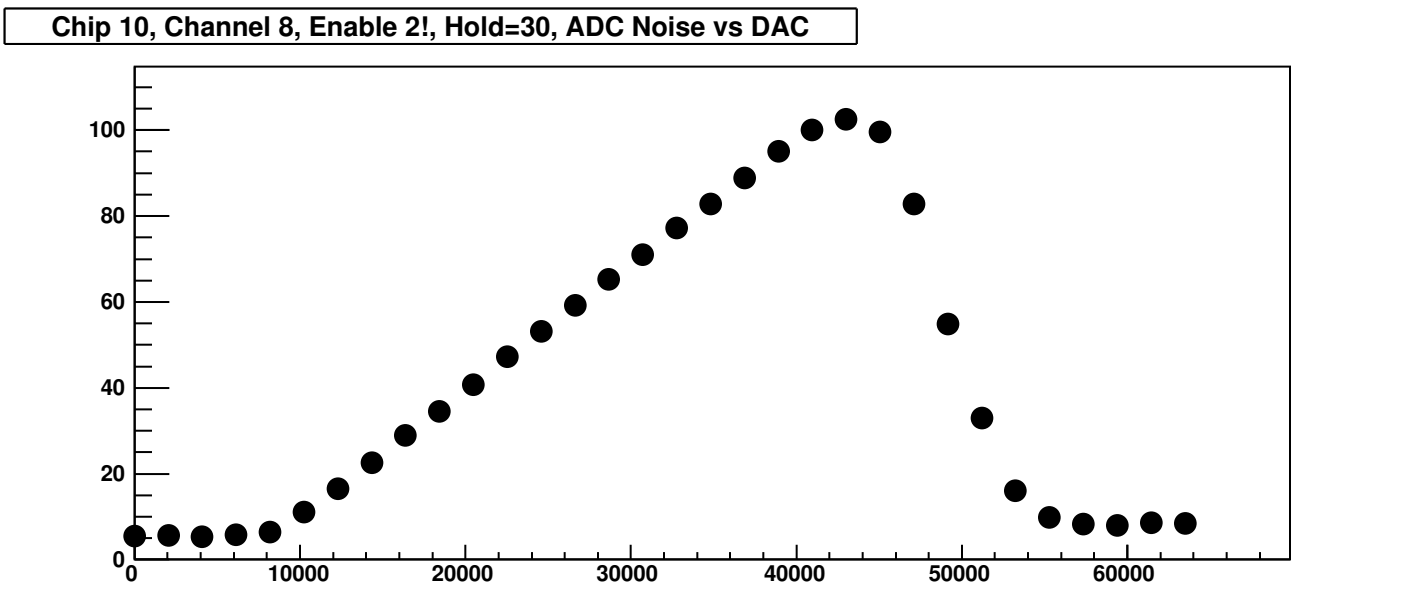

Chip 10, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

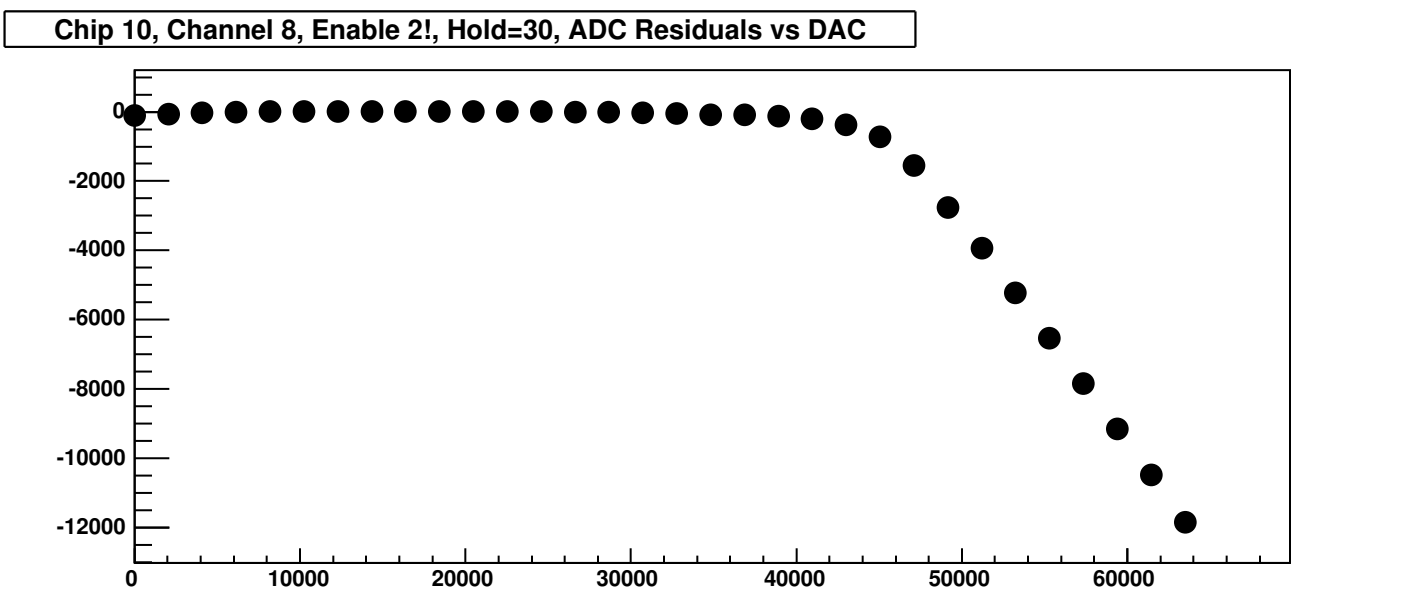

Chip 10, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2393 / 15
p0 1395 ± 0.105
p1 7.129e-05 ± 4.301e-06
1400
1350
1300
1250
1200
1150
1100
1050
0
10000
20000
30000
40000
50000
60000

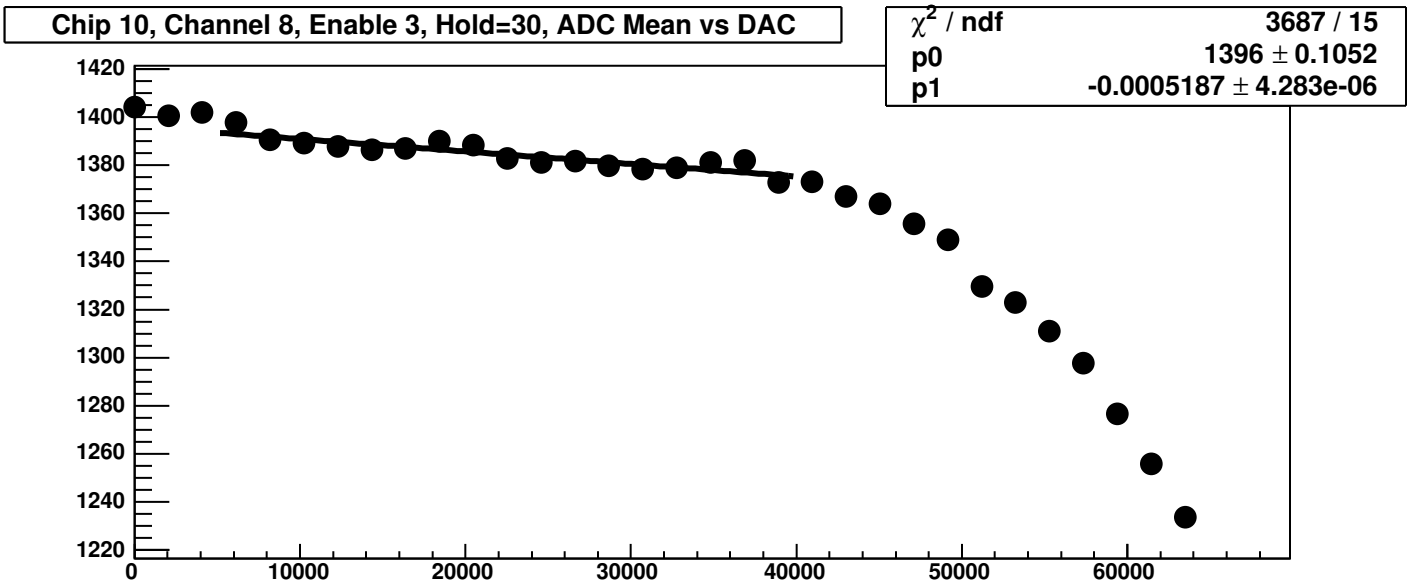

Chip 10, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

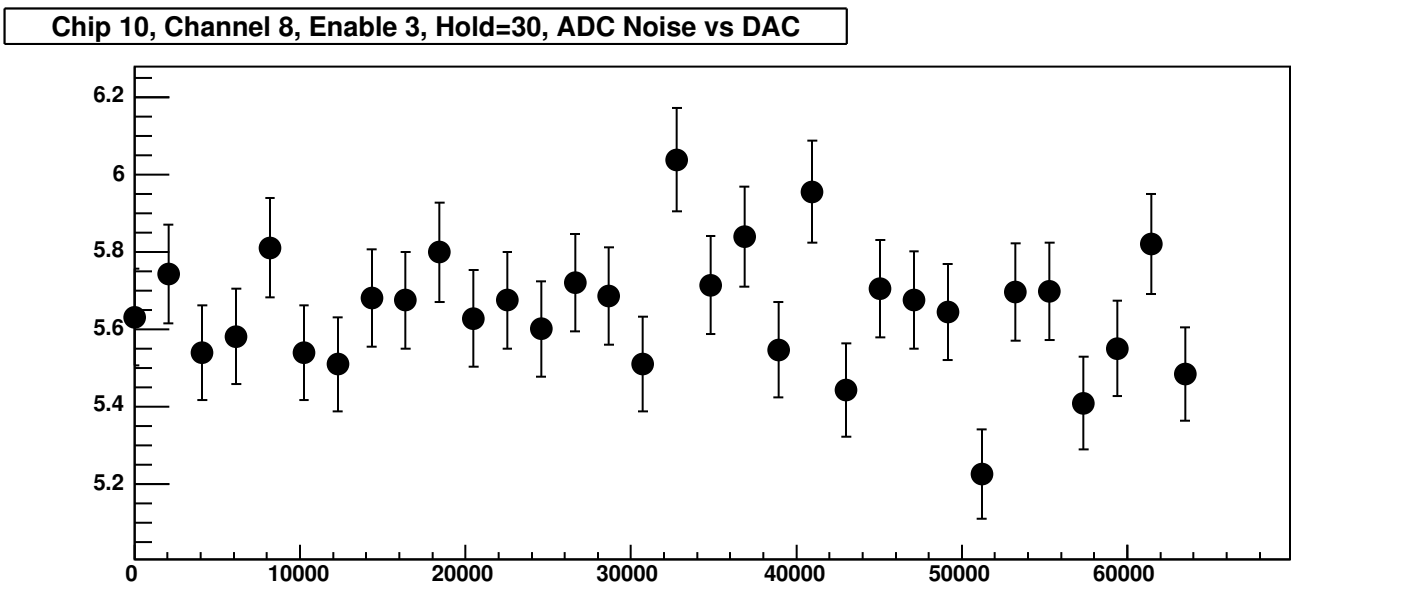

Chip 10, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

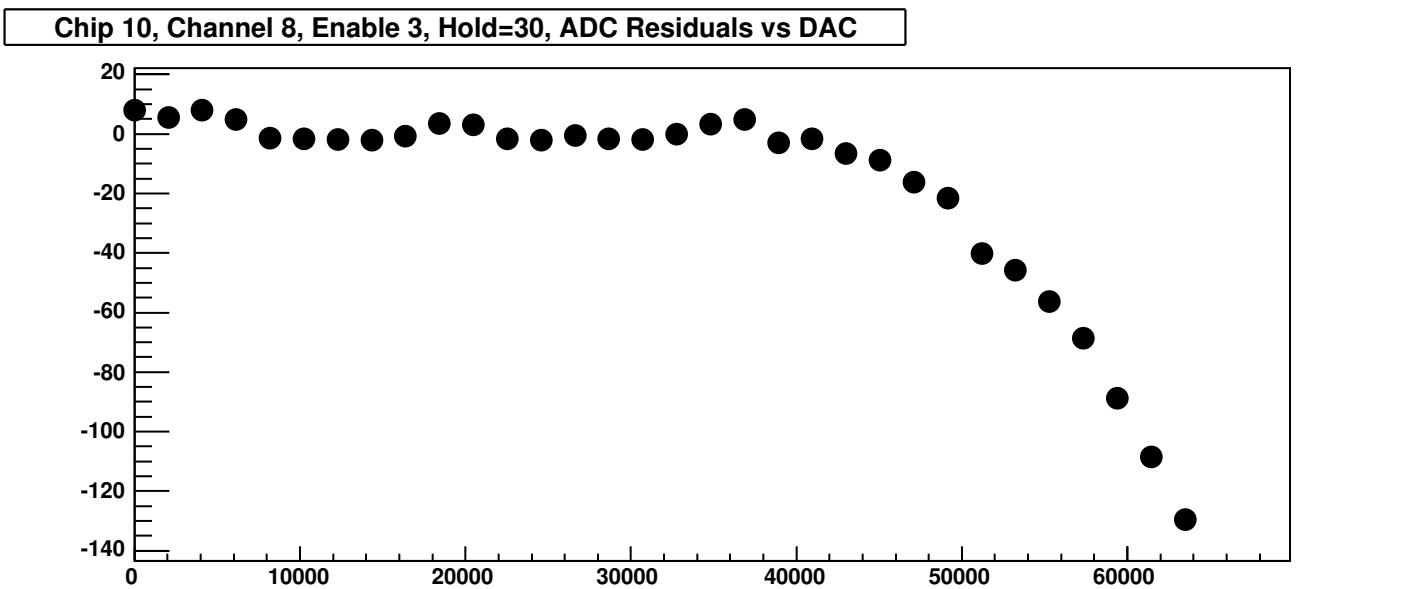

Chip 10, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 9499 / 15
p0 1514 ± 0.2618
p1 0.637 ± 2.631e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

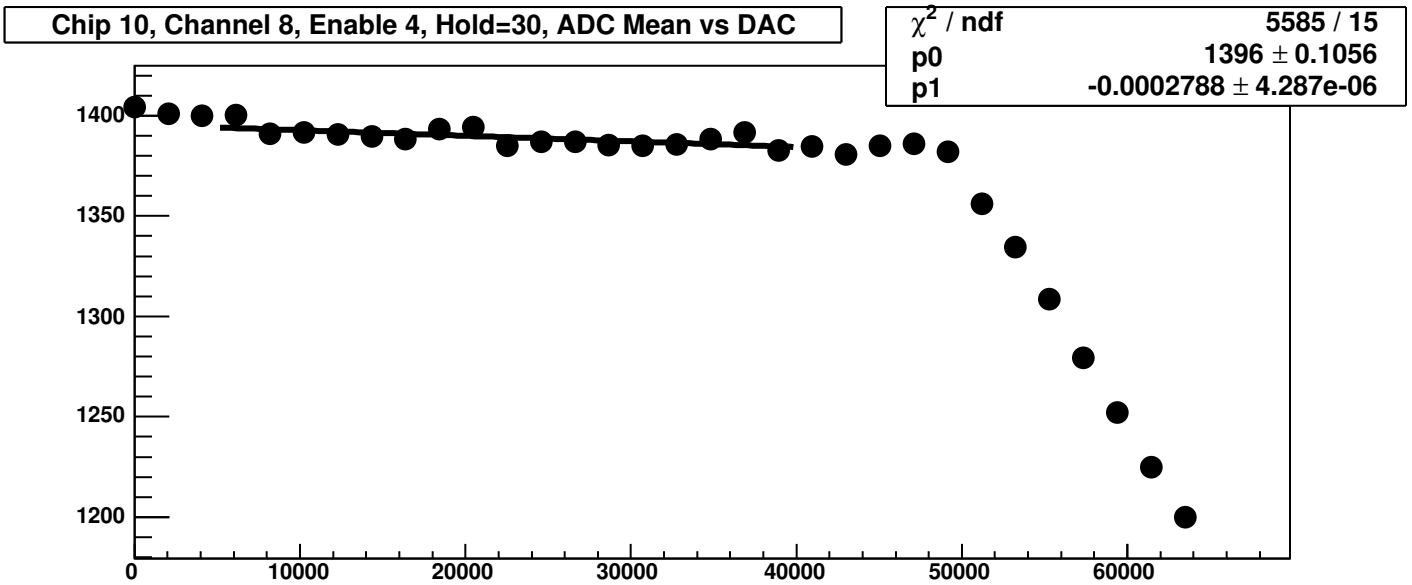

Chip 10, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

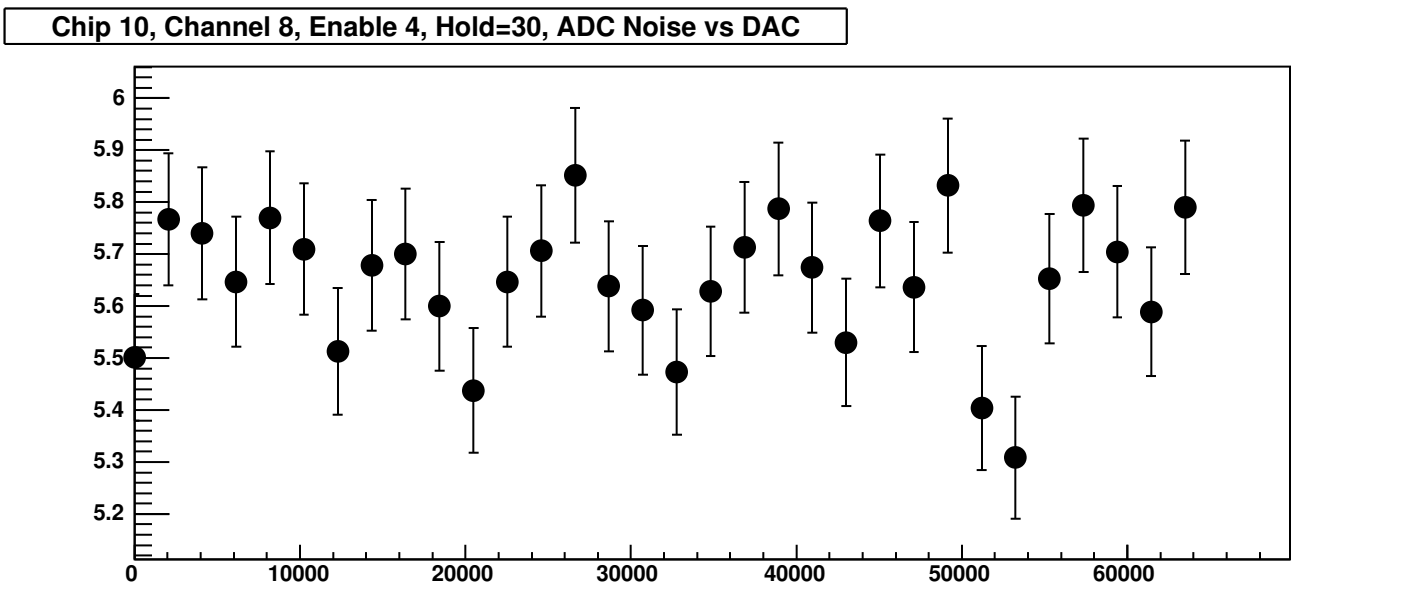

Chip 10, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

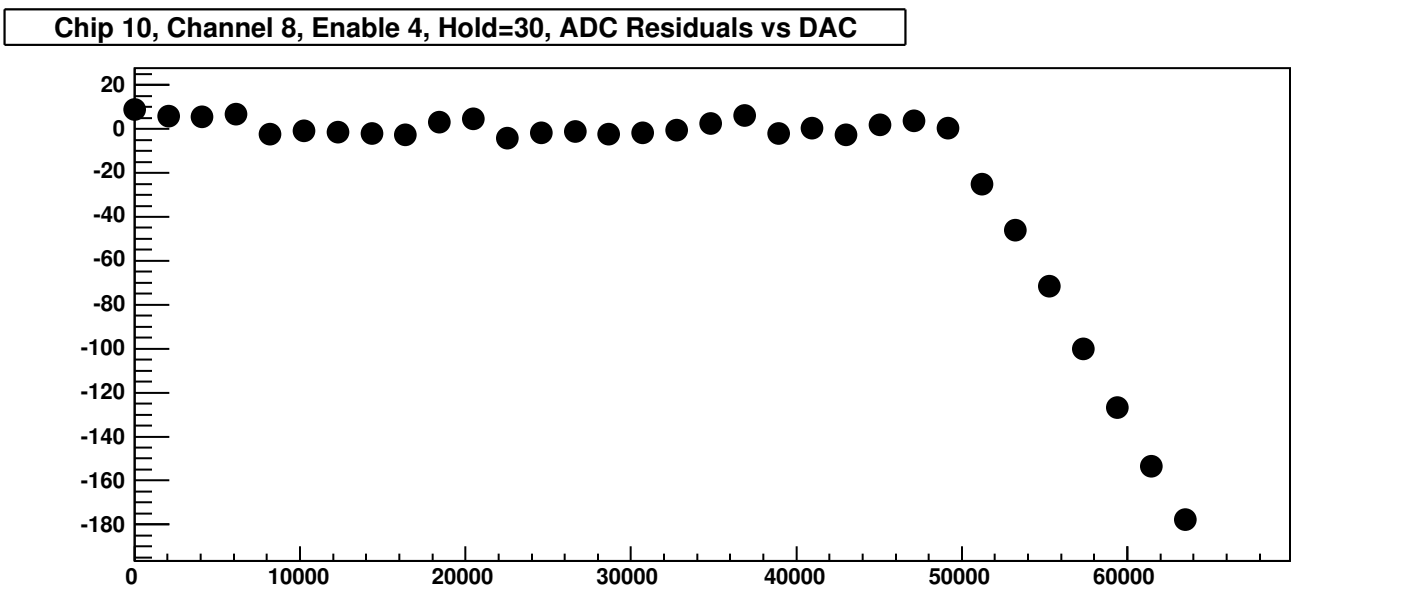

Chip 10, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3687 / 15
p0 1396 ± 0.1052
p1 -0.0005187 ± 4.283e-06
1420
1400
1380
1360
1340
1320
1300
1280
1260
1240
1220
0
10000
20000
30000
40000
50000
60000

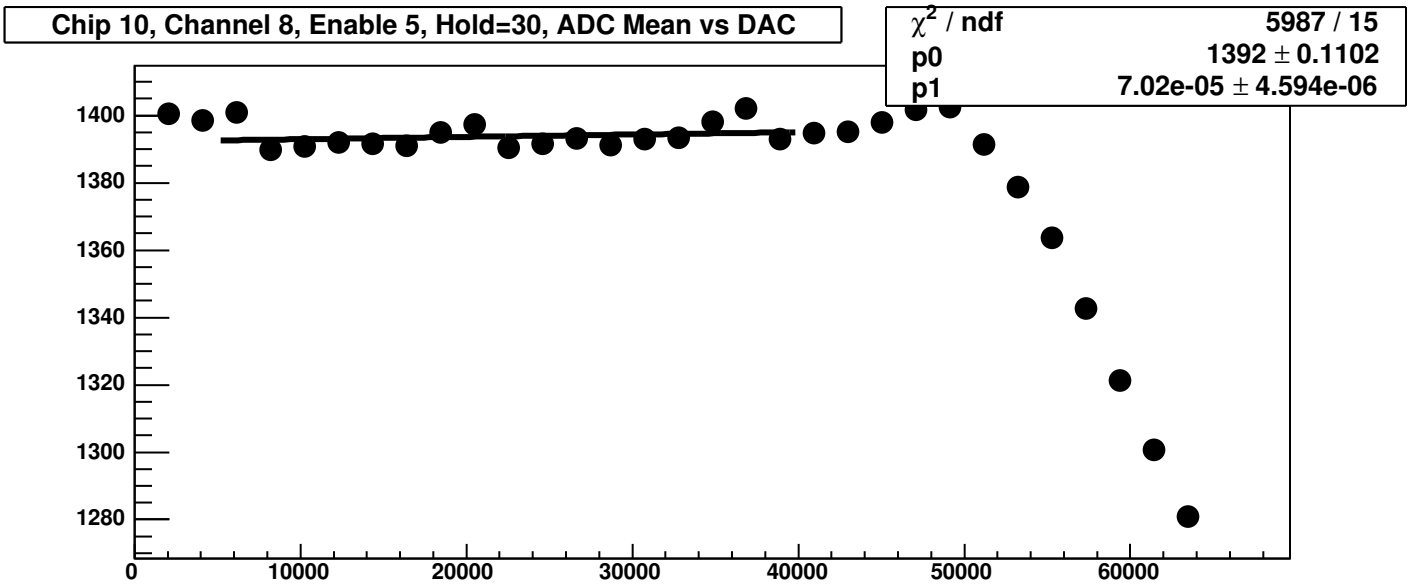

Chip 10, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

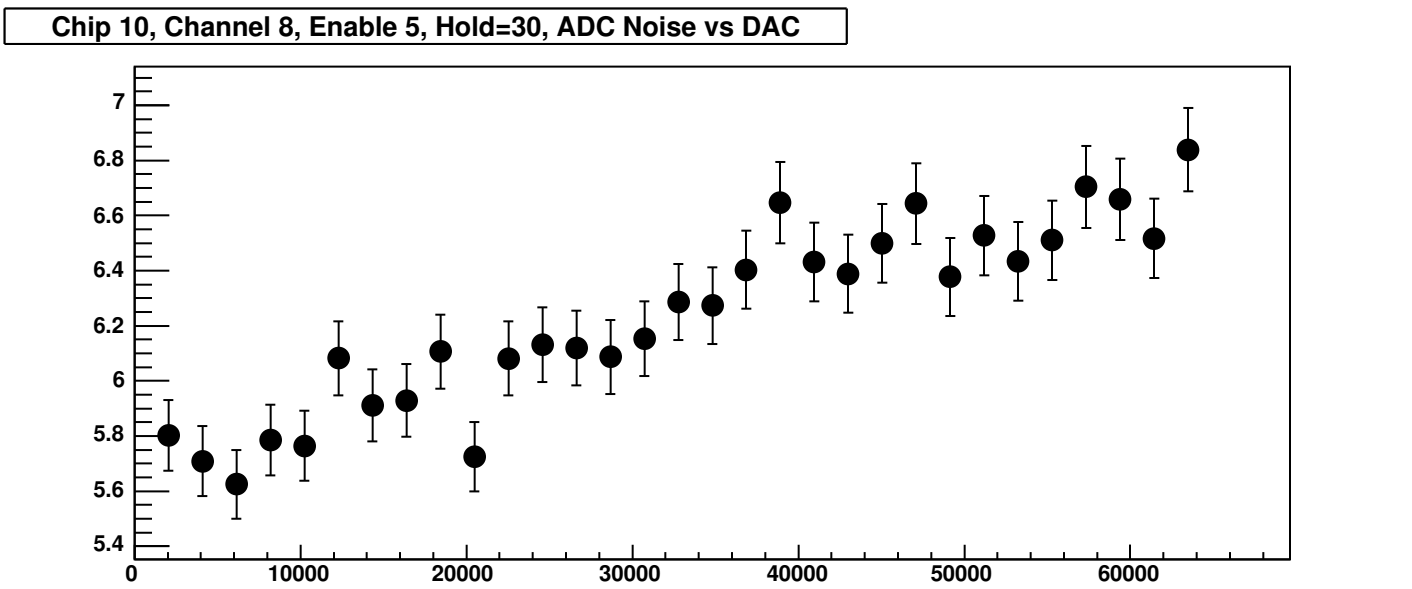

Chip 10, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

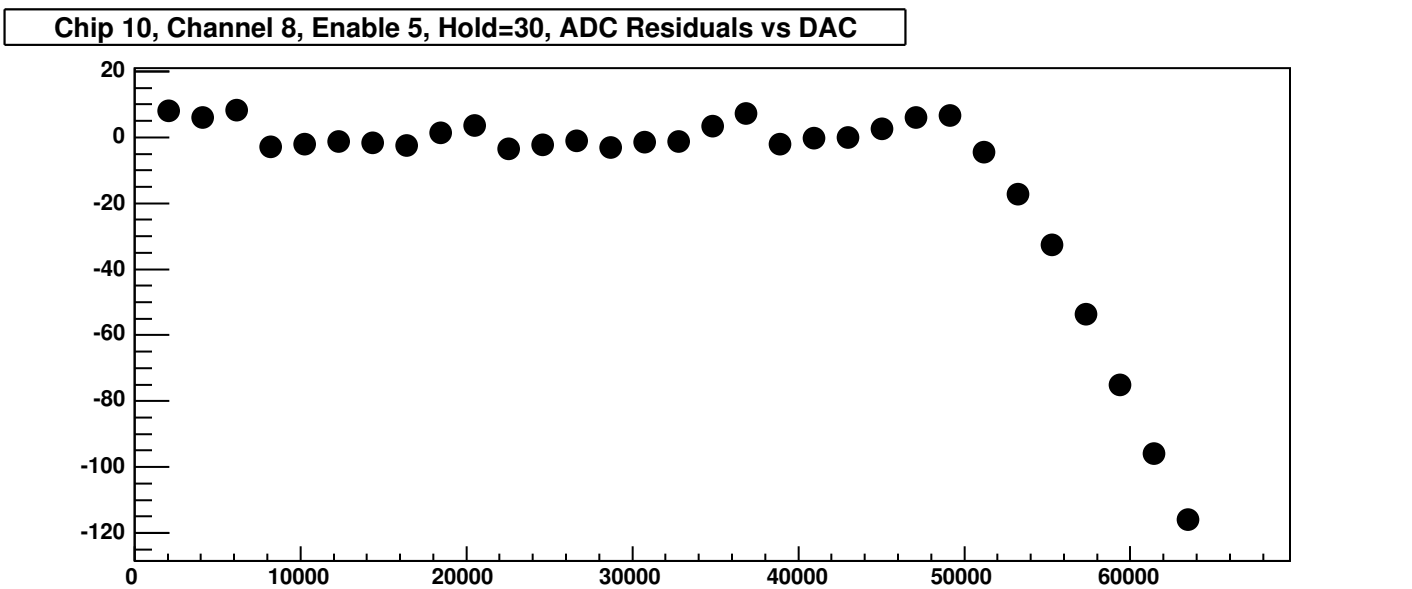

Chip 10, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5585 / 15
p0 1396 ± 0.1056
p1 -0.0002788 ± 4.287e-06
1400
1350
1300
1250
1200
0
10000
20000
30000
40000
50000
60000

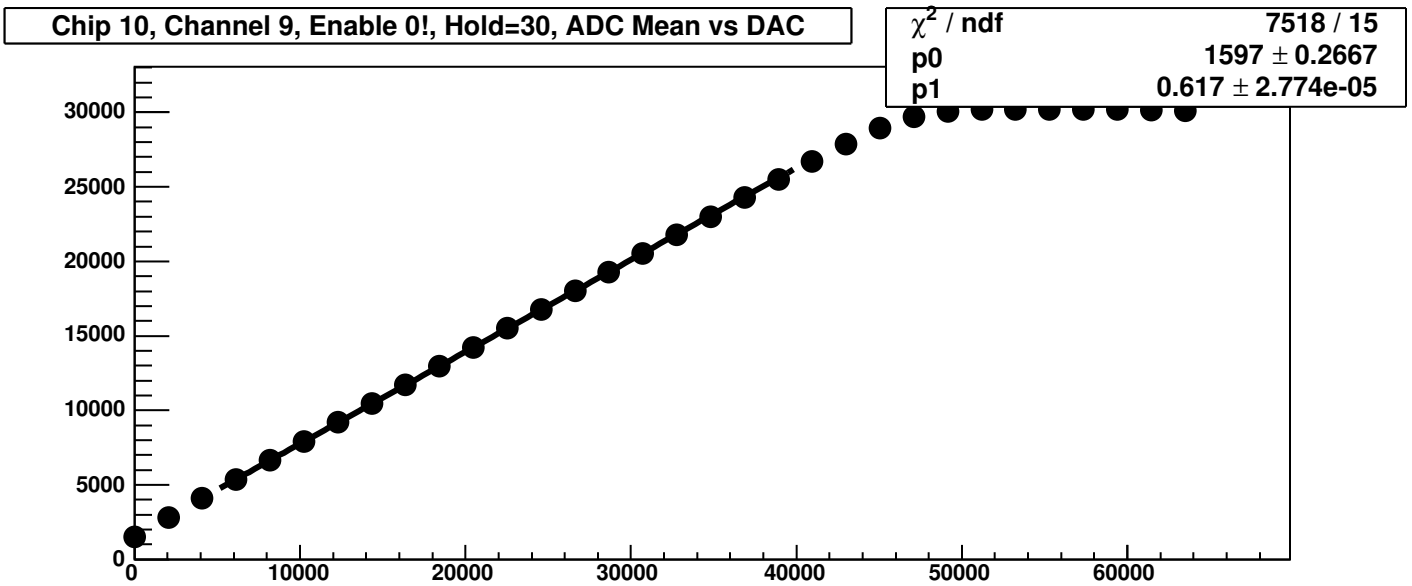

Chip 10, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
0
10000
20000
30000
40000
50000
60000

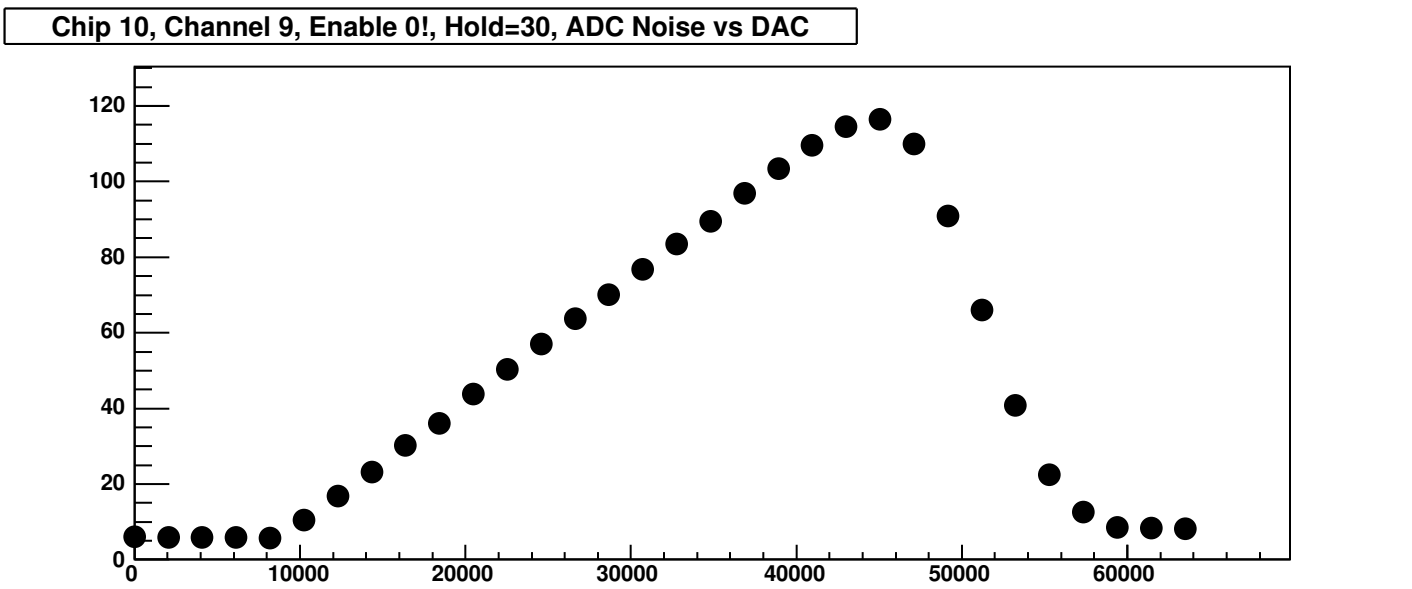

Chip 10, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

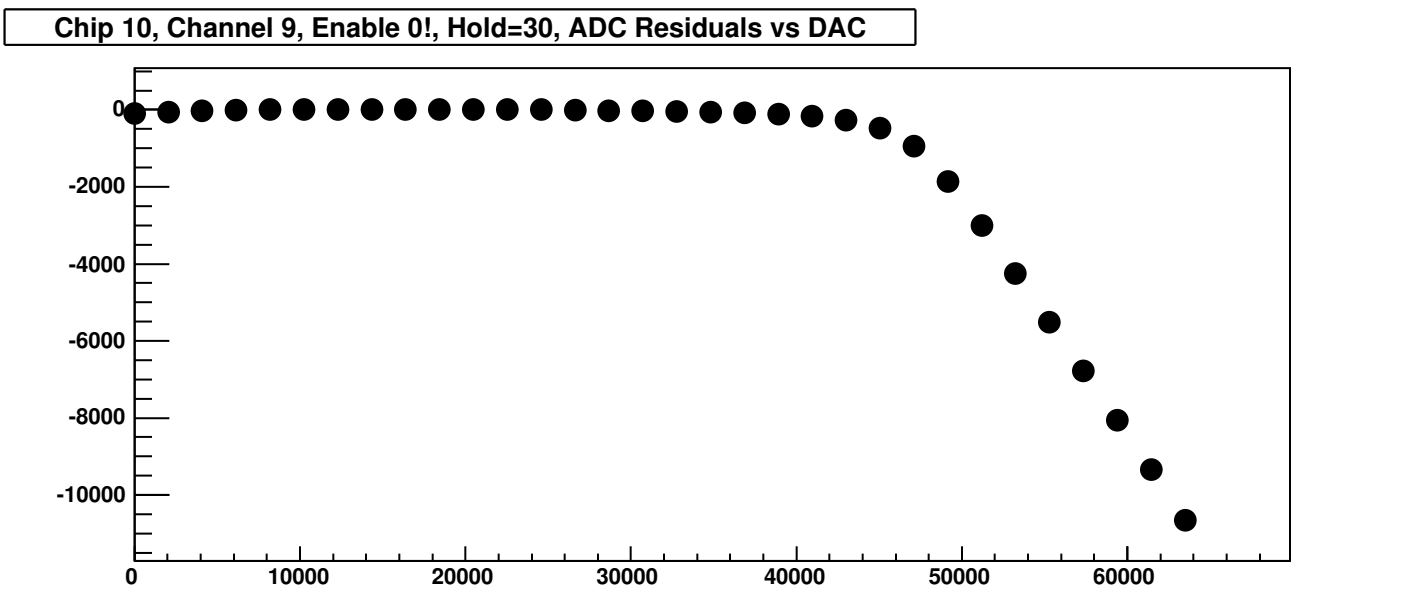

Chip 10, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5987 / 15
p0 1392 ± 0.1102
p1 7.02e-05 ± 4.594e-06
1400
1380
1360
1340
1320
1300
1280
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

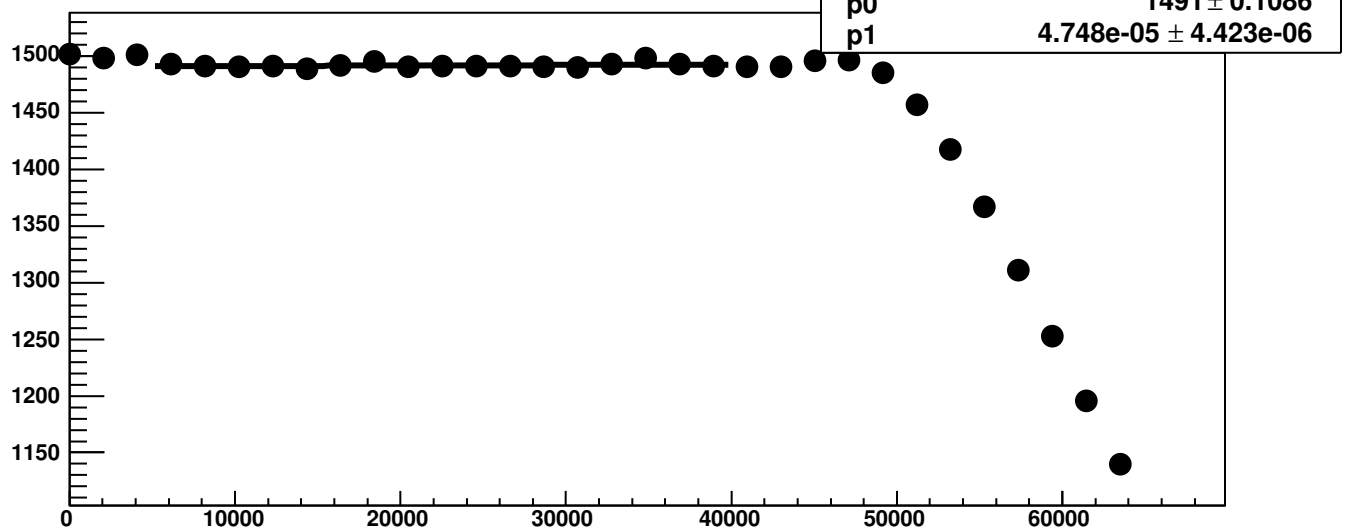

Chip 10, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 7518 / 15
p0 1597 ± 0.2667
p1 0.617 ± 2.774e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

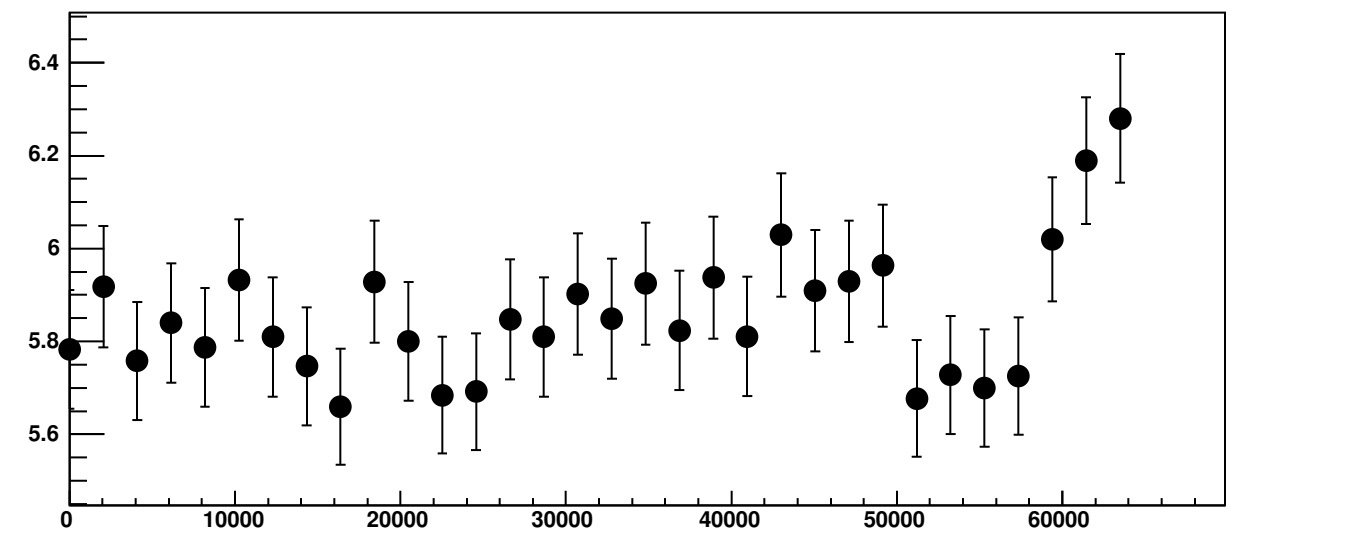

Chip 10, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

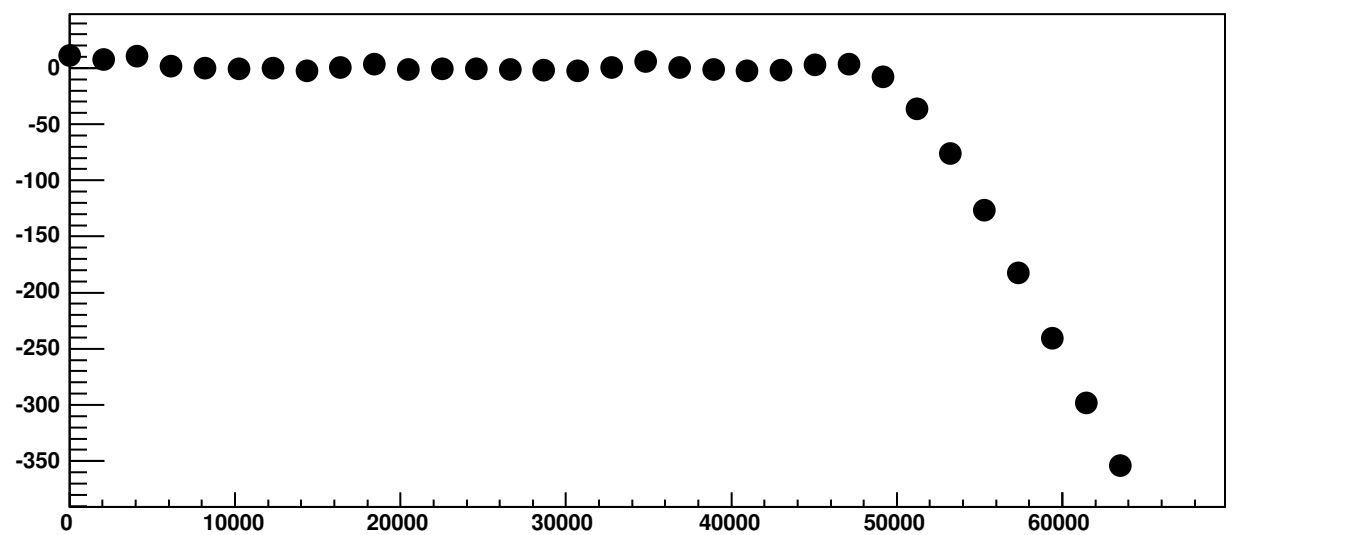
Chip 10, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

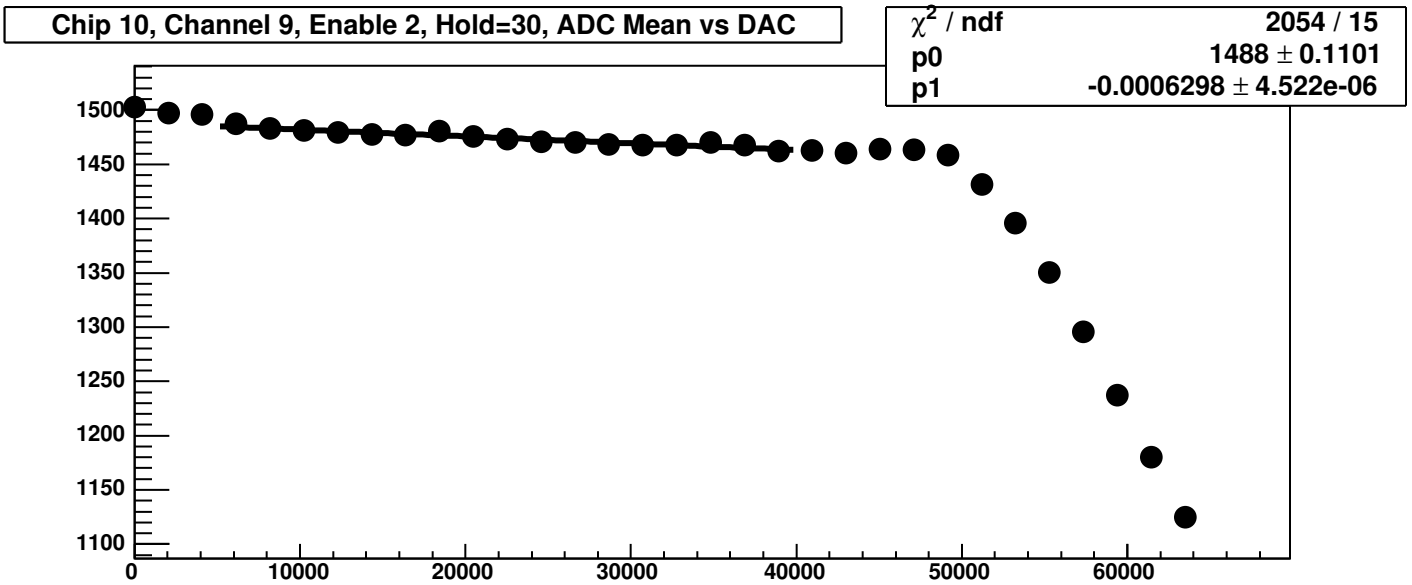


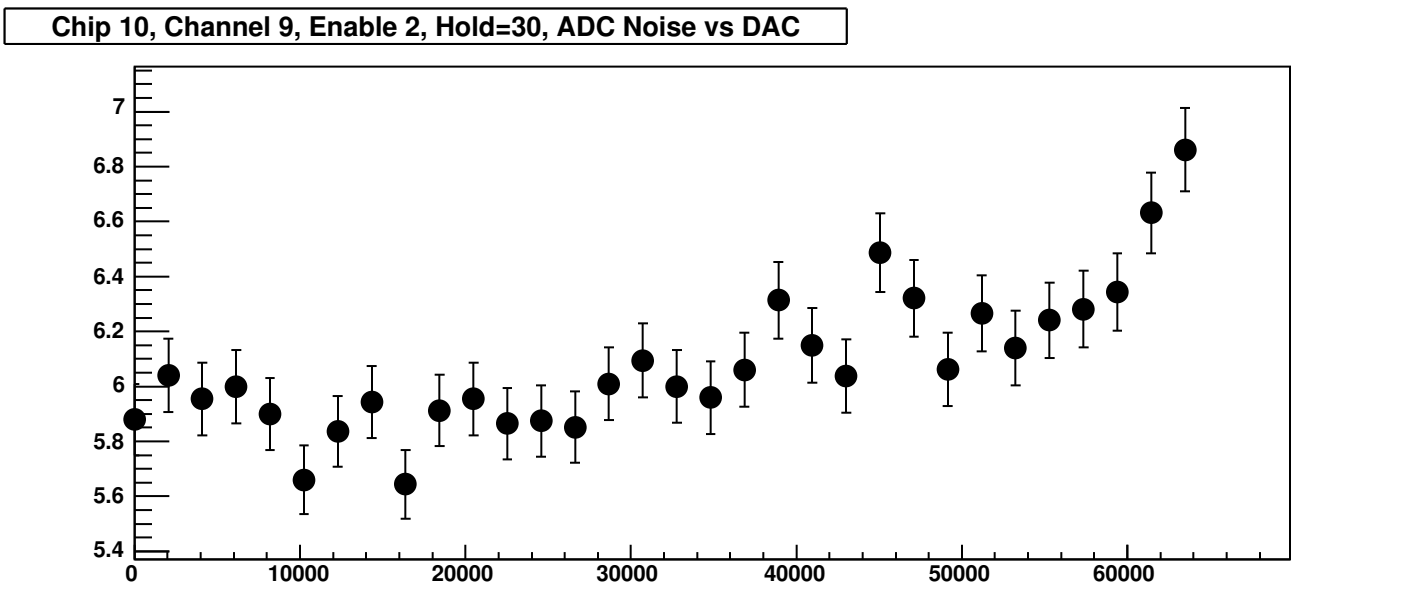
Chip 10, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC

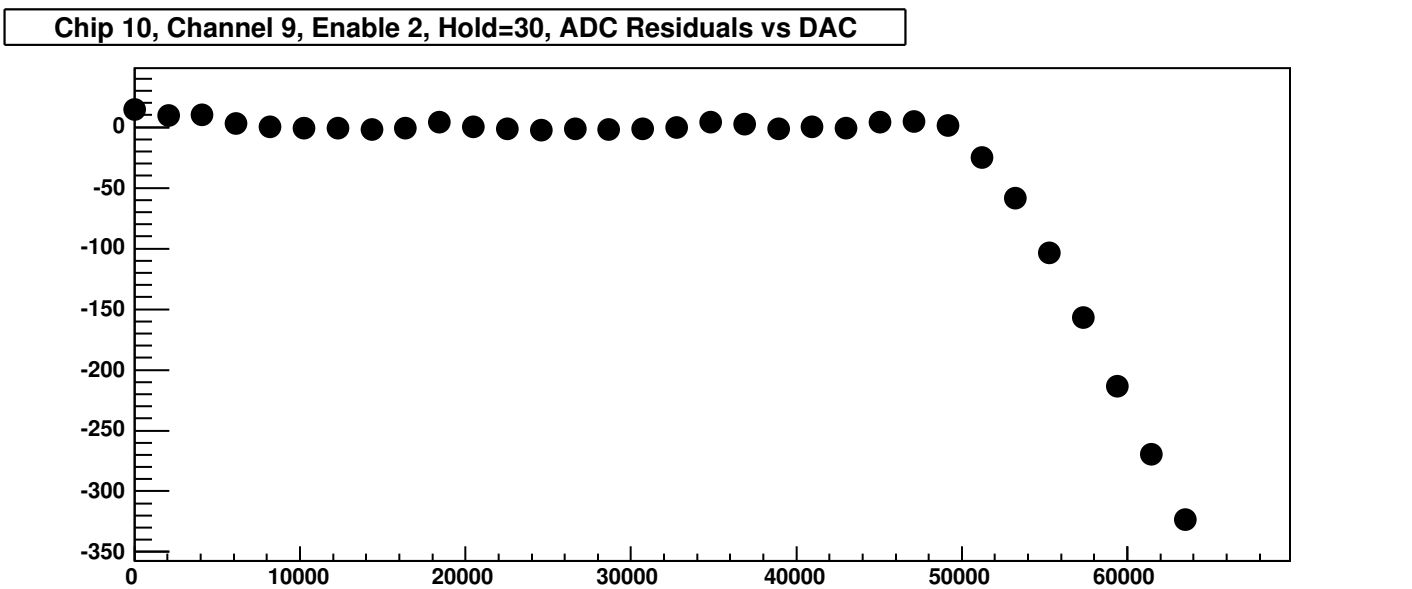


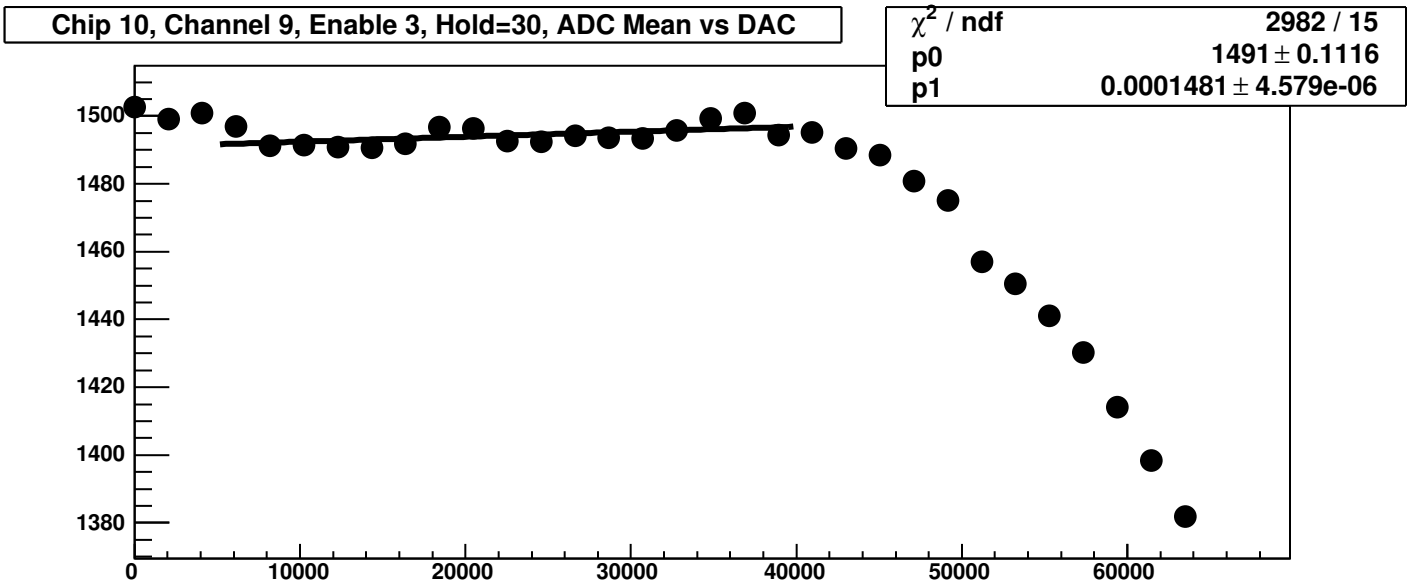
Chip 10, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

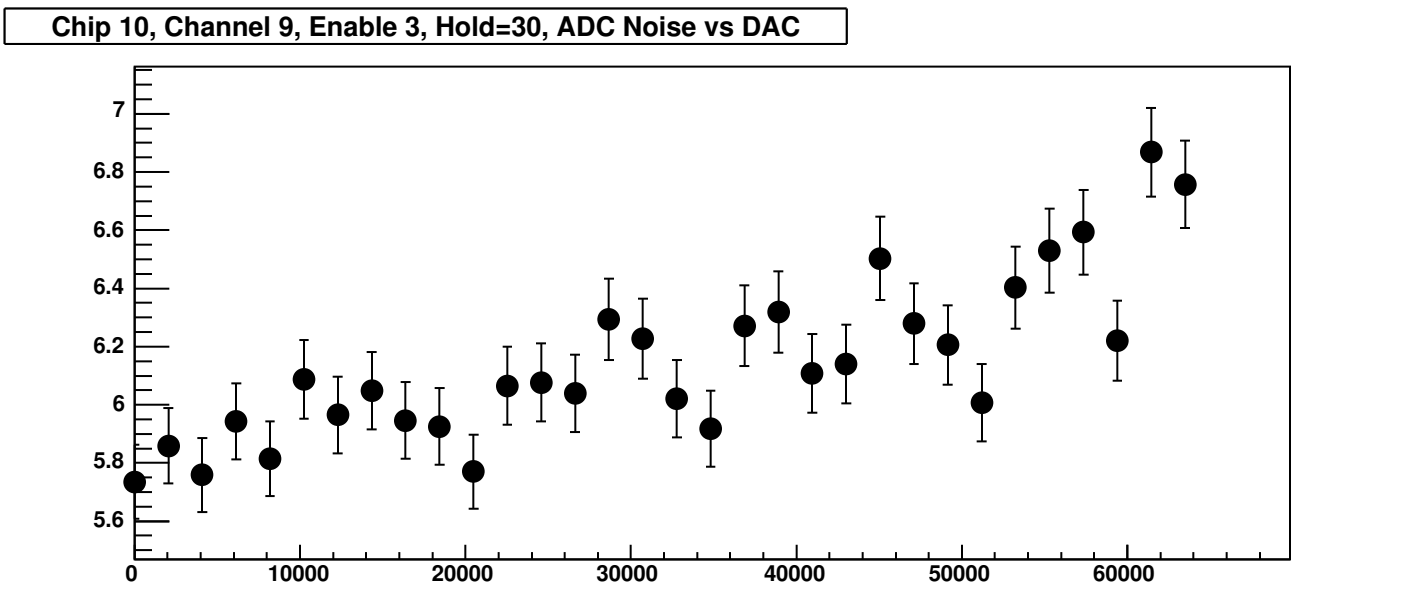


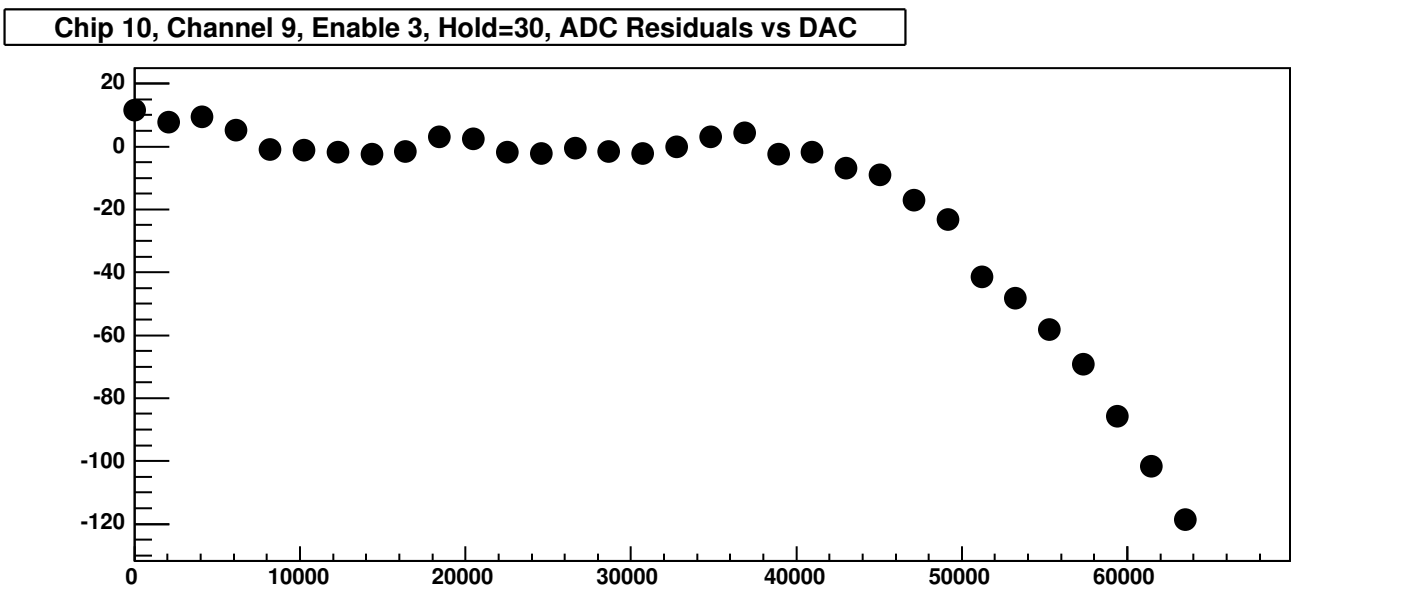

Chip 10, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2054 / 15
p0 1488 ± 0.1101
p1 -0.0006298 ± 4.522e-06
1500
1450
1400
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

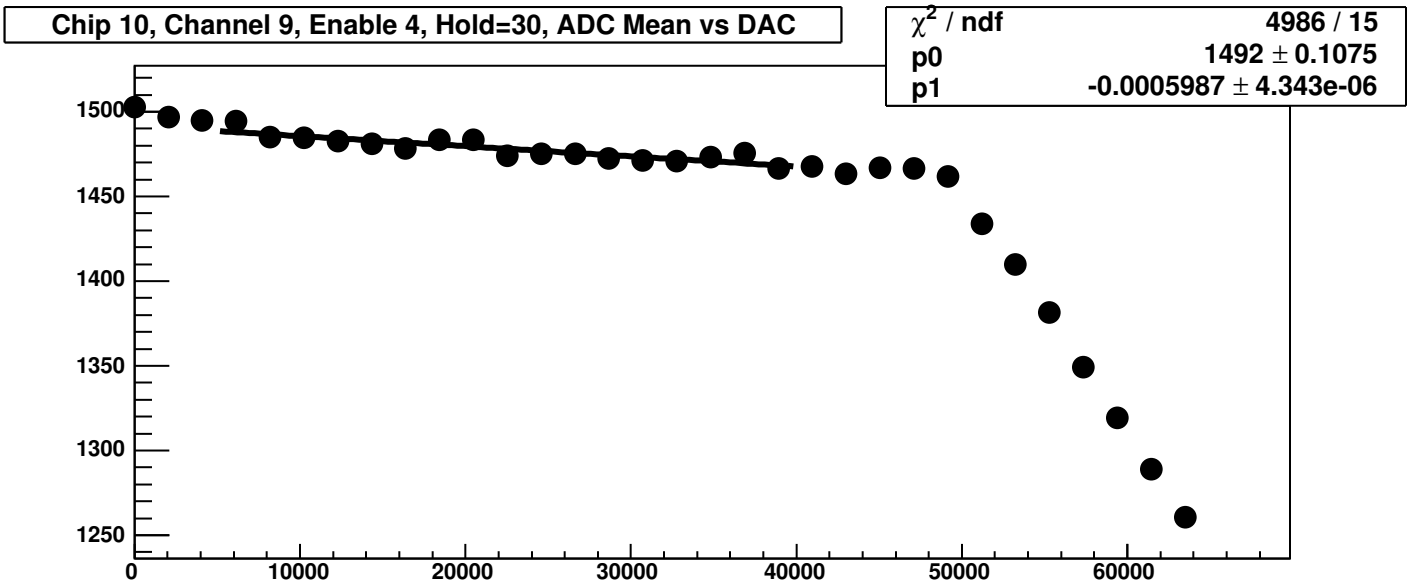

Chip 10, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

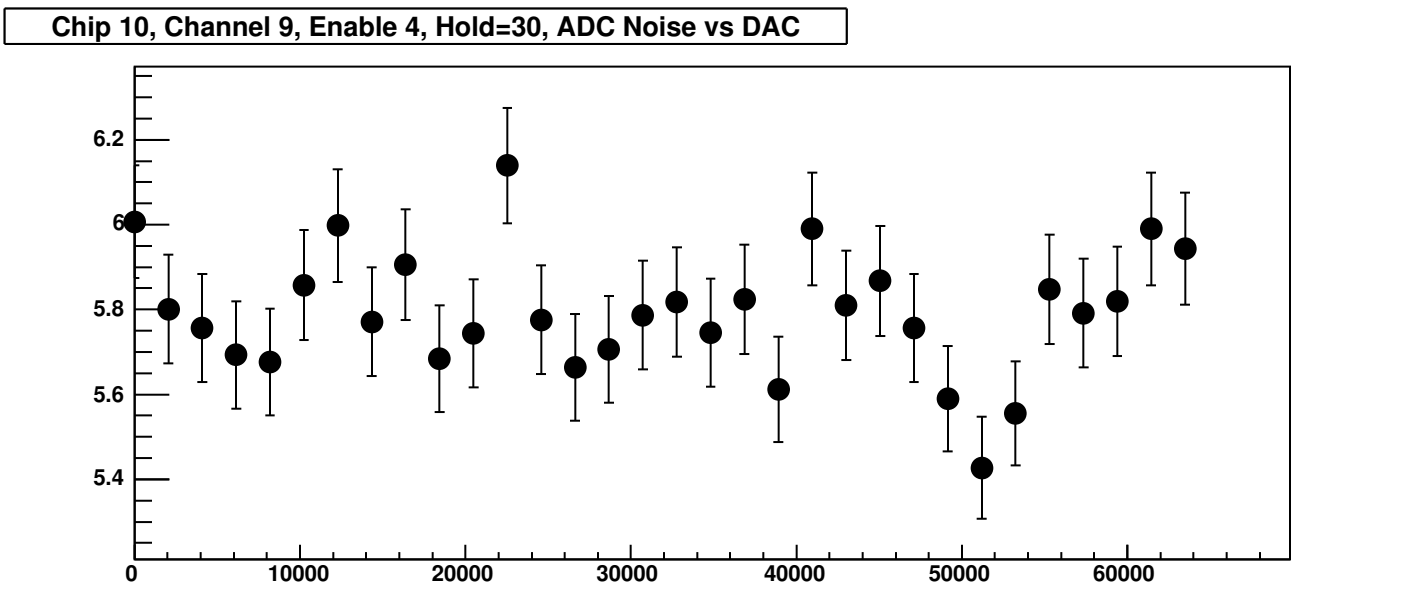

Chip 10, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

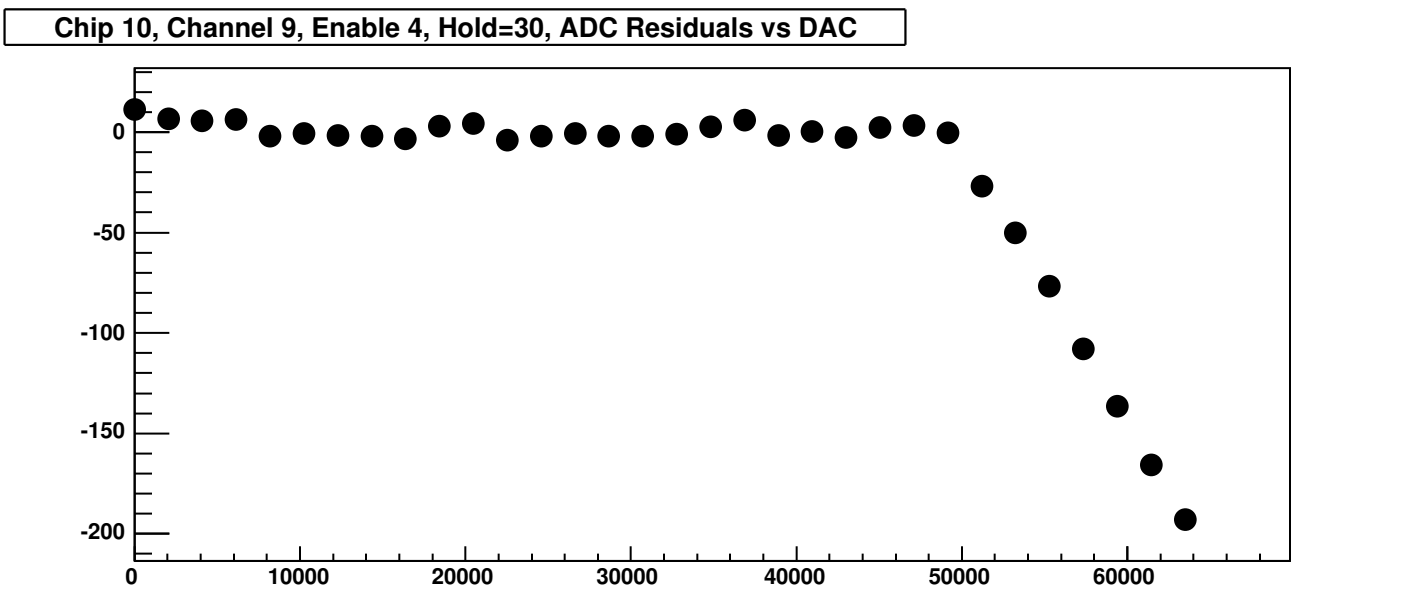

Chip 10, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2982 / 15
p0 1491 ± 0.1116
p1 0.0001481 ± 4.579e-06
1500
1480
1460
1440
1420
1400
1380
0
10000
20000
30000
40000
50000
60000

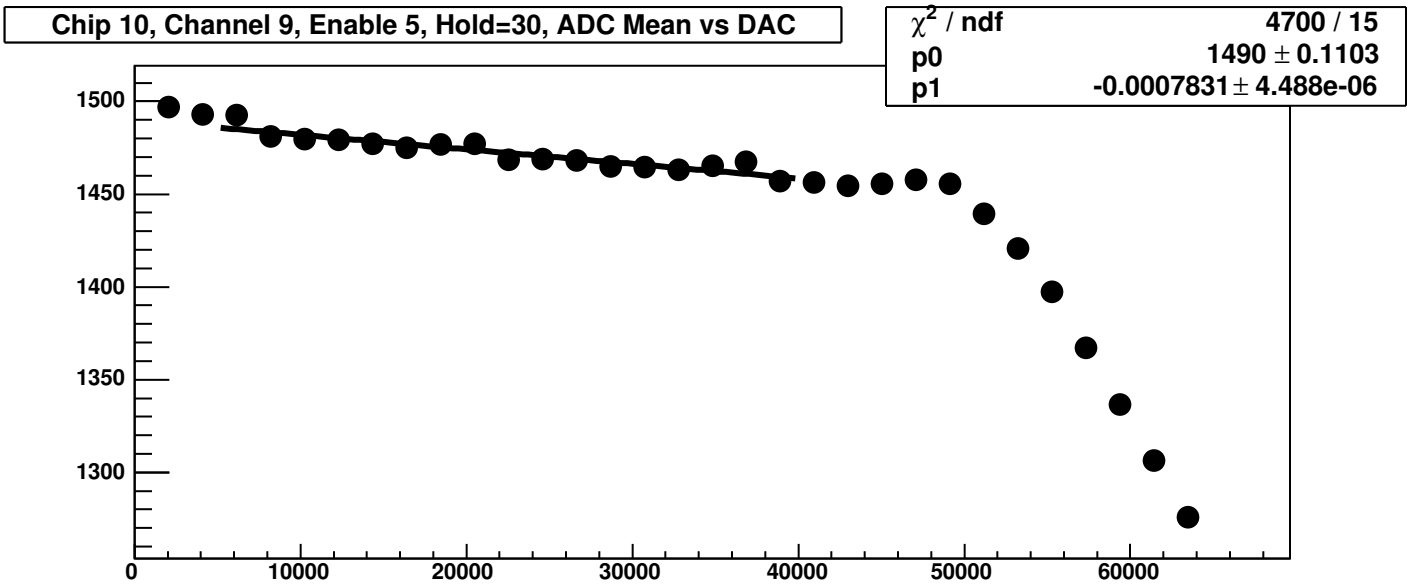

Chip 10, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

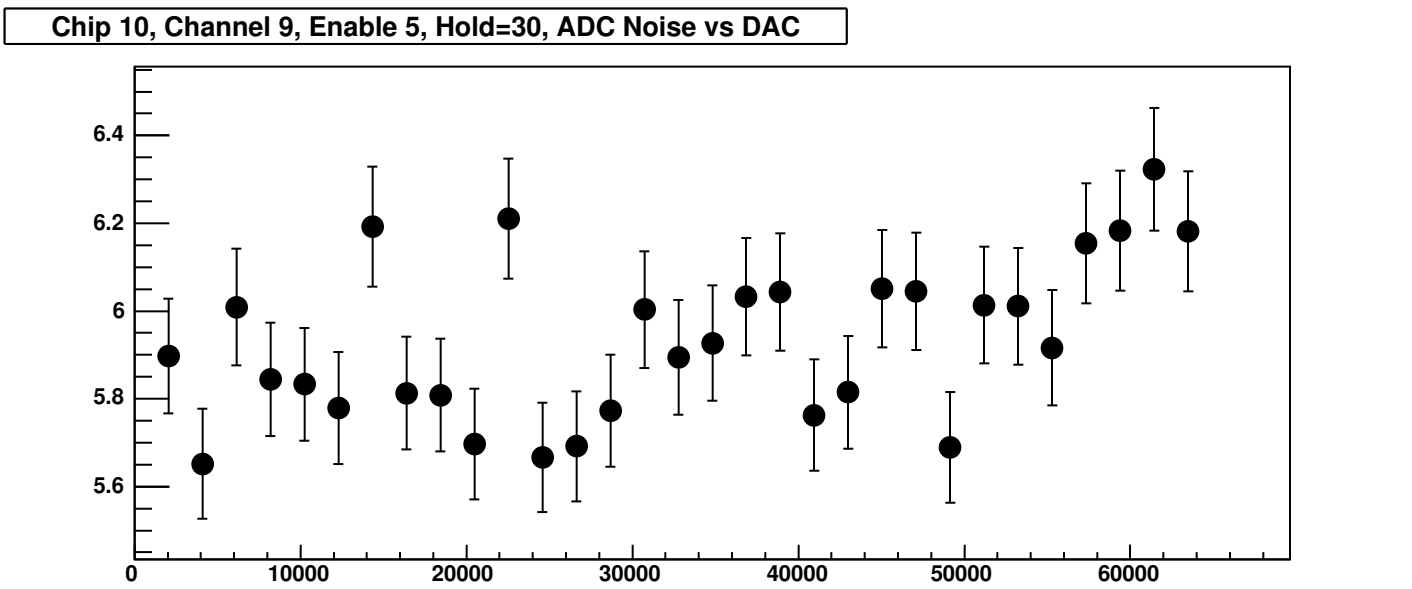

Chip 10, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

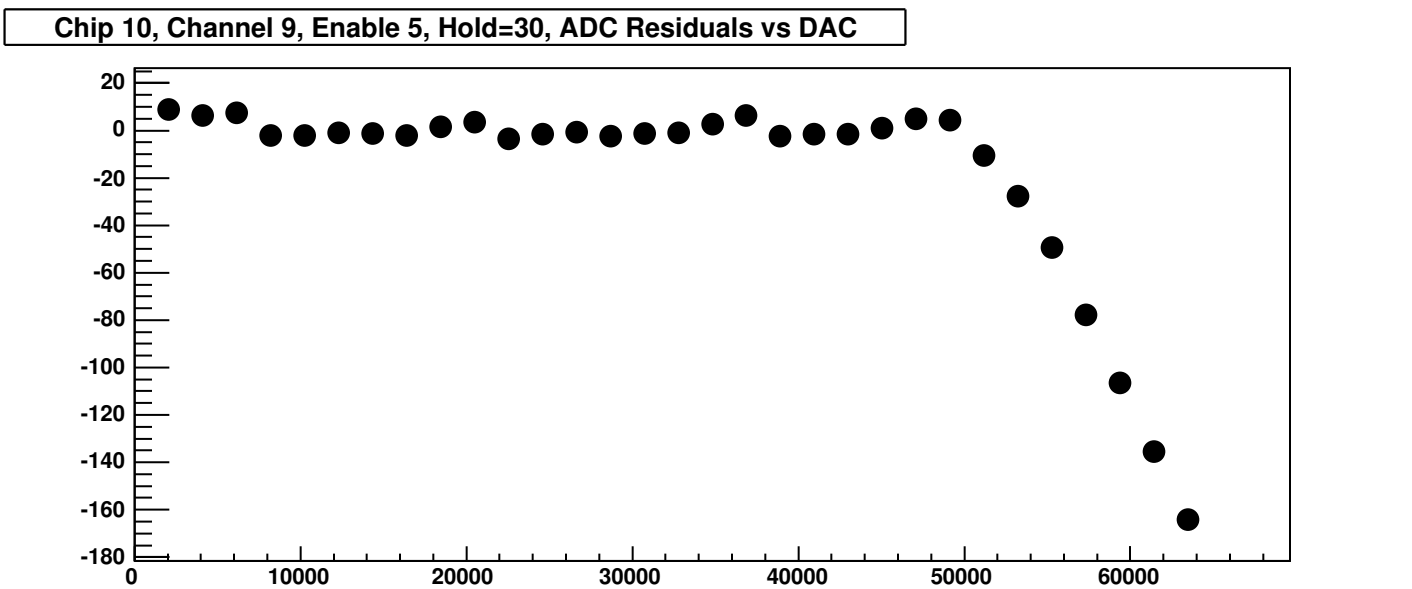

Chip 10, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4986 / 15
p0 1492 ± 0.1075
p1 -0.0005987 ± 4.343e-06
1500
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

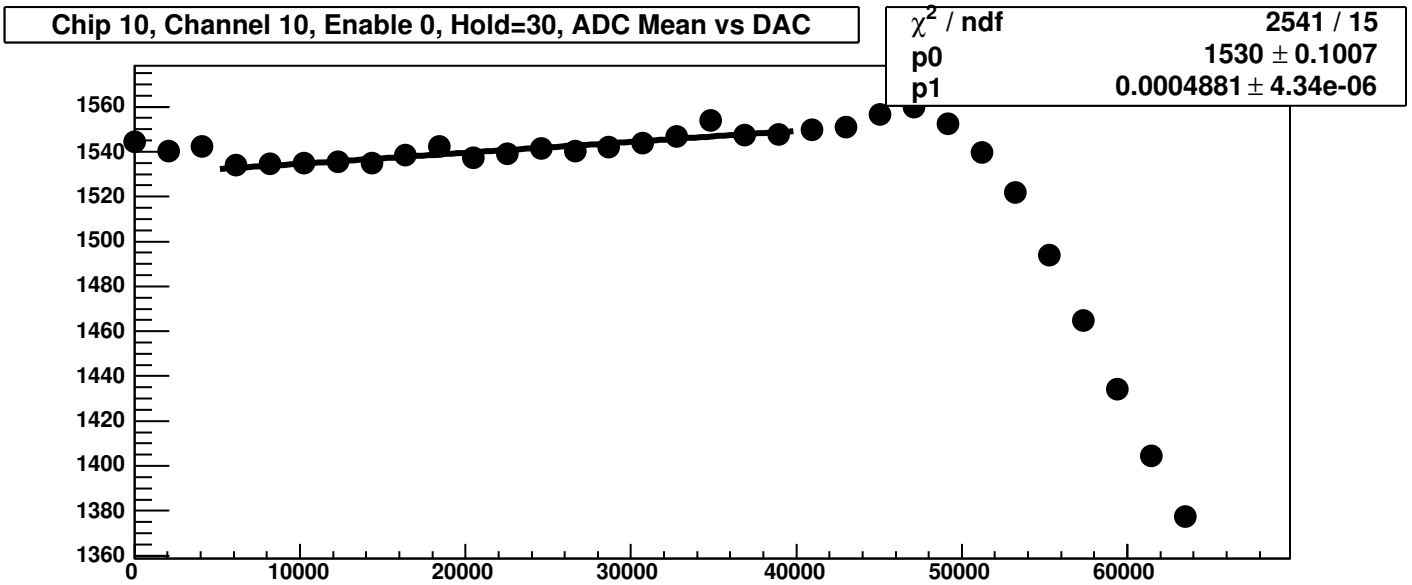

Chip 10, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

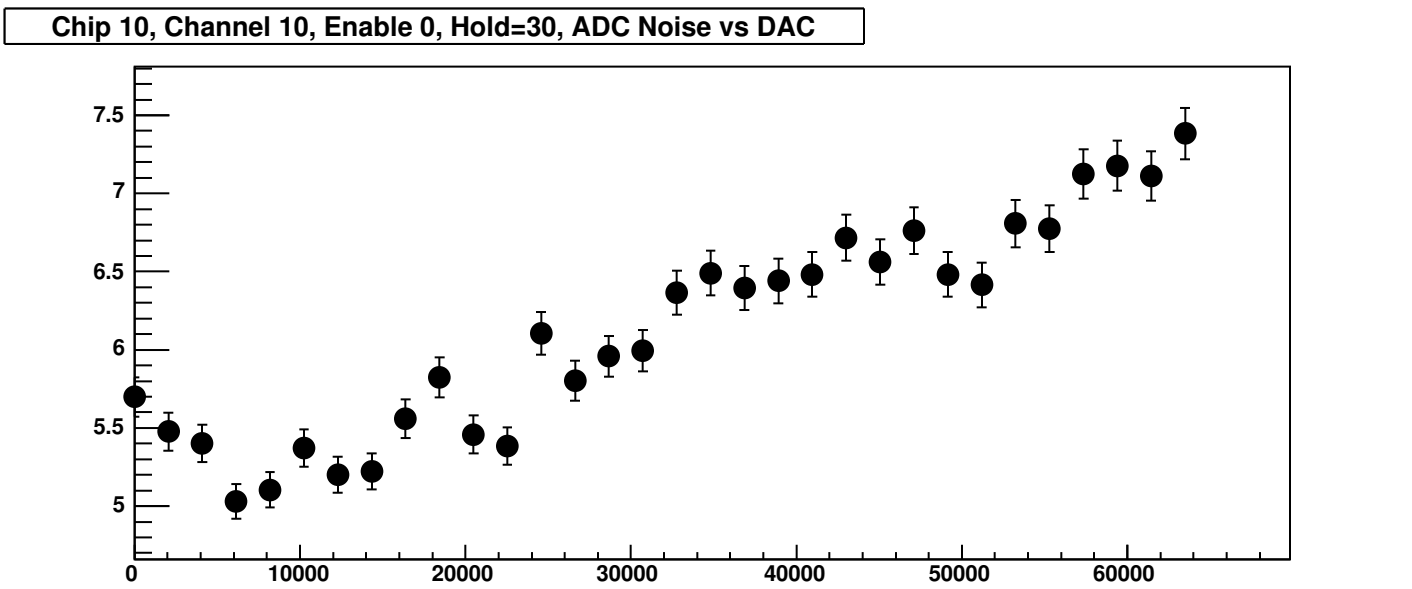

Chip 10, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

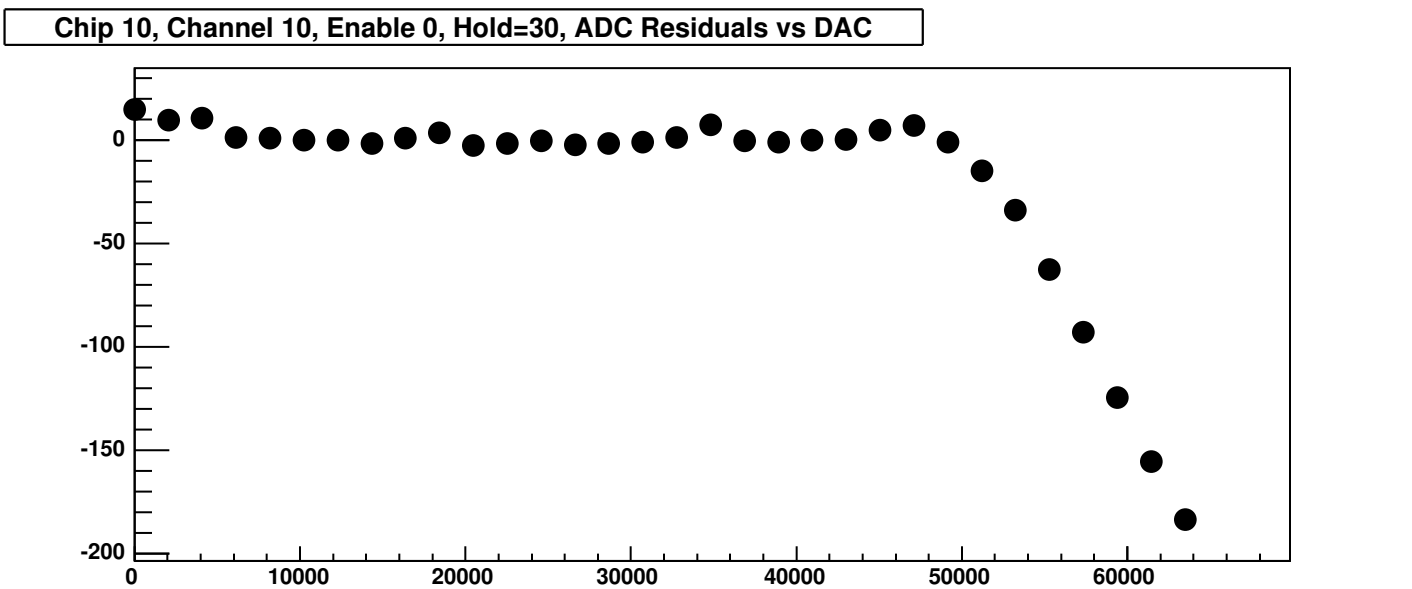

Chip 10, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4700 / 15
p0 1490 ± 0.1103
p1 -0.0007831 ± 4.488e-06
1500
1450
1400
1350
1300
0
10000
20000
30000
40000
50000
60000

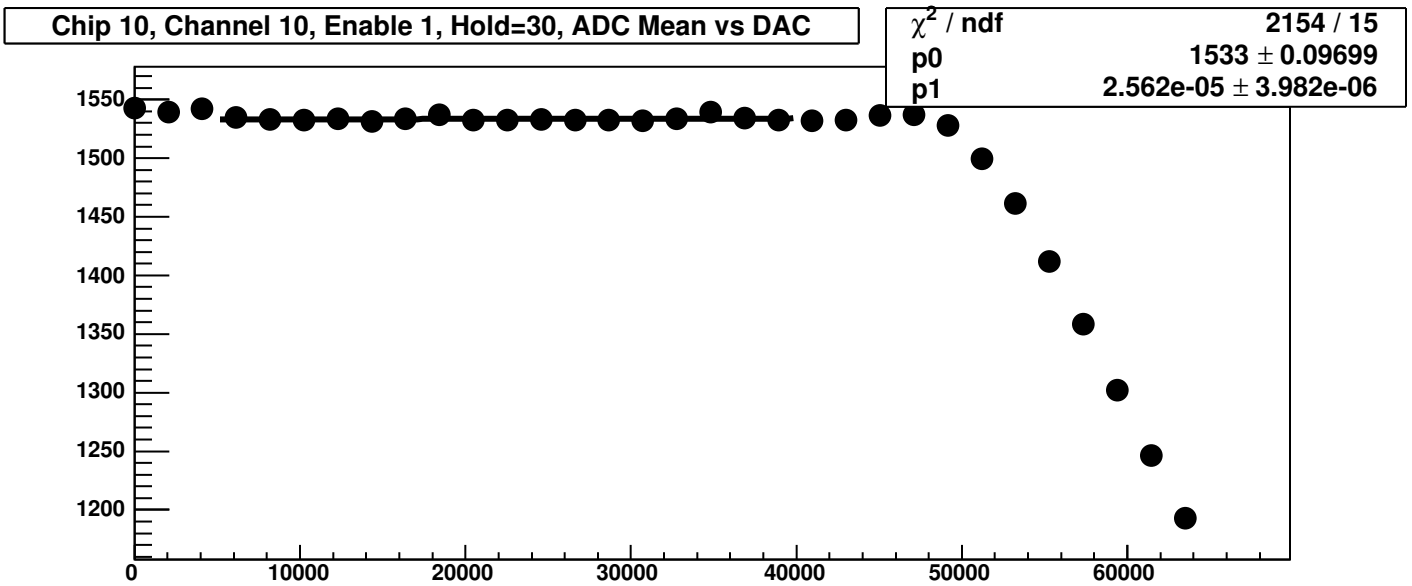

Chip 10, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

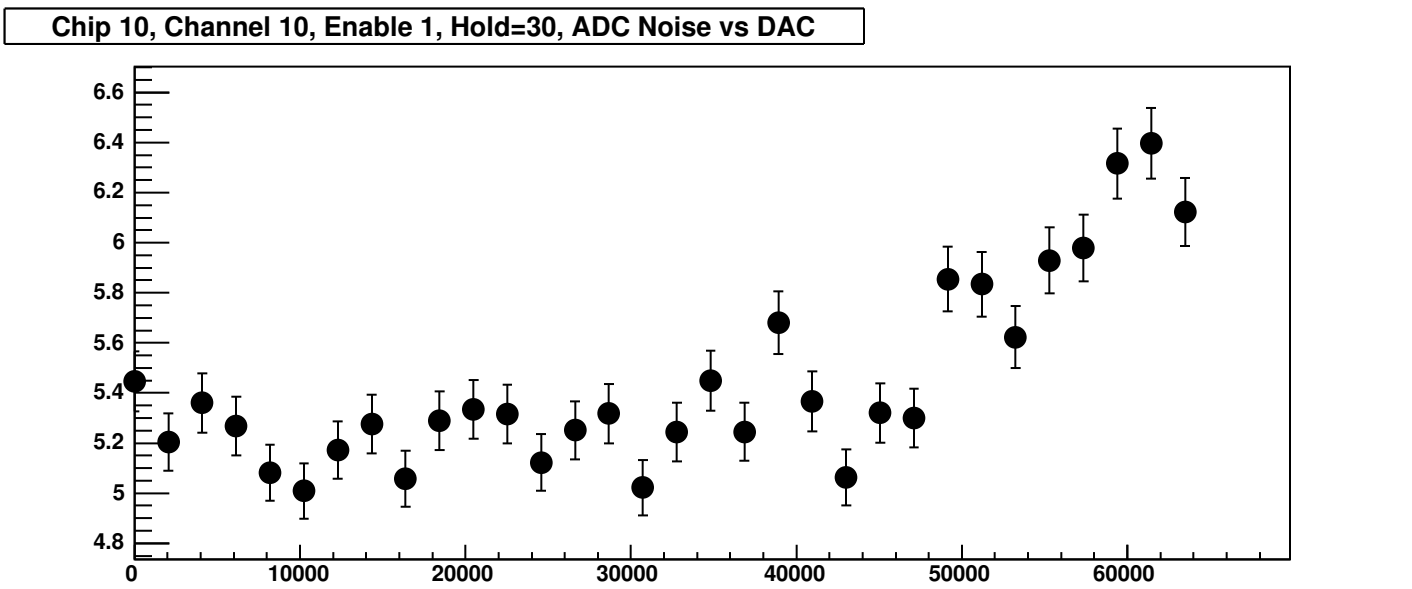

Chip 10, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

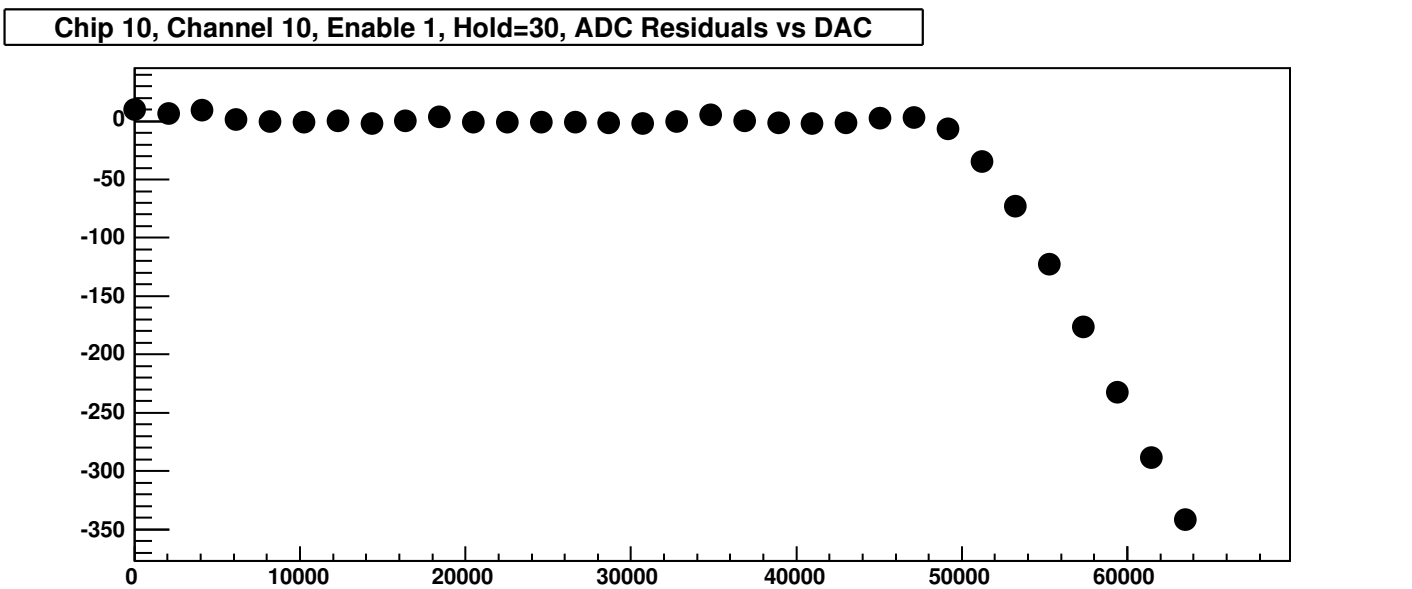

Chip 10, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2541 / 15
p0 1530 ± 0.1007
p1 0.0004881 ± 4.34e-06
1560
1540
1520
1500
1480
1460
1440
1420
1400
1380
1360
0
10000
20000
30000
40000
50000
60000

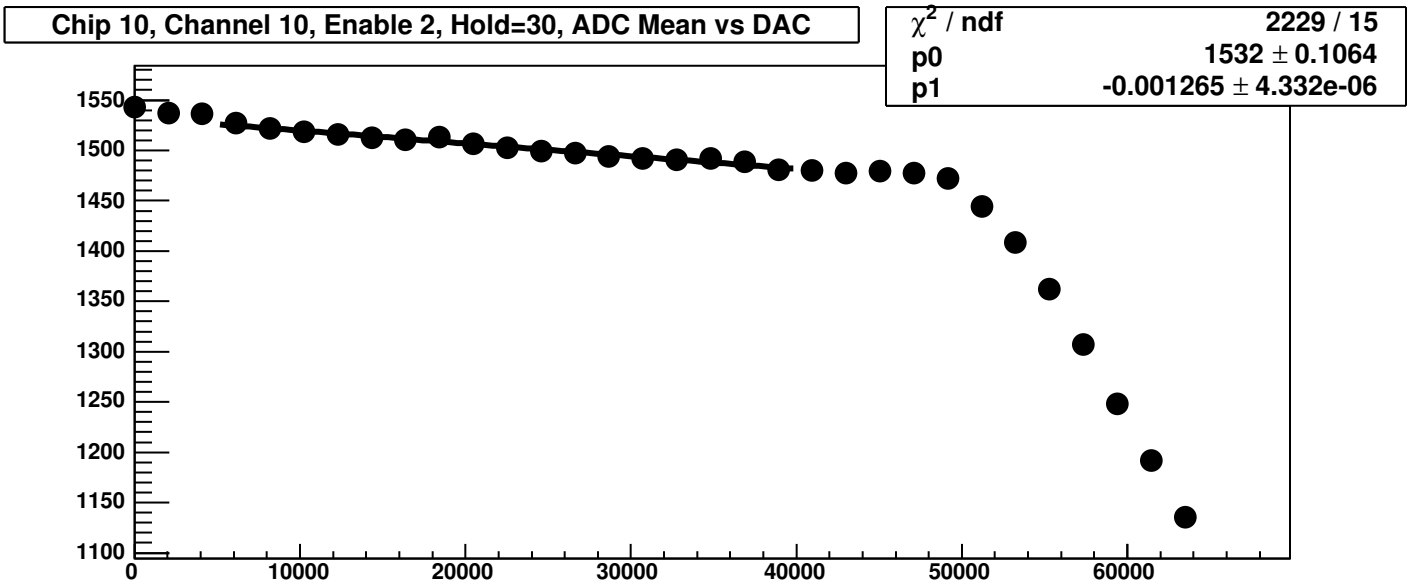

Chip 10, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
5
0
10000
20000
30000
40000
50000
60000

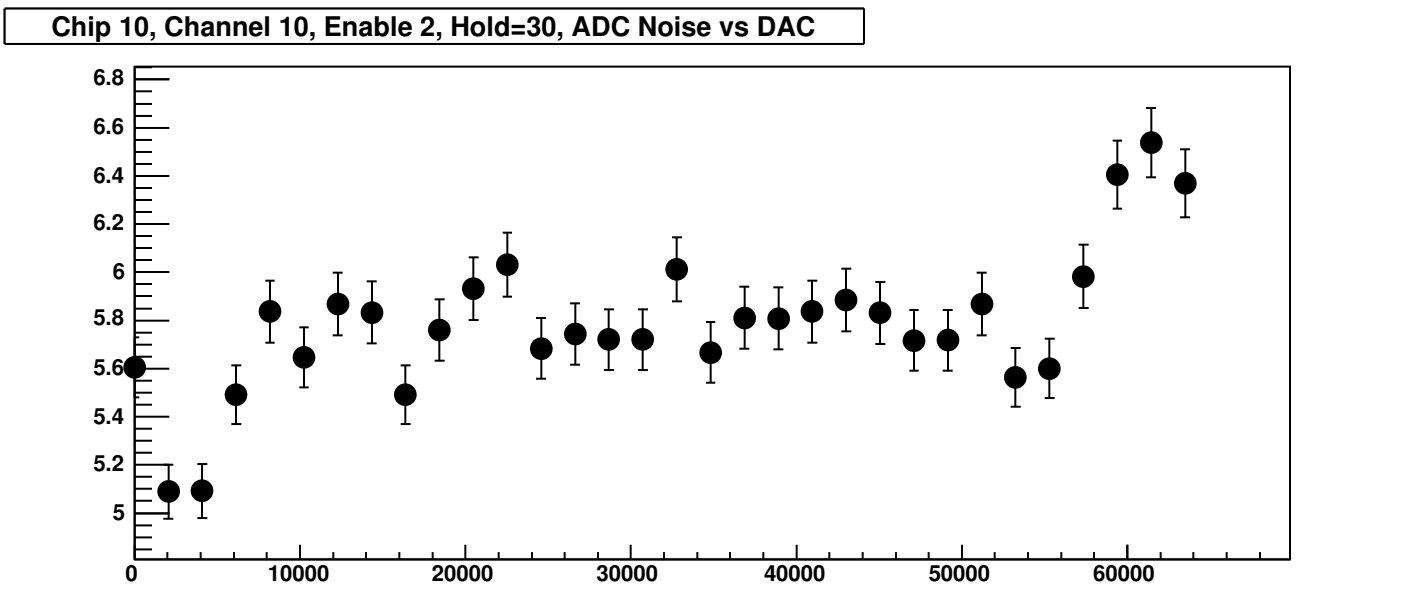

Chip 10, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

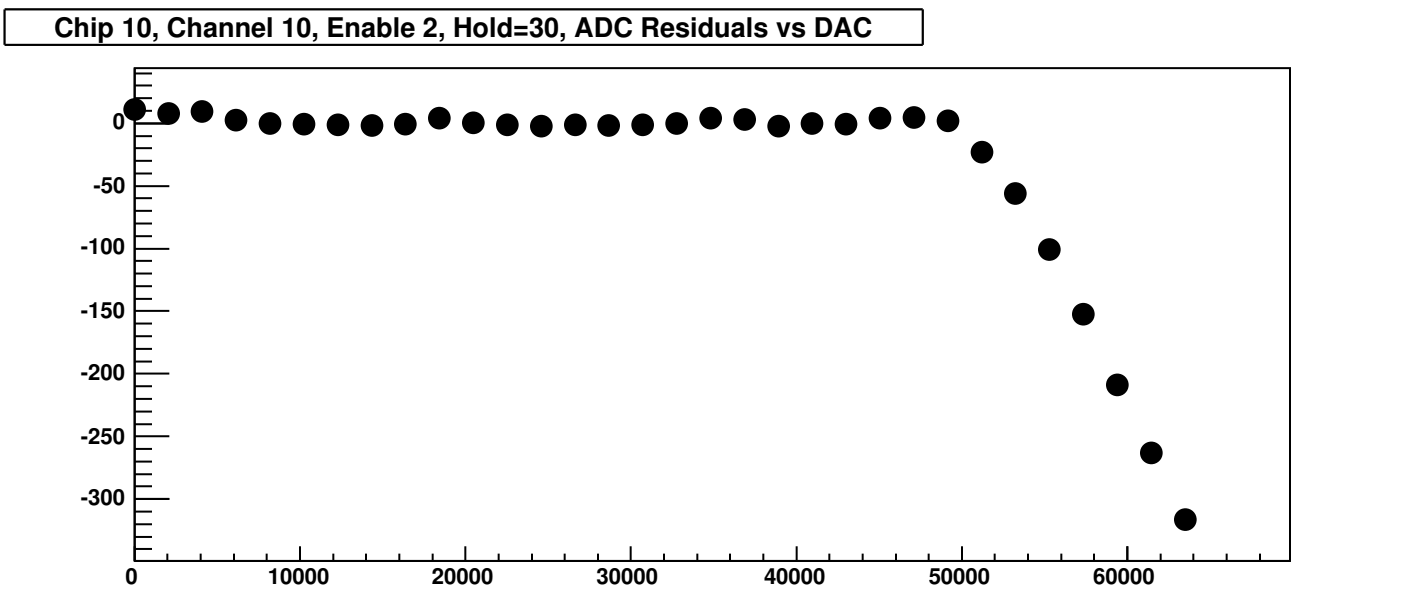

Chip 10, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2154 / 15
p0 1533 ± 0.09699
p1 2.562e-05 ± 3.982e-06
1550
1500
1450
1400
1350
1300
1250
1200
0
10000
20000
30000
40000
50000
60000

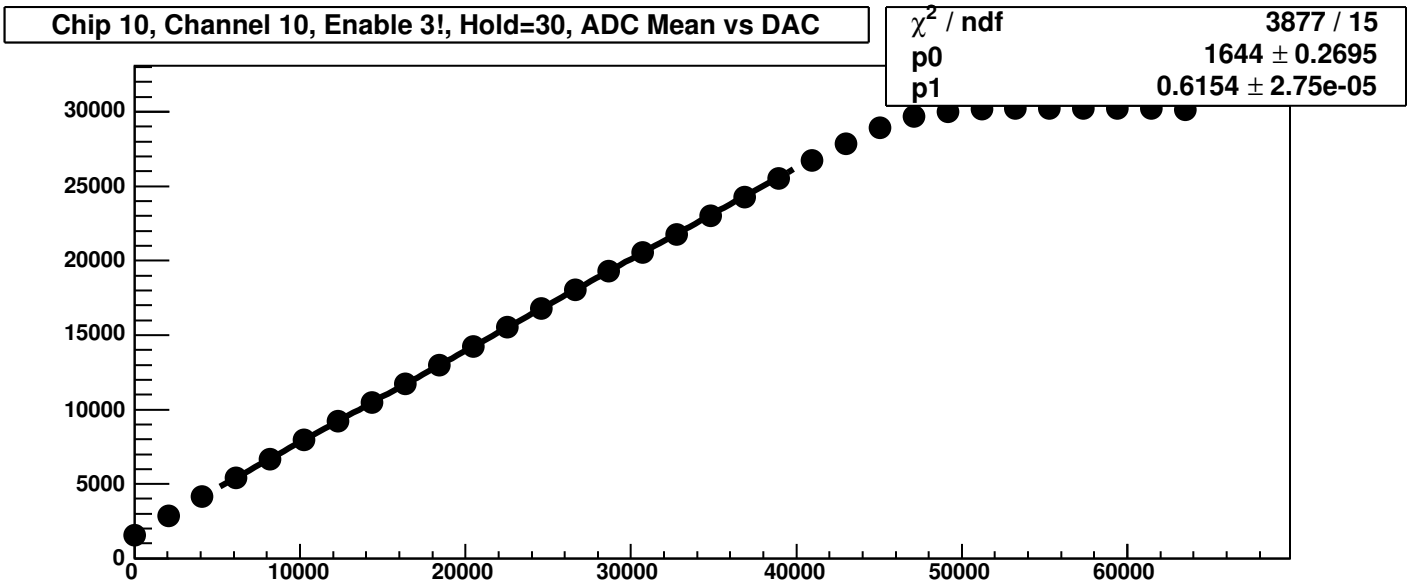

Chip 10, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
5
4.8
0
10000
20000
30000
40000
50000
60000

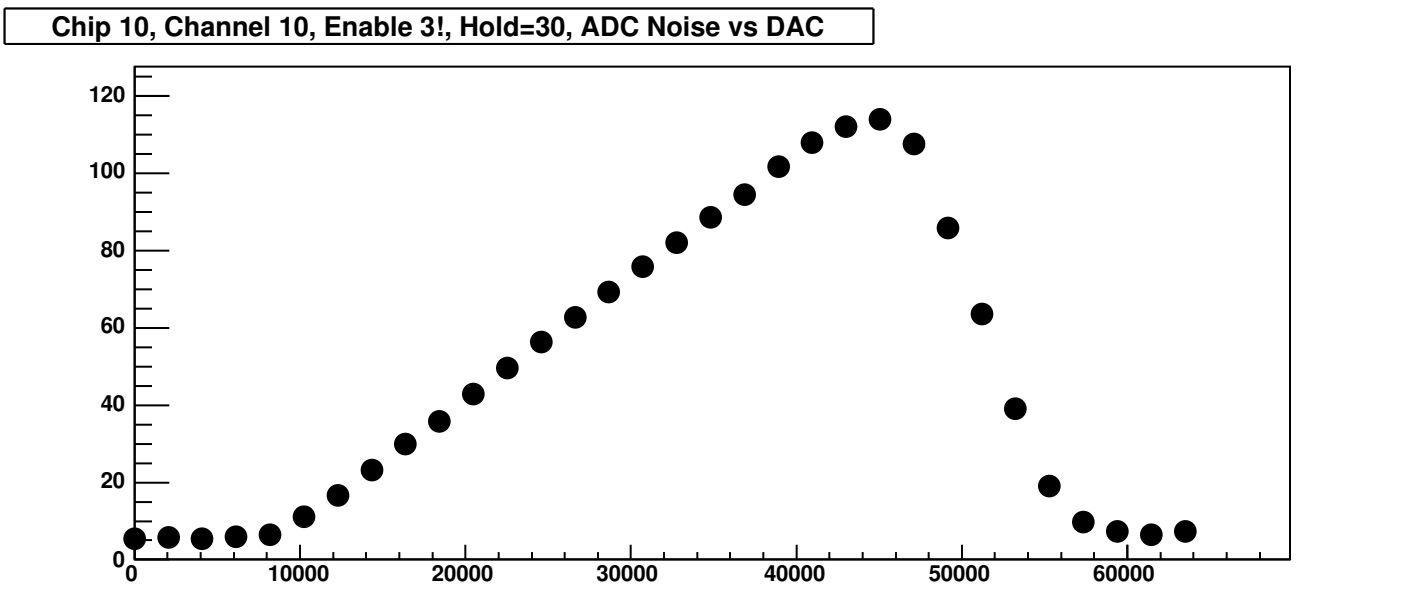

Chip 10, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

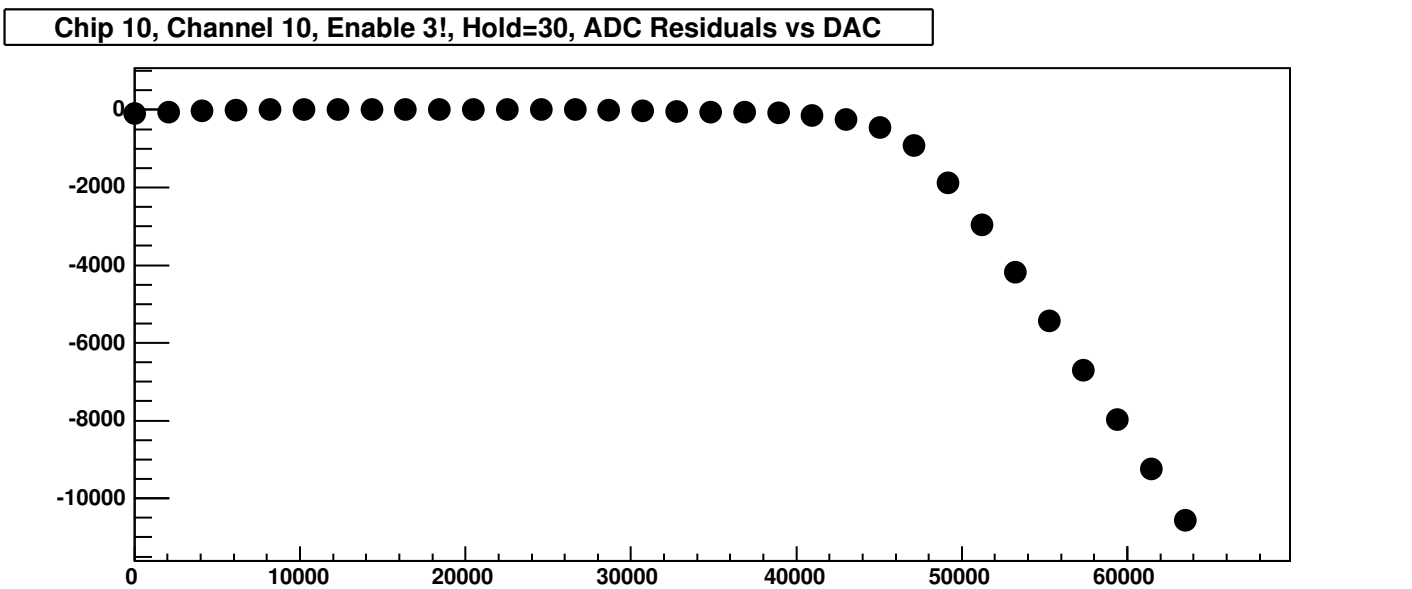

Chip 10, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2229 / 15
p0 1532 ± 0.1064
p1 -0.001265 ± 4.332e-06
1550
1500
1450
1400
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

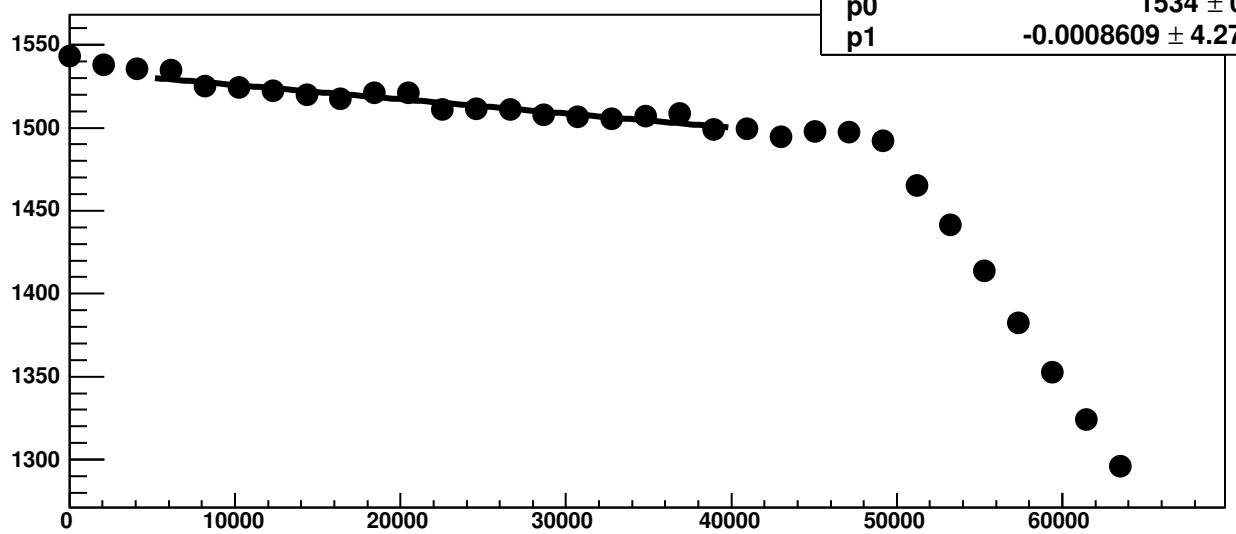

Chip 10, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 3877 / 15
p0 1644 ± 0.2695
p1 0.6154 ± 2.75e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

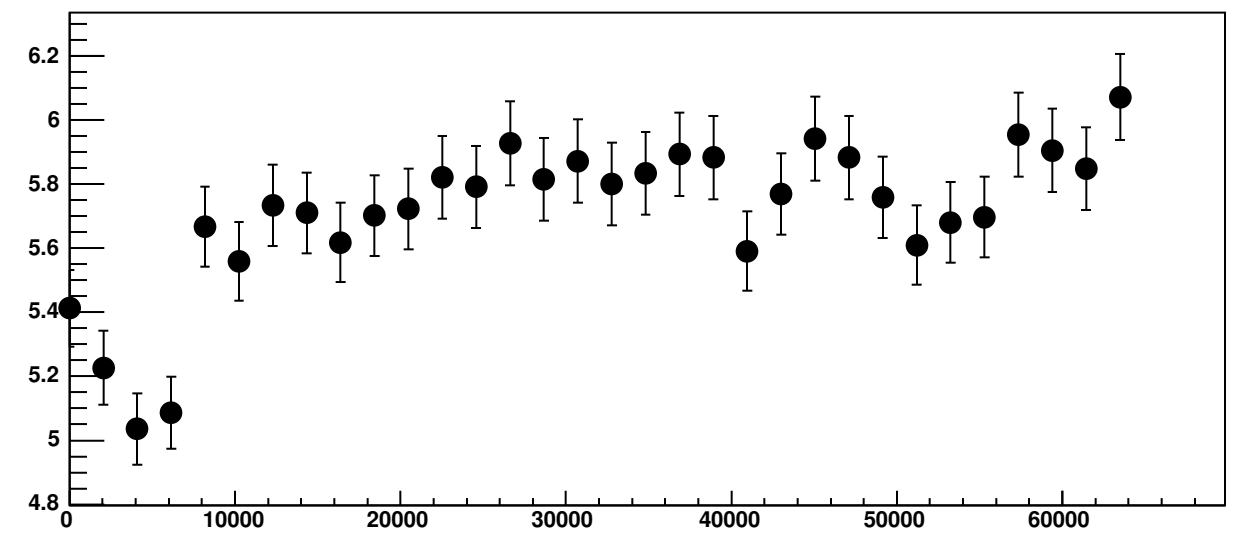

Chip 10, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

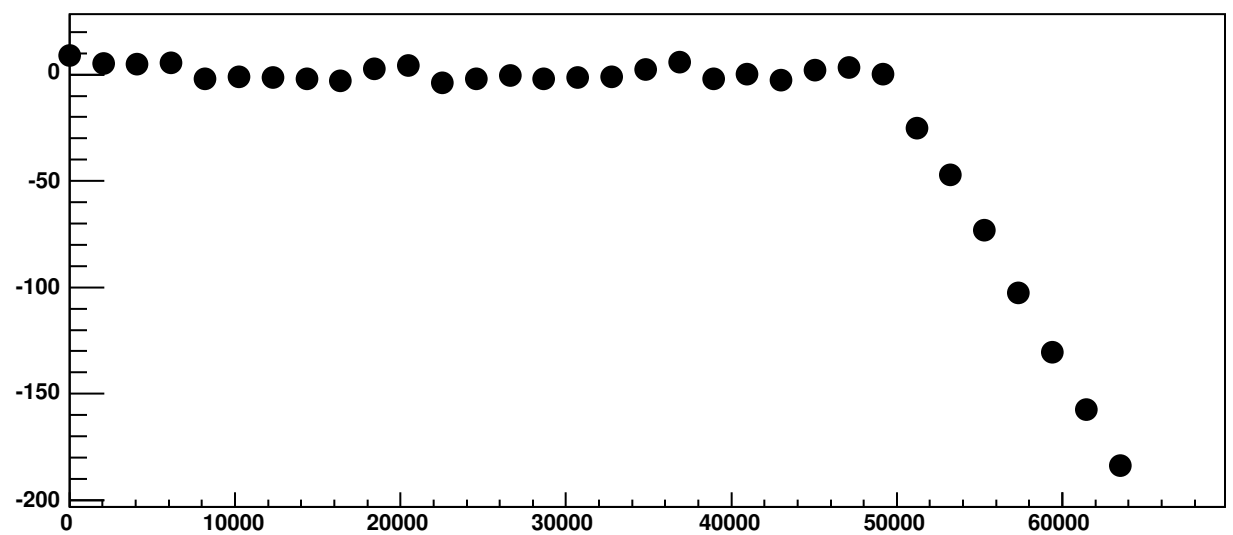
Chip 10, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

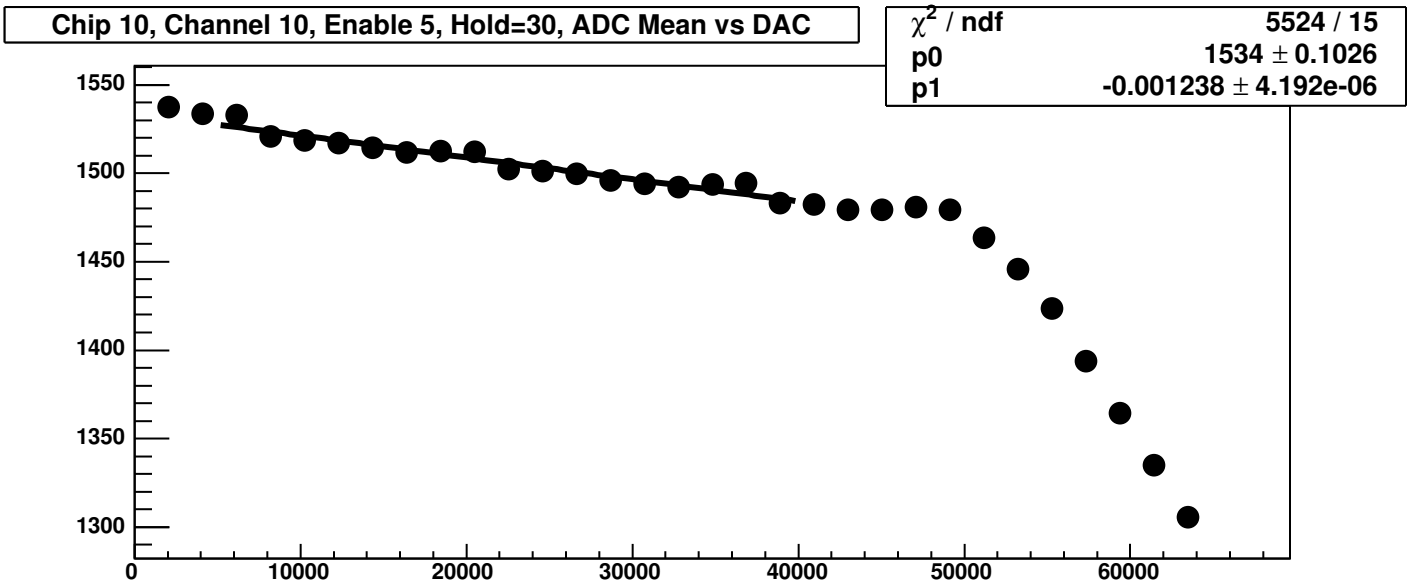


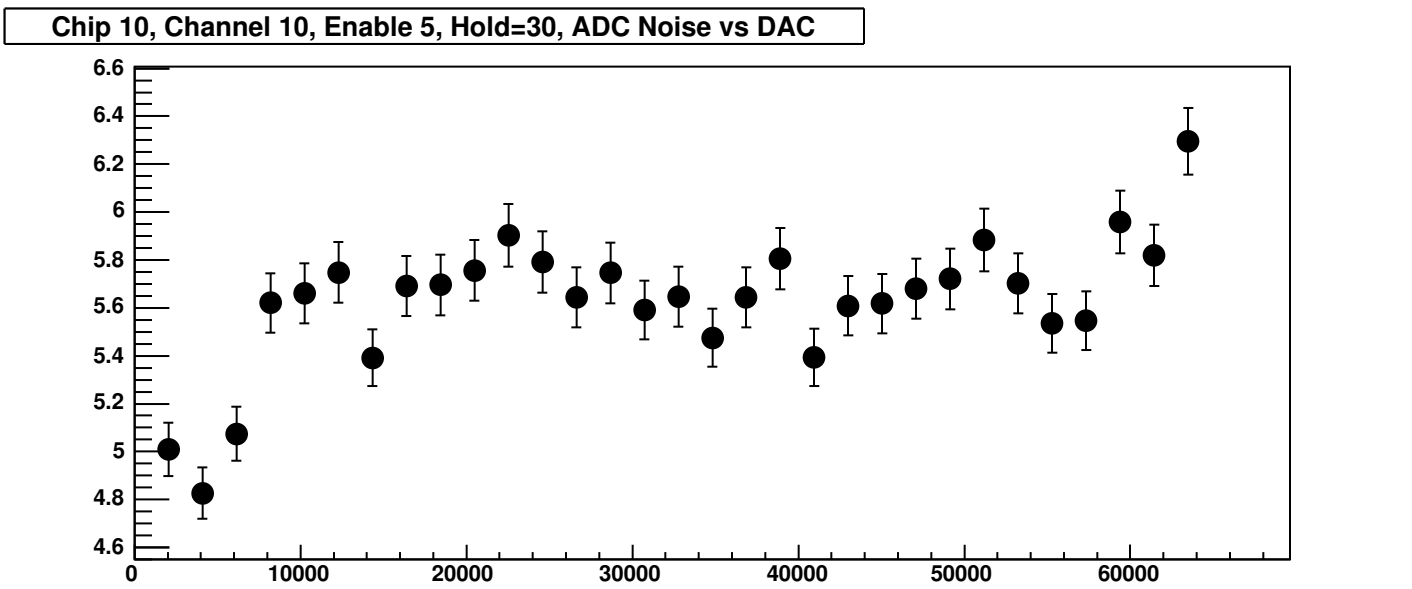
Chip 10, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC

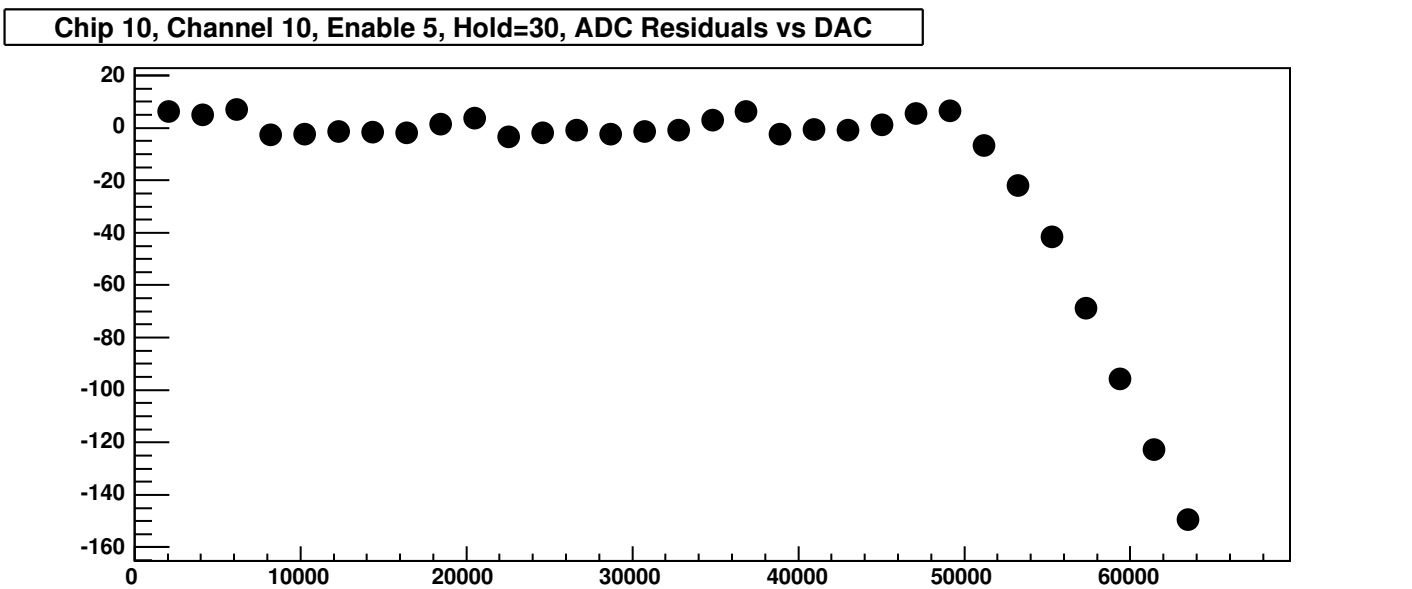


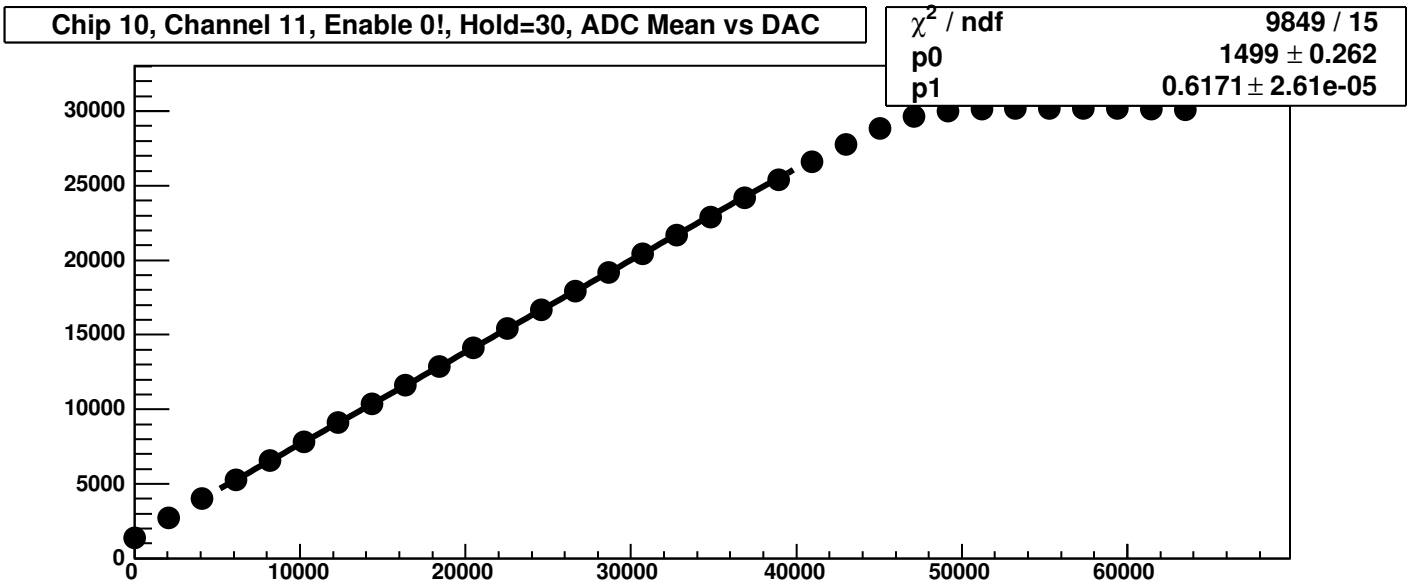
Chip 10, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC

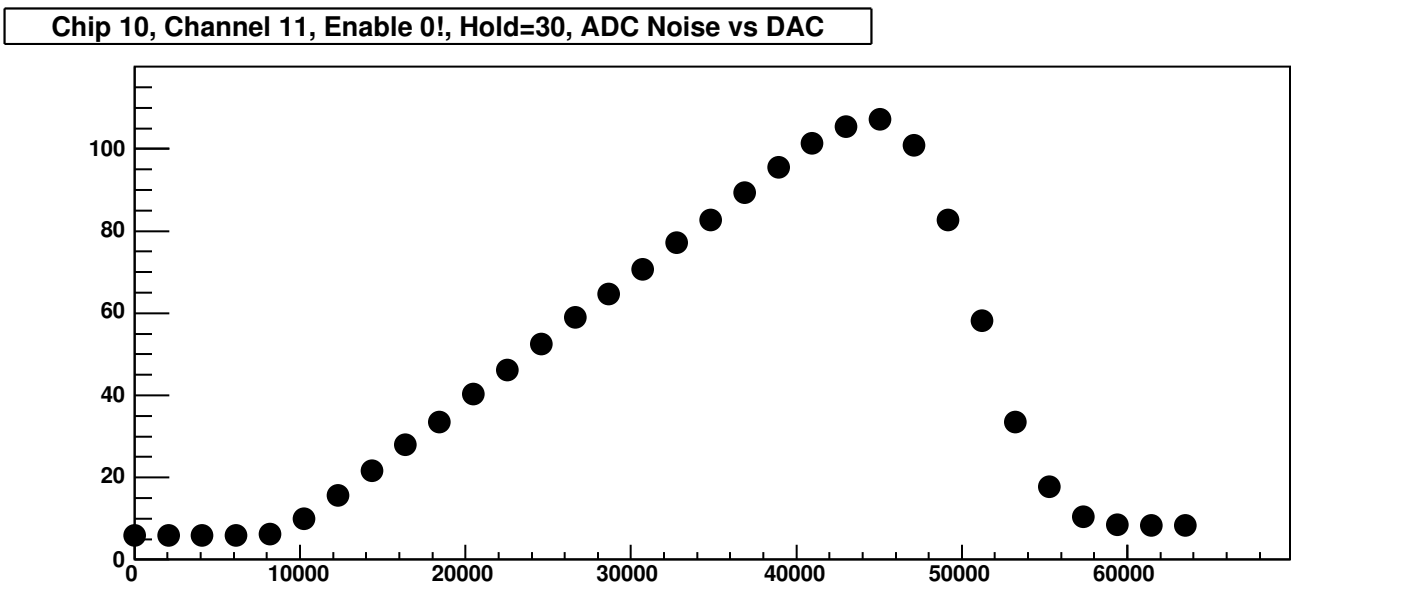


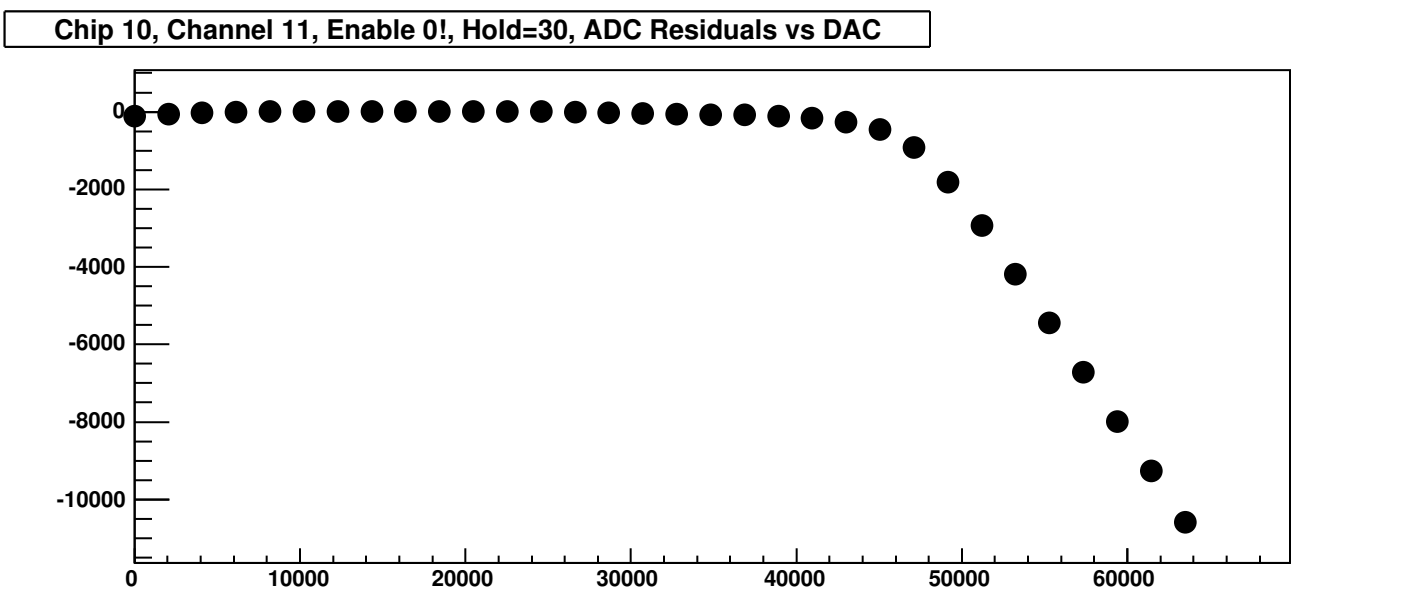

Chip 10, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5524 / 15
p0 1534 ± 0.1026
p1 -0.001238 ± 4.192e-06
1550
1500
1450
1400
1350
1300
0
10000
20000
30000
40000
50000
60000

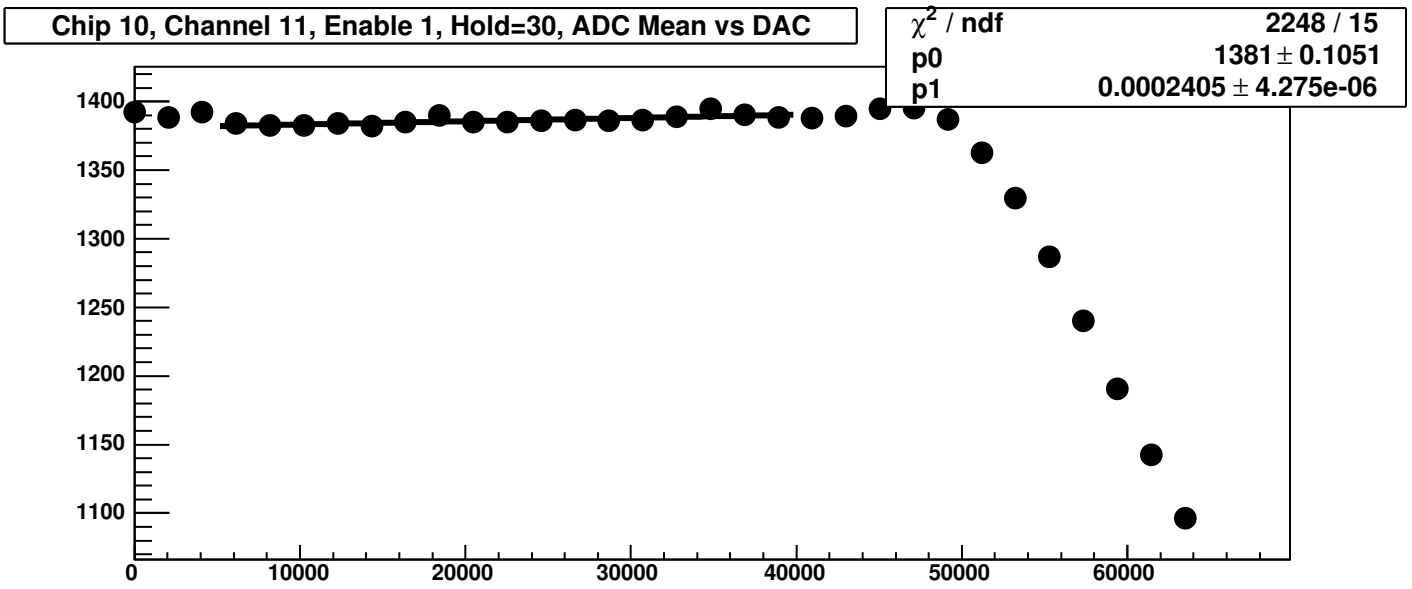

Chip 10, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
5
4.8
4.6
0
10000
20000
30000
40000
50000
60000

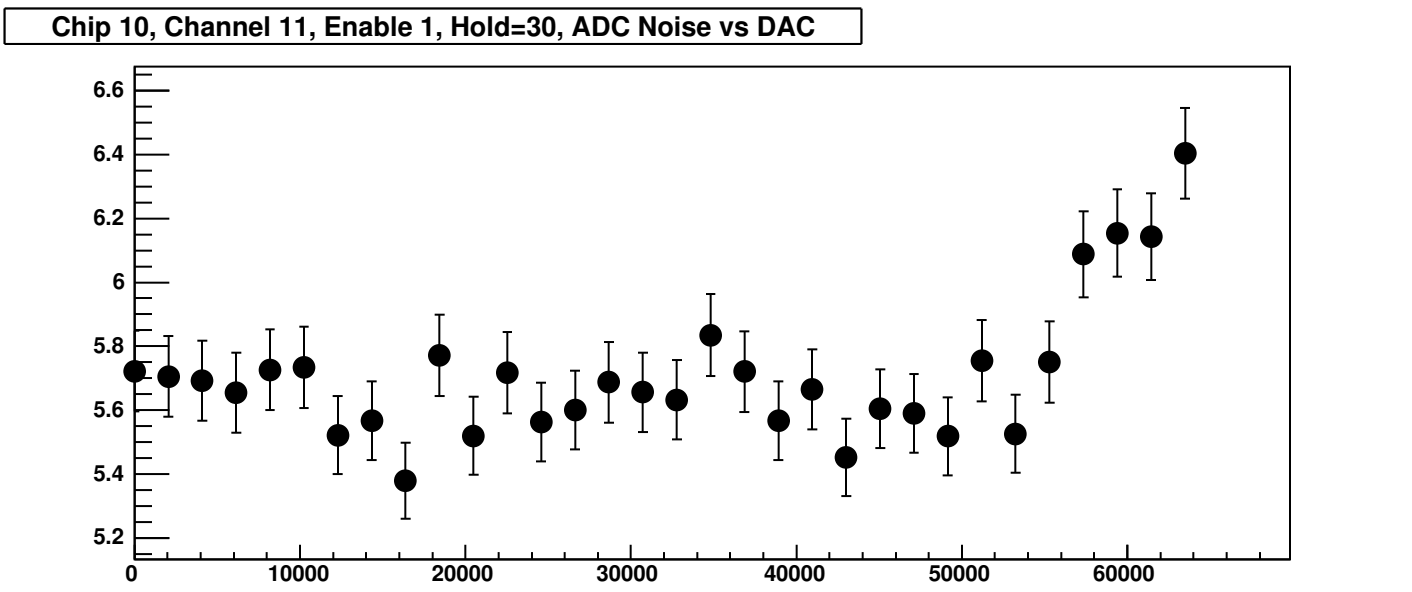

Chip 10, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

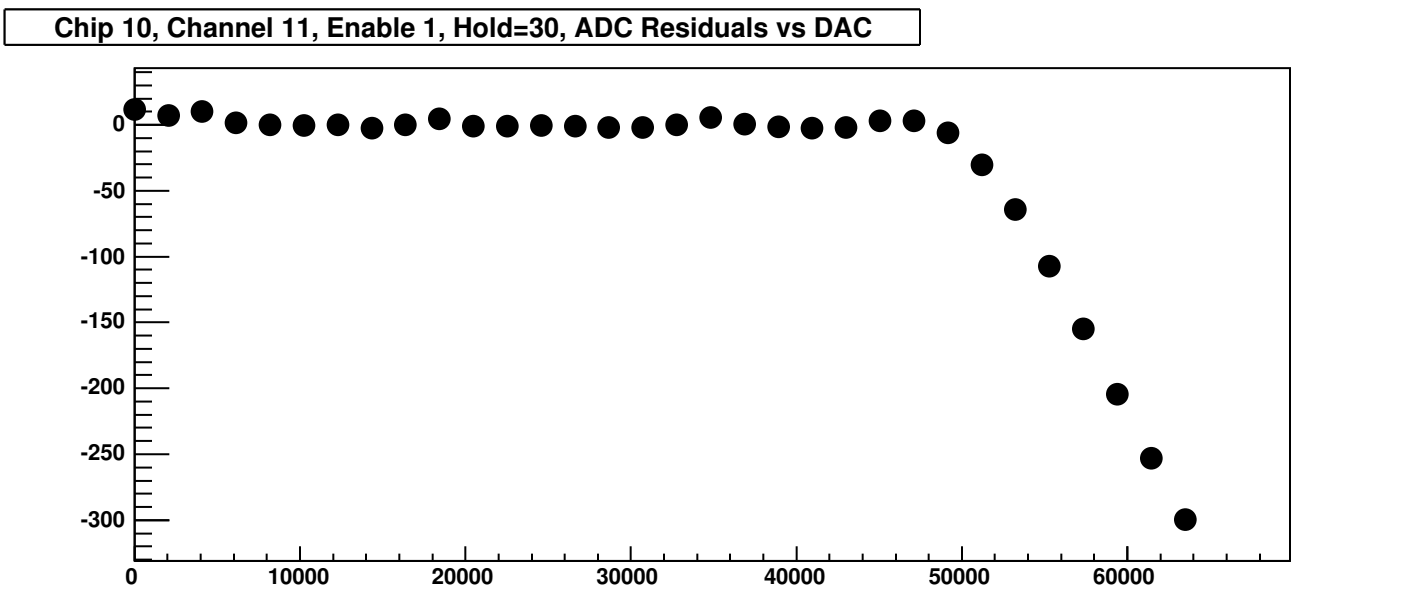

Chip 10, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 9849 / 15
p0 1499 ± 0.262
p1 0.6171 ± 2.61e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

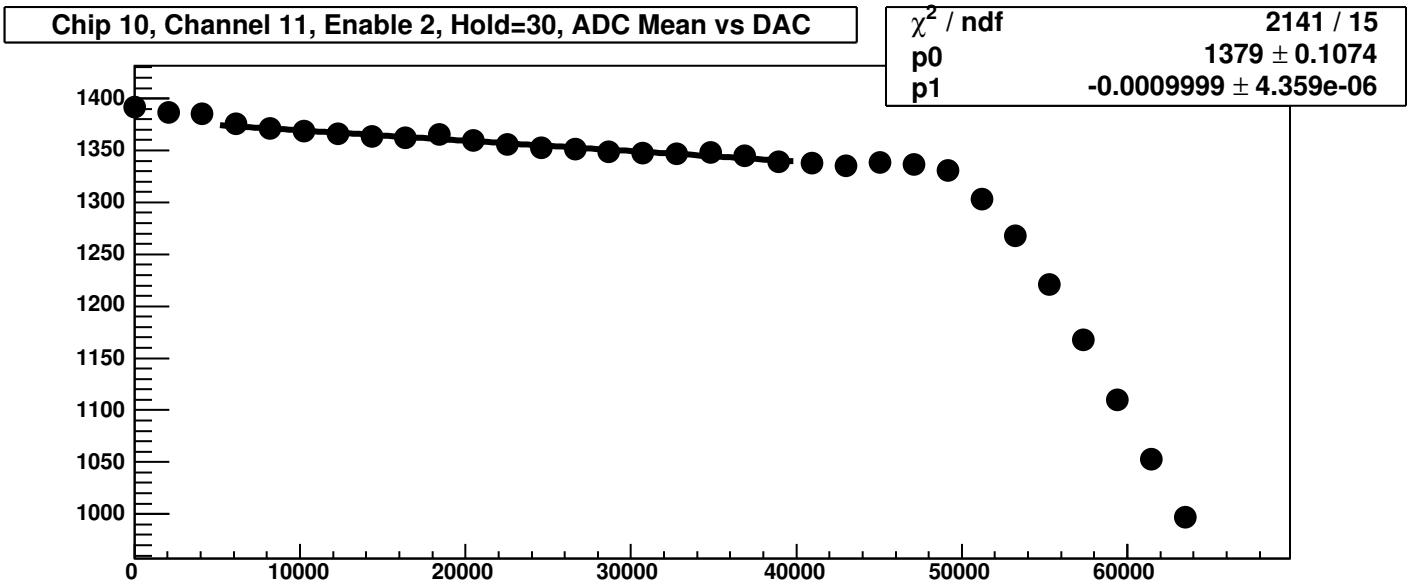

Chip 10, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

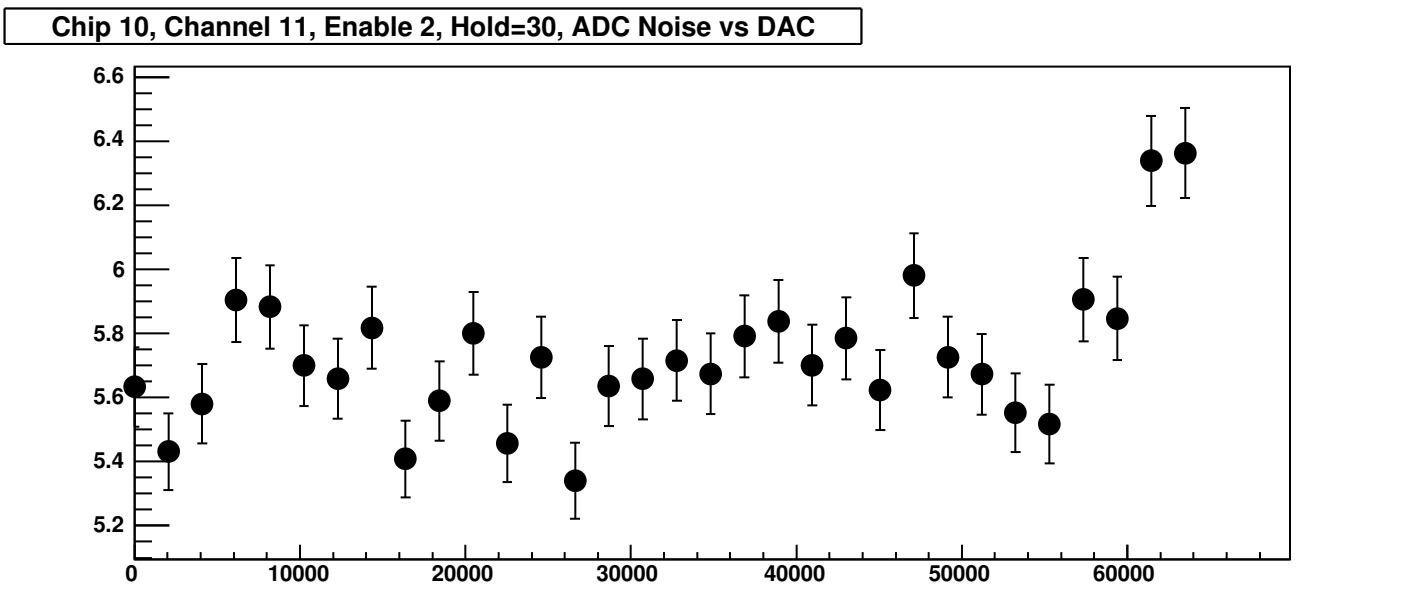

Chip 10, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

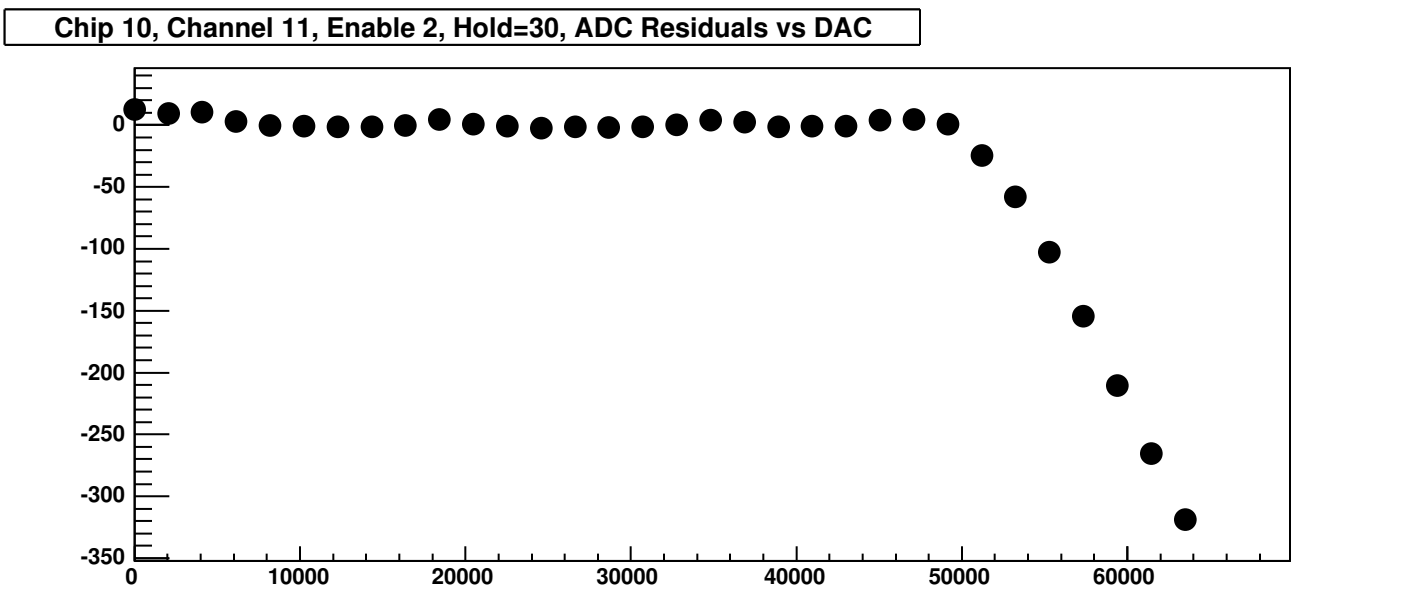

Chip 10, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2248 / 15
p0 1381 ± 0.1051
p1 0.0002405 ± 4.275e-06
1400
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

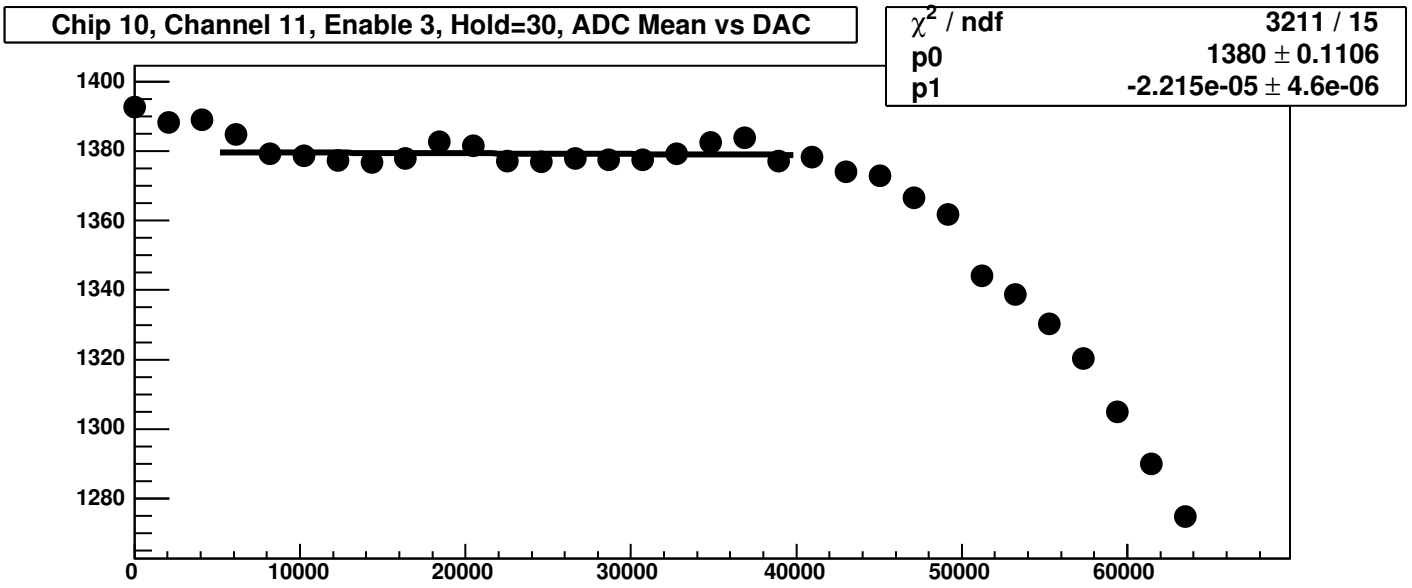

Chip 10, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

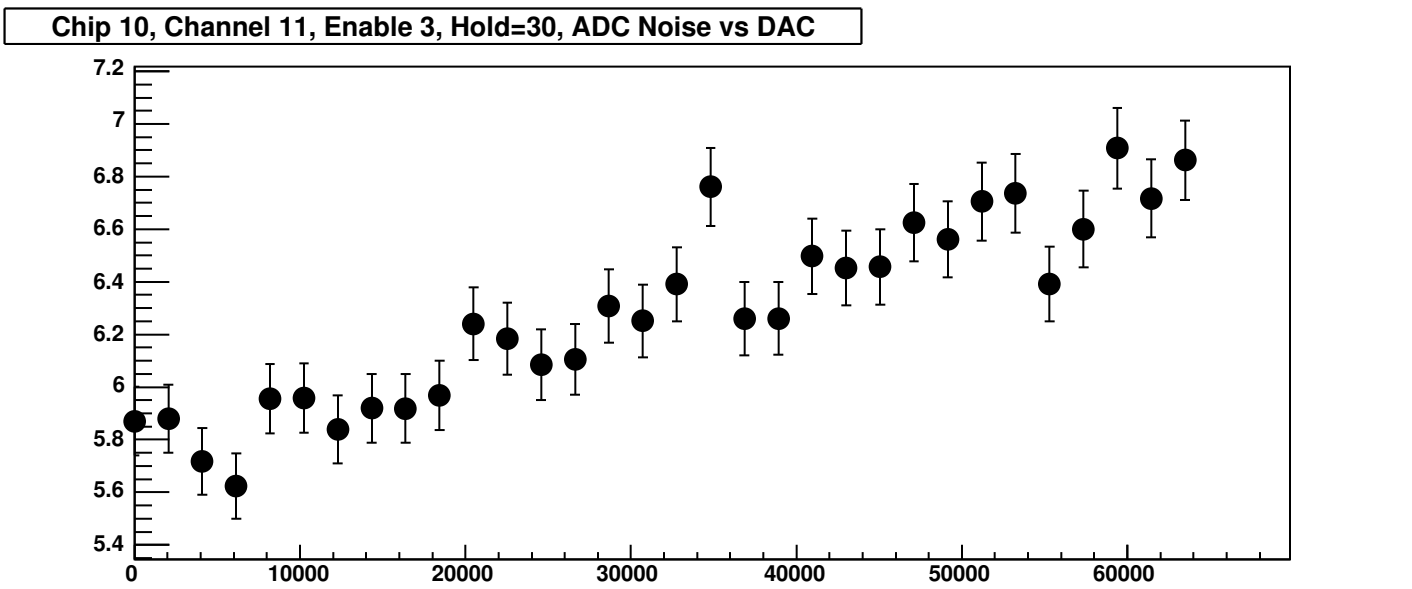

Chip 10, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

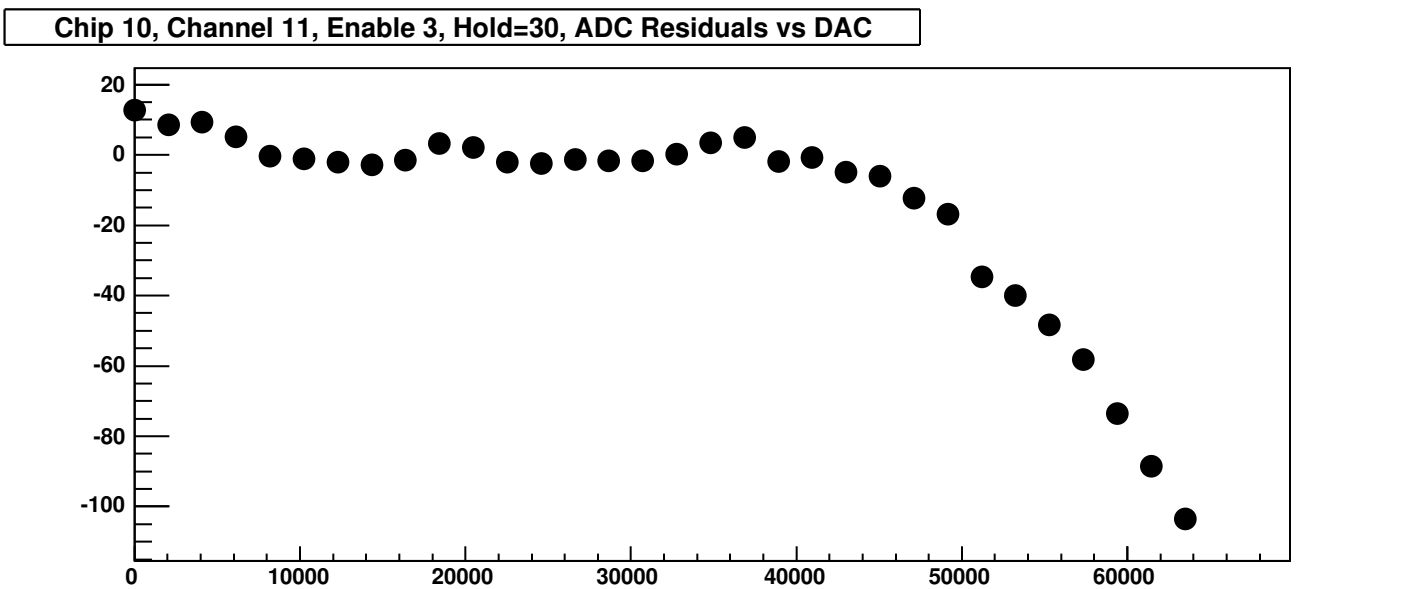

Chip 10, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2141 / 15
p0 1379 ± 0.1074
p1 -0.0009999 ± 4.359e-06
1400
1350
1300
1250
1200
1150
1100
1050
1000
0
10000
20000
30000
40000
50000
60000

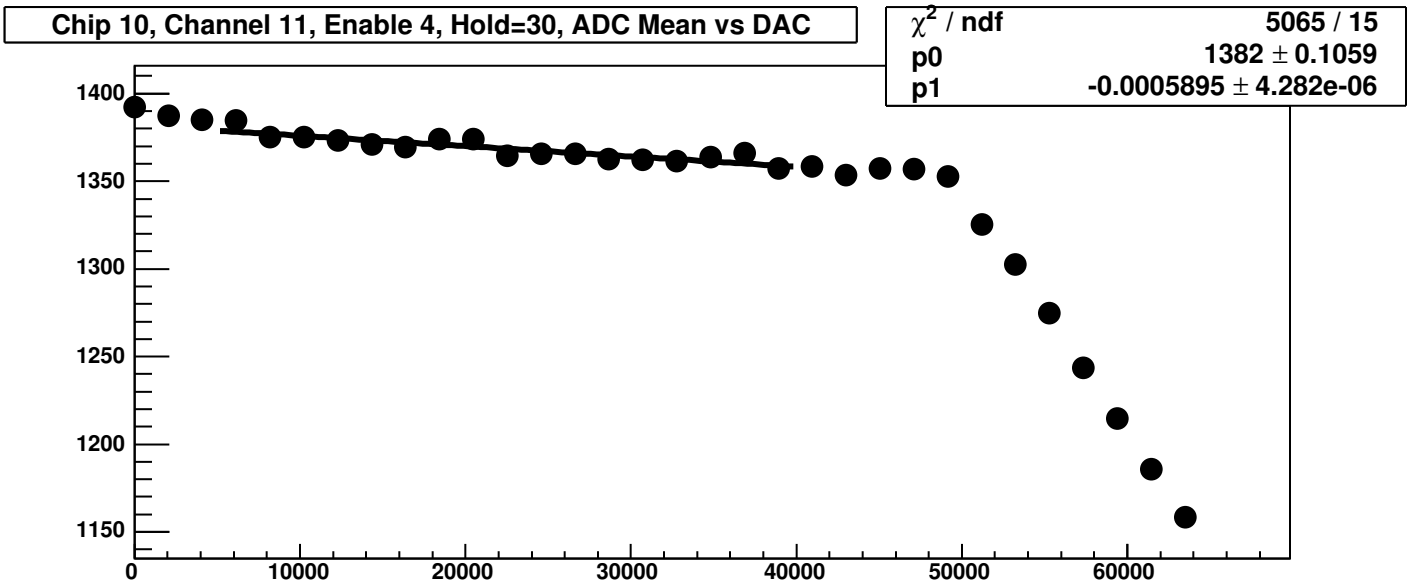

Chip 10, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

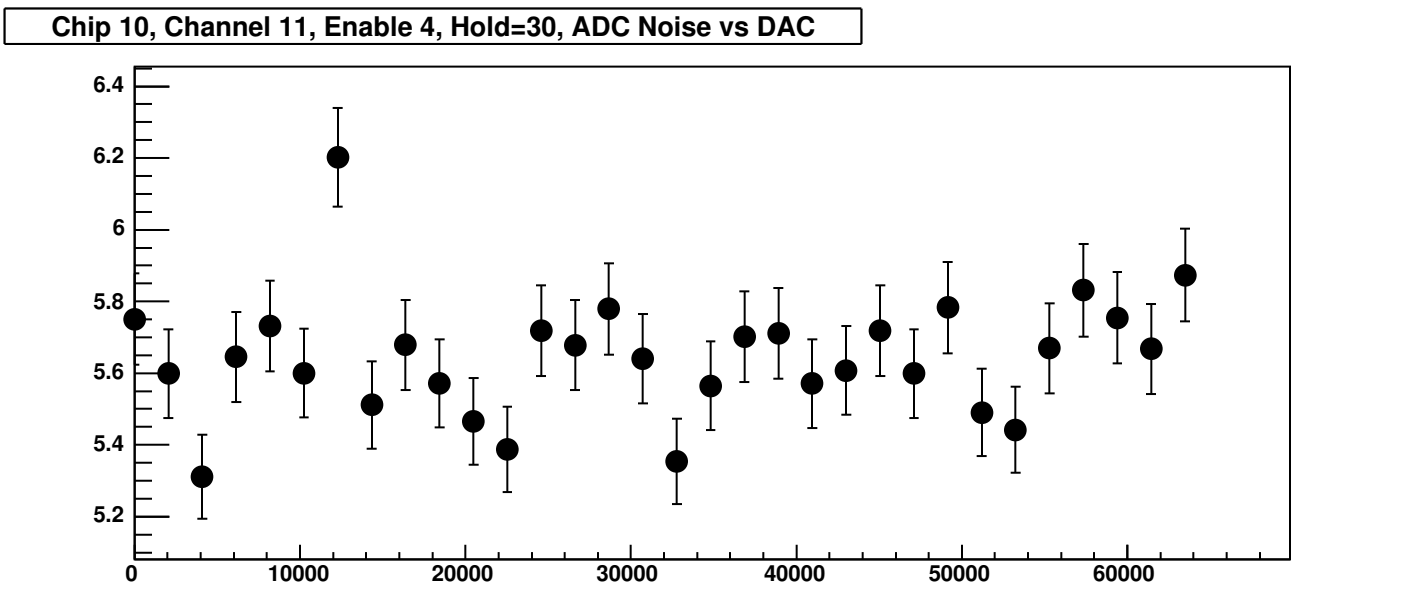

Chip 10, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

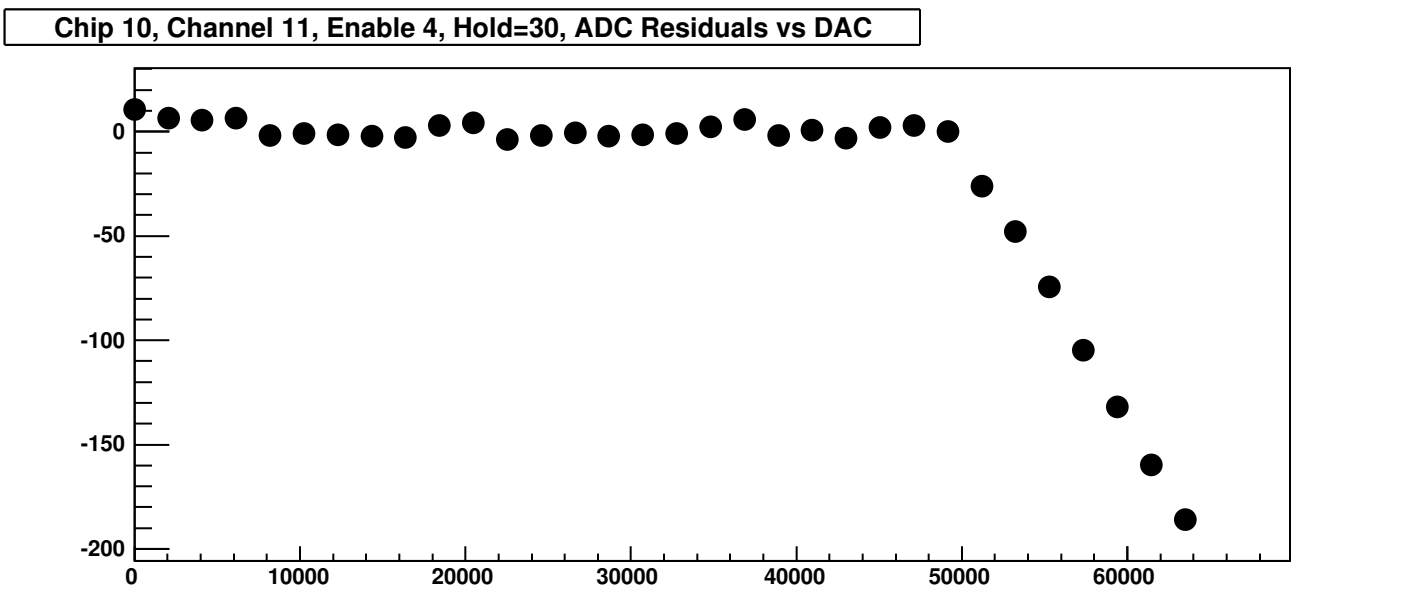

Chip 10, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3211 / 15
p0 1380 ± 0.1106
p1 -2.215e-05 ± 4.6e-06
1400
1380
1360
1340
1320
1300
1280
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

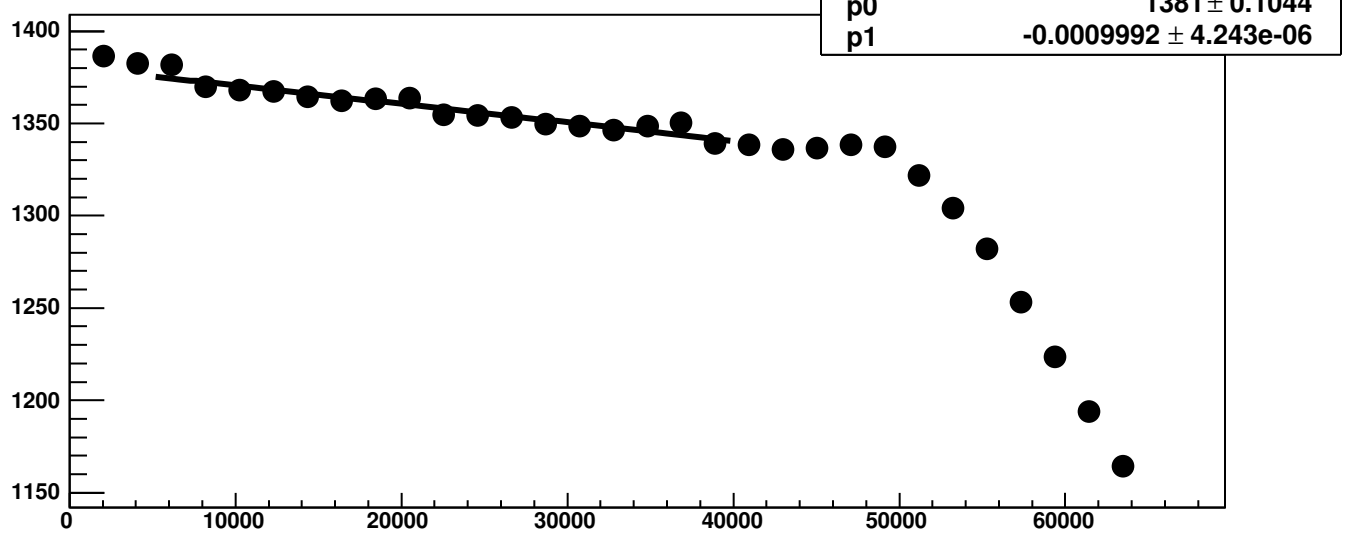

Chip 10, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5065 / 15
p0 1382 ± 0.1059
p1 -0.0005895 ± 4.282e-06
1400
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

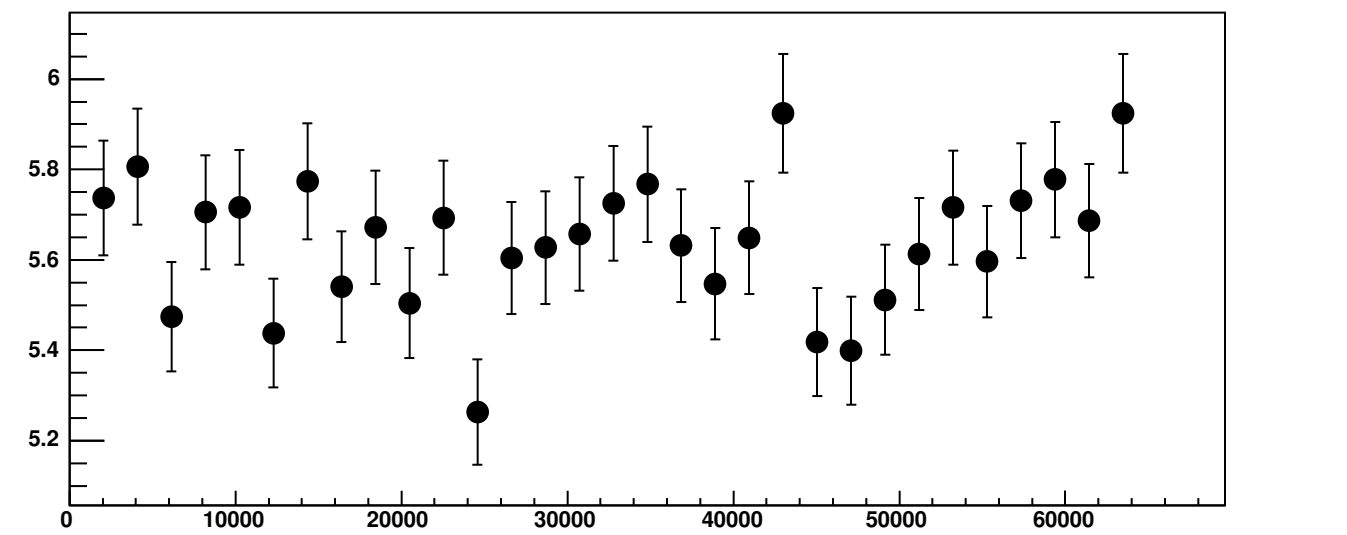

Chip 10, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

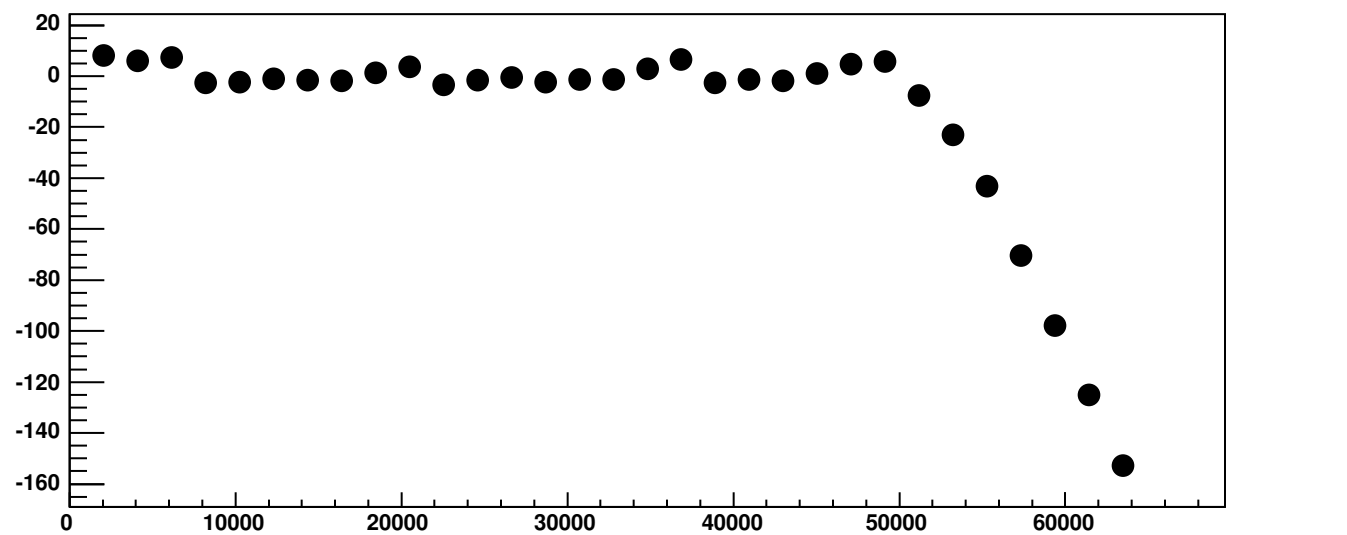
Chip 10, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC



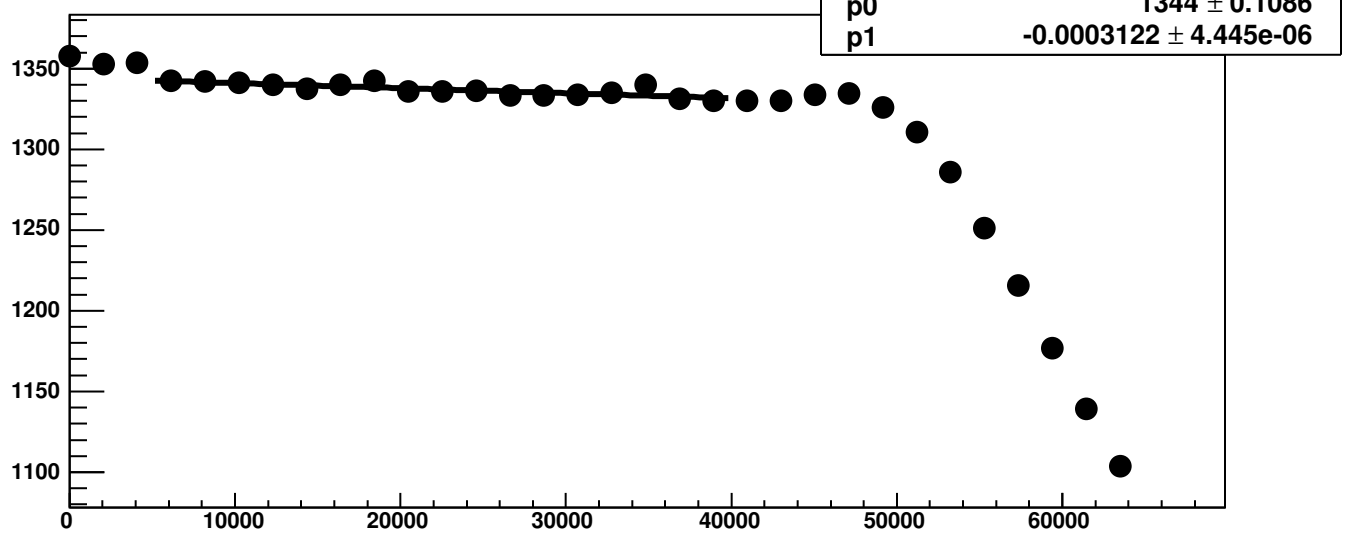
Chip 10, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



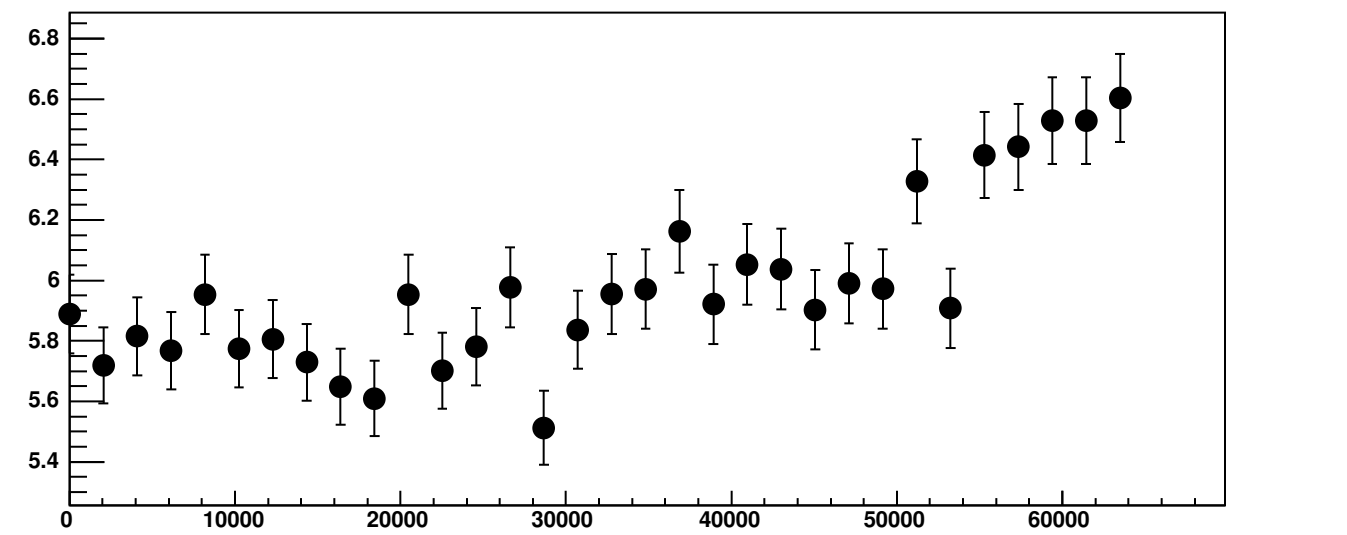
Chip 10, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC



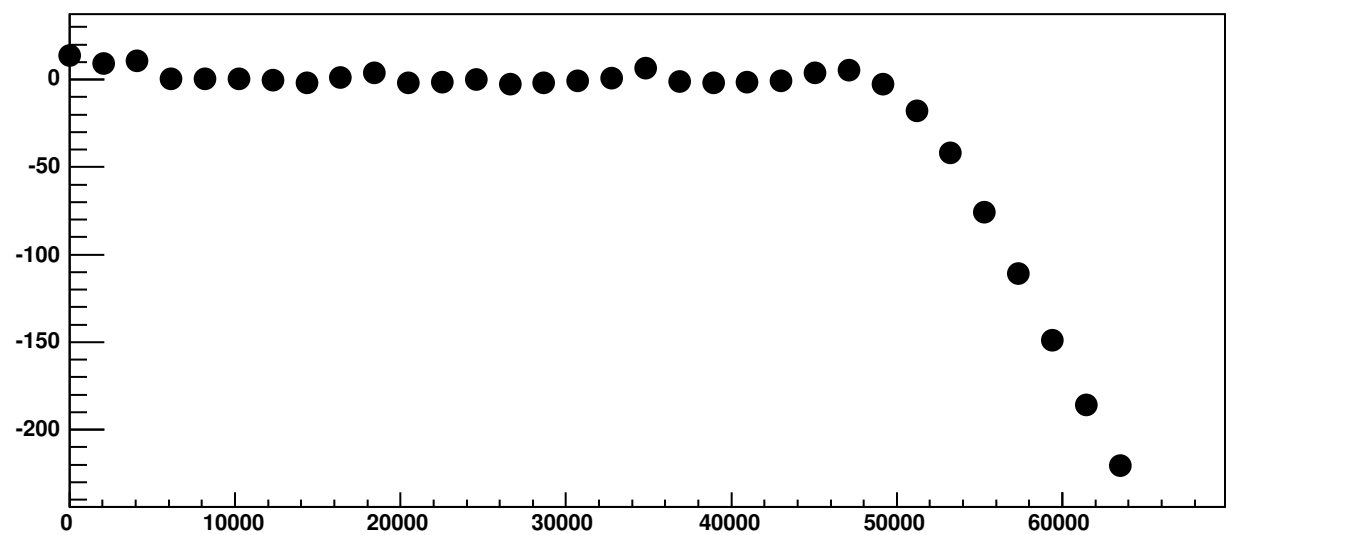
Chip 10, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

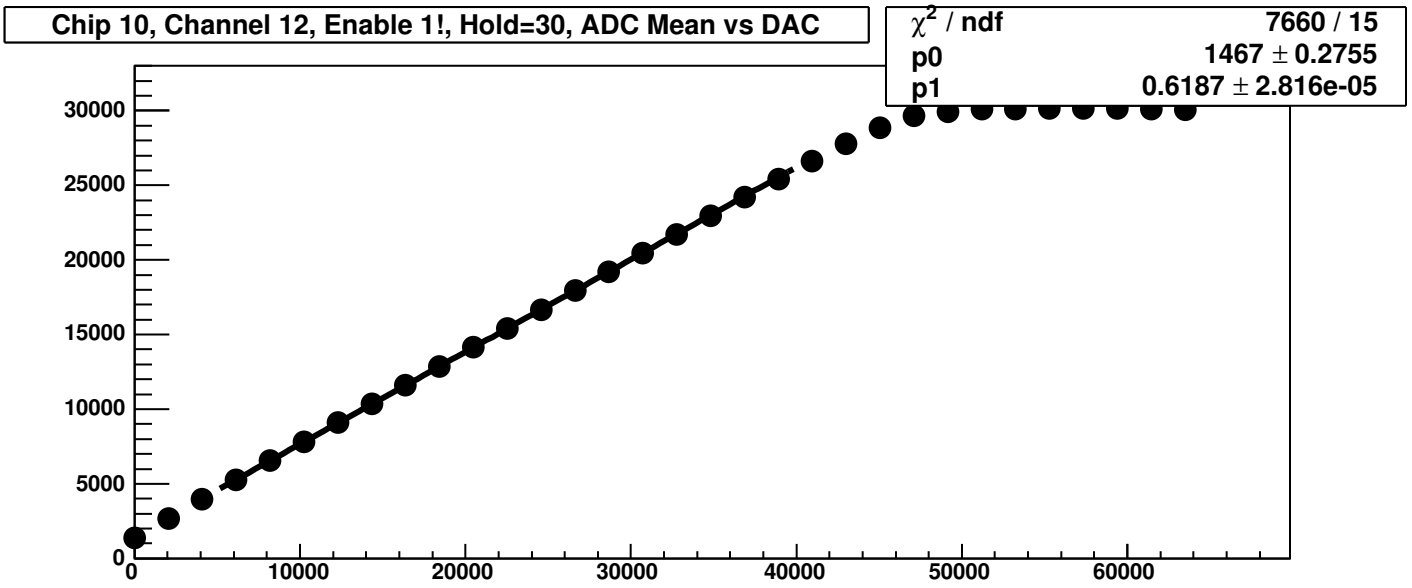


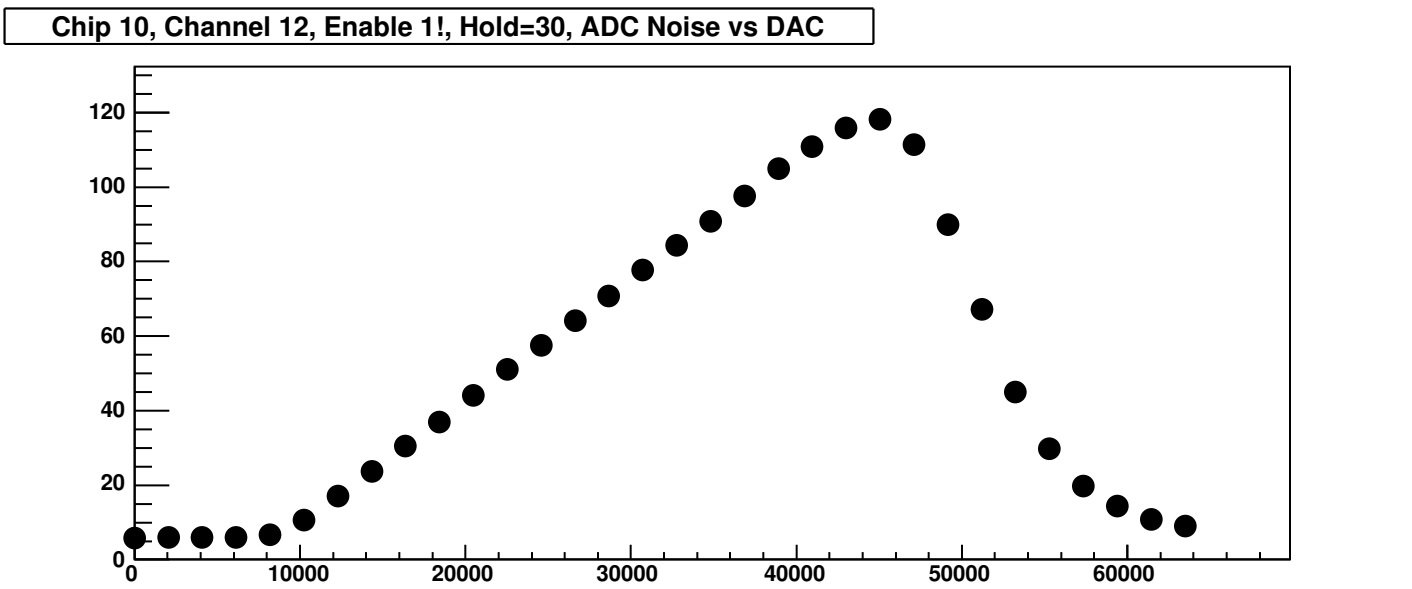
Chip 10, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC

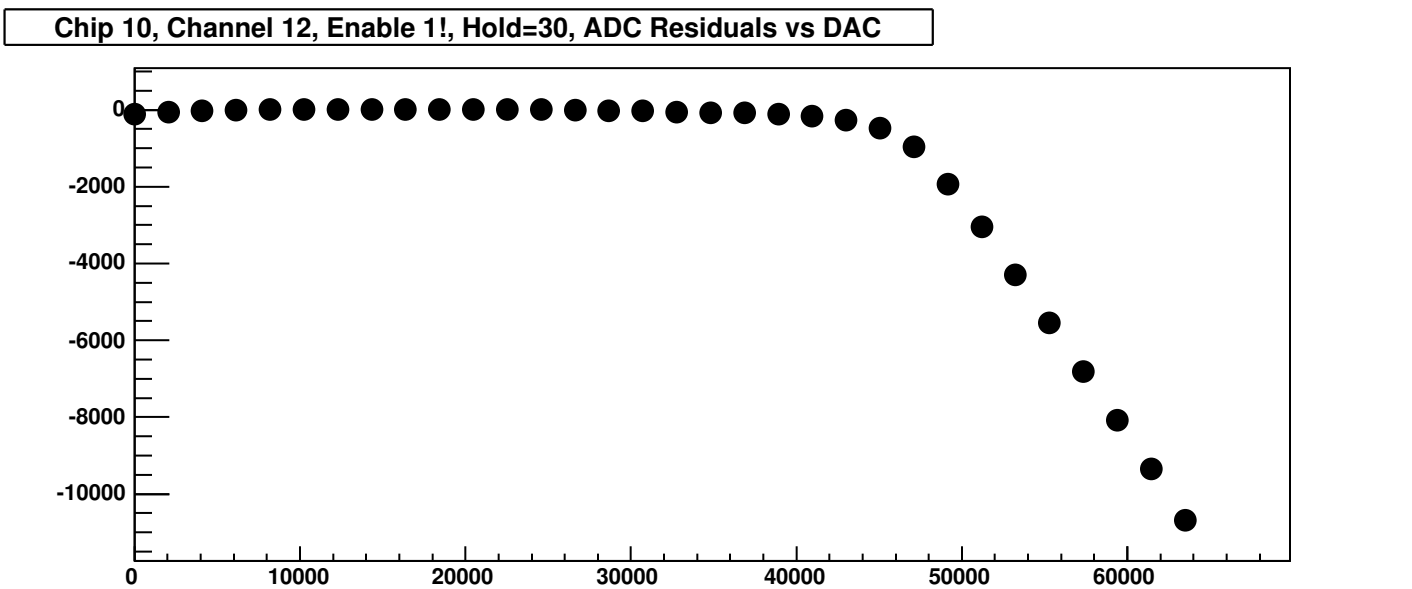


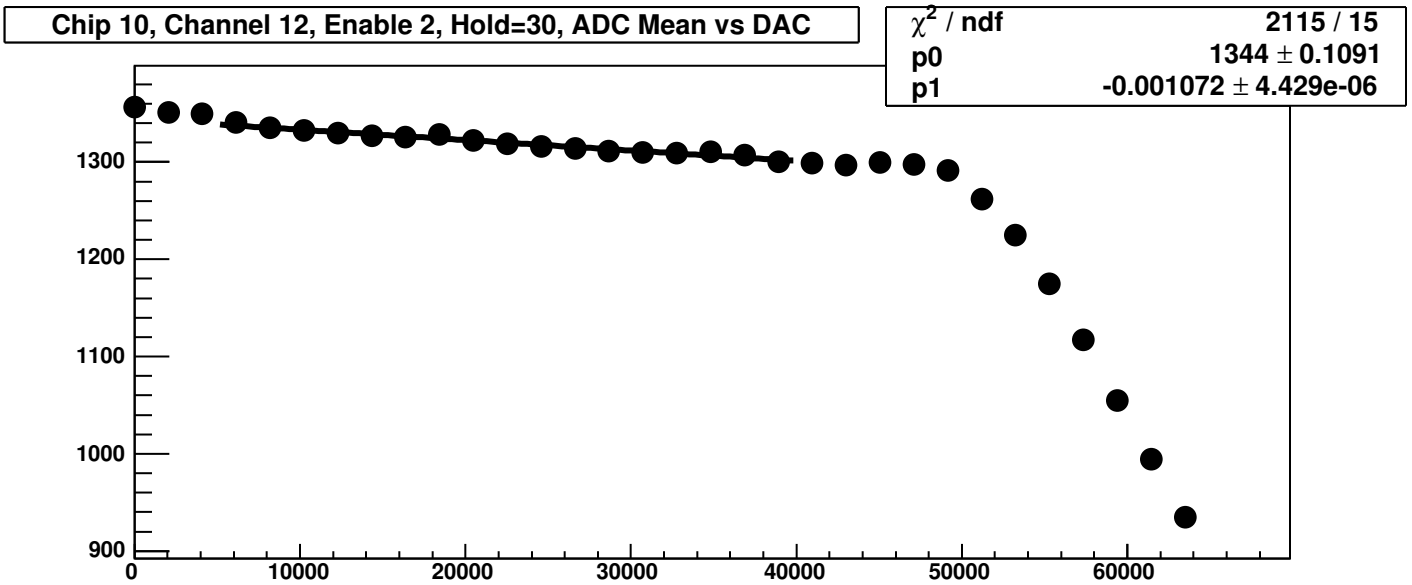
Chip 10, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

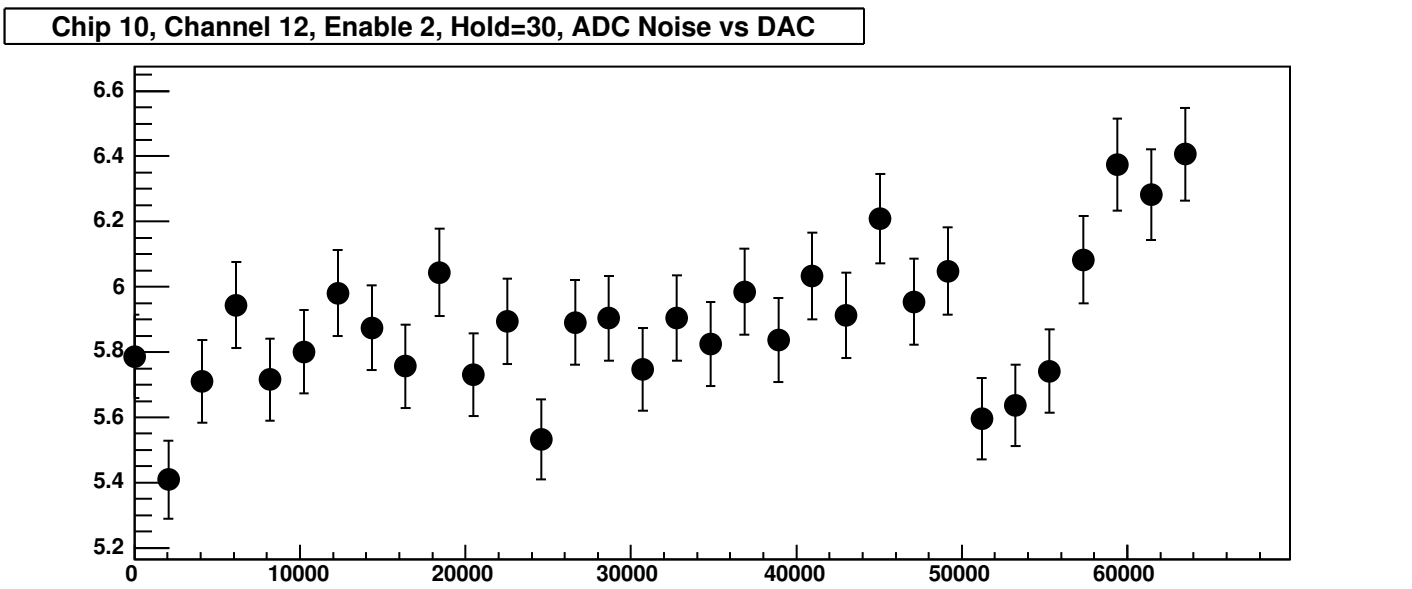


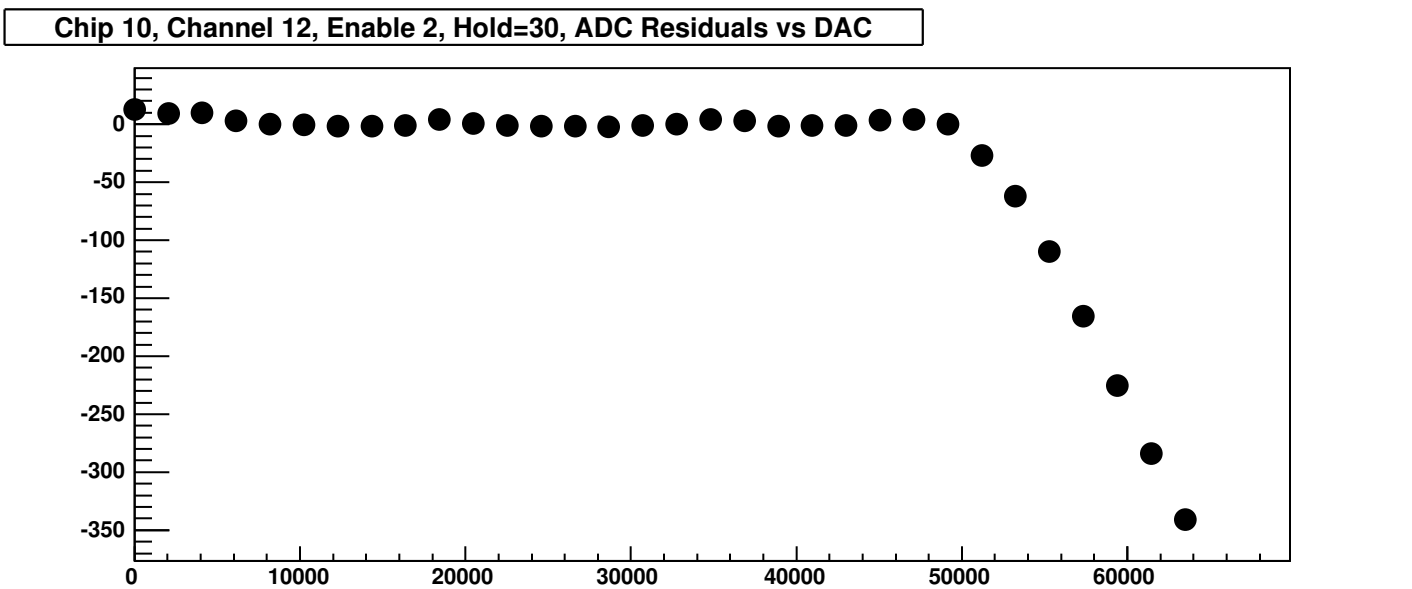

Chip 10, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 7660 / 15
p0 1467 ± 0.2755
p1 0.6187 ± 2.816e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

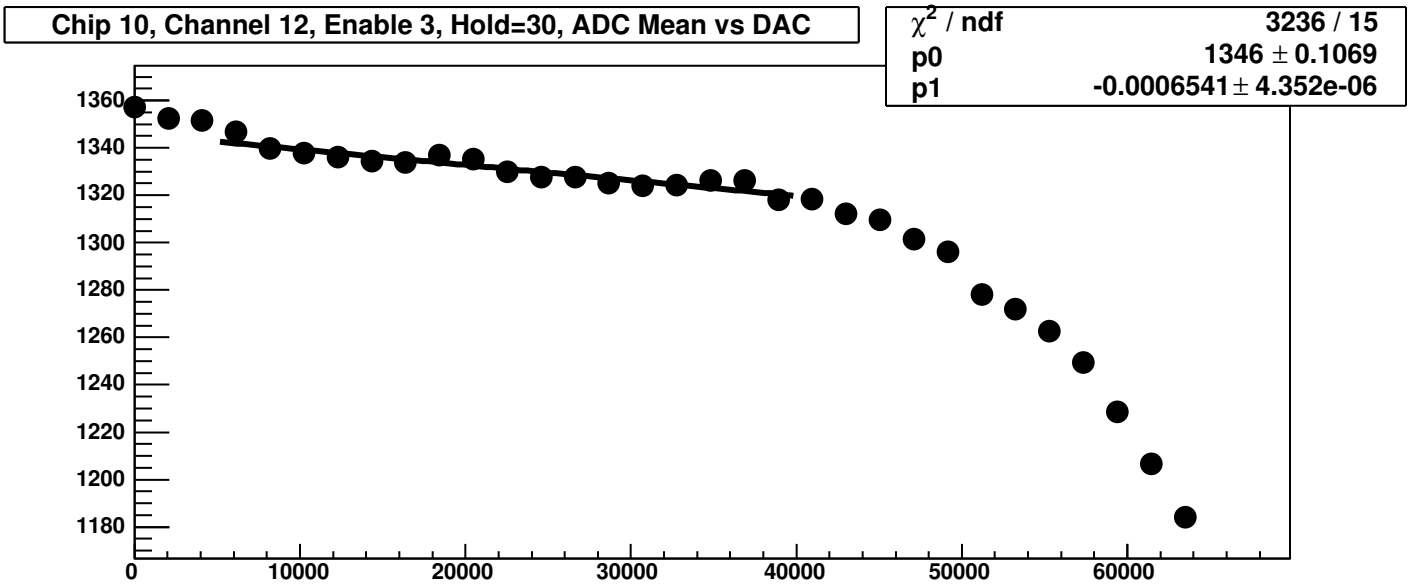

Chip 10, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

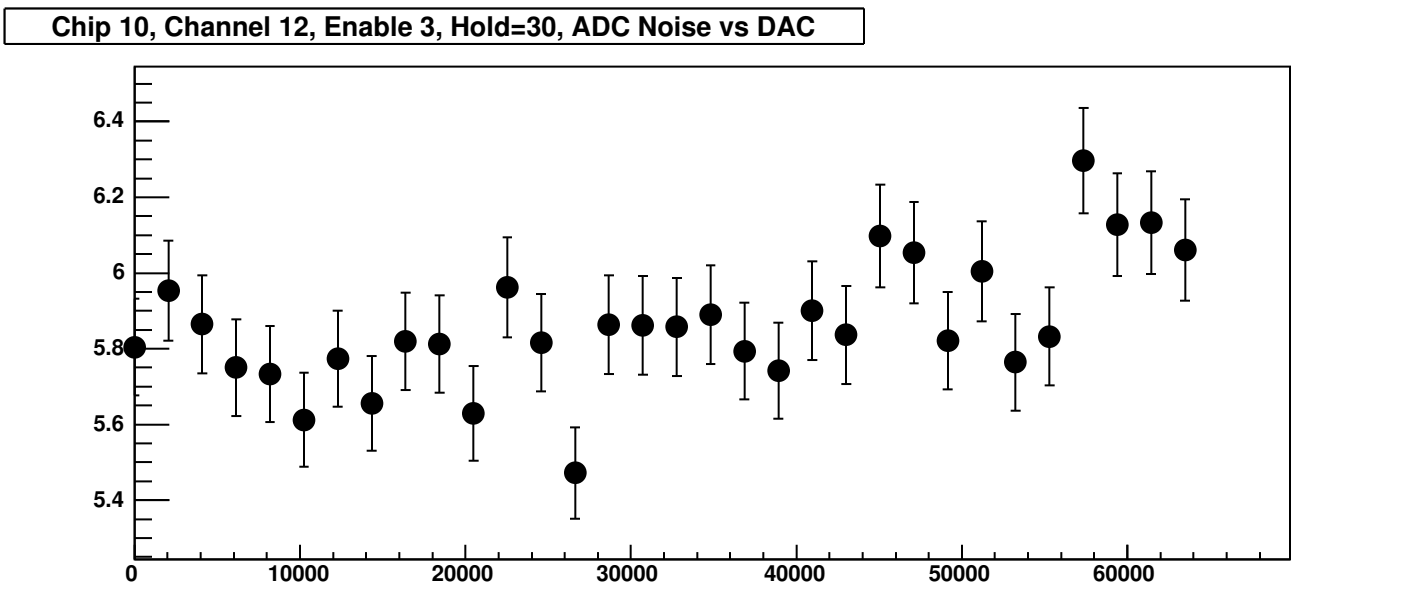

Chip 10, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

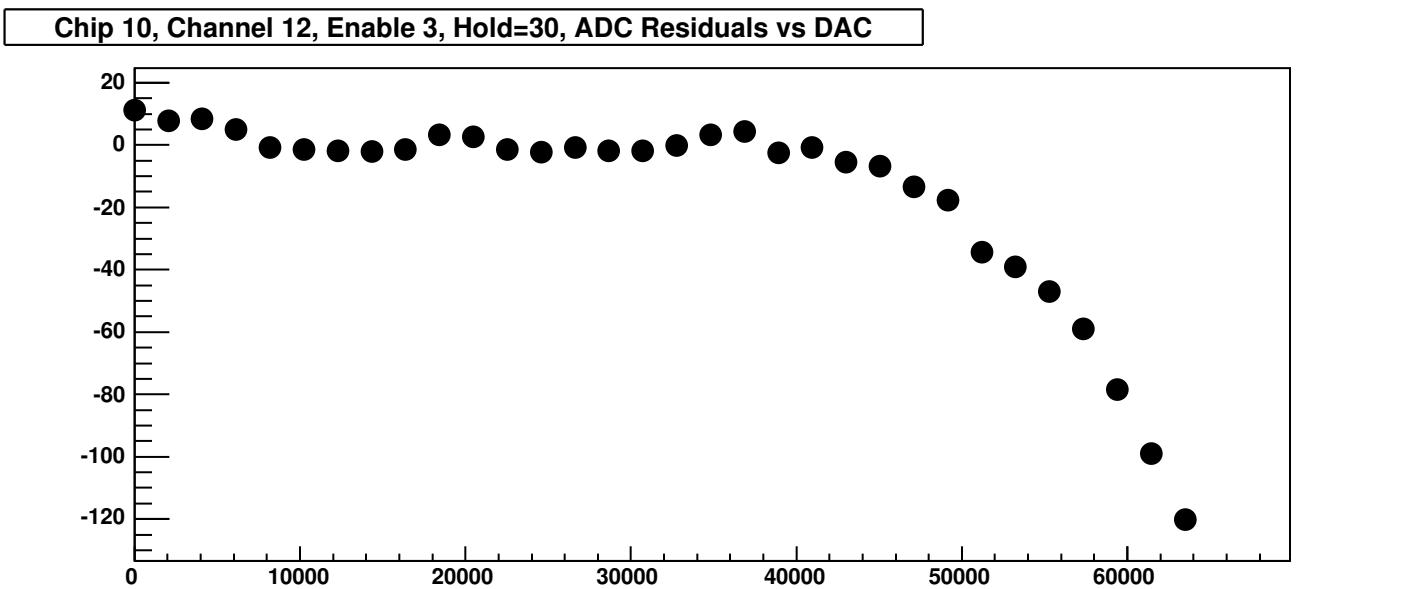

Chip 10, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2115 / 15
p0 1344 ± 0.1091
p1 -0.001072 ± 4.429e-06
1300
1200
1100
1000
900
0
10000
20000
30000
40000
50000
60000

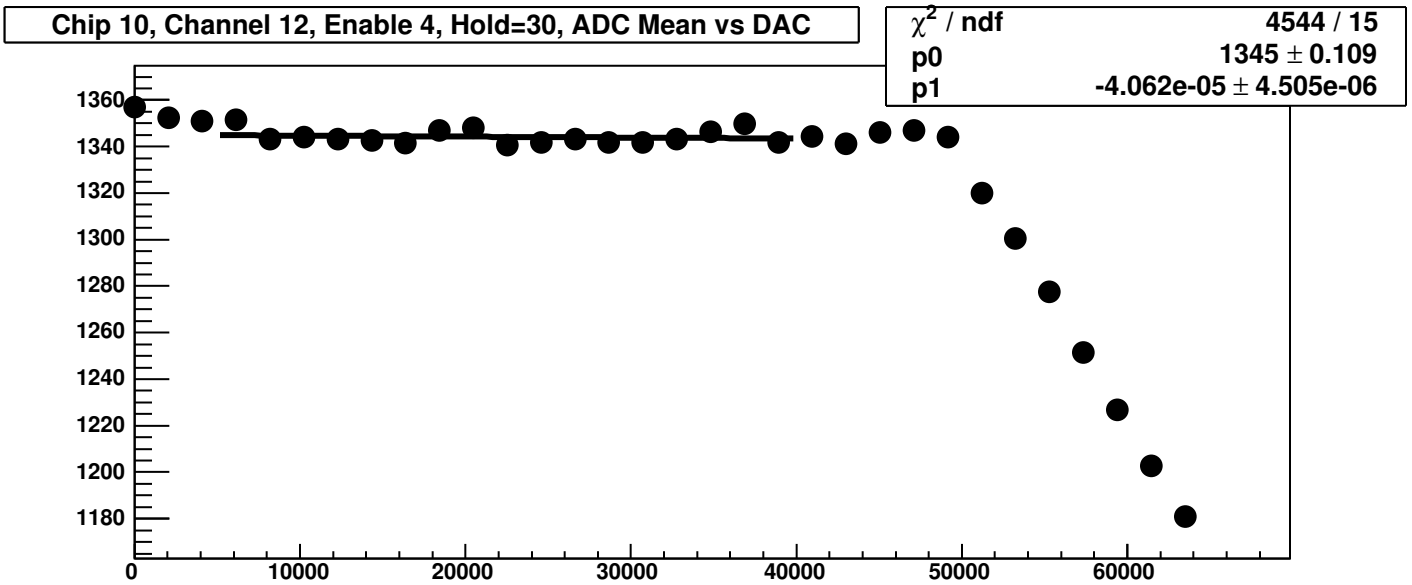

Chip 10, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

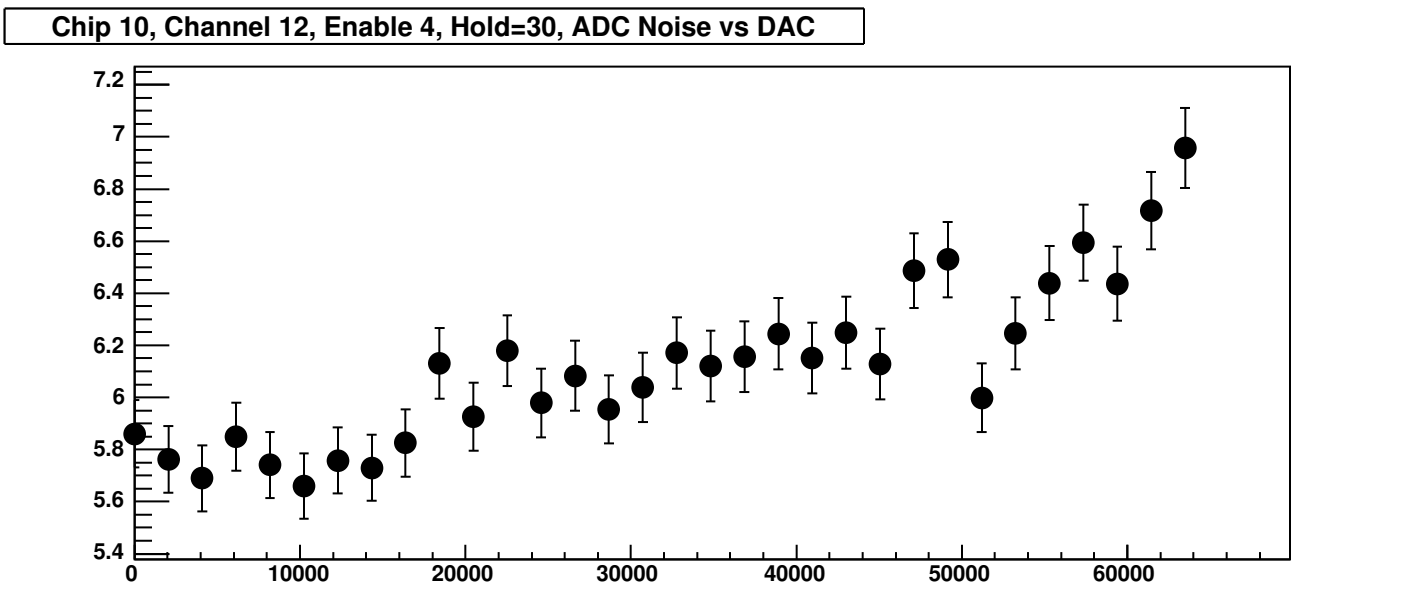

Chip 10, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

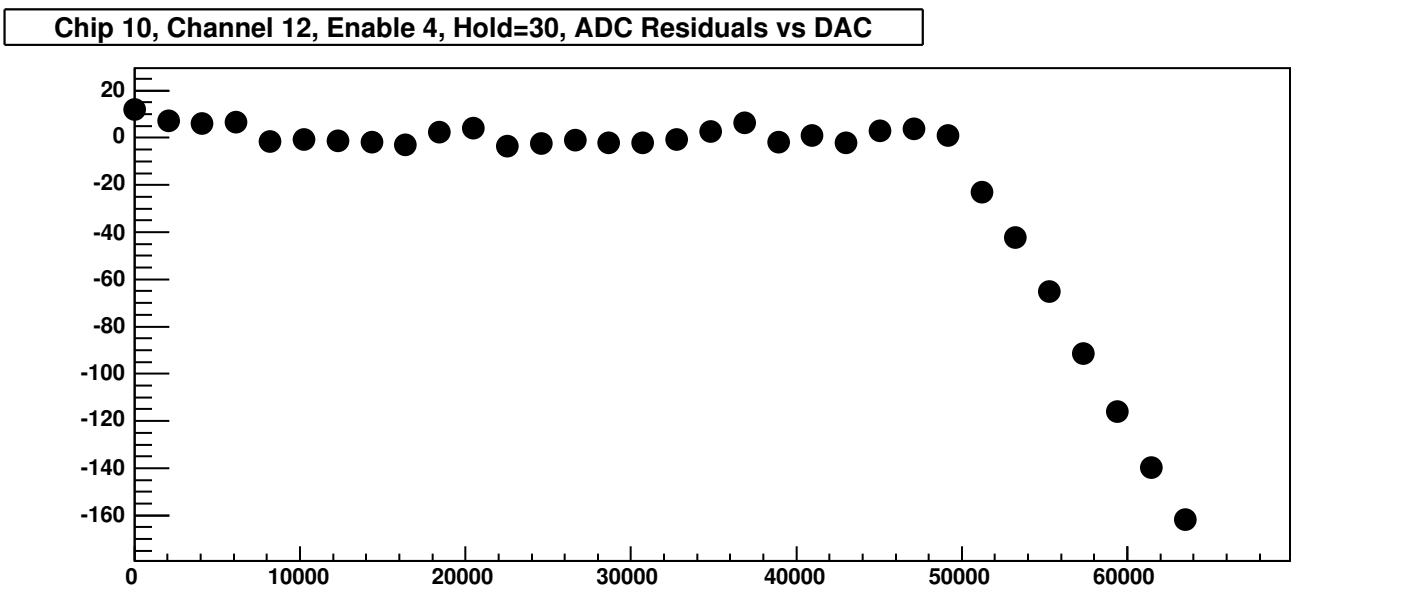

Chip 10, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3236 / 15
p0 1346 ± 0.1069
p1 -0.0006541 ± 4.352e-06
1360
1340
1320
1300
1280
1260
1240
1220
1200
1180
0
10000
20000
30000
40000
50000
60000

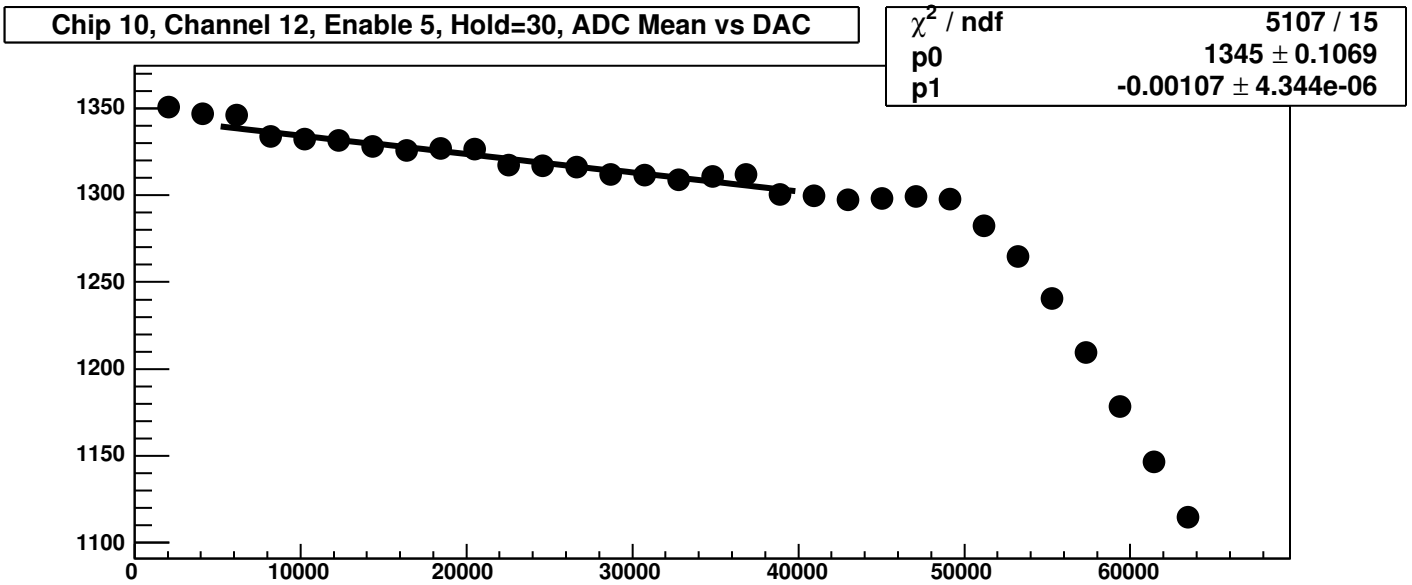

Chip 10, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

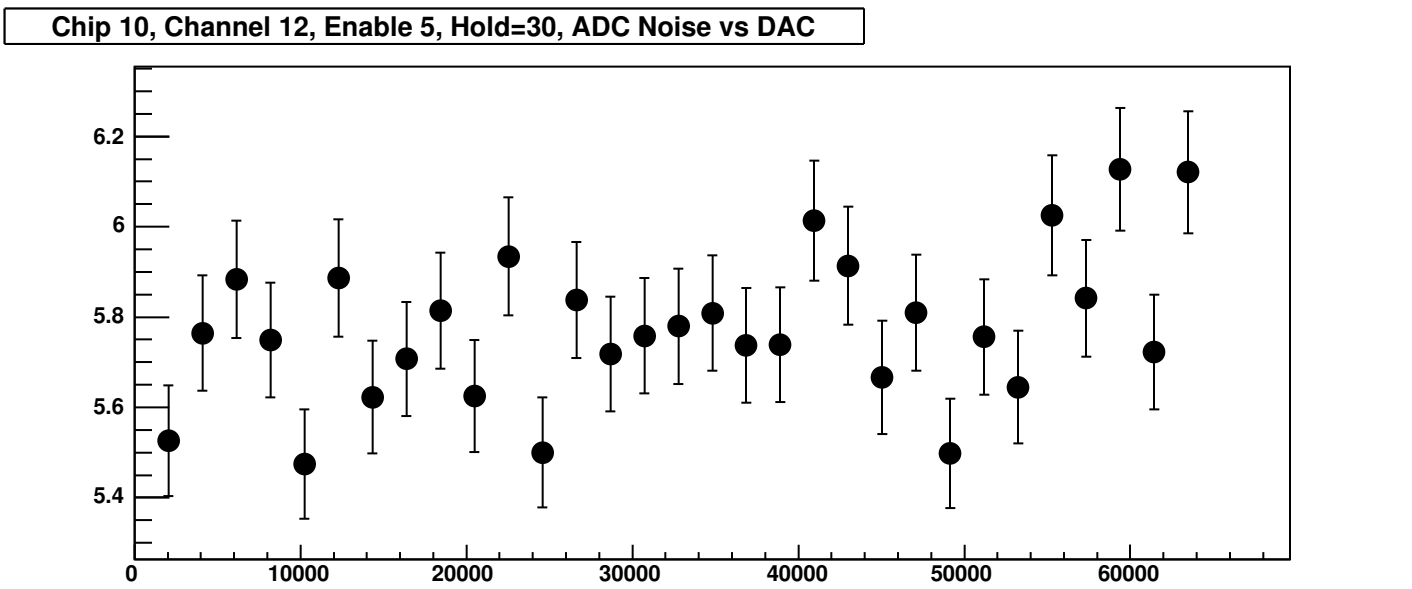

Chip 10, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

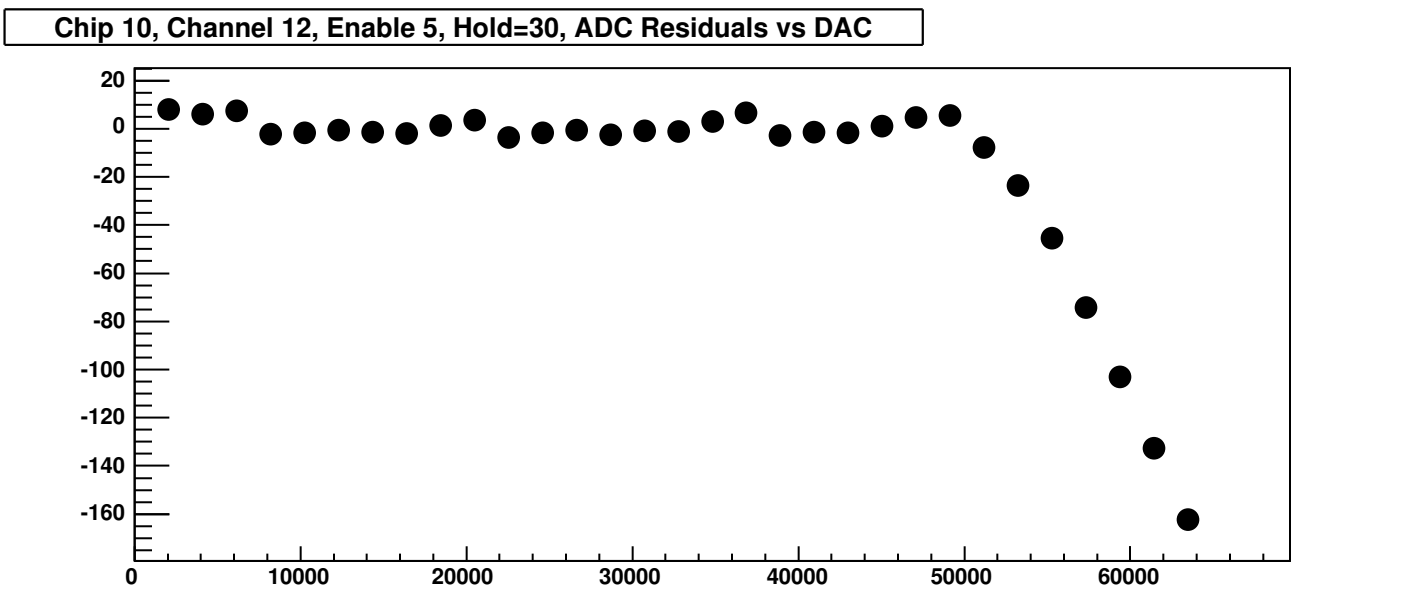

Chip 10, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4544 / 15
p0 1345 ± 0.109
p1 -4.062e-05 ± 4.505e-06
1360
1340
1320
1300
1280
1260
1240
1220
1200
1180
0
10000
20000
30000
40000
50000
60000

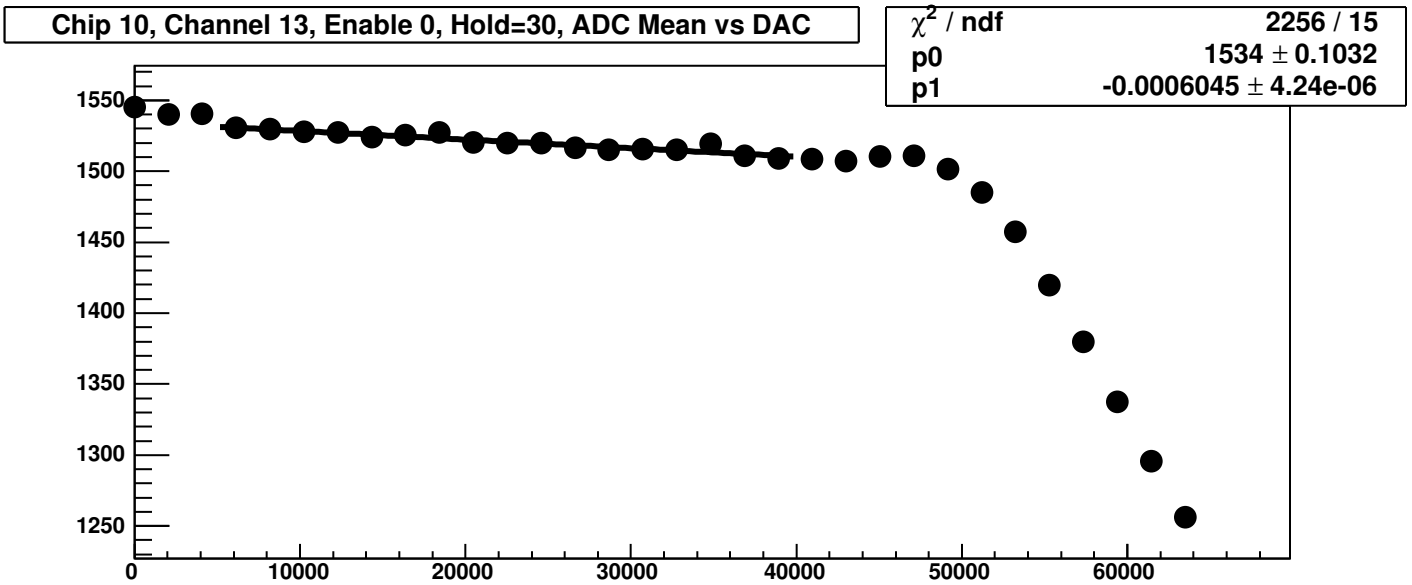

Chip 10, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

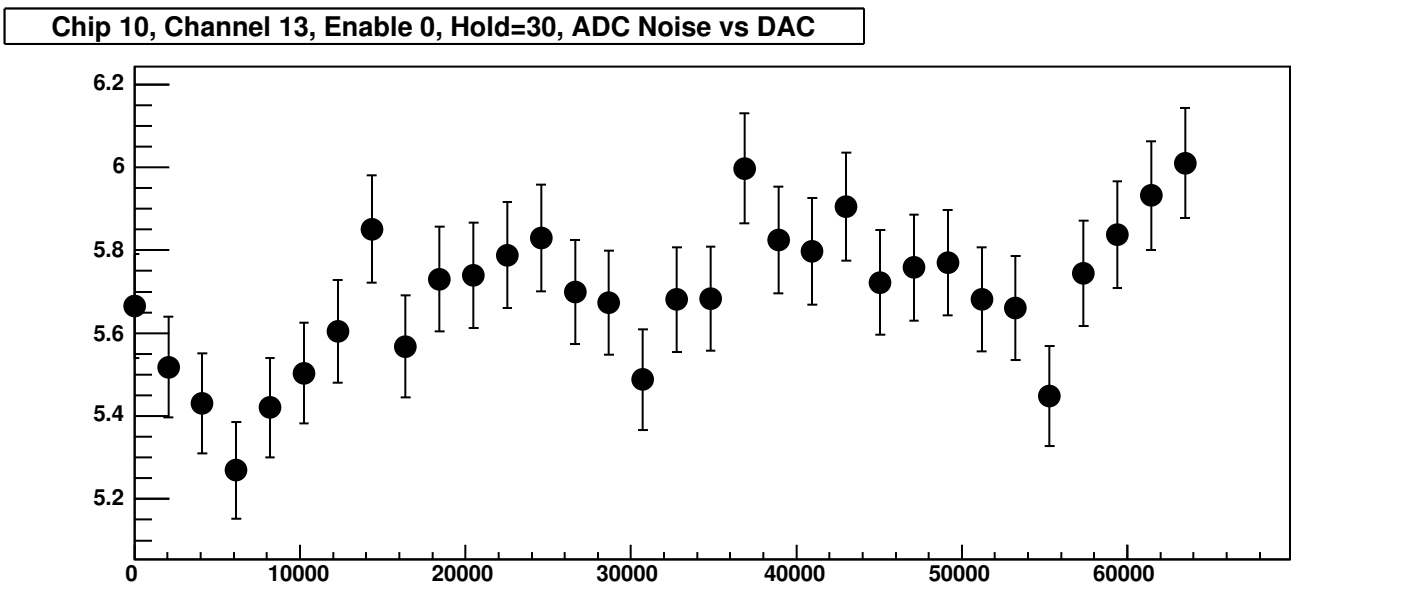

Chip 10, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

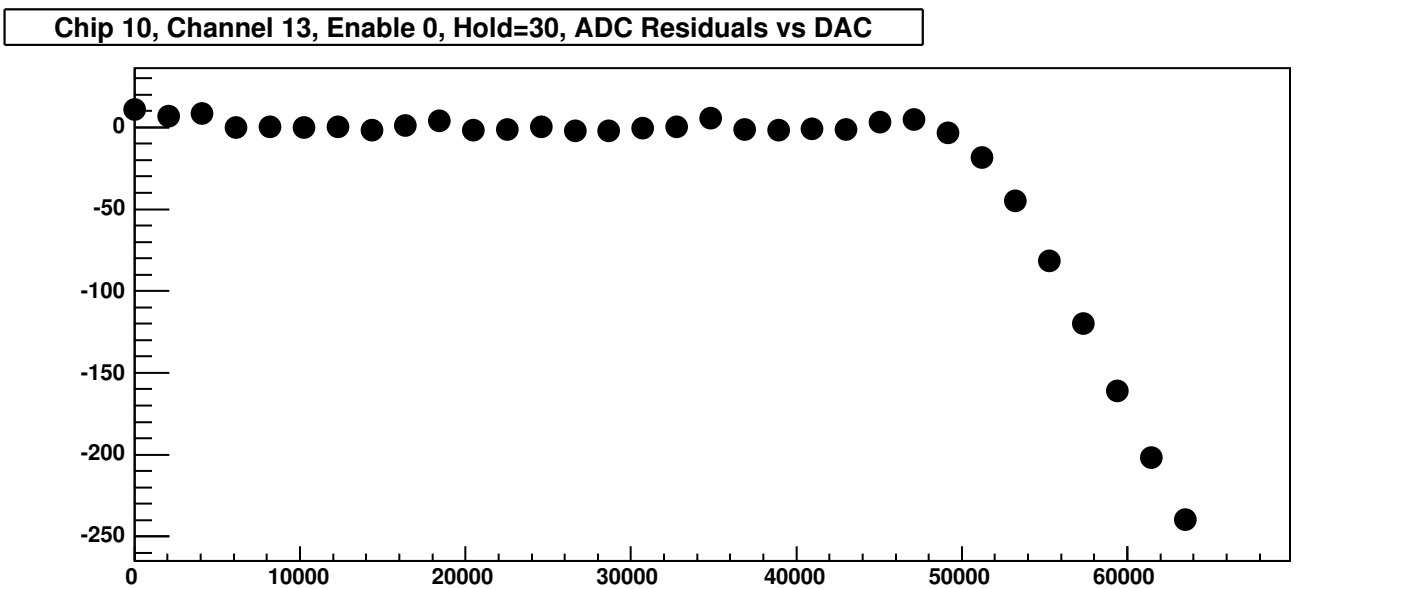

Chip 10, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5107 / 15
p0 1345 ± 0.1069
p1 -0.00107 ± 4.344e-06
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

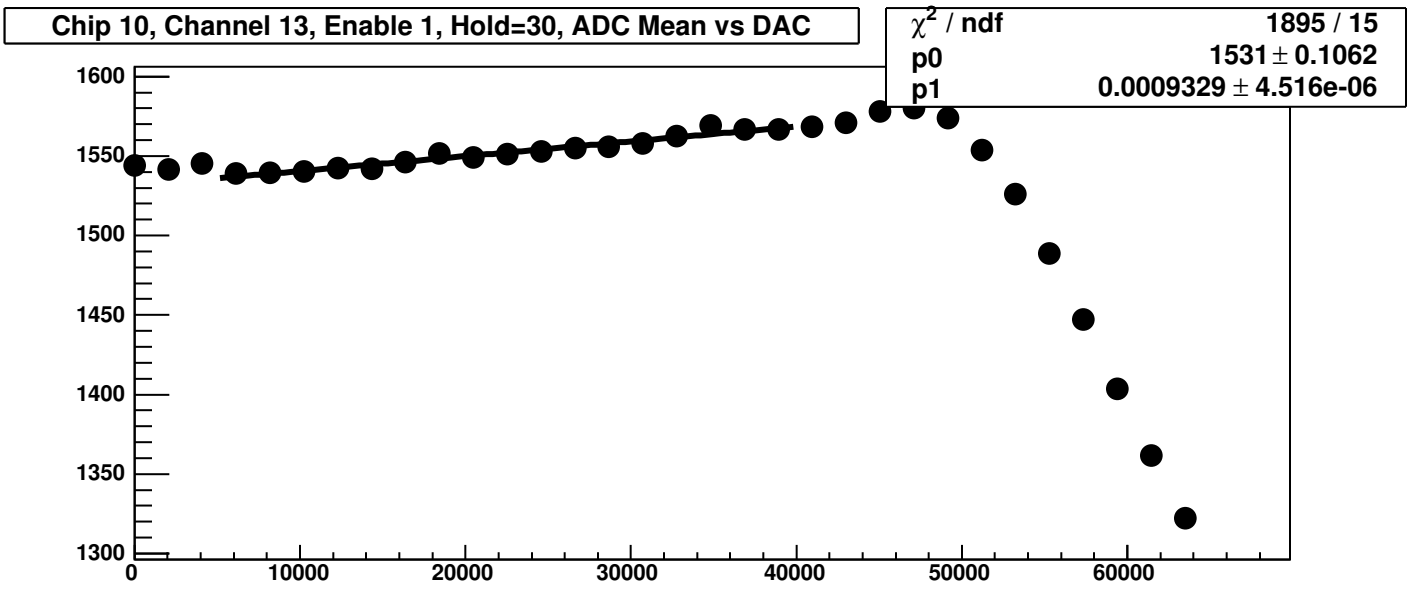

Chip 10, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

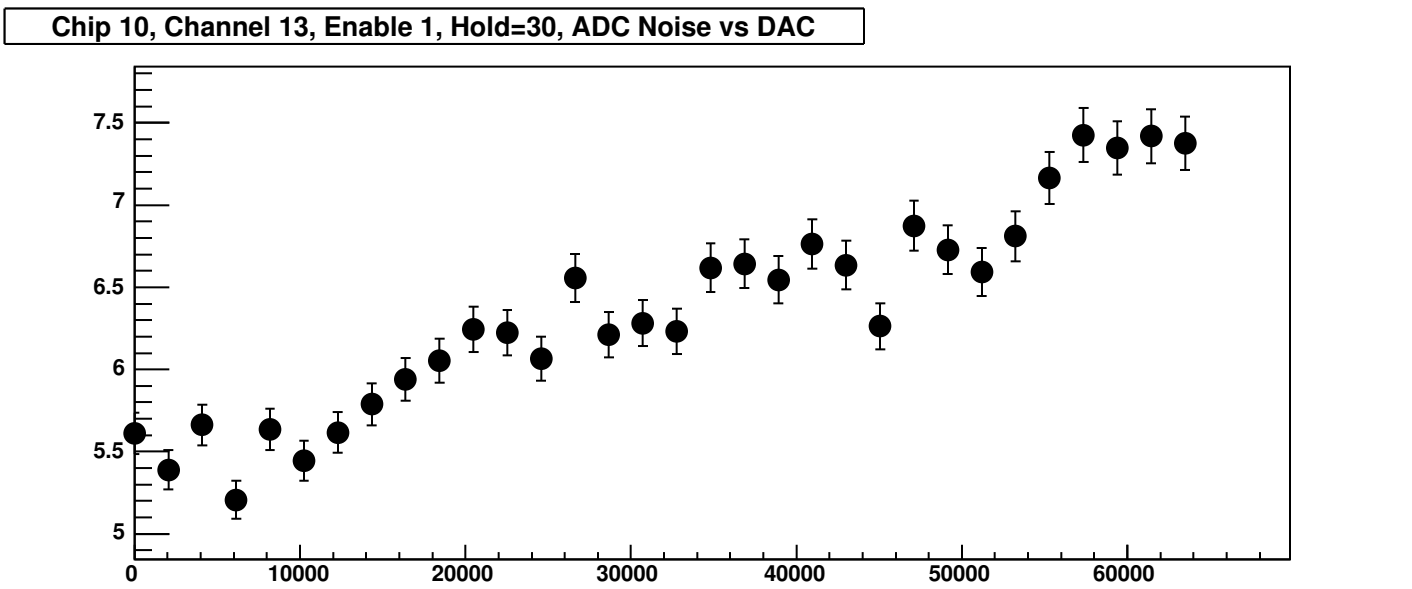

Chip 10, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

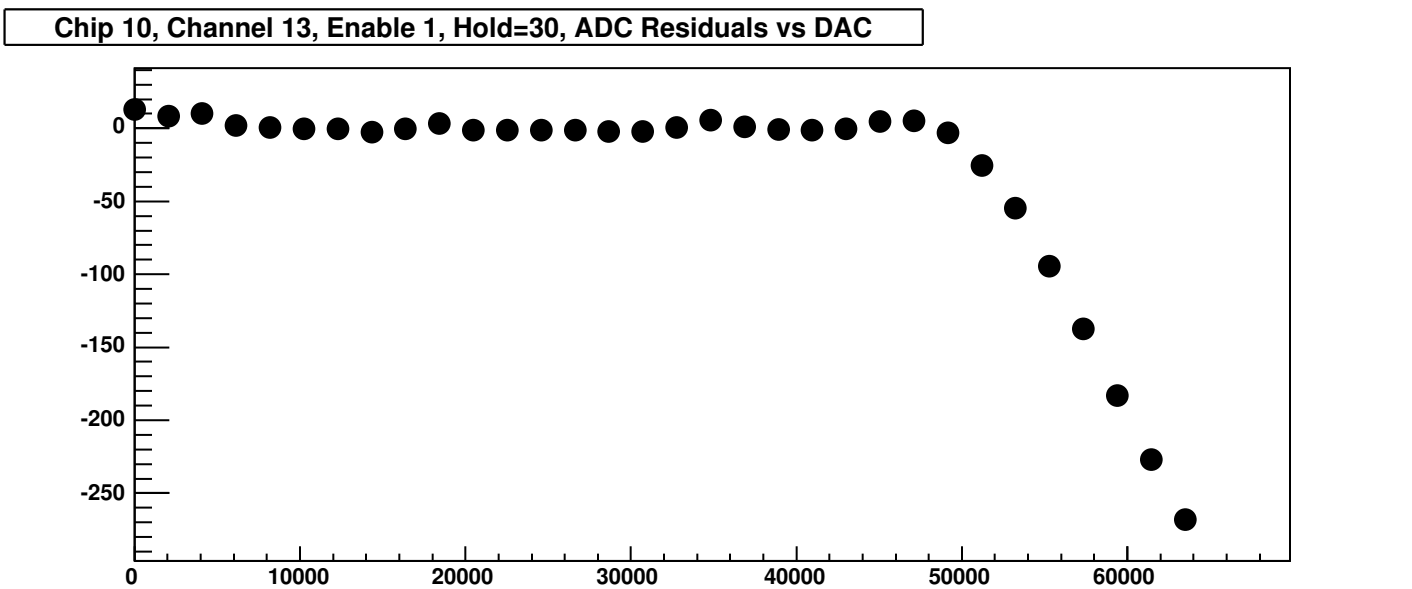

Chip 10, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2256 / 15
p0 1534 ± 0.1032
p1 -0.0006045 ± 4.24e-06
1550
1500
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

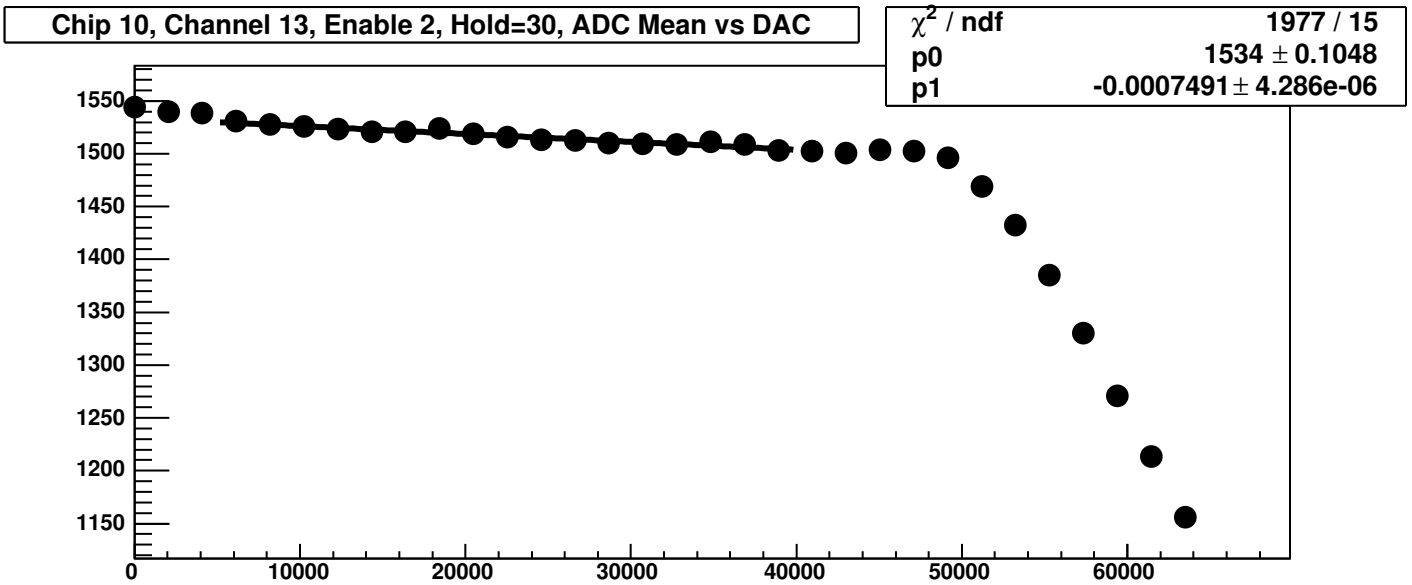

Chip 10, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

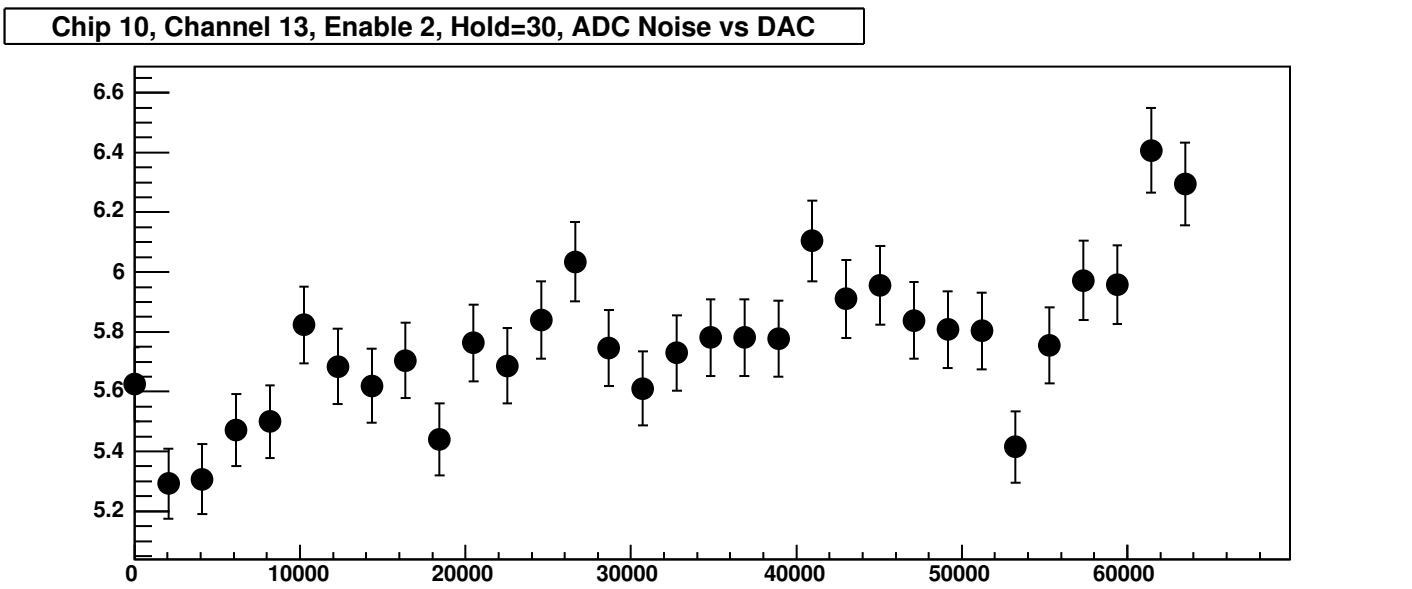

Chip 10, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

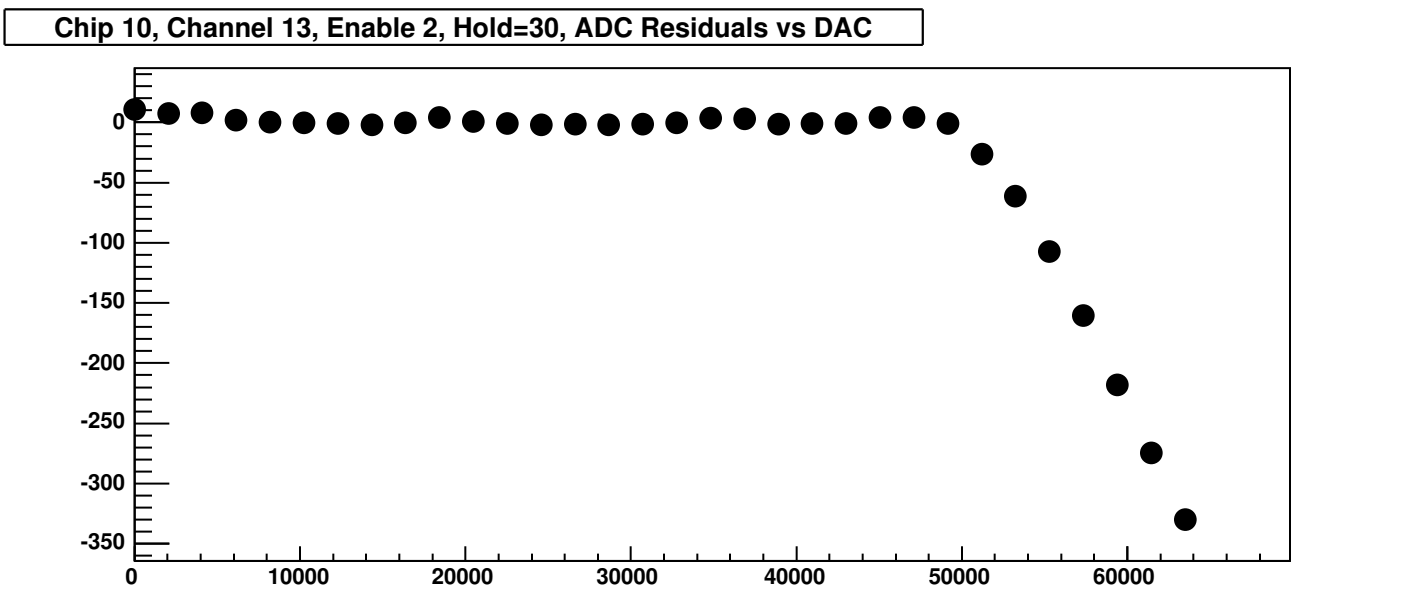

Chip 10, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1895 / 15
p0 1531 ± 0.1062
p1 0.0009329 ± 4.516e-06
1600
1550
1500
1450
1400
1350
1300
0
10000
20000
30000
40000
50000
60000

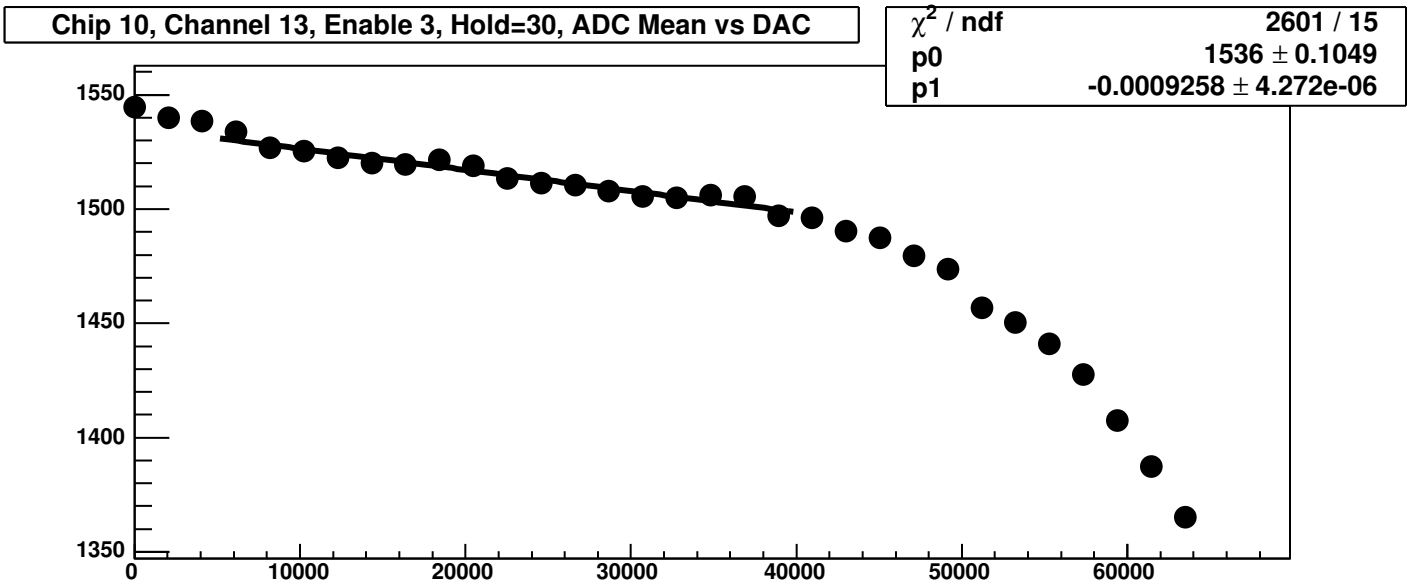

Chip 10, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
5
0
10000
20000
30000
40000
50000
60000

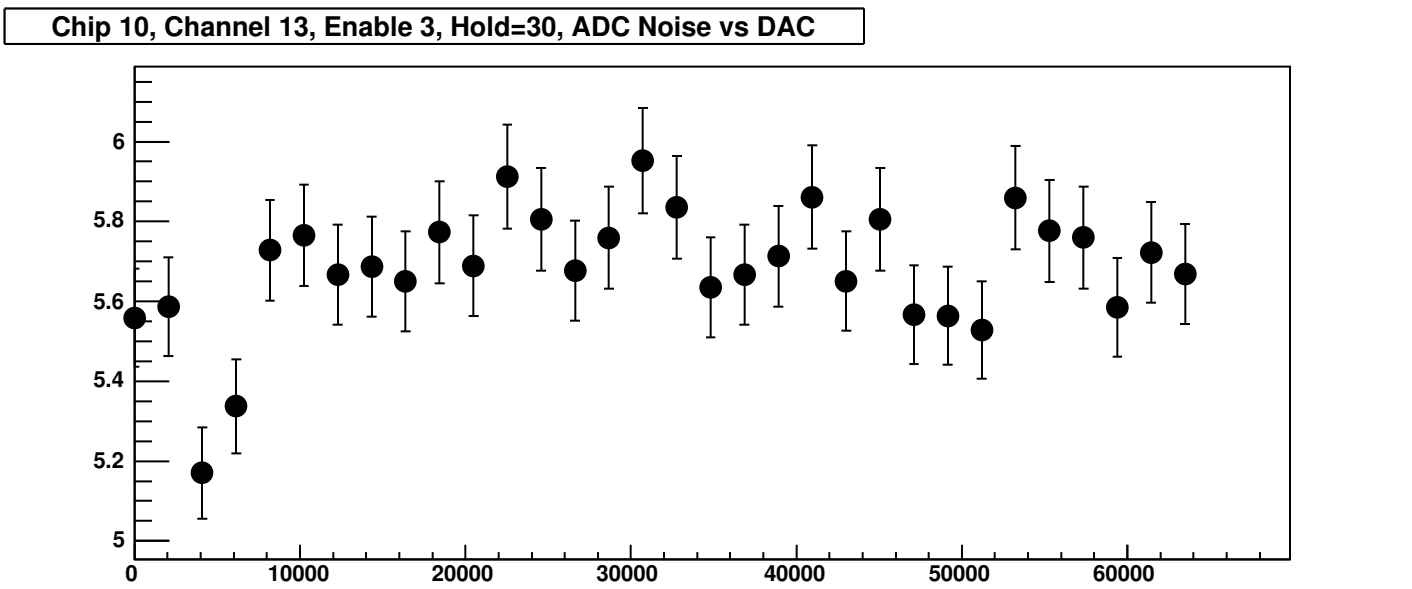

Chip 10, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

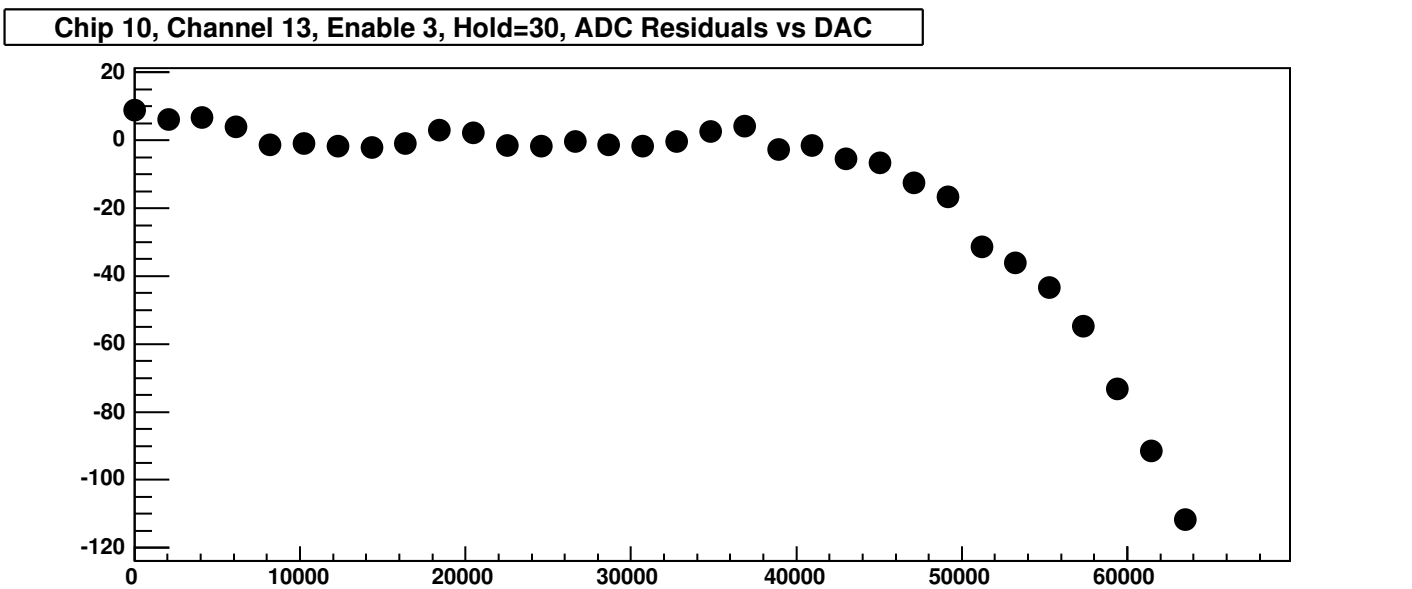

Chip 10, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 1977 / 15
p0 1534 ± 0.1048
p1 -0.0007491 ± 4.286e-06
1550
1500
1450
1400
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

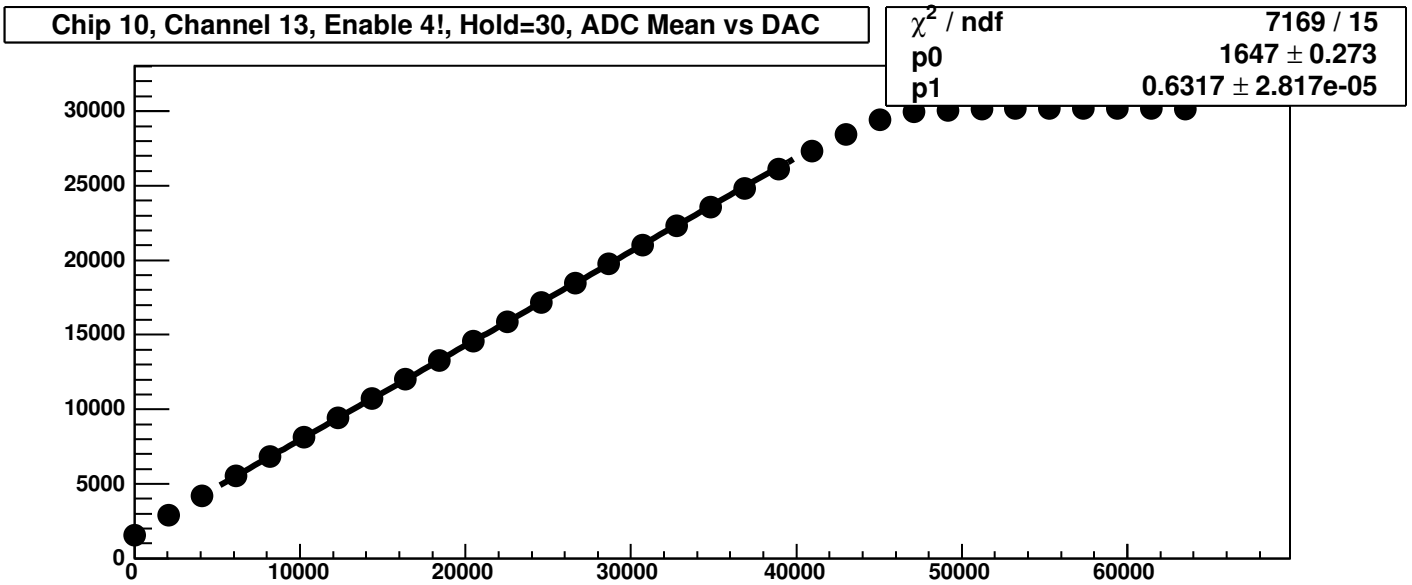

Chip 10, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

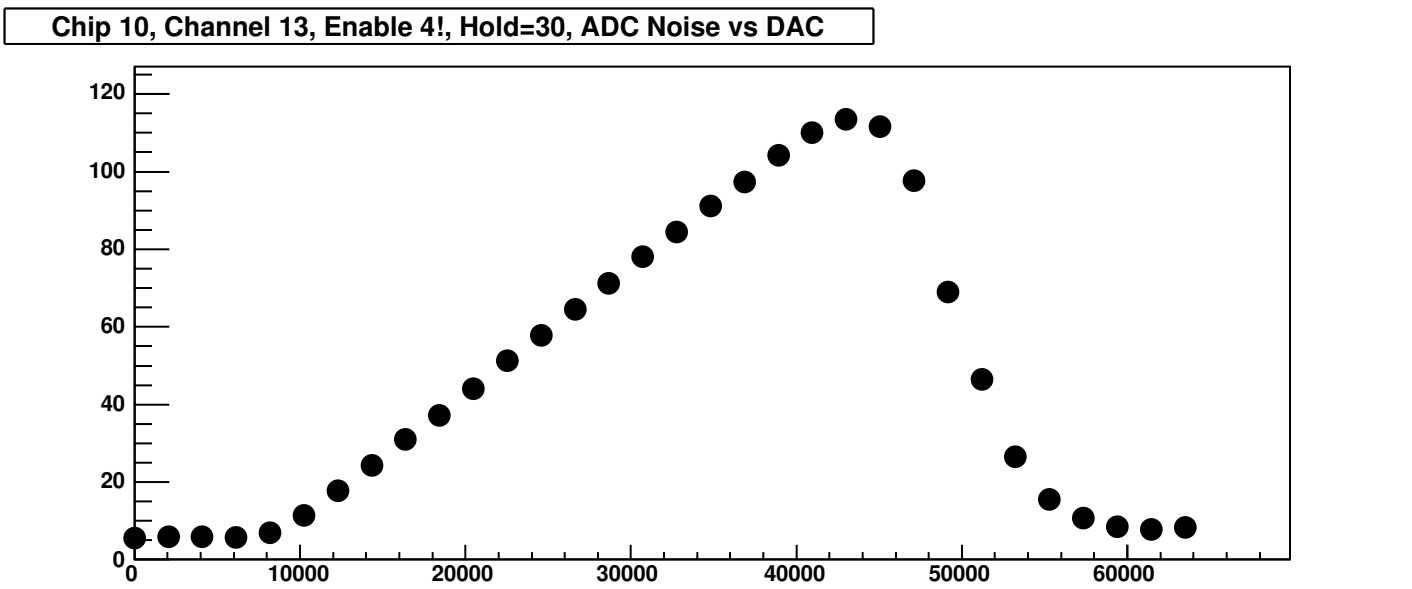

Chip 10, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

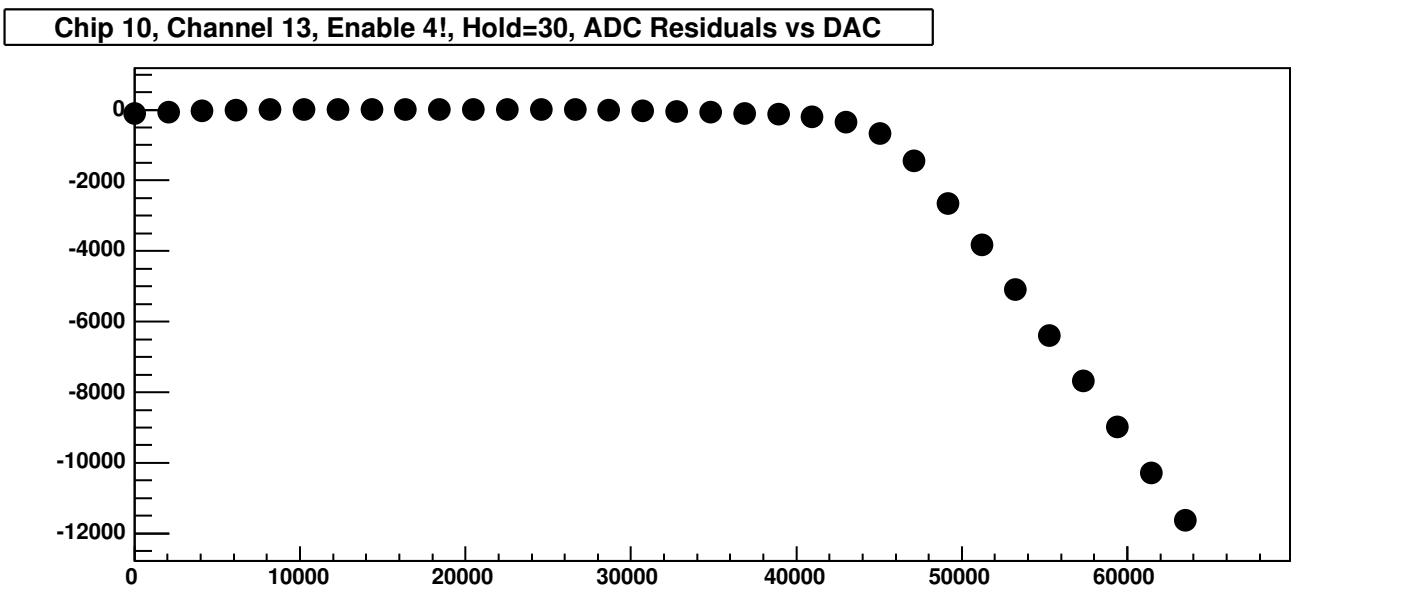

Chip 10, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 2601 / 15
p0 1536 ± 0.1049
p1 -0.0009258 ± 4.272e-06
1550
1500
1450
1400
1350
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

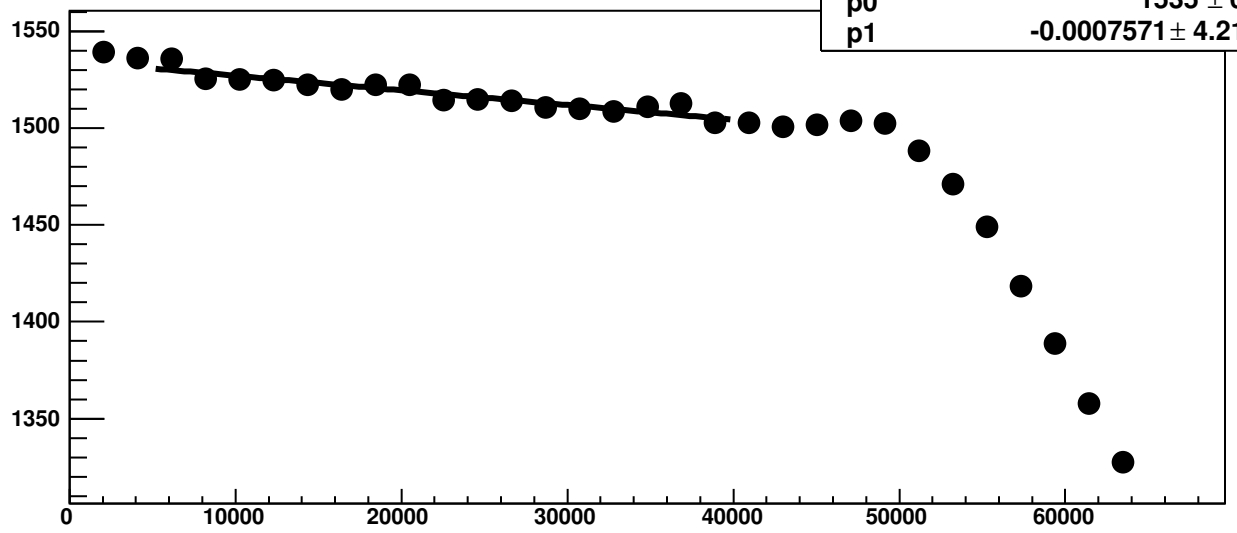

Chip 10, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 7169 / 15
p0 1647 ± 0.273
p1 0.6317 ± 2.817e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

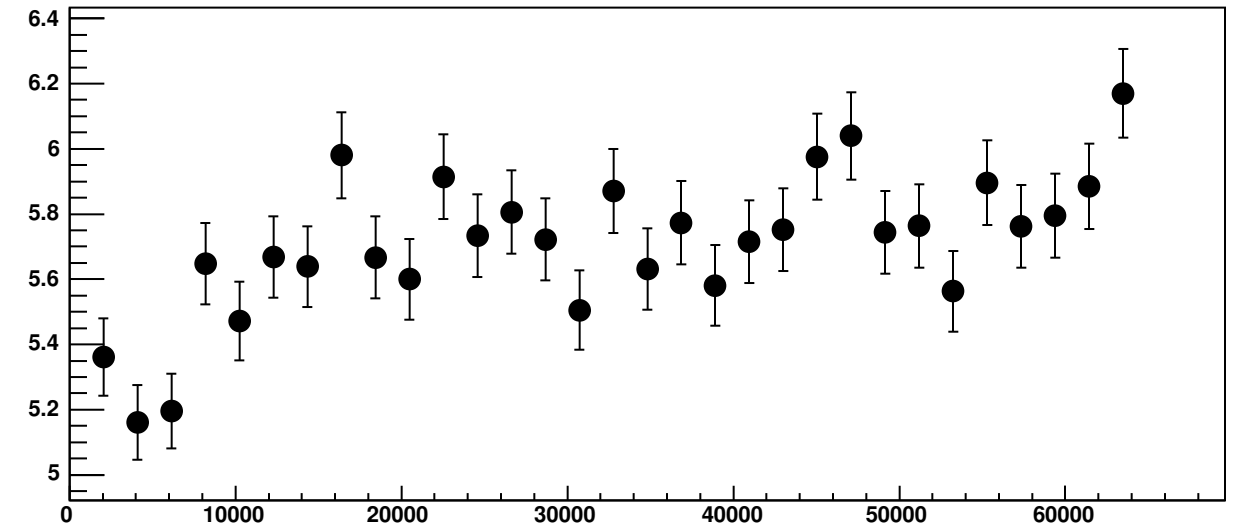

Chip 10, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

Chip 10, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC

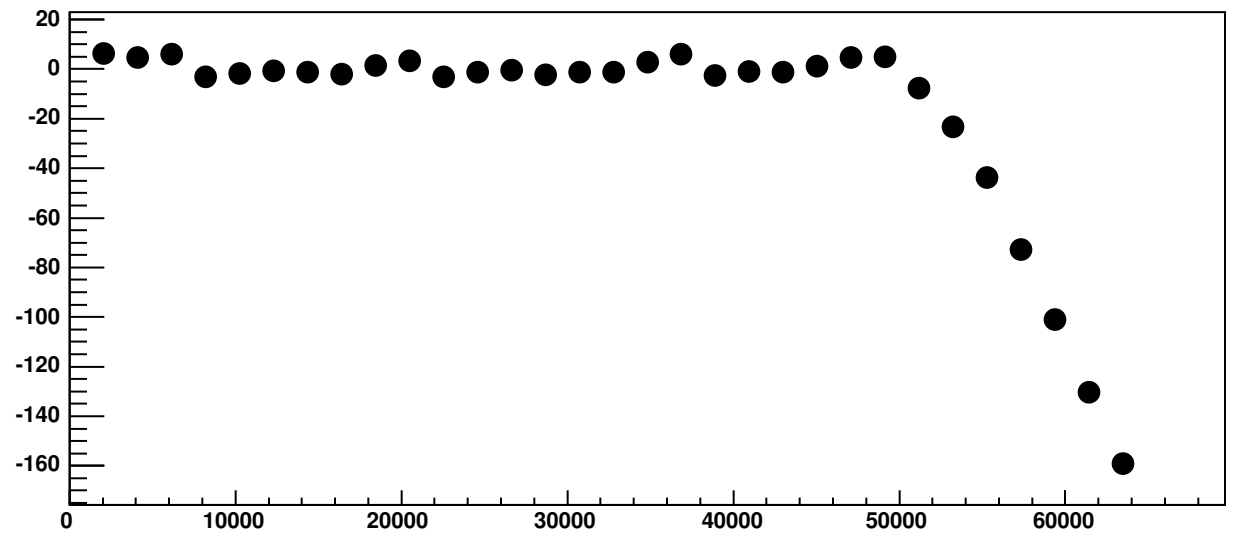
χ^2 / ndf	4644 / 15
p0	1535 ± 0.1032
p1	-0.0007571 ± 4.211e-06

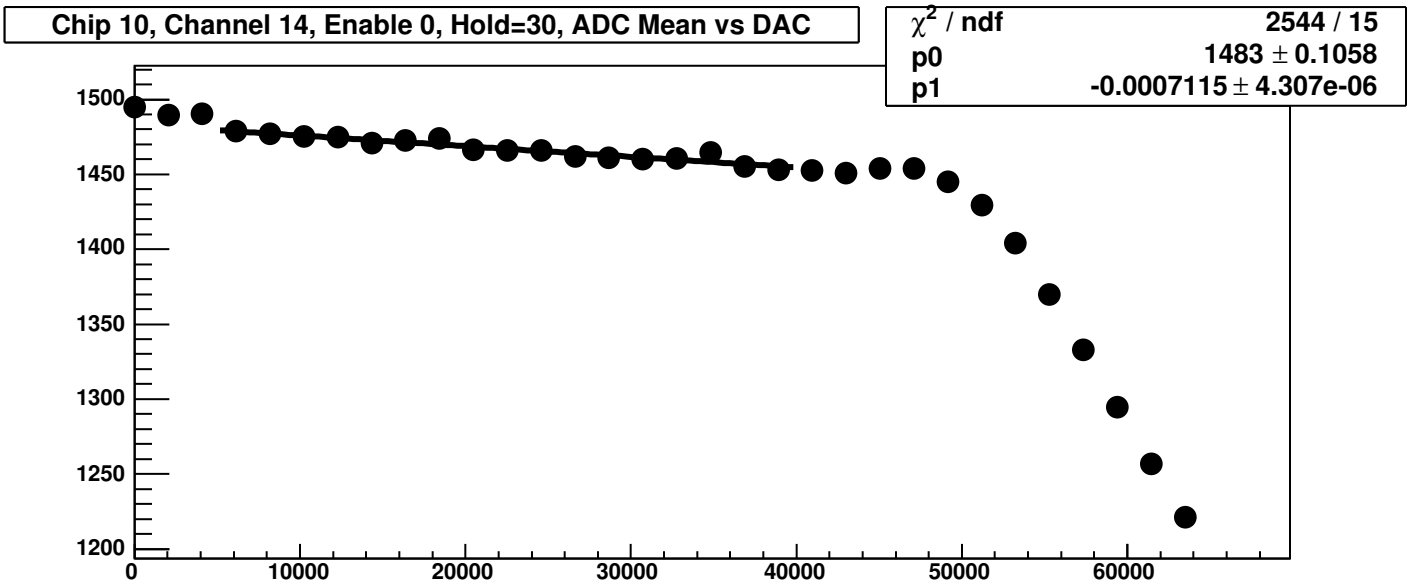


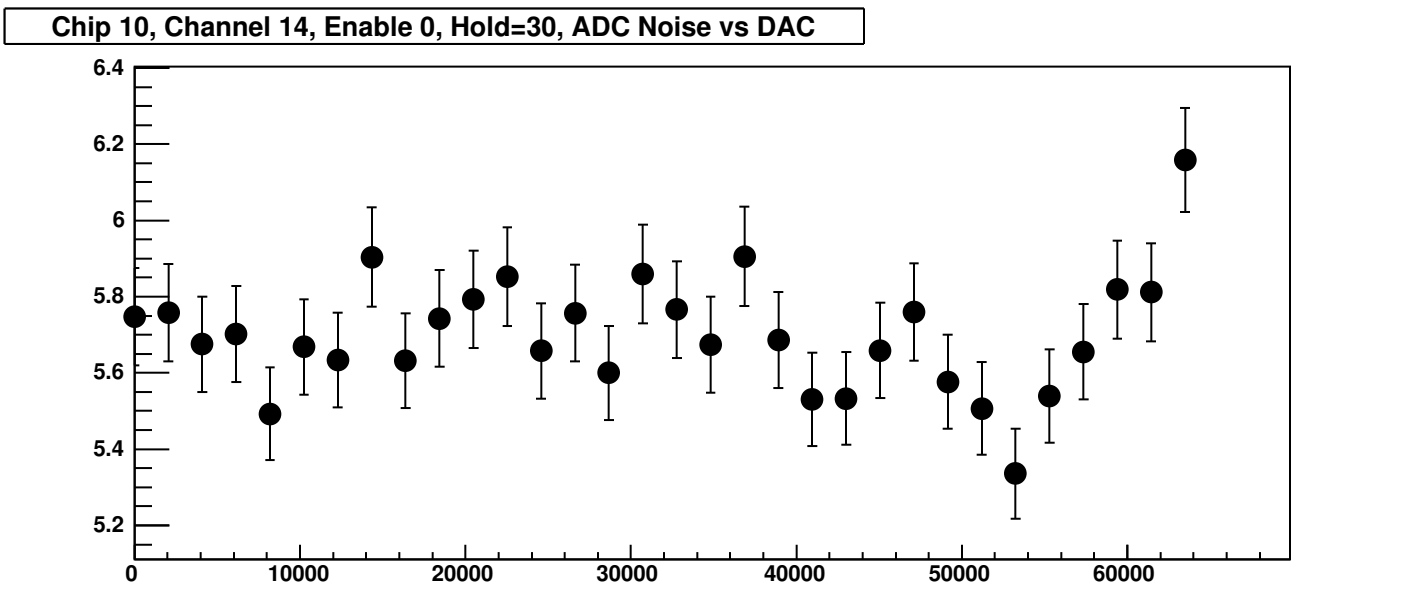
Chip 10, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC

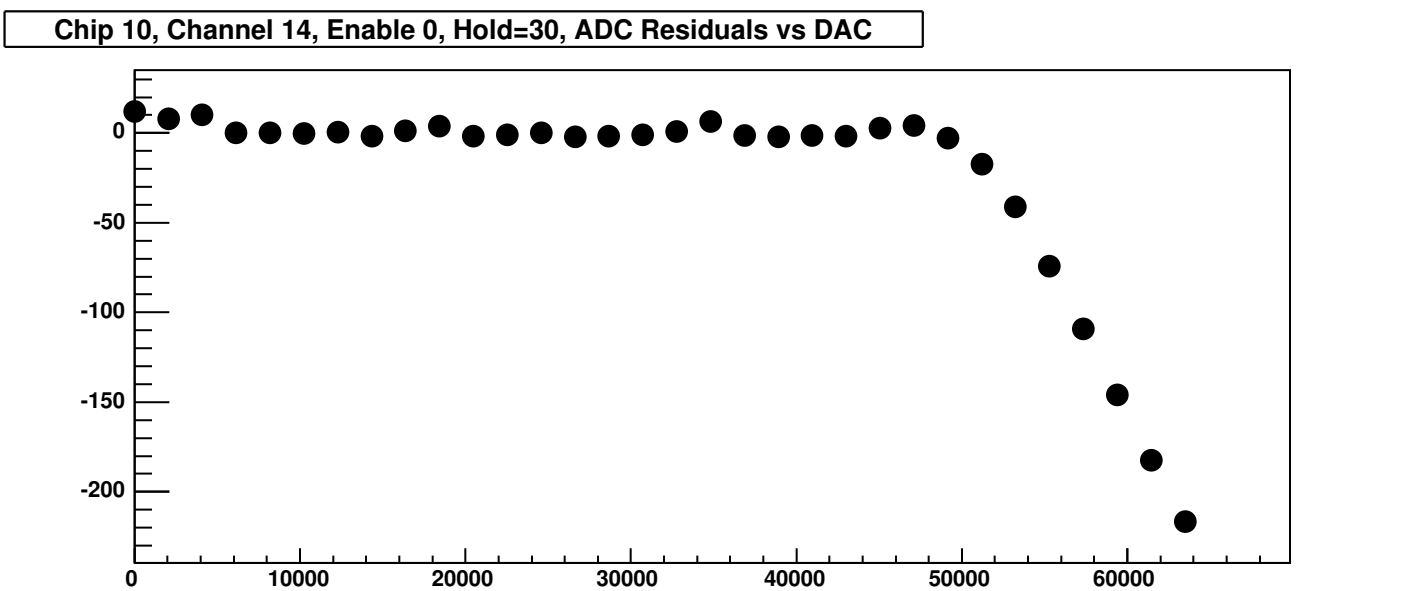


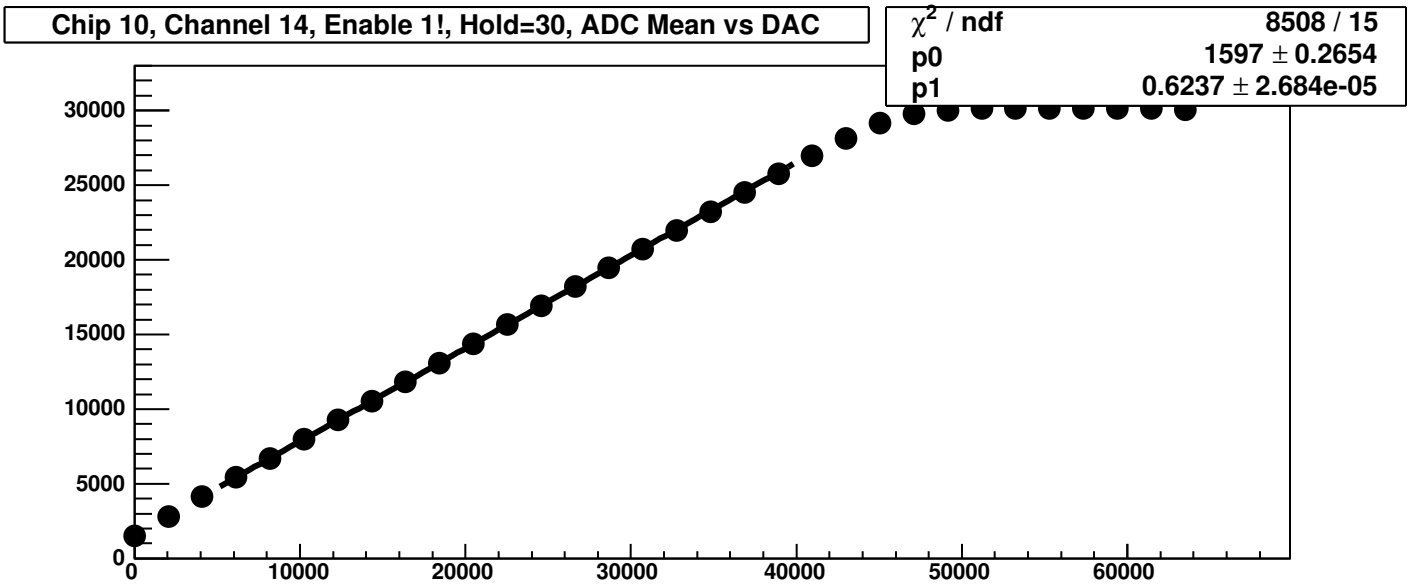
Chip 10, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC

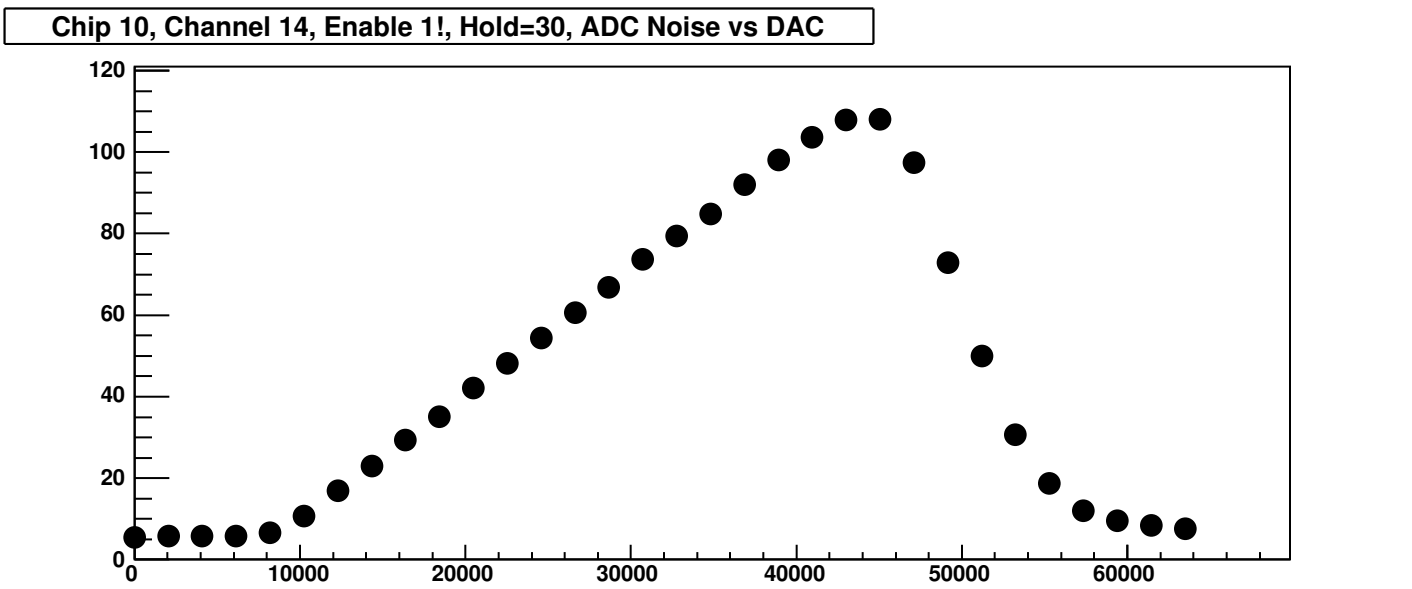


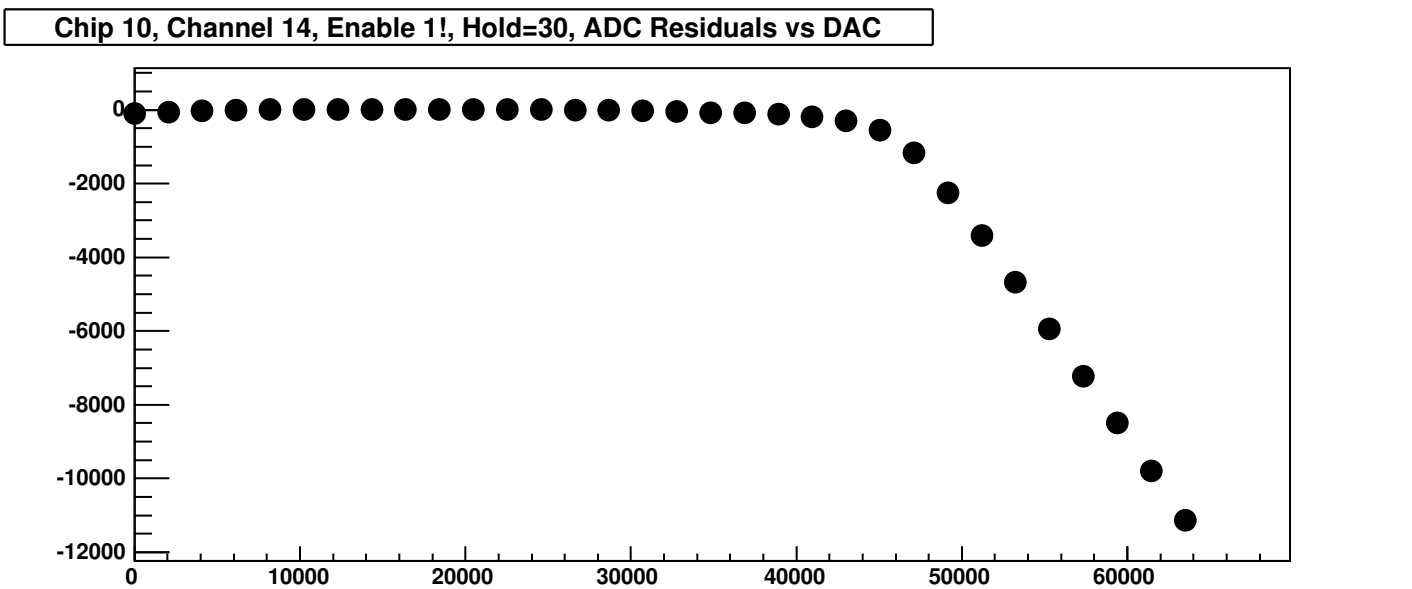

Chip 10, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2544 / 15
p0 1483 ± 0.1058
p1 -0.0007115 ± 4.307e-06
1500
1450
1400
1350
1300
1250
1200
0
10000
20000
30000
40000
50000
60000

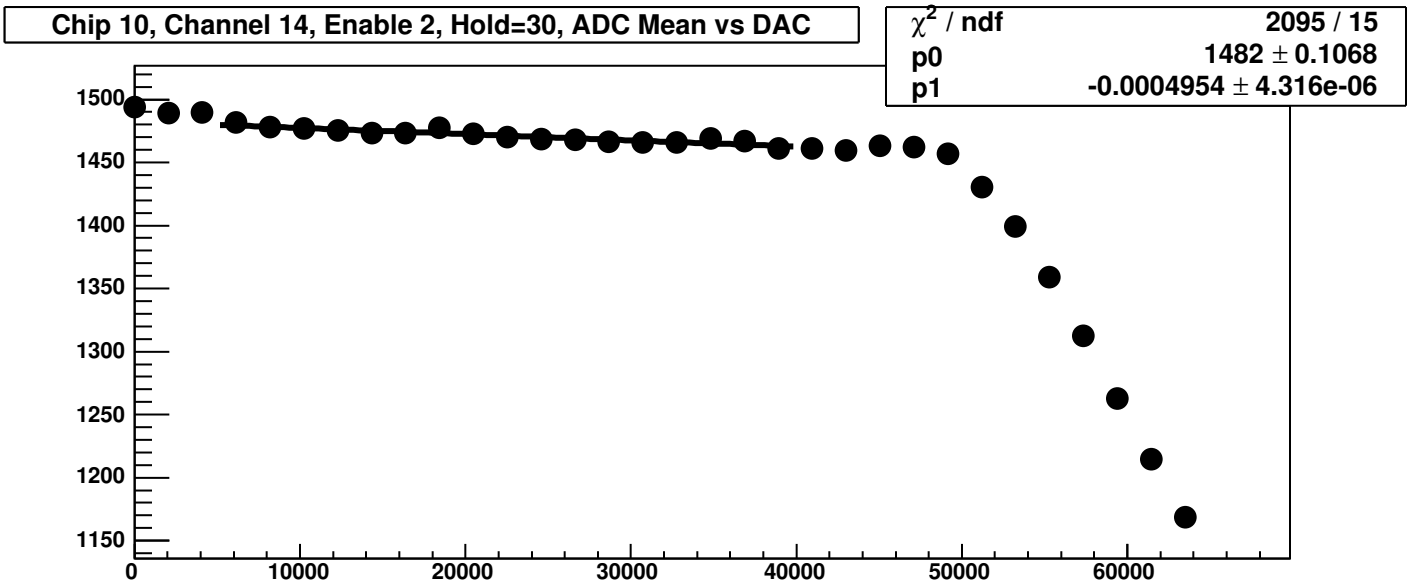

Chip 10, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

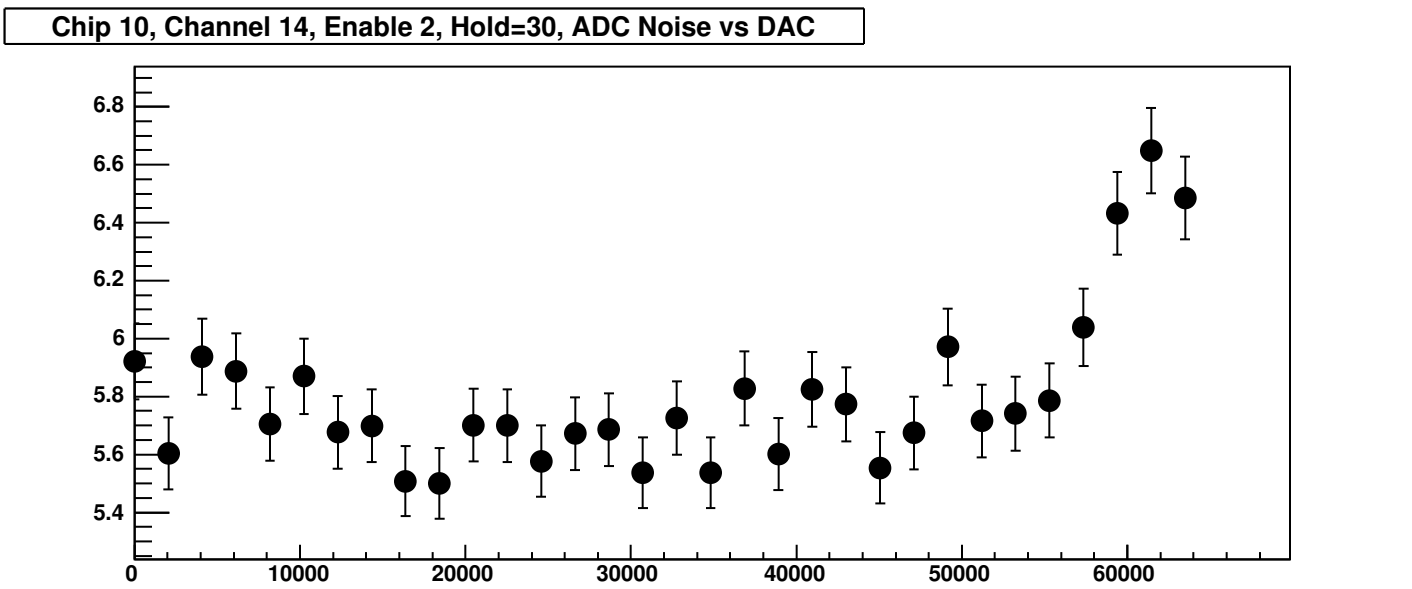

Chip 10, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

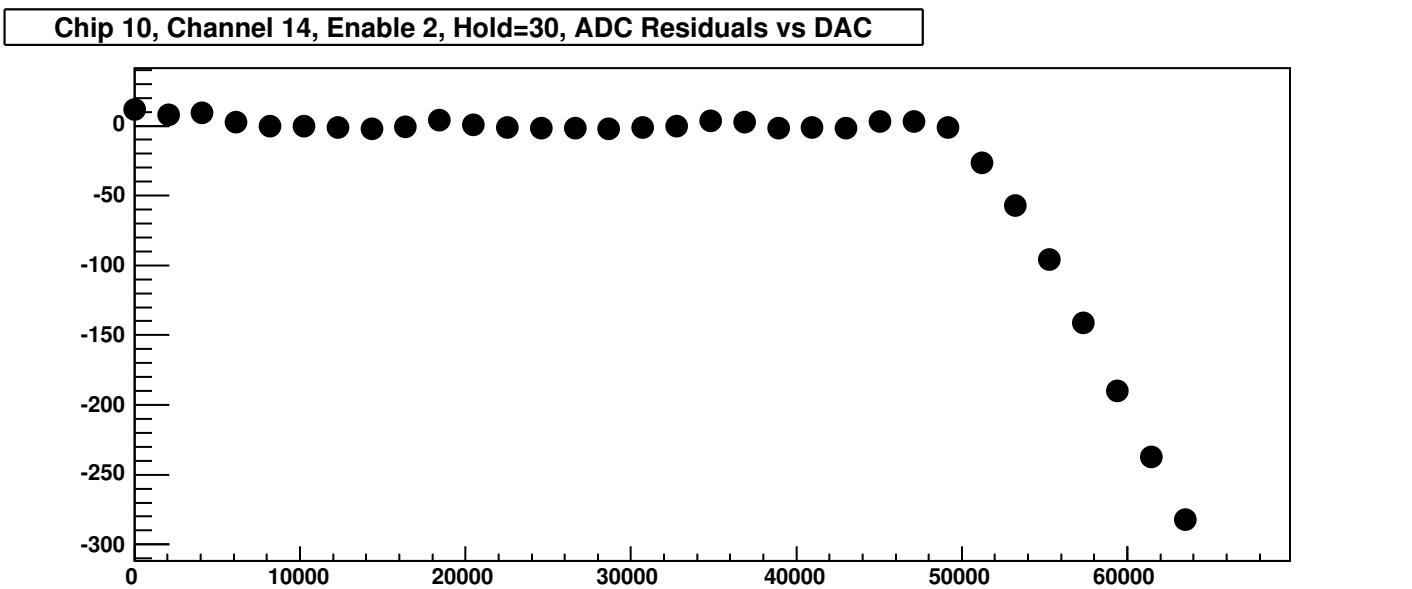

Chip 10, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 8508 / 15
p0 1597 ± 0.2654
p1 0.6237 ± 2.684e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

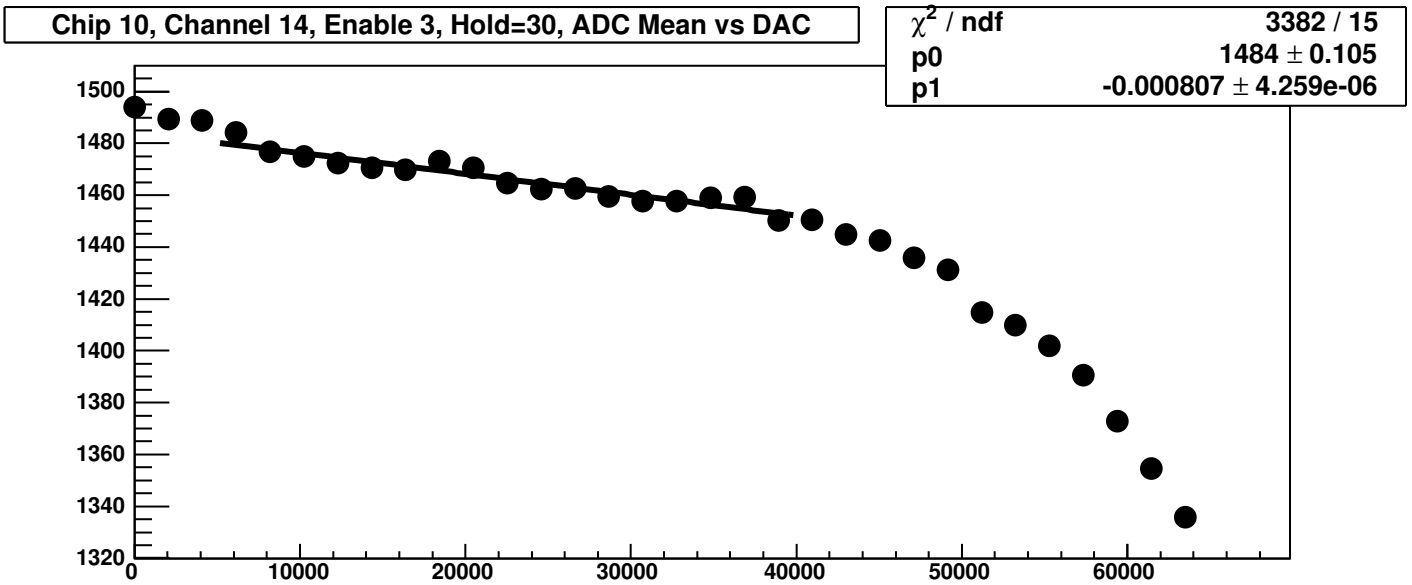

Chip 10, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

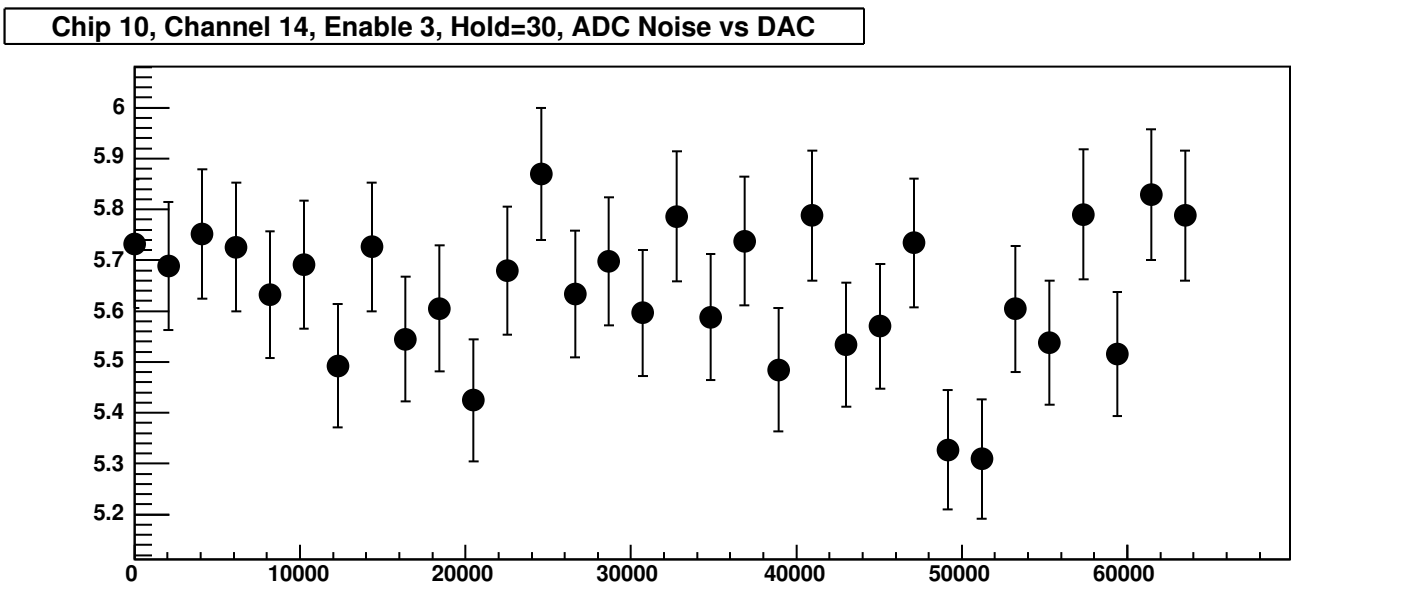

Chip 10, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

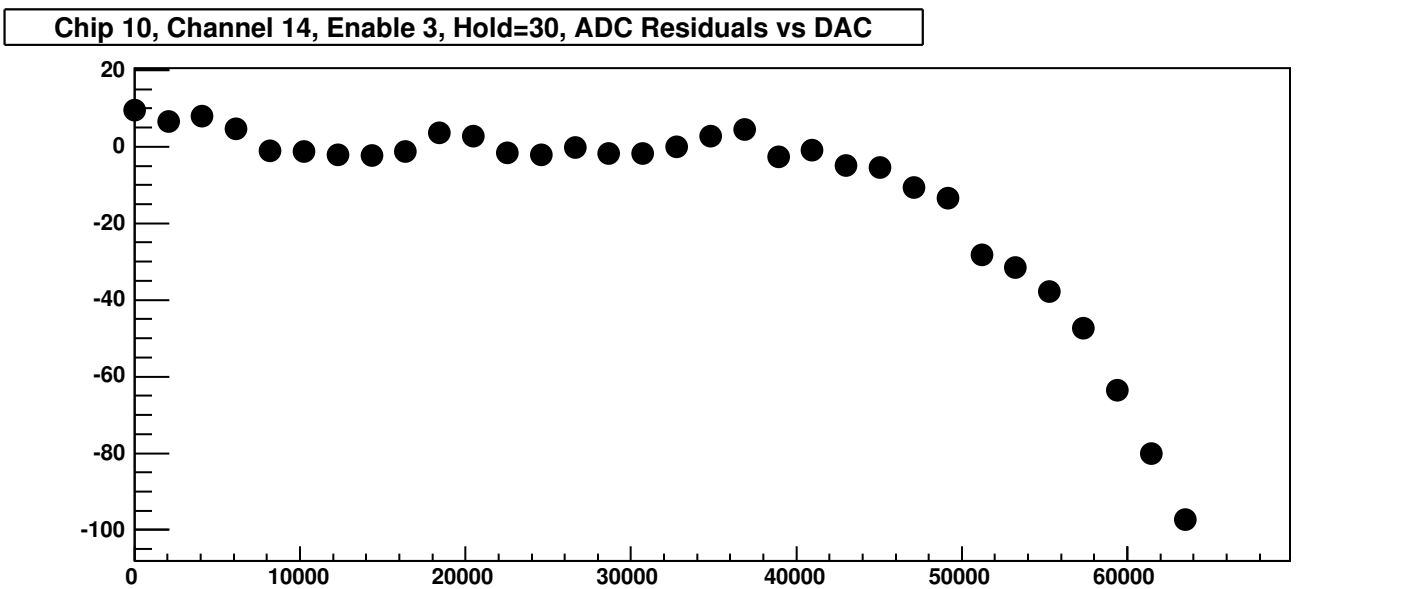

Chip 10, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2095 / 15
p0 1482 ± 0.1068
p1 -0.0004954 ± 4.316e-06
1500
1450
1400
1350
1300
1250
1200
1150
0
10000
20000
30000
40000
50000
60000

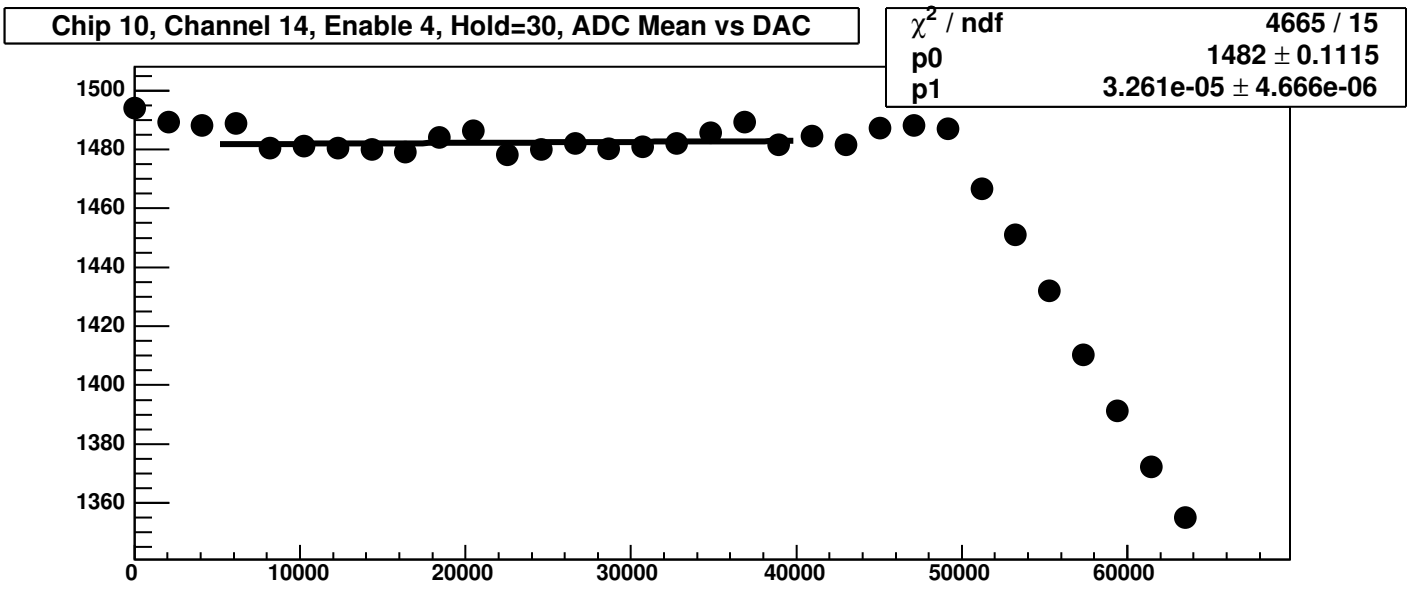

Chip 10, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

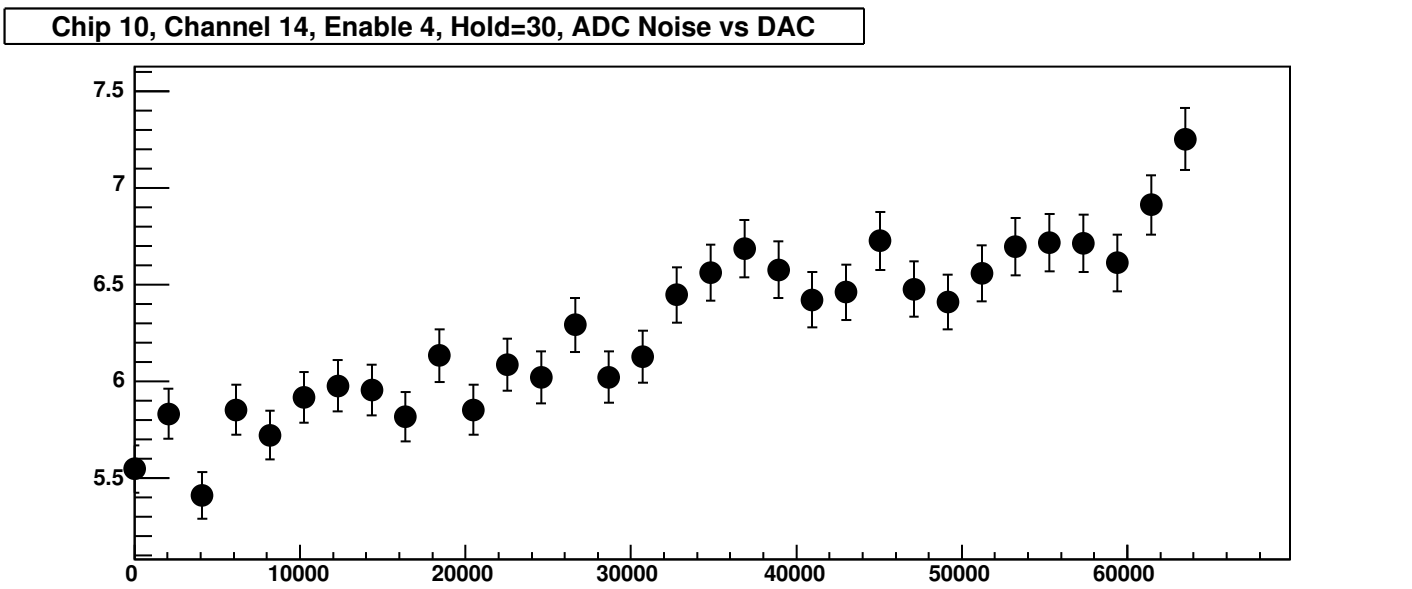

Chip 10, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

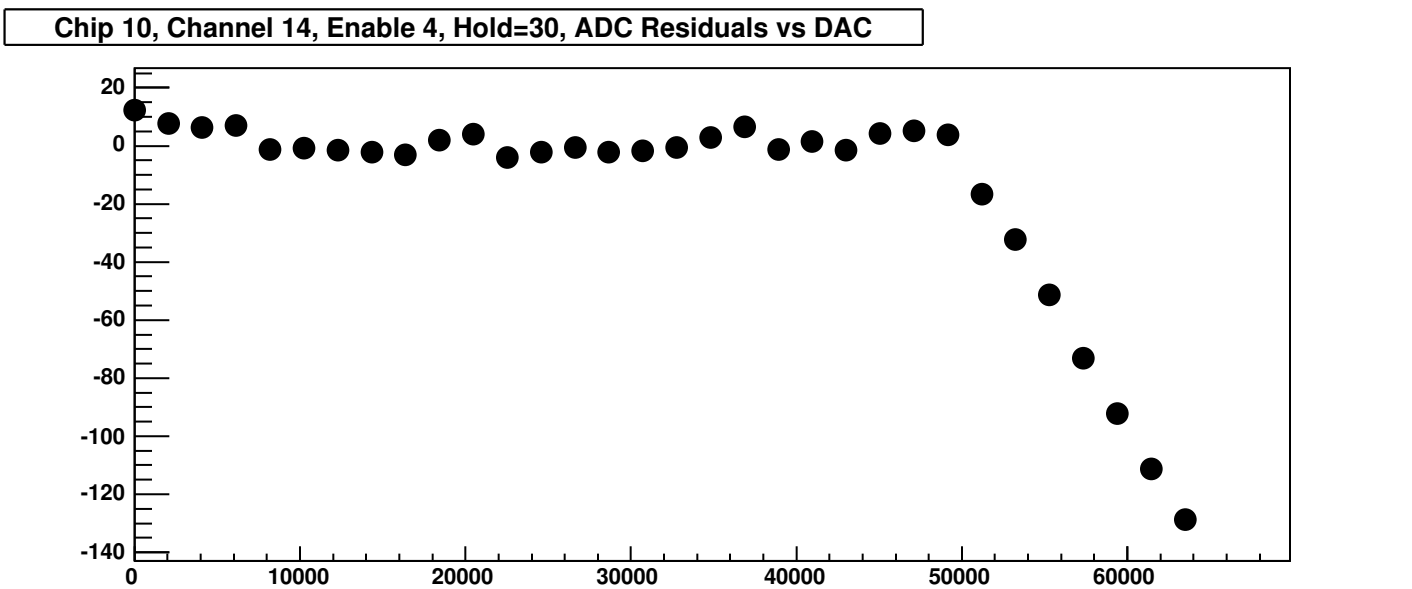

Chip 10, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3382 / 15
p0 1484 ± 0.105
p1 -0.000807 ± 4.259e-06
1500
1480
1460
1440
1420
1400
1380
1360
1340
1320
0
10000
20000
30000
40000
50000
60000

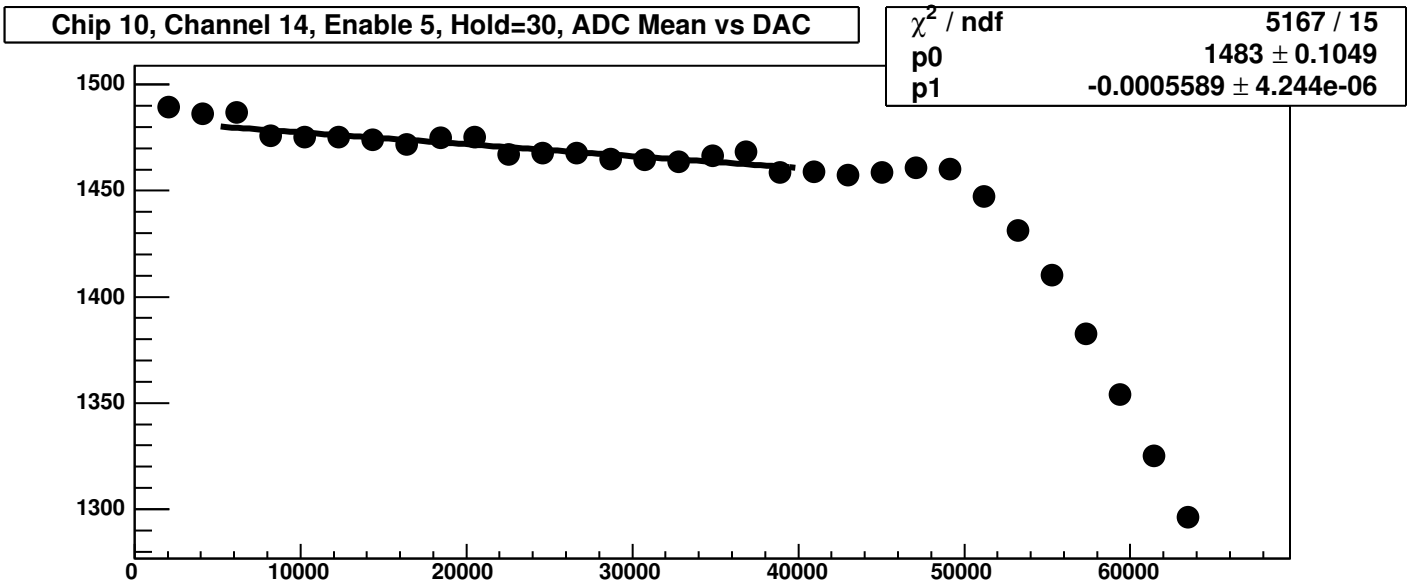

Chip 10, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
5.2
0
10000
20000
30000
40000
50000
60000

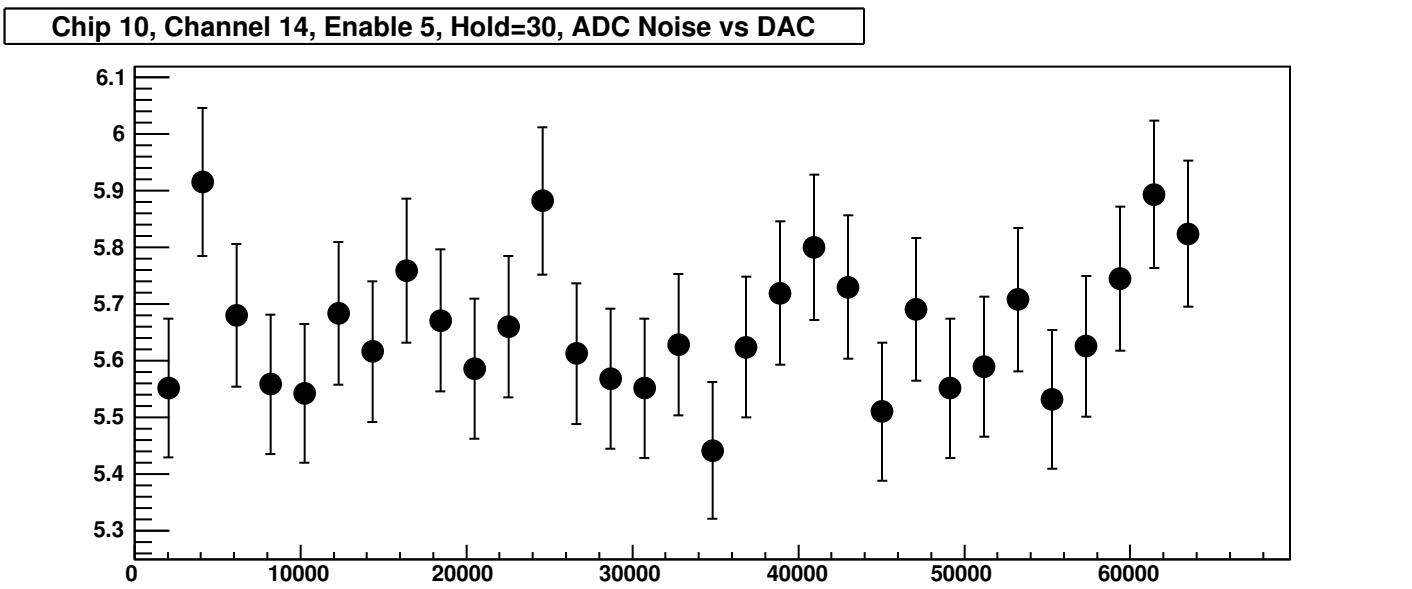

Chip 10, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

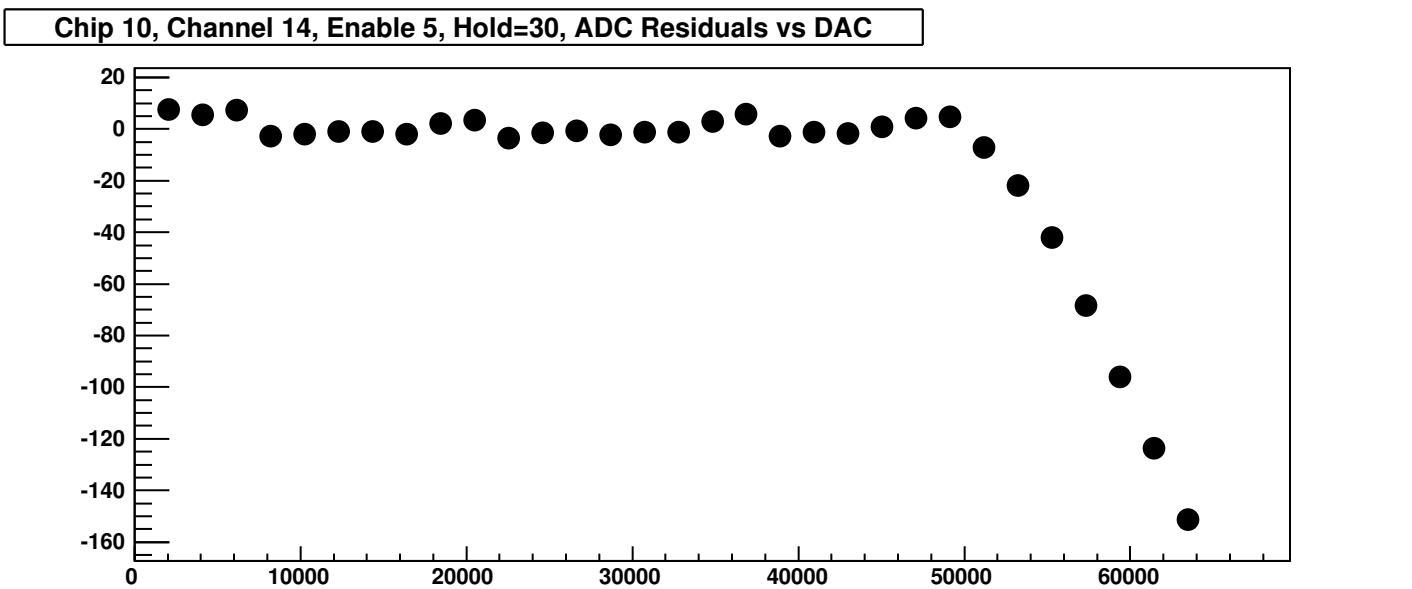

Chip 10, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4665 / 15
p0 1482 ± 0.1115
p1 3.261e-05 ± 4.666e-06
1500
1480
1460
1440
1420
1400
1380
1360
0
10000
20000
30000
40000
50000
60000

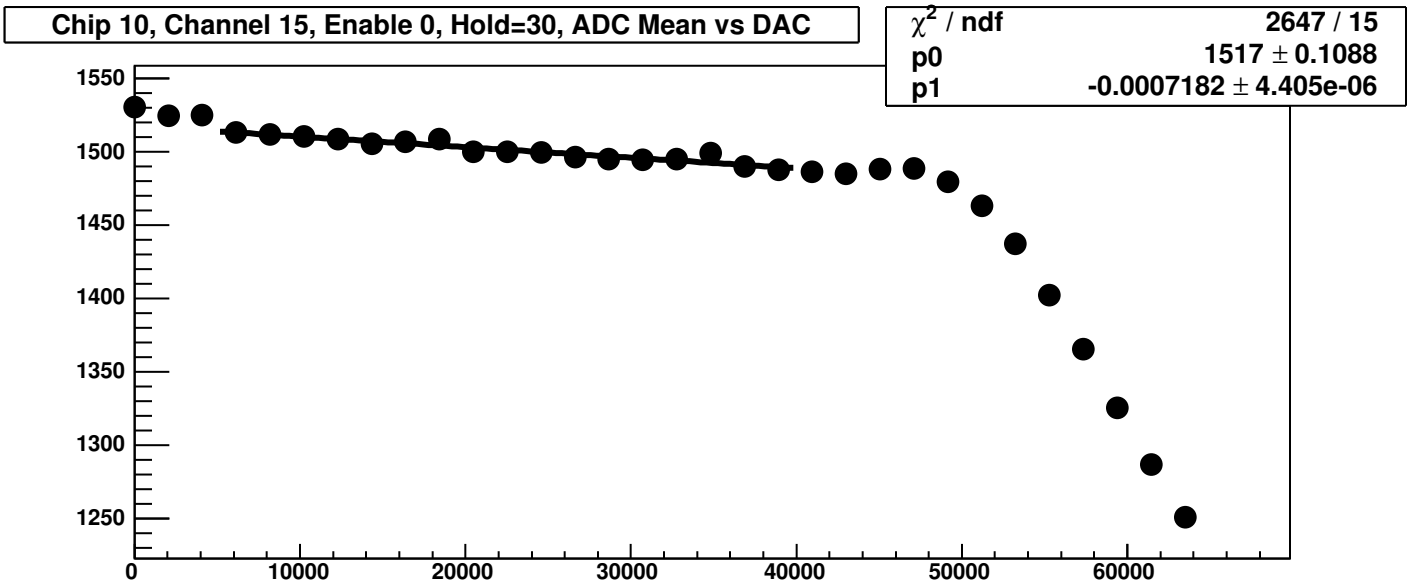

Chip 10, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000

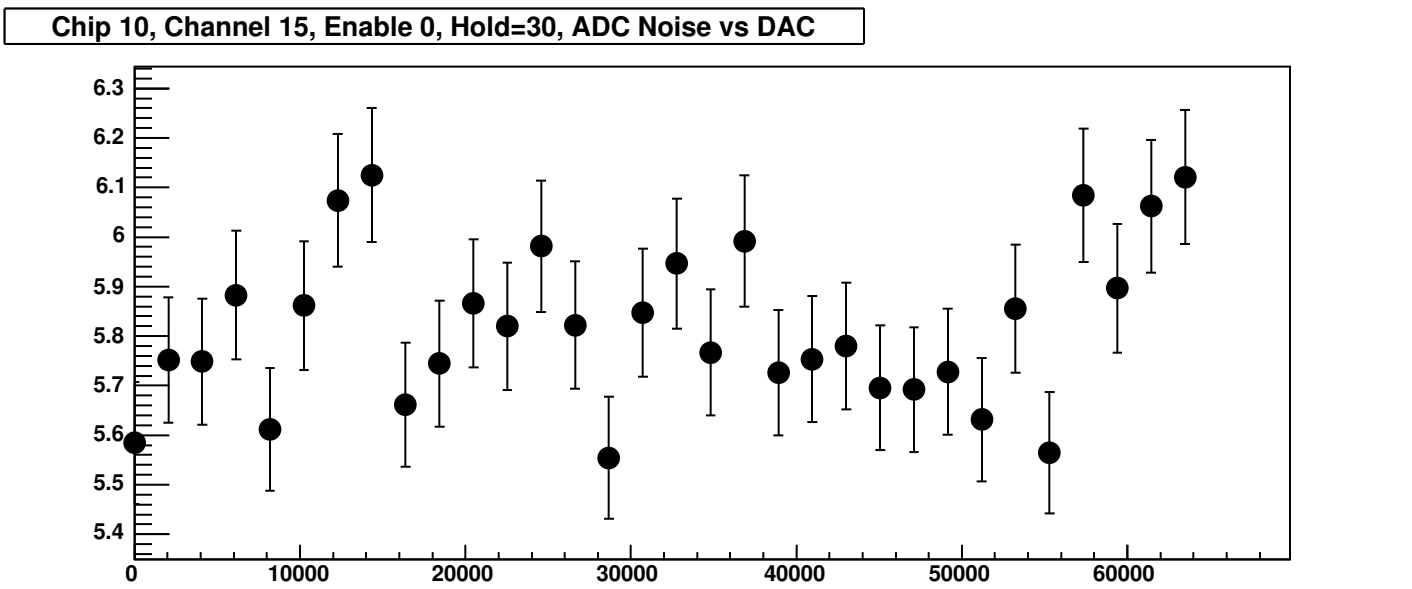

Chip 10, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

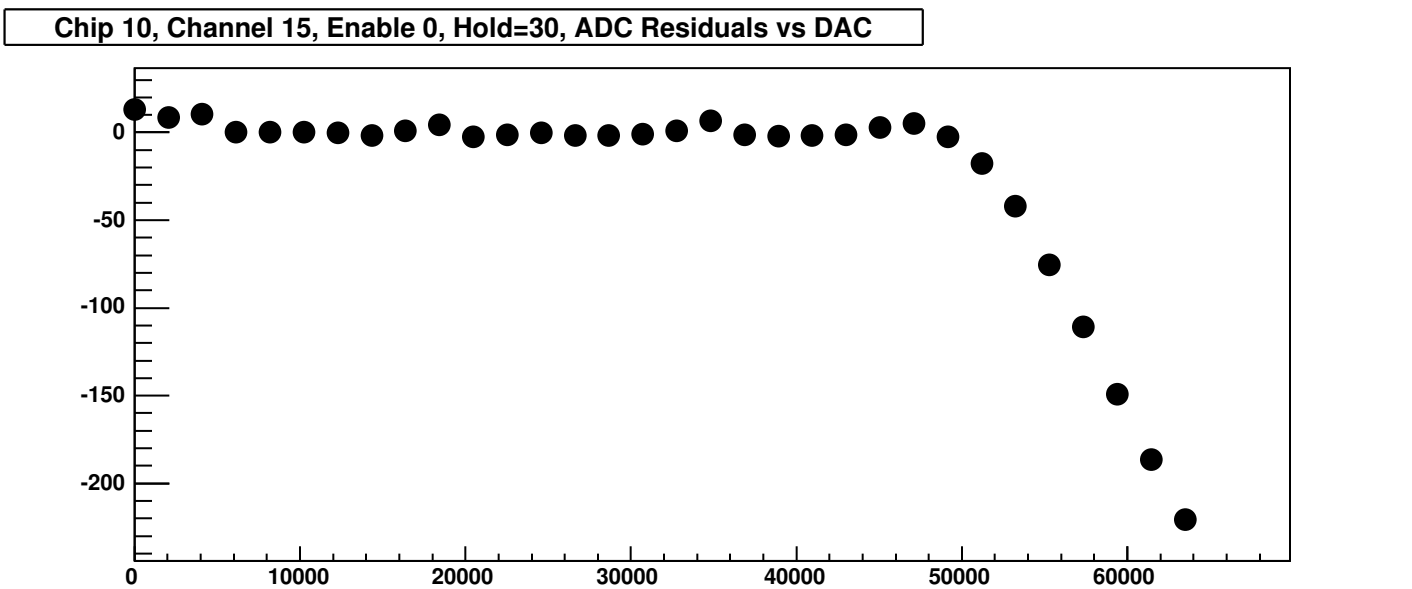

Chip 10, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5167 / 15
p0 1483 ± 0.1049
p1 -0.0005589 ± 4.244e-06
1500
1450
1400
1350
1300
0
10000
20000
30000
40000
50000
60000

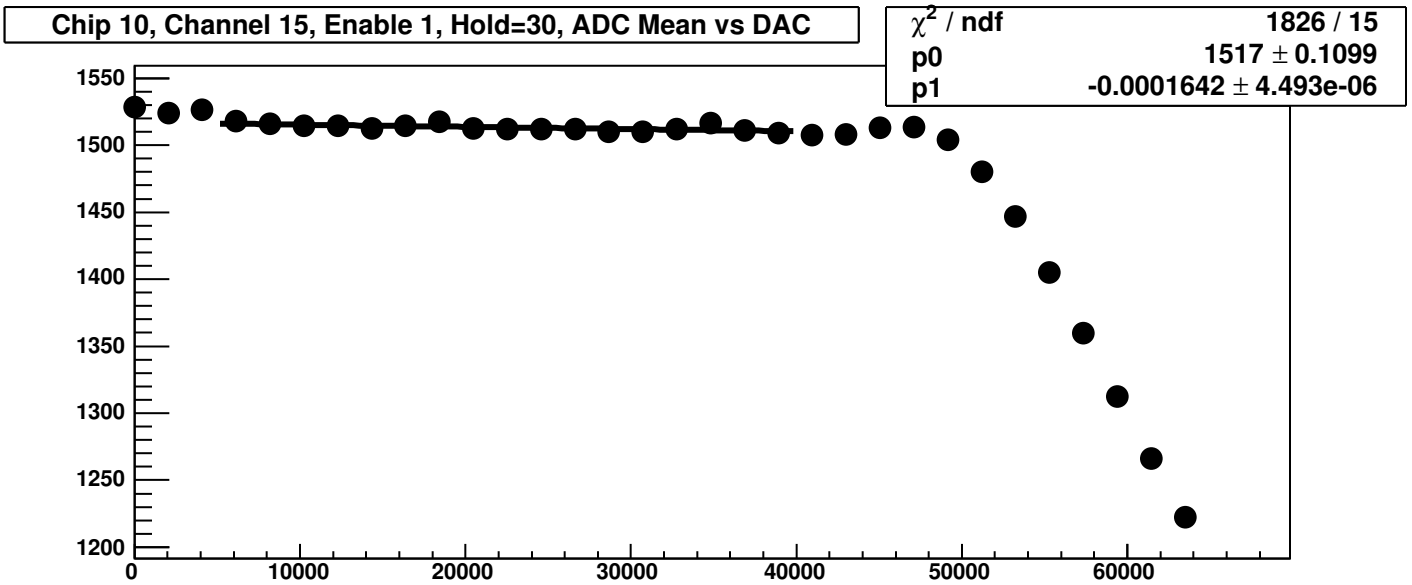

Chip 10, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
0
10000
20000
30000
40000
50000
60000

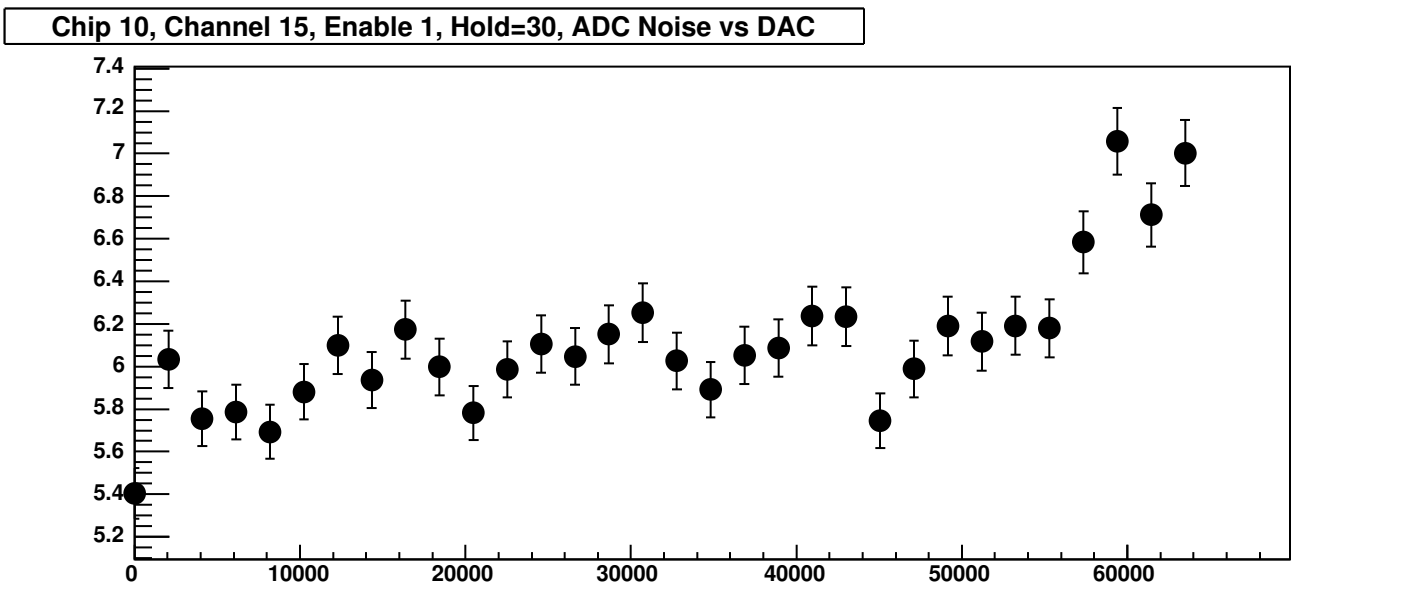

Chip 10, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

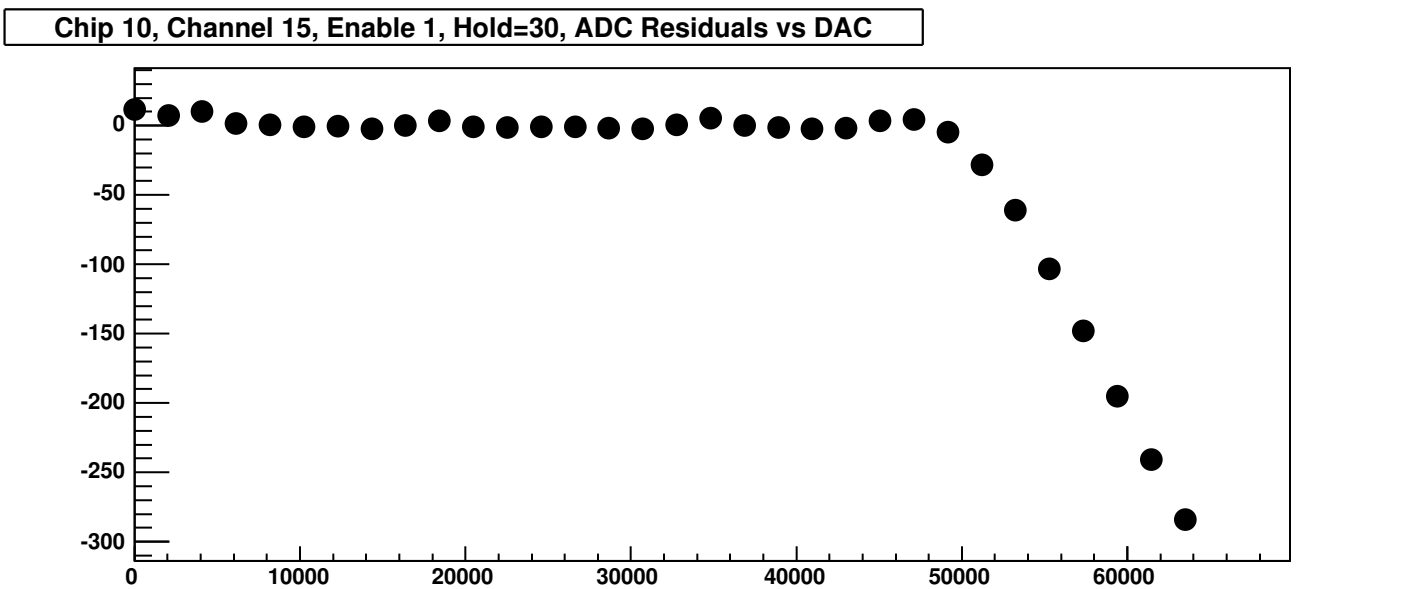

Chip 10, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2647 / 15
p0 1517 ± 0.1088
p1 -0.0007182 ± 4.405e-06
1550
1500
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

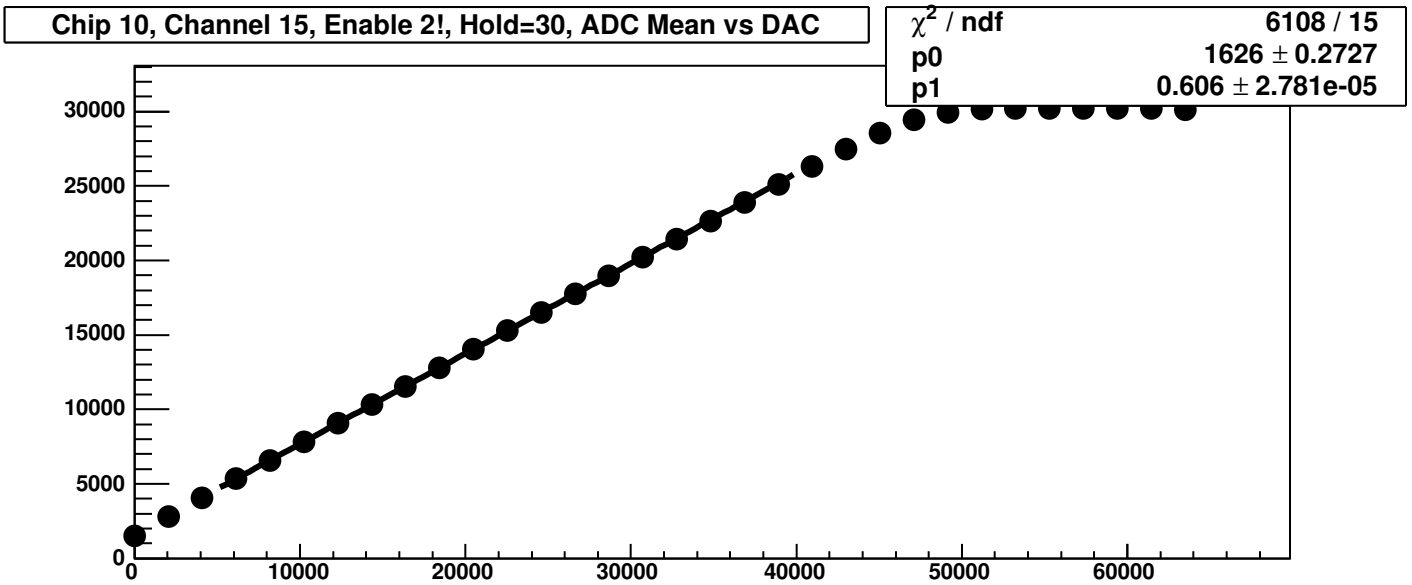

Chip 10, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

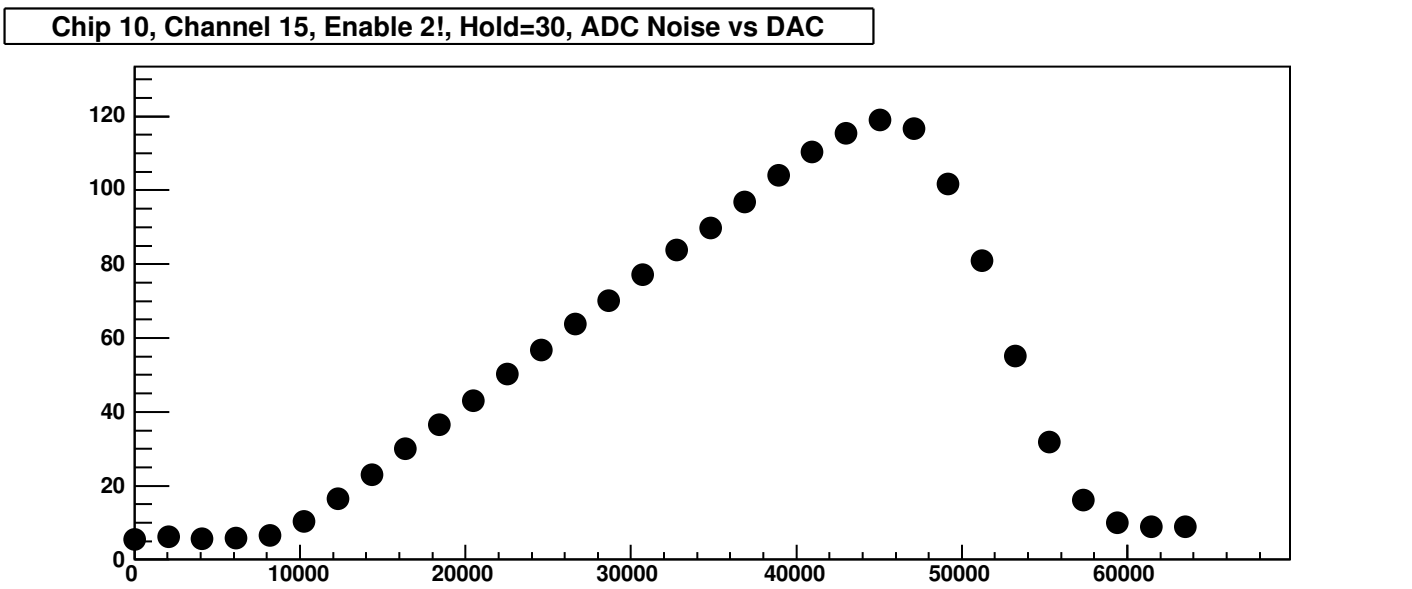

Chip 10, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

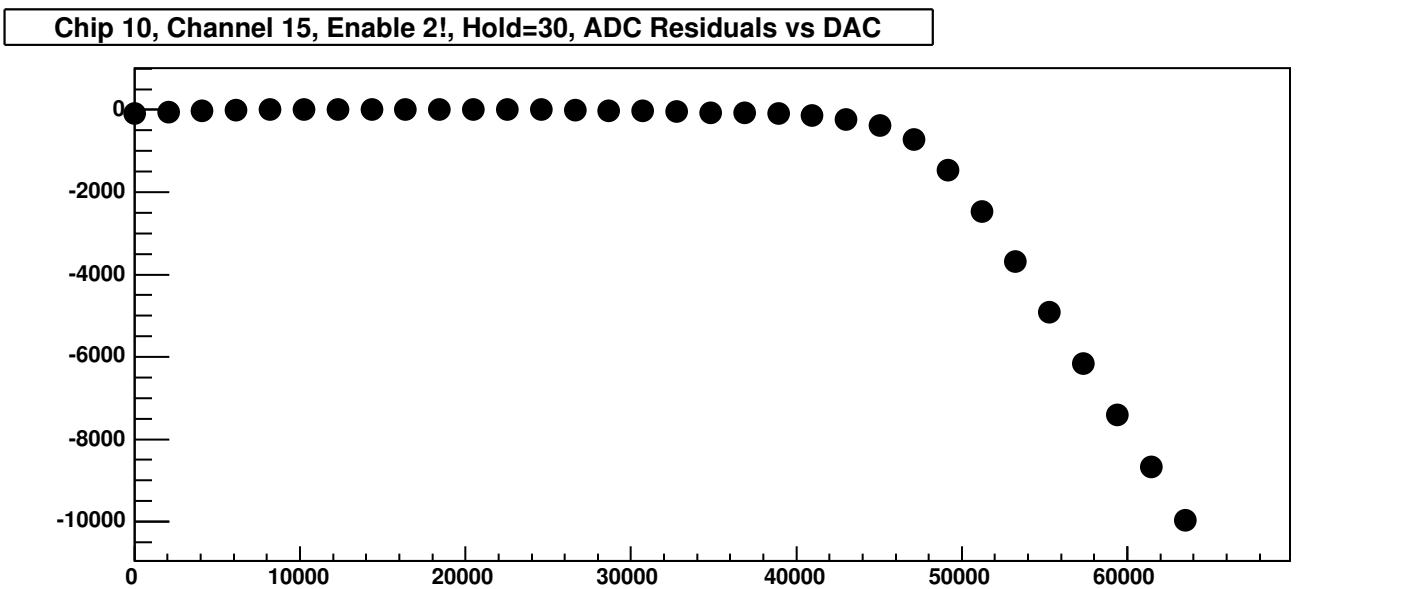

Chip 10, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1826 / 15
p0 1517 ± 0.1099
p1 -0.0001642 ± 4.493e-06
1550
1500
1450
1400
1350
1300
1250
1200
0
10000
20000
30000
40000
50000
60000

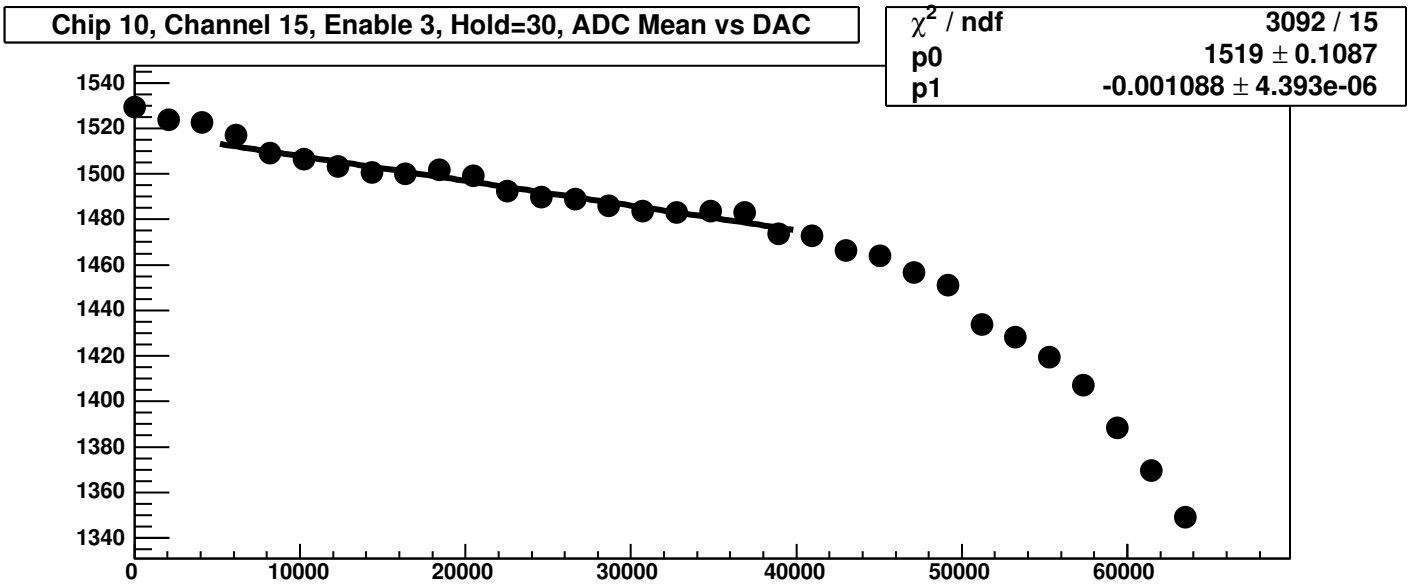

Chip 10, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

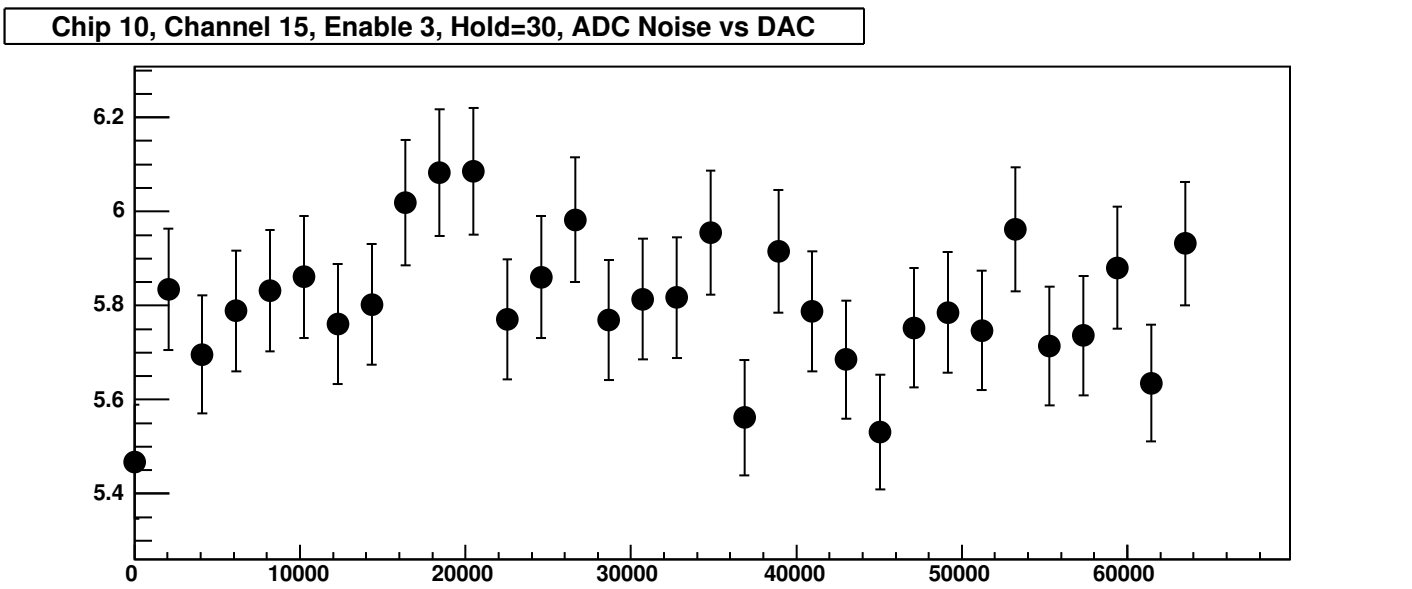

Chip 10, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

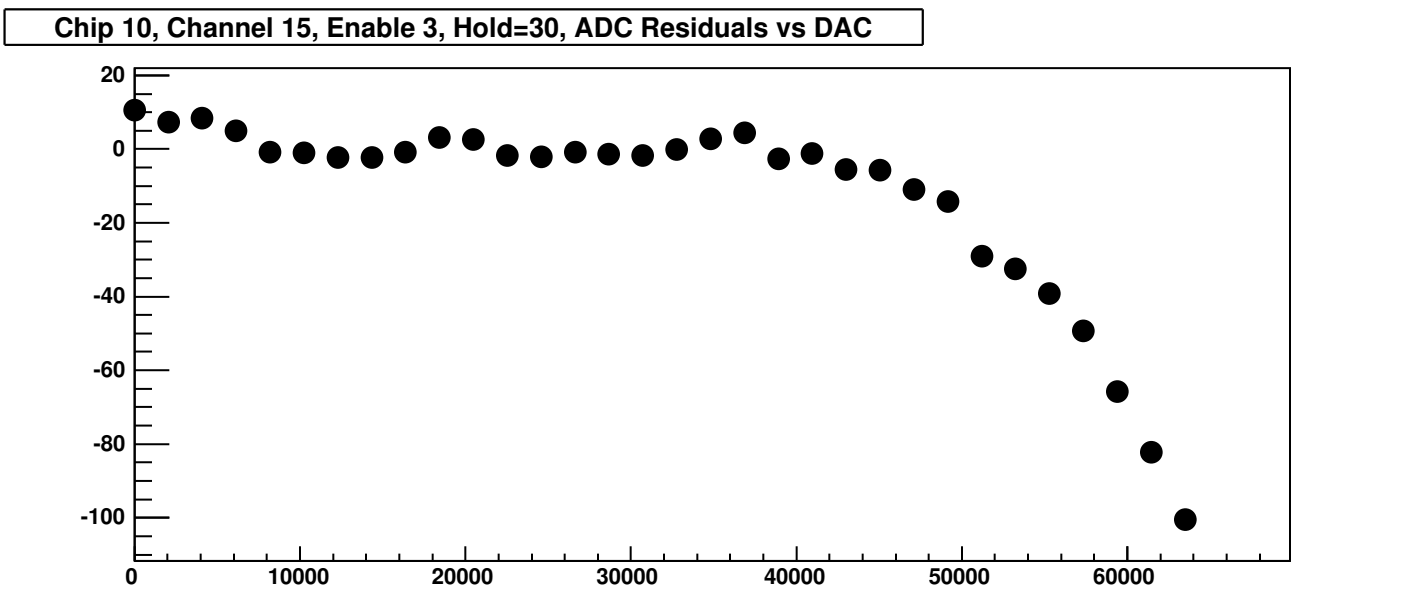

Chip 10, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 6108 / 15
p0 1626 ± 0.2727
p1 0.606 ± 2.781e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

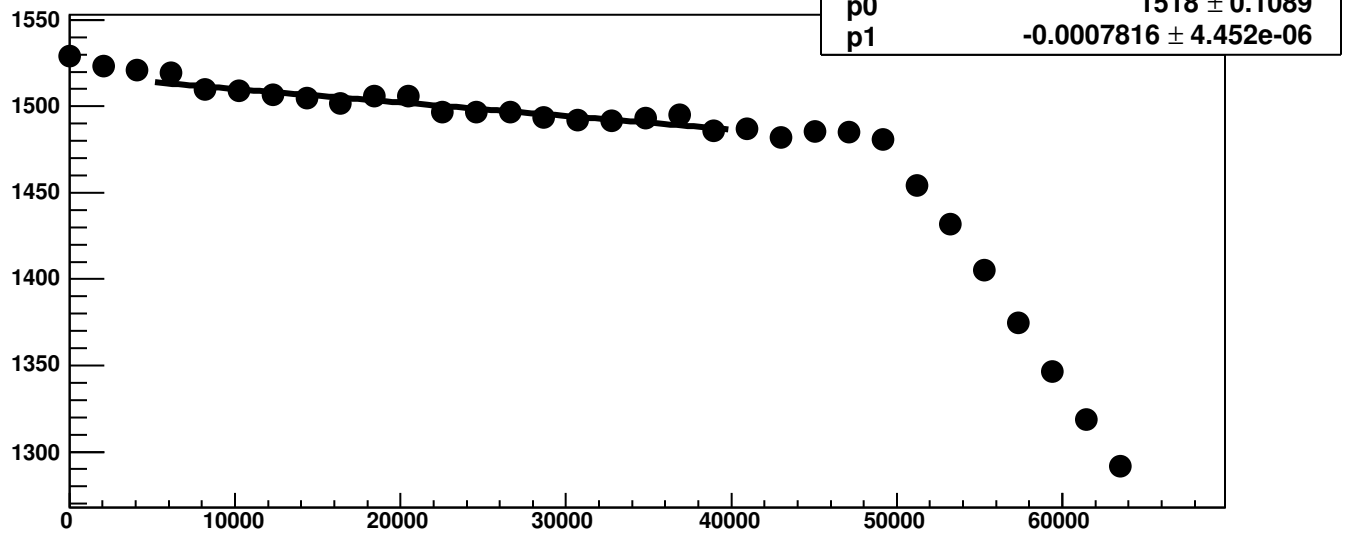

Chip 10, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3092 / 15
p0 1519 ± 0.1087
p1 -0.001088 ± 4.393e-06
1540
1520
1500
1480
1460
1440
1420
1400
1380
1360
1340
0
10000
20000
30000
40000
50000
60000

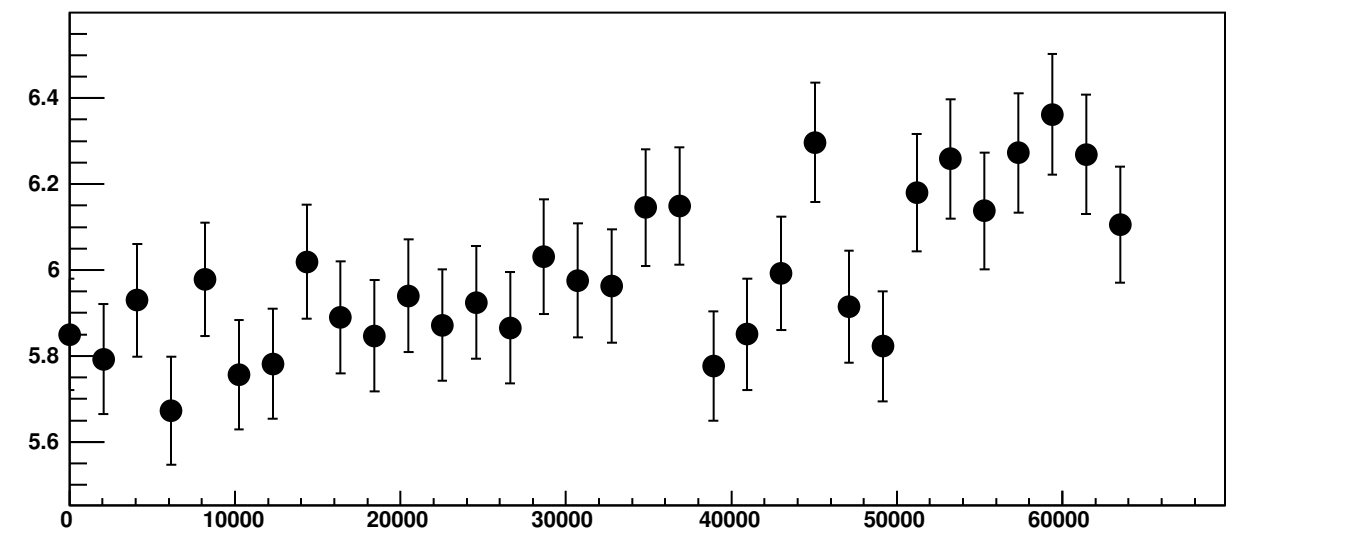

Chip 10, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

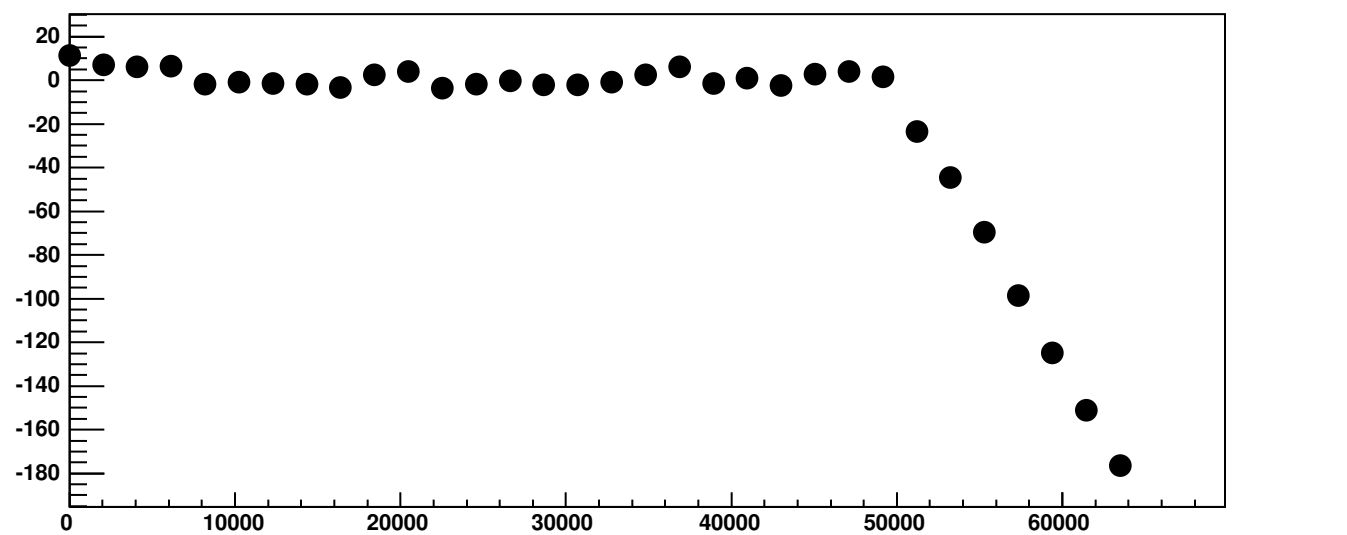
Chip 10, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

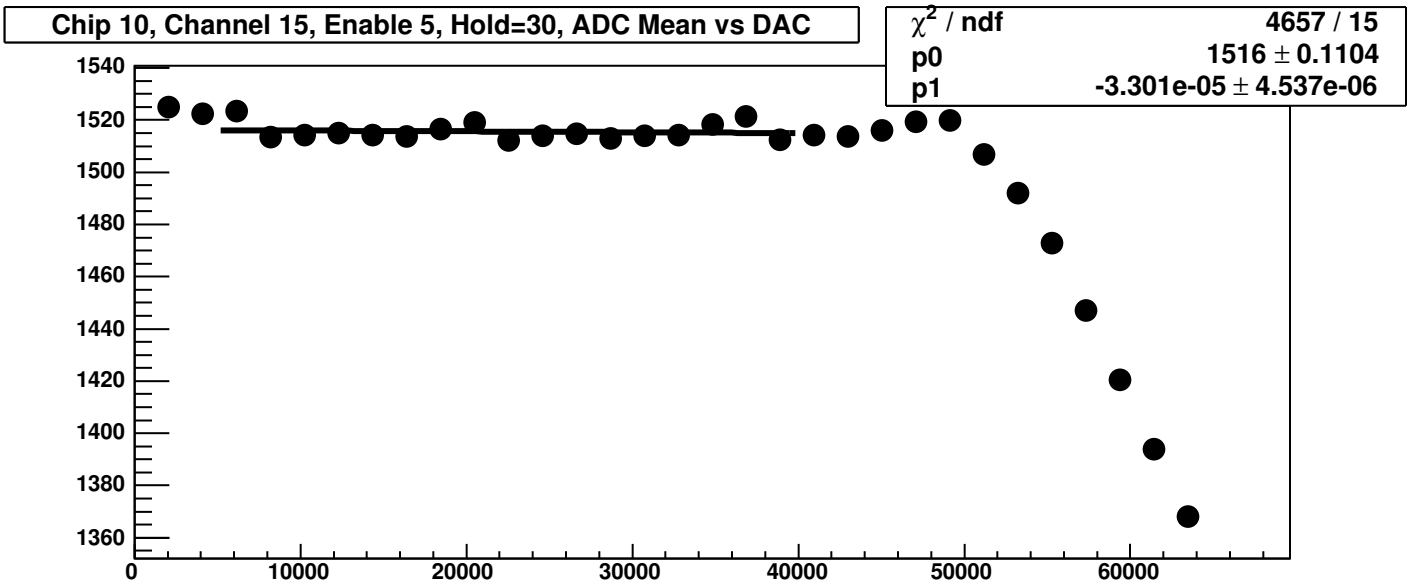


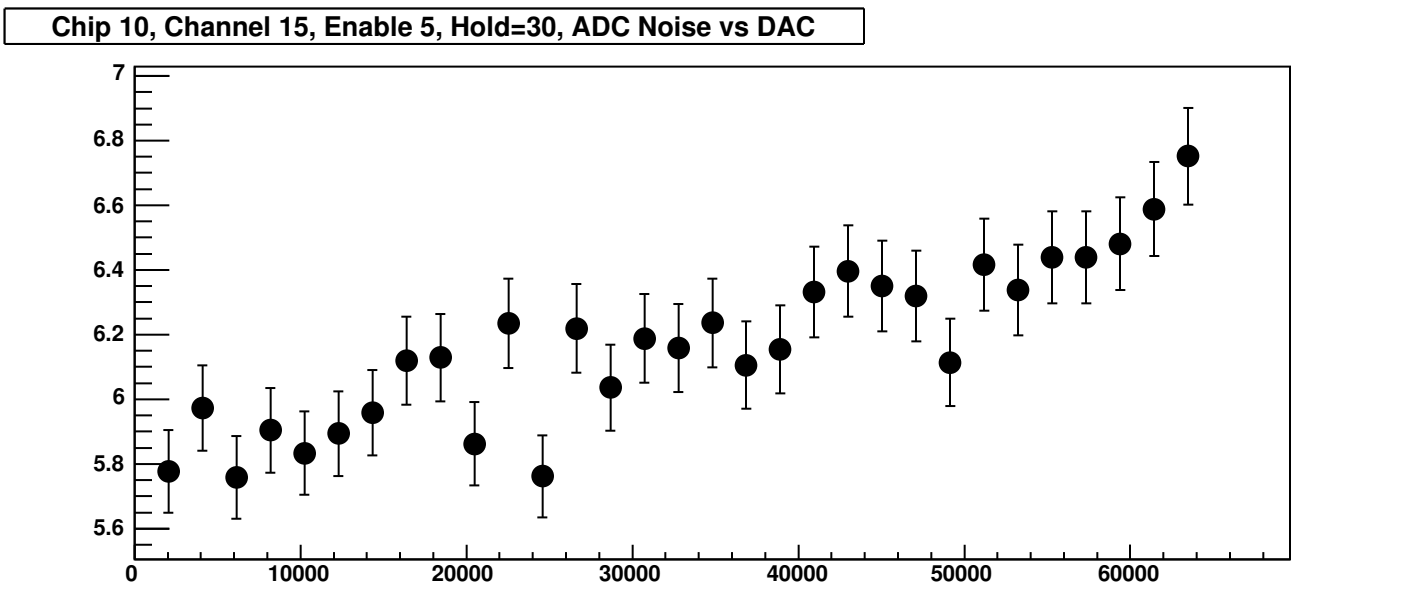
Chip 10, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC

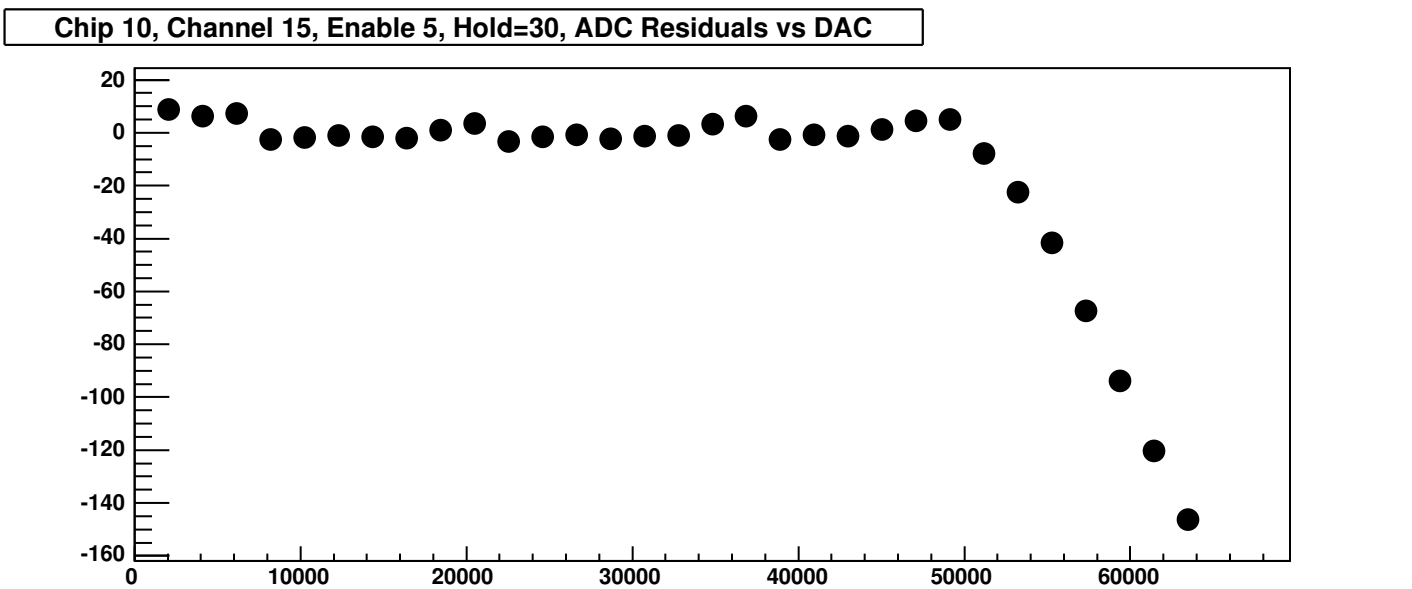


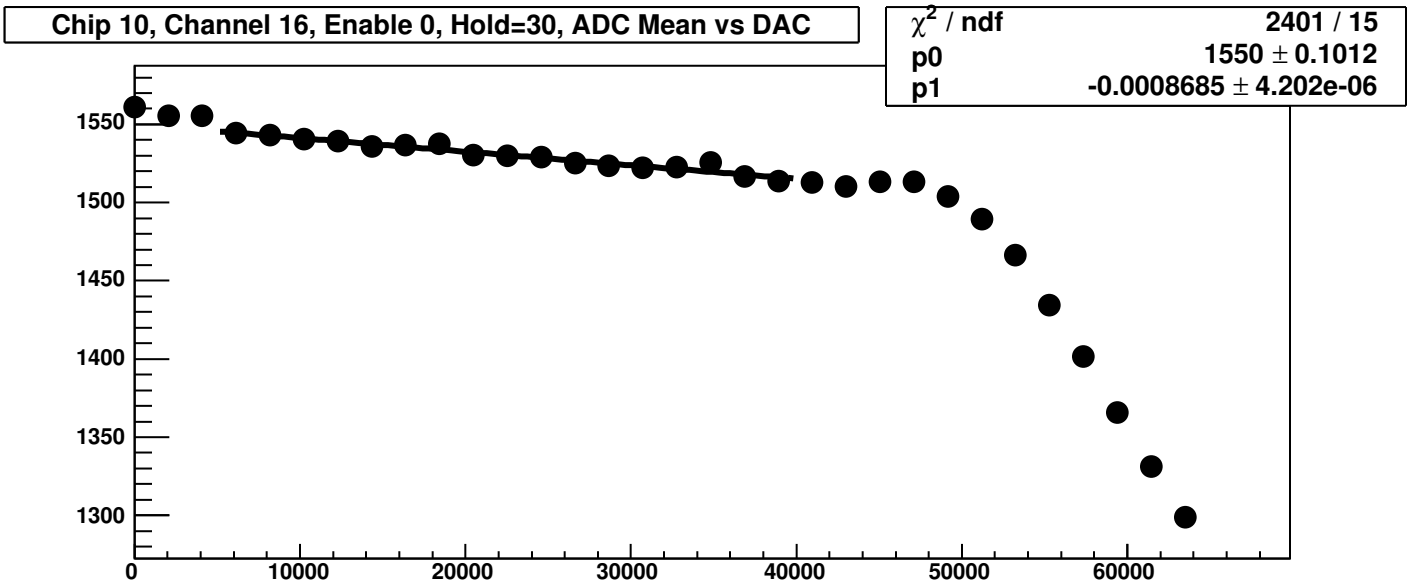
Chip 10, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

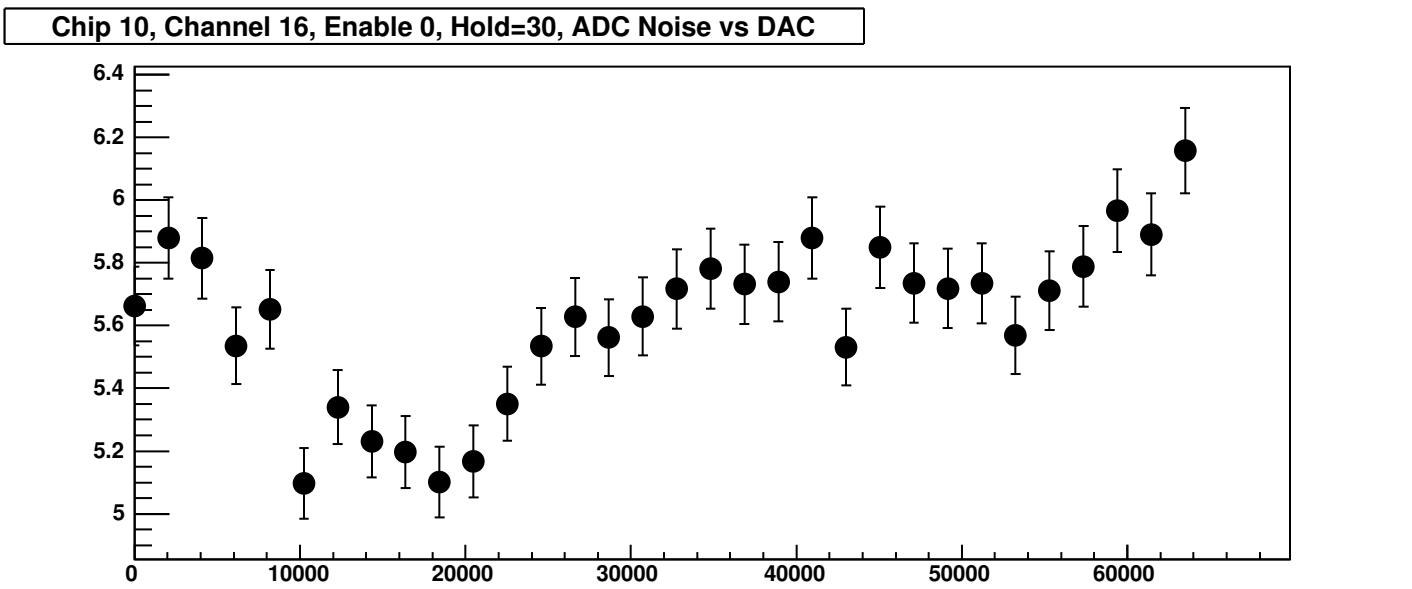


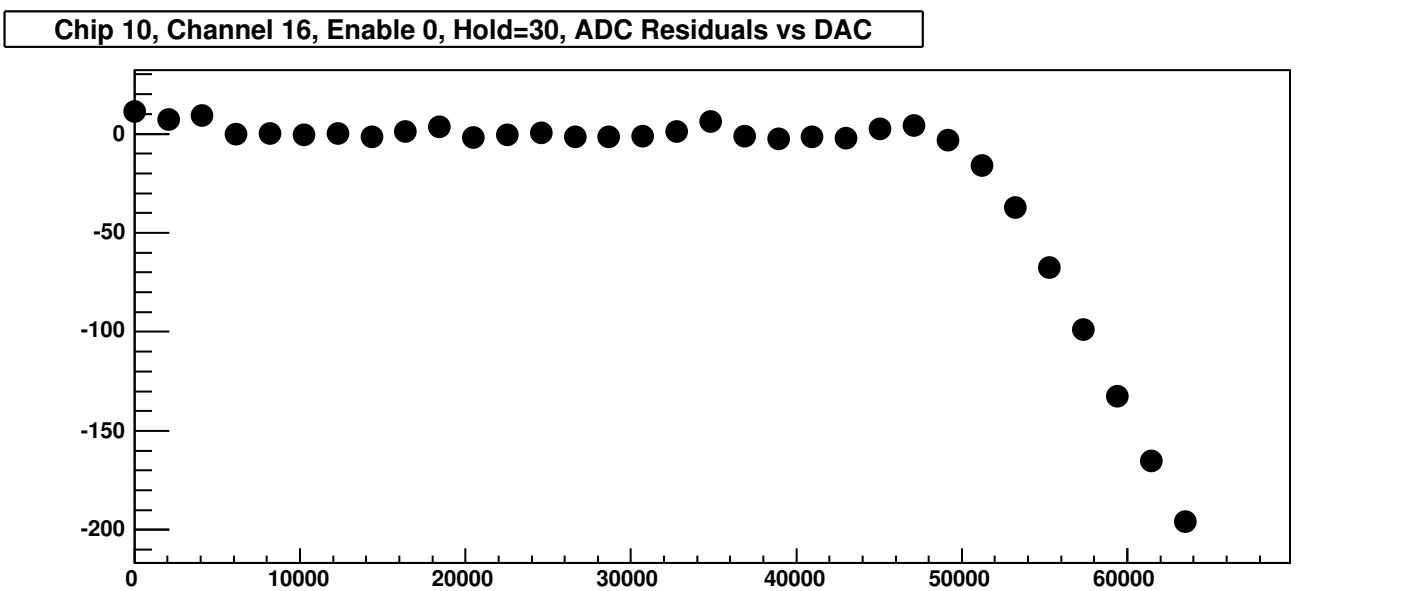

Chip 10, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4657 / 15
p0 1516 ± 0.1104
p1 -3.301e-05 ± 4.537e-06
1540
1520
1500
1480
1460
1440
1420
1400
1380
1360
0
10000
20000
30000
40000
50000
60000

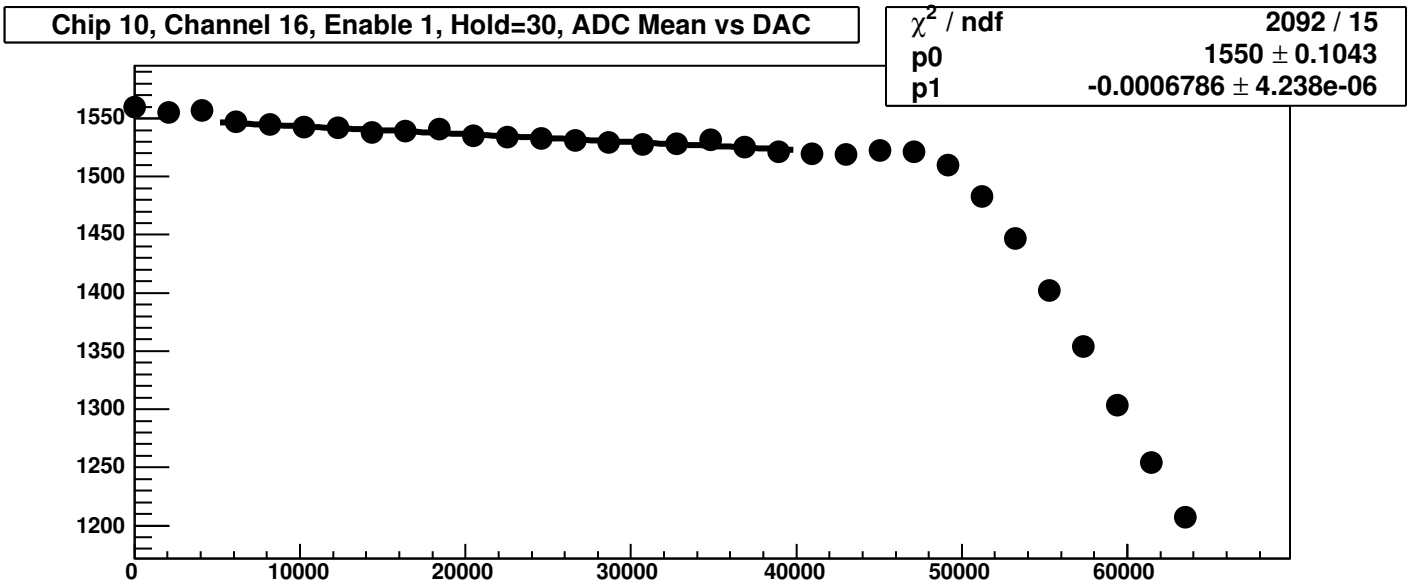

Chip 10, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

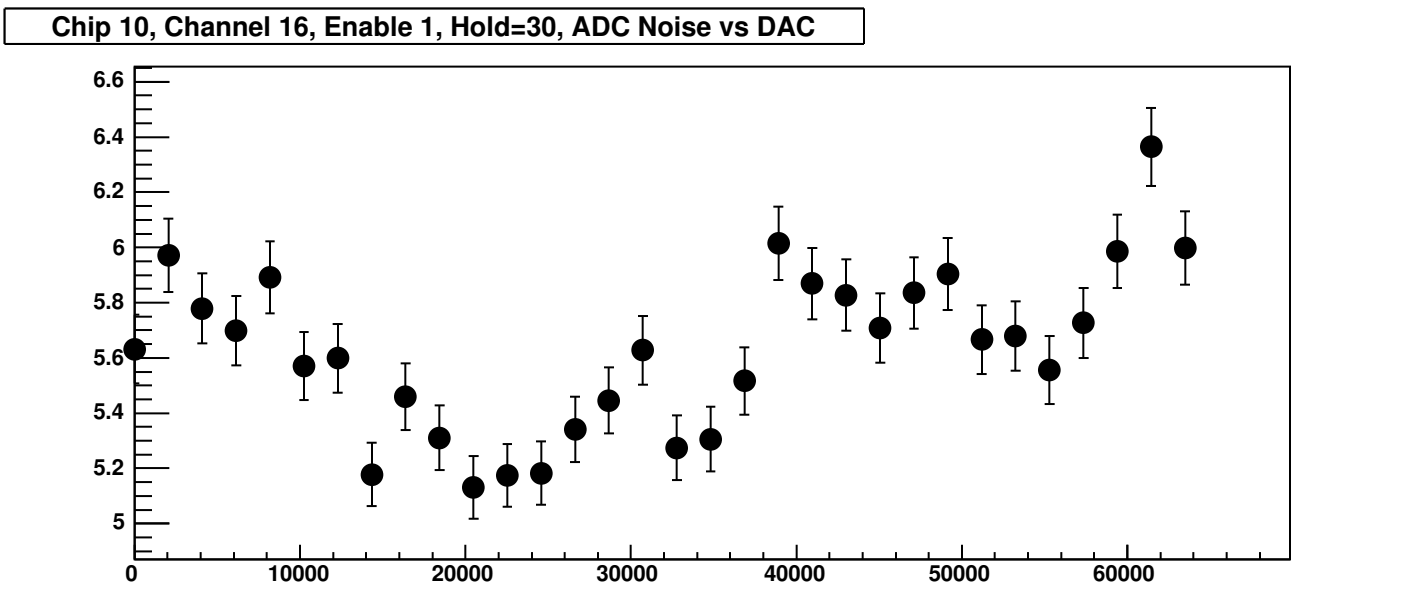

Chip 10, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

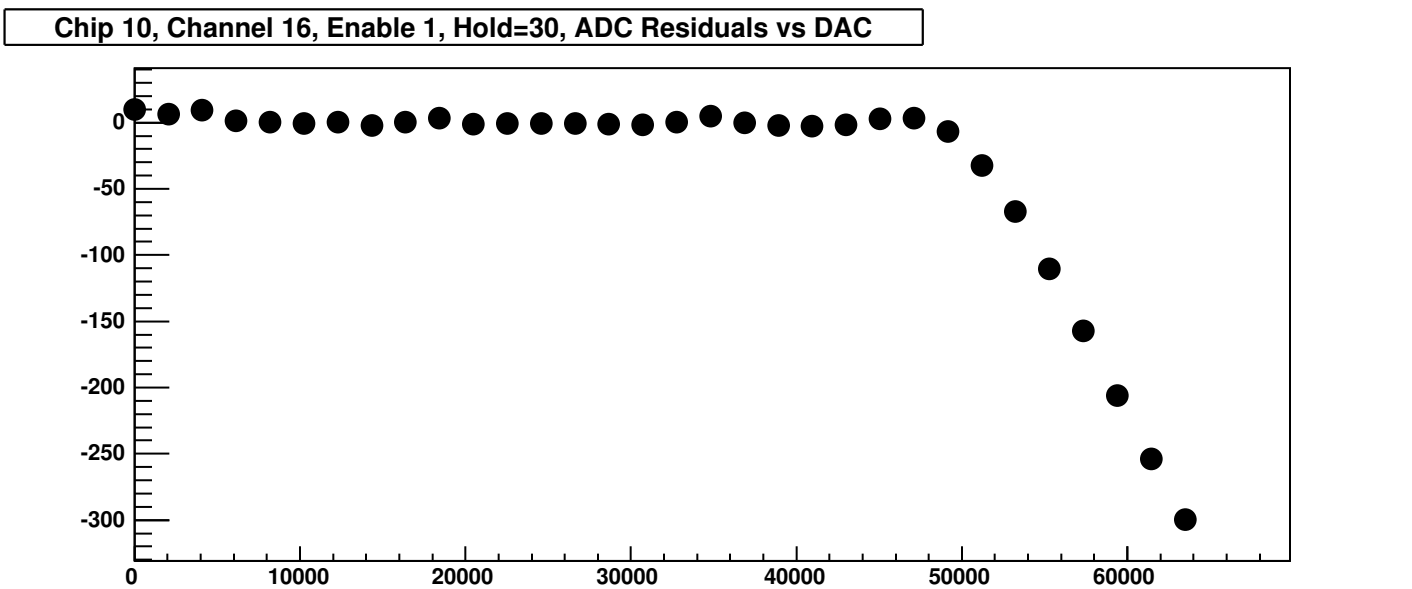

Chip 10, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 2401 / 15
p0 1550 ± 0.1012
p1 -0.0008685 ± 4.202e-06
1550
1500
1450
1400
1350
1300
0
10000
20000
30000
40000
50000
60000

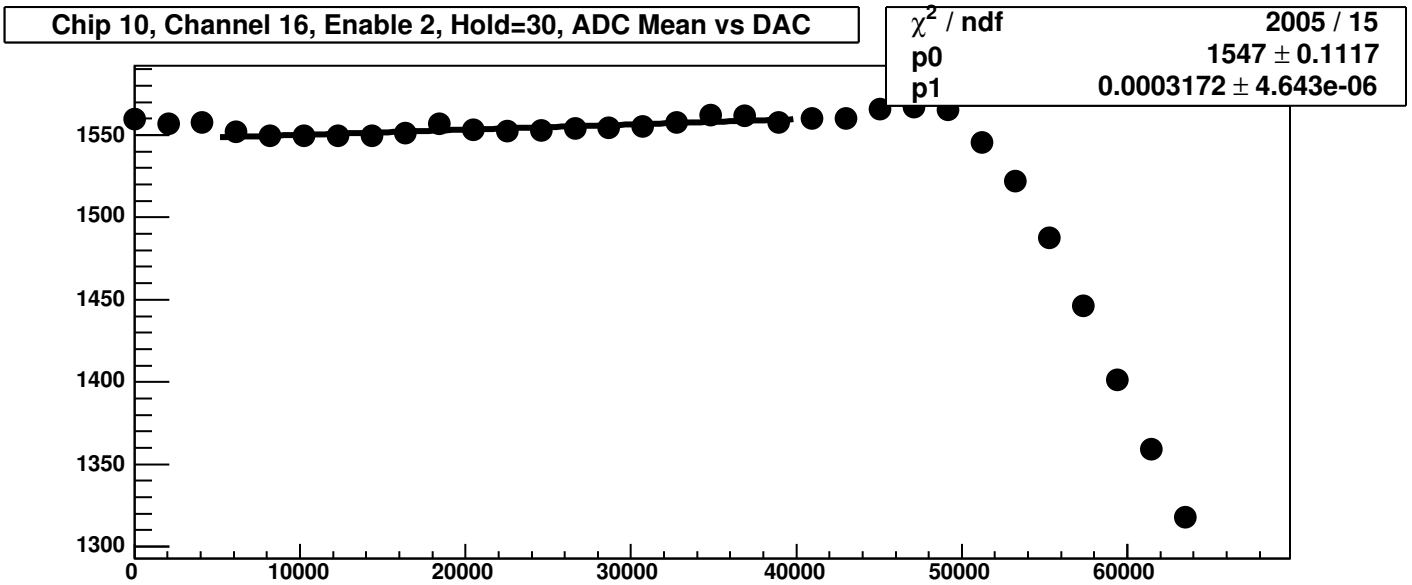

Chip 10, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

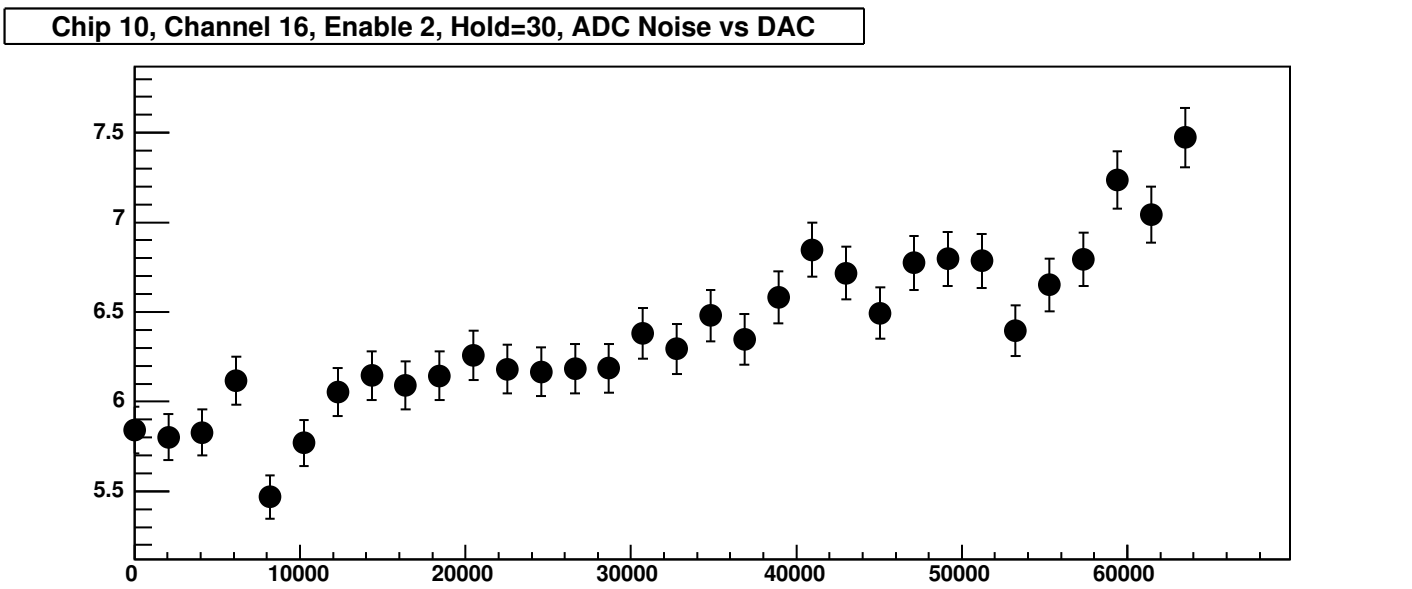

Chip 10, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

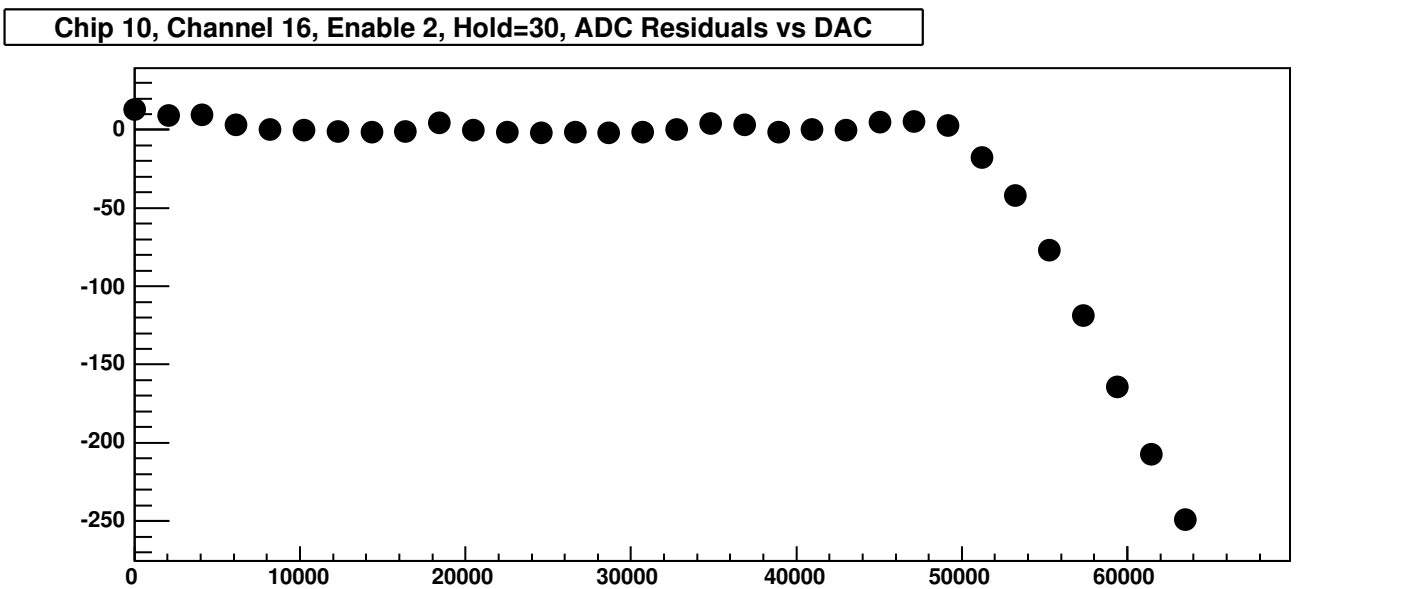

Chip 10, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2092 / 15
p0 1550 ± 0.1043
p1 -0.0006786 ± 4.238e-06
1550
1500
1450
1400
1350
1300
1250
1200
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
5
0
10000
20000
30000
40000
50000
60000

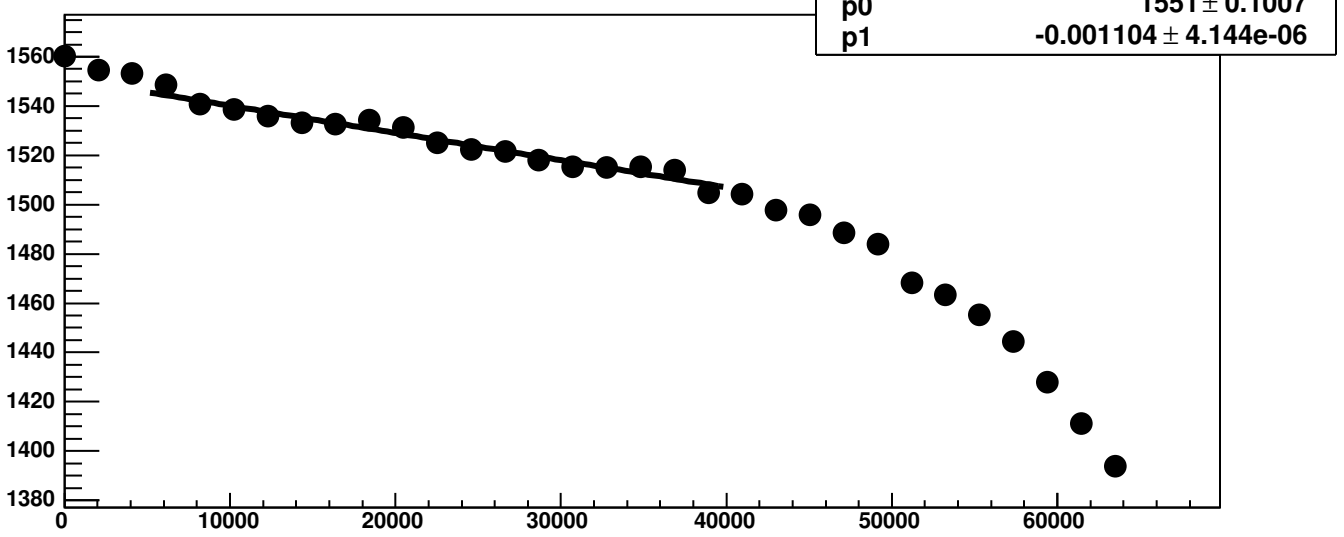

Chip 10, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2005 / 15
p0 1547 ± 0.1117
p1 0.0003172 ± 4.643e-06
1550
1500
1450
1400
1350
1300
0
10000
20000
30000
40000
50000
60000

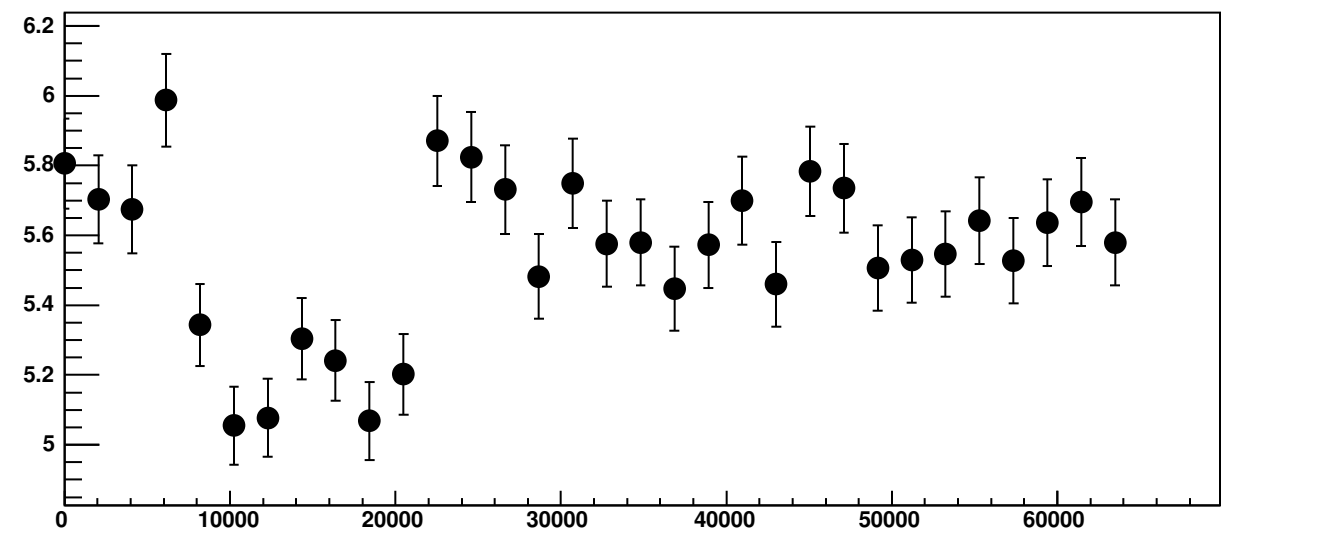

Chip 10, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

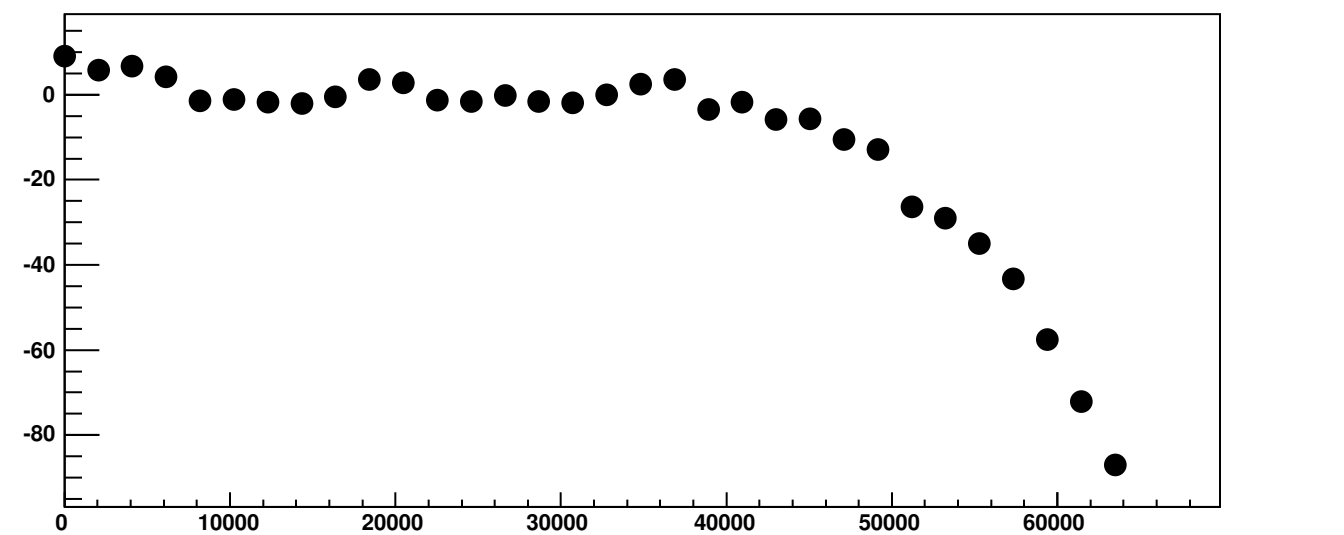
Chip 10, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

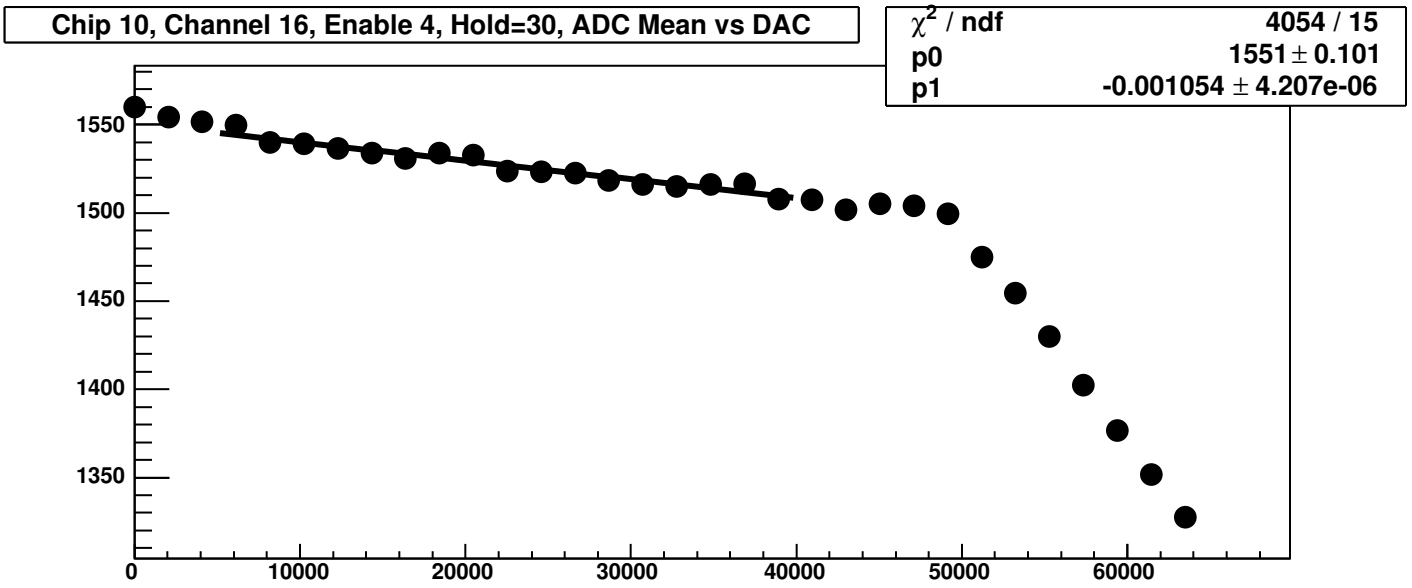


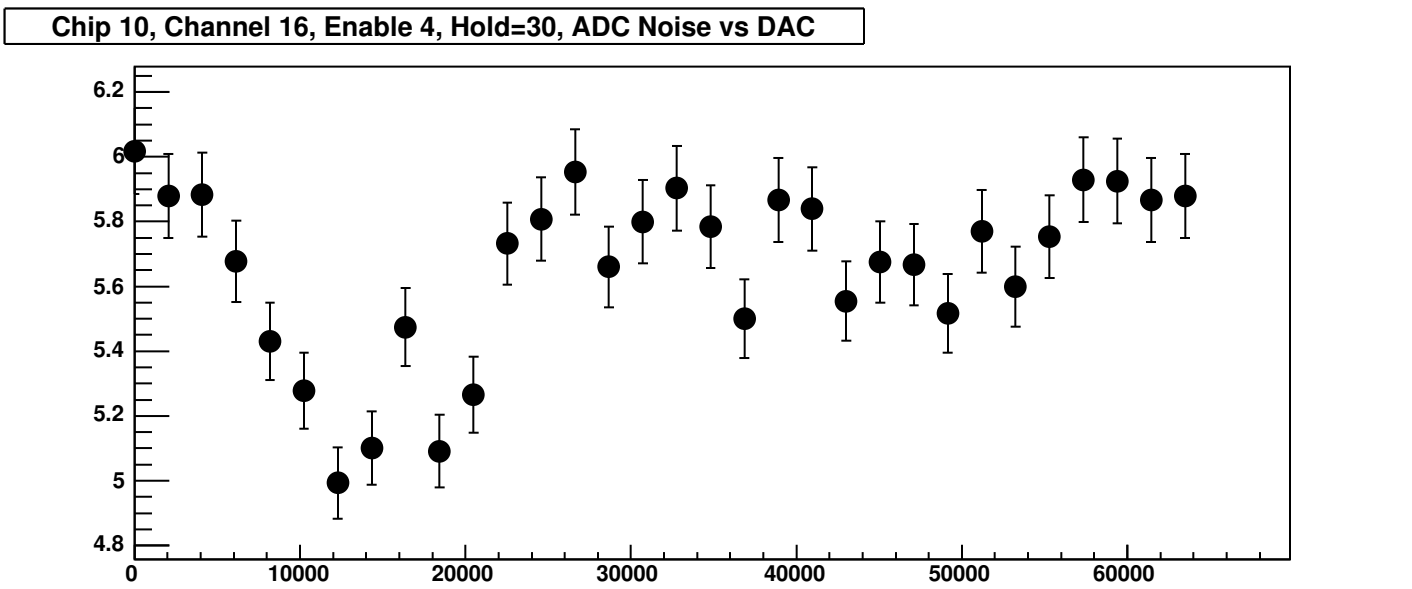
Chip 10, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC

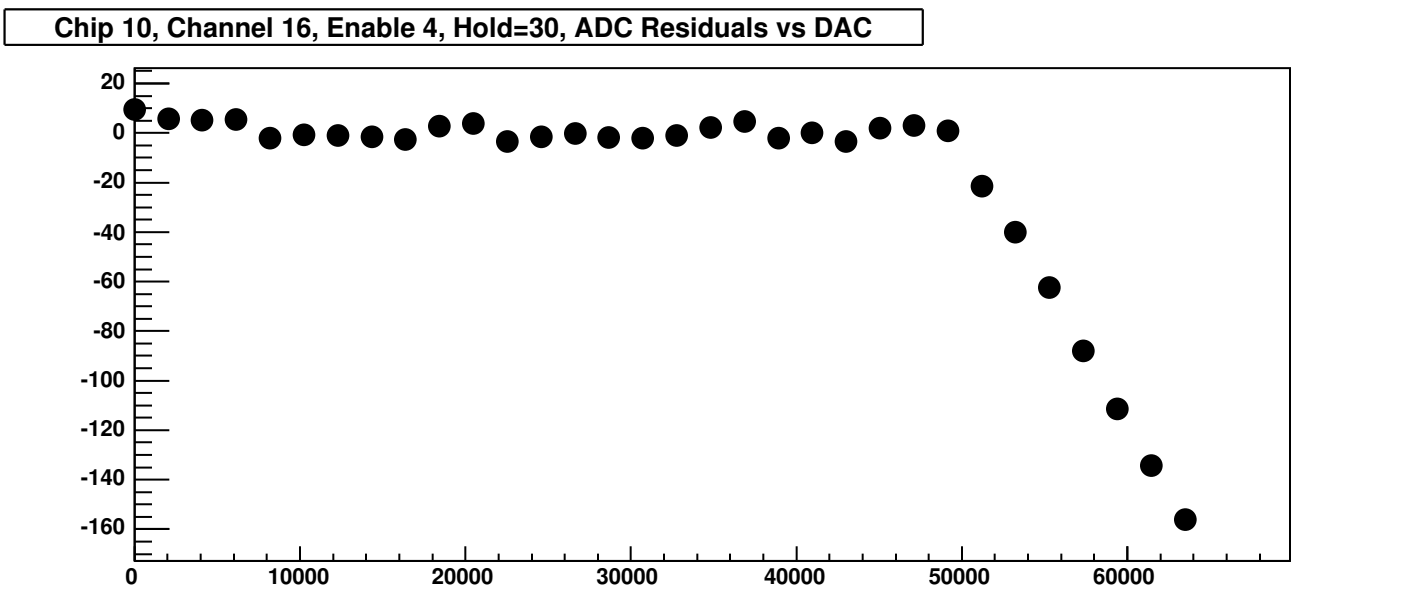


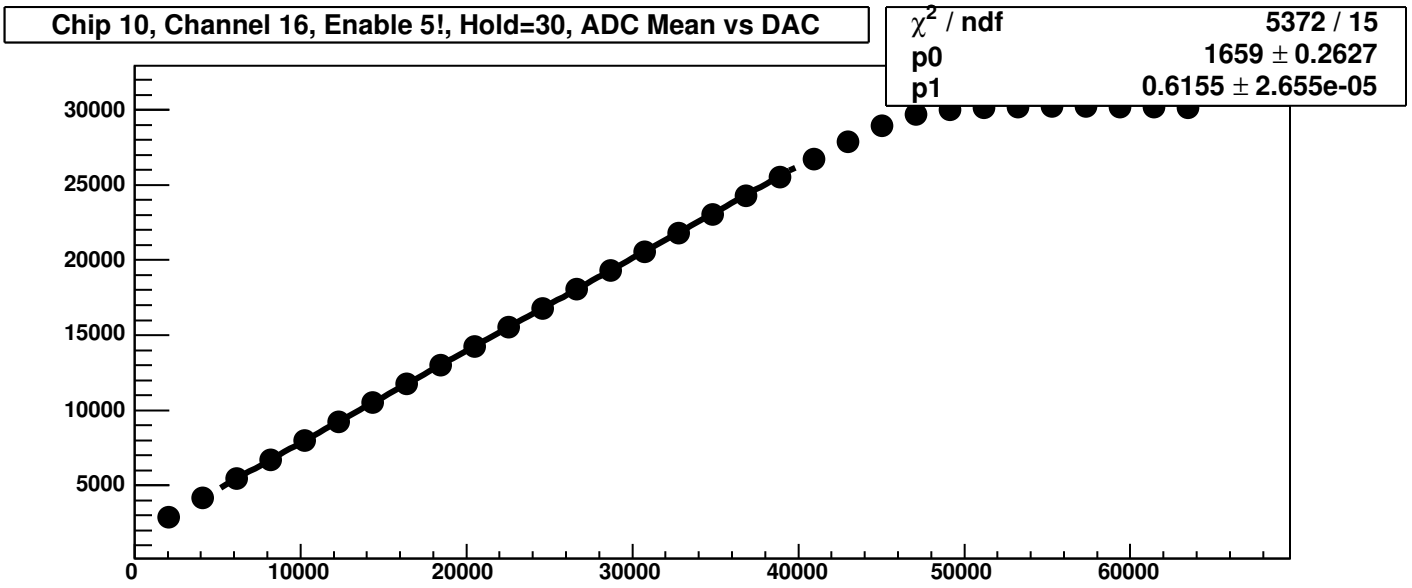
Chip 10, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

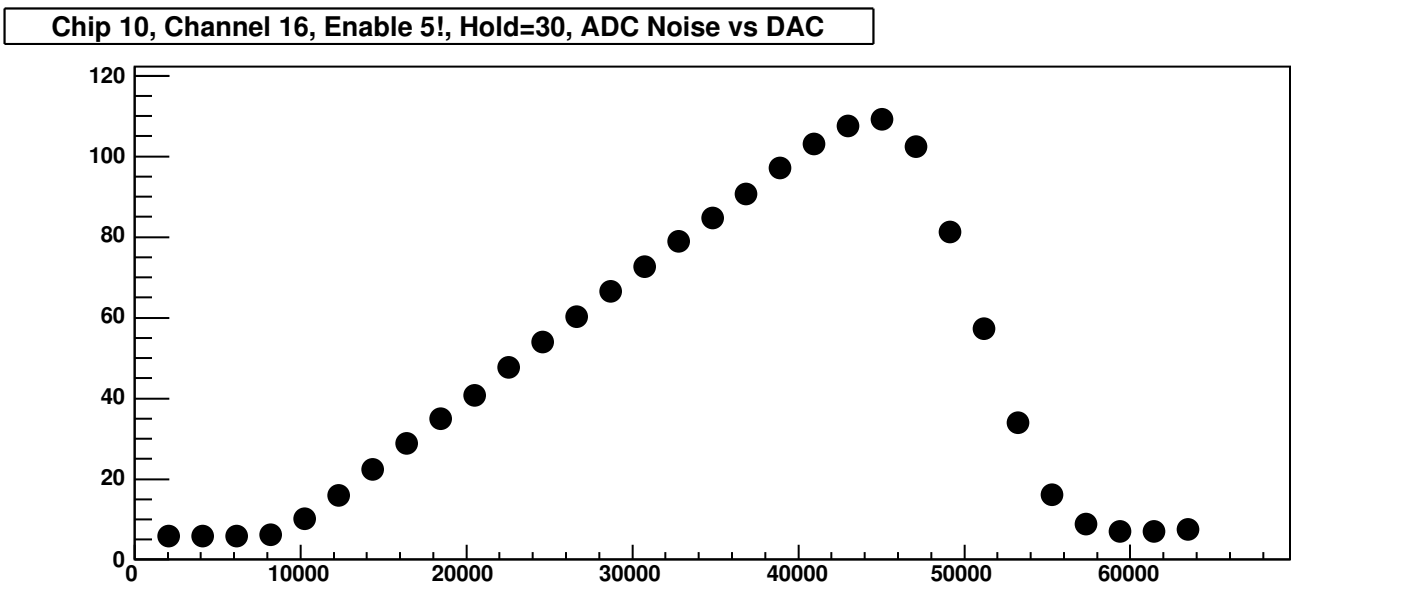


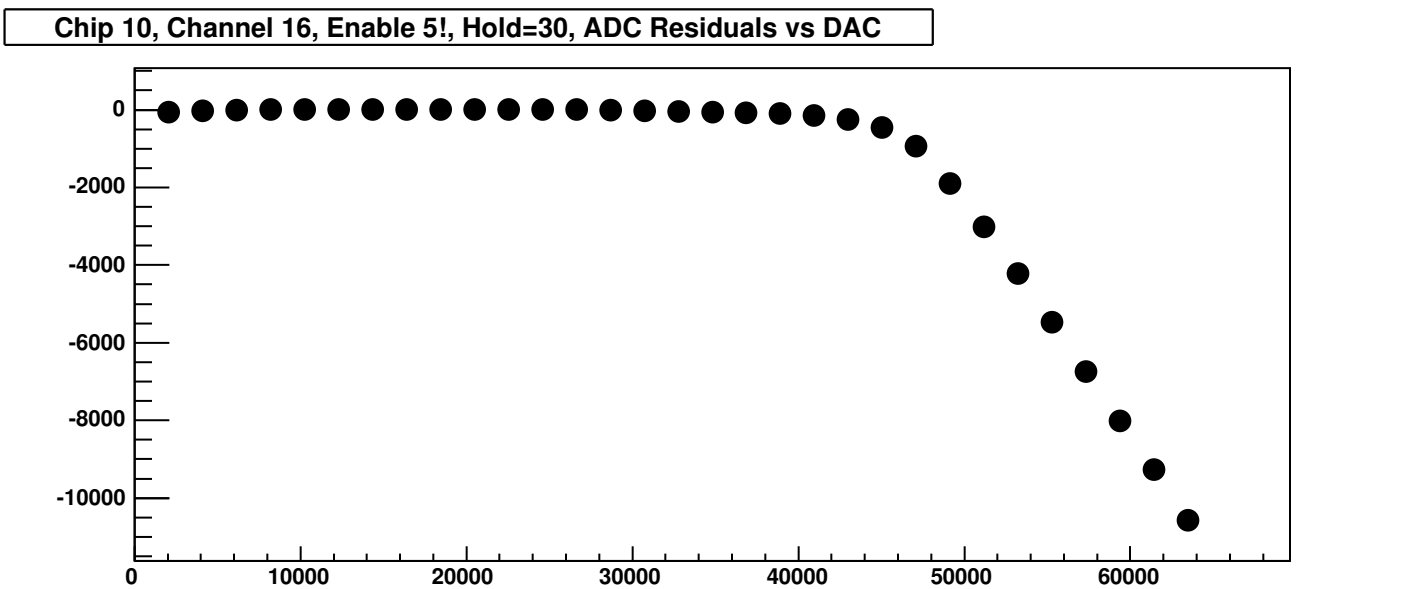

Chip 10, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4054 / 15
p0 1551 ± 0.101
p1 -0.001054 ± 4.207e-06
1550
1500
1450
1400
1350
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
6.2
6
5.8
5.6
5.4
5.2
5
4.8
0
10000
20000
30000
40000
50000
60000

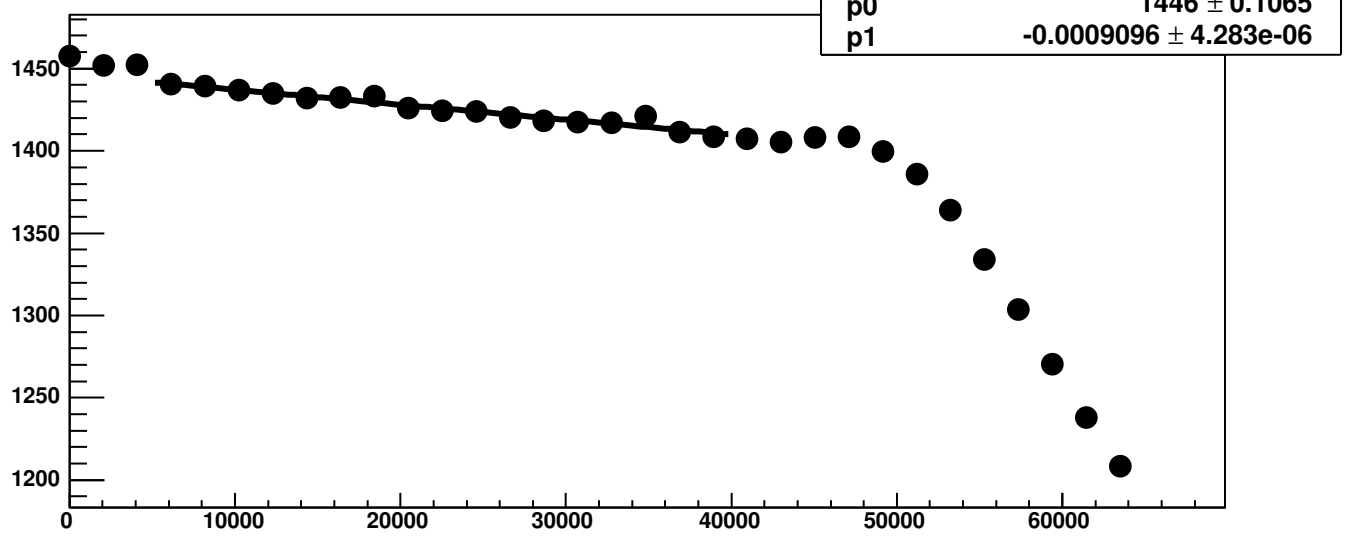

Chip 10, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 5372 / 15
p0 1659 ± 0.2627
p1 0.6155 ± 2.655e-05
30000
25000
20000
15000
10000
5000
0
10000
20000
30000
40000
50000
60000

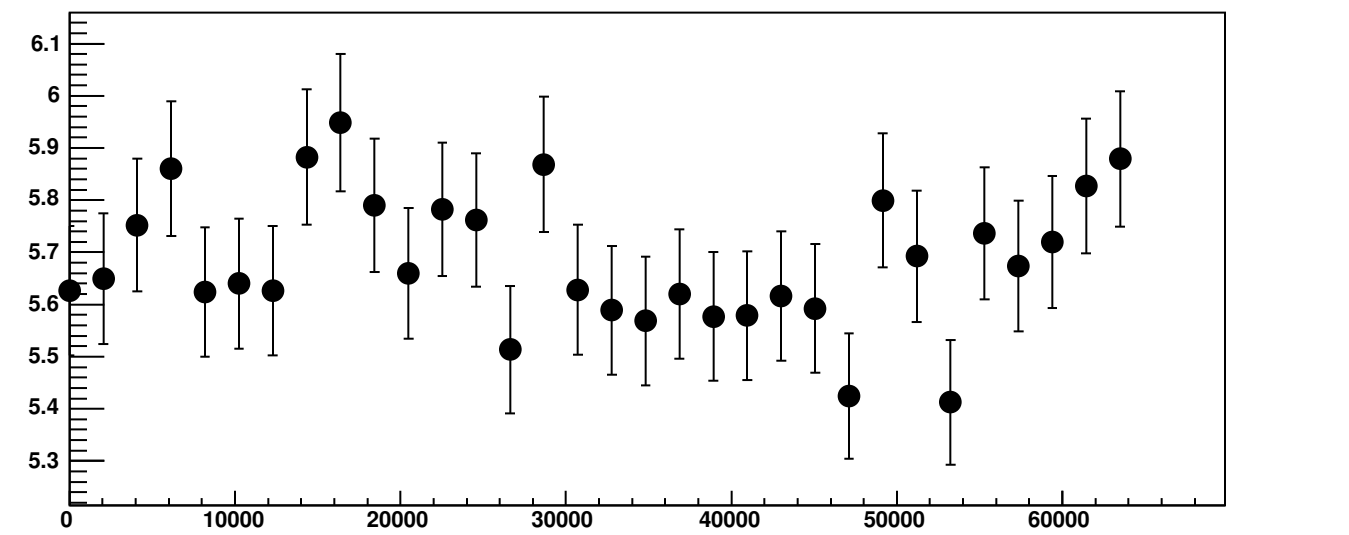

Chip 10, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000


Chip 10, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
10000
20000
30000
40000
50000
60000

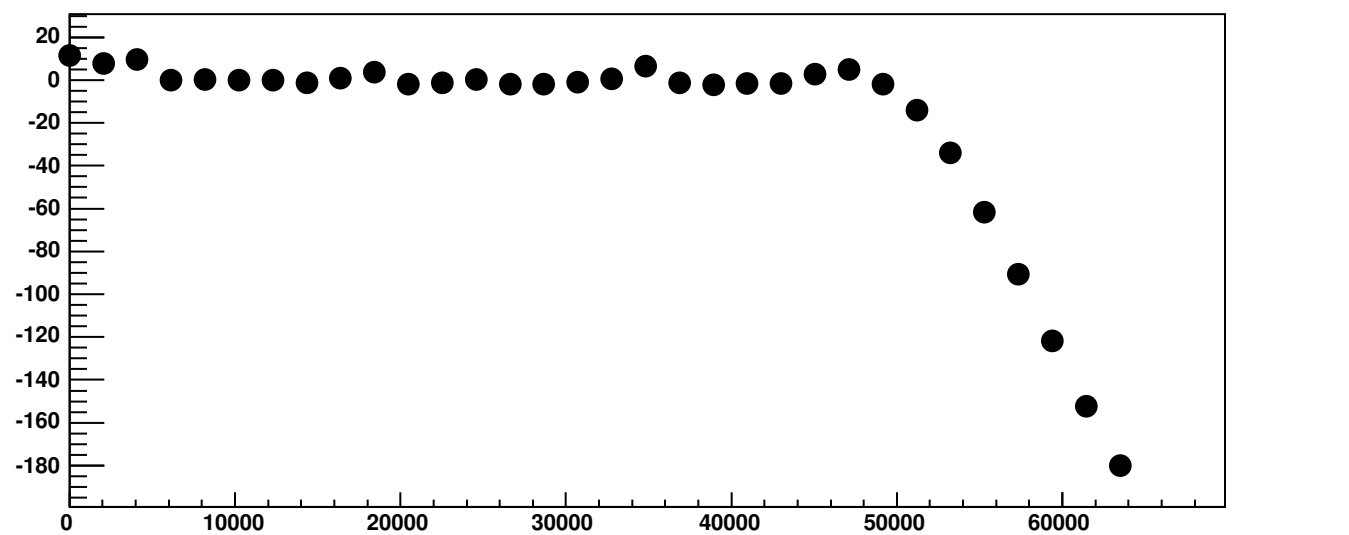
Chip 10, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC

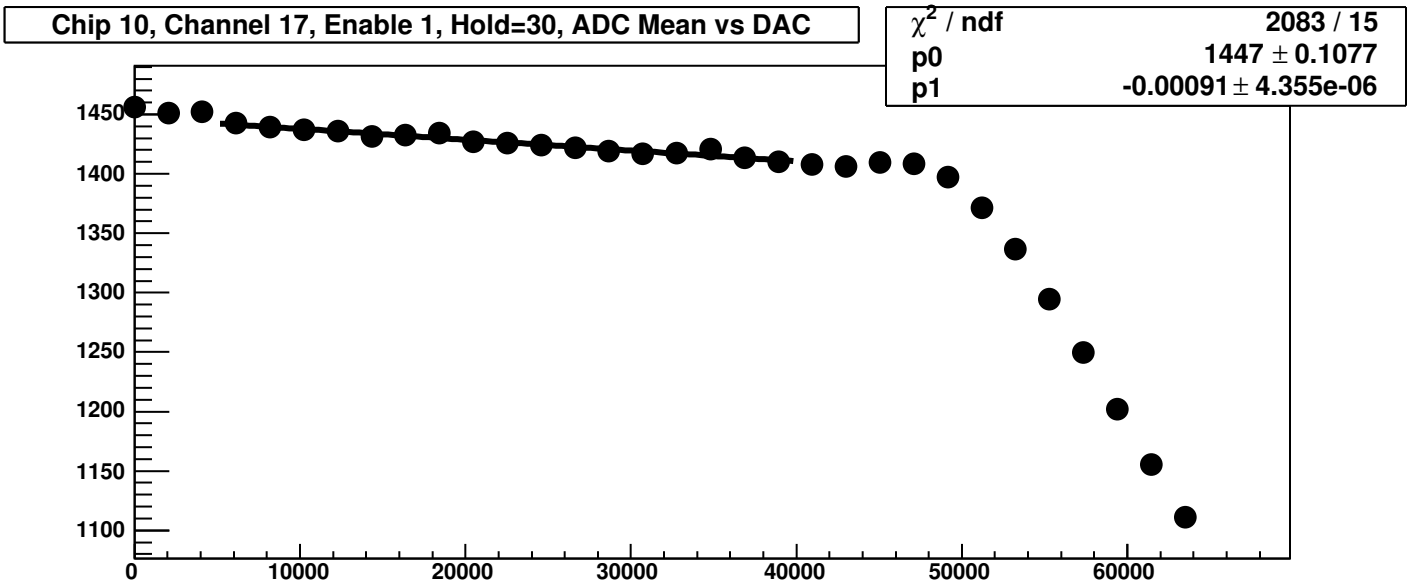


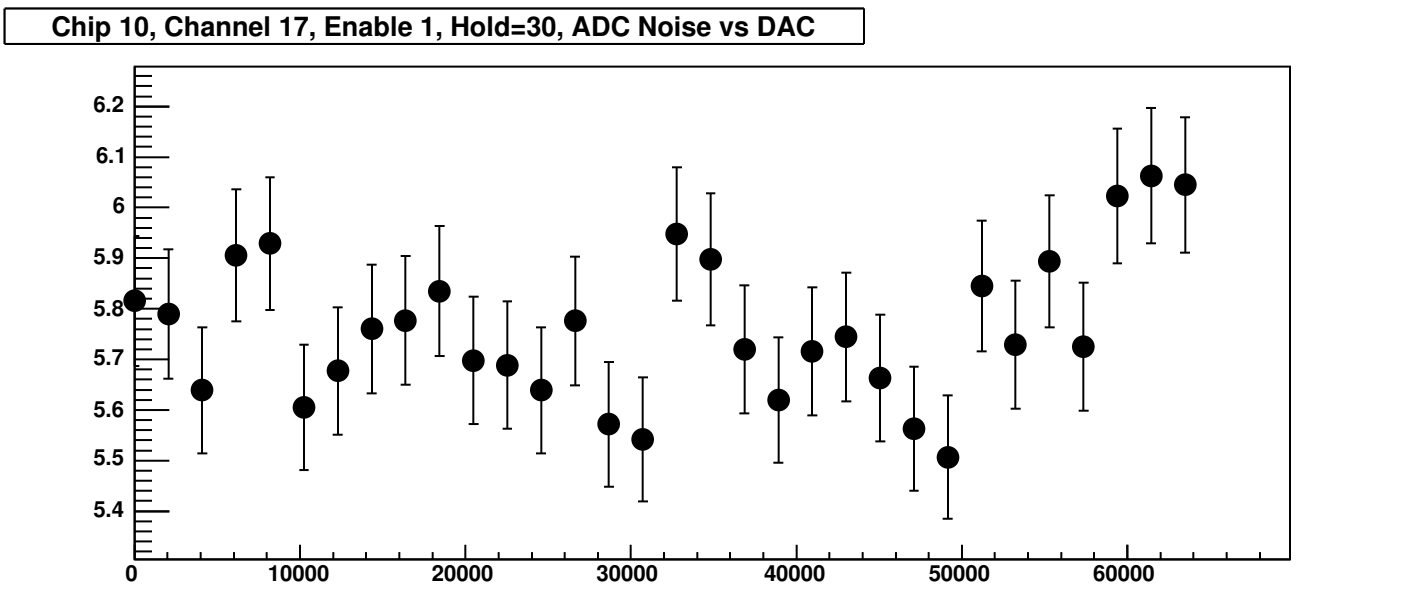
Chip 10, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC

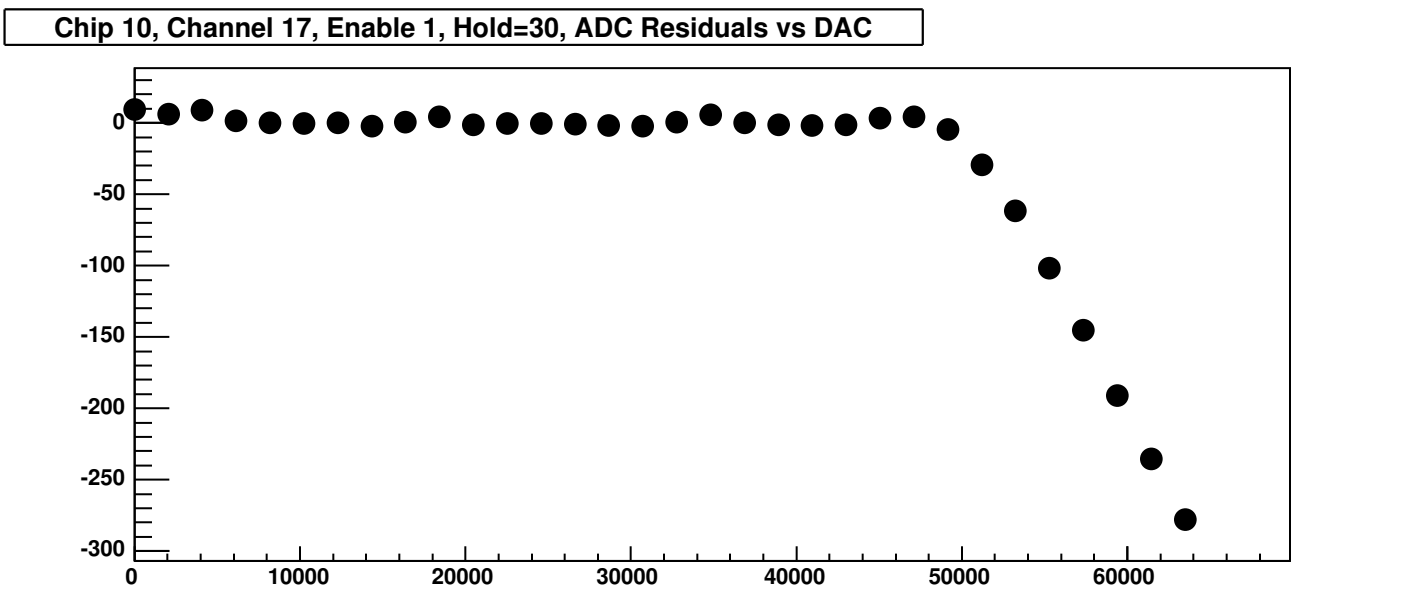


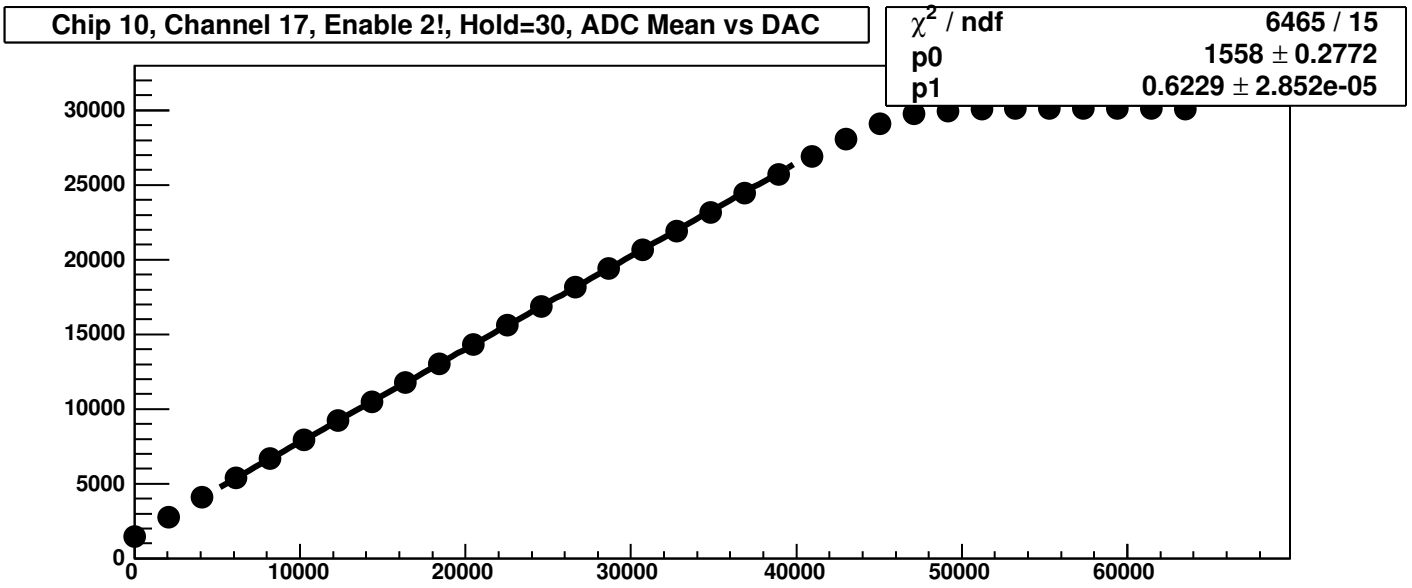
Chip 10, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

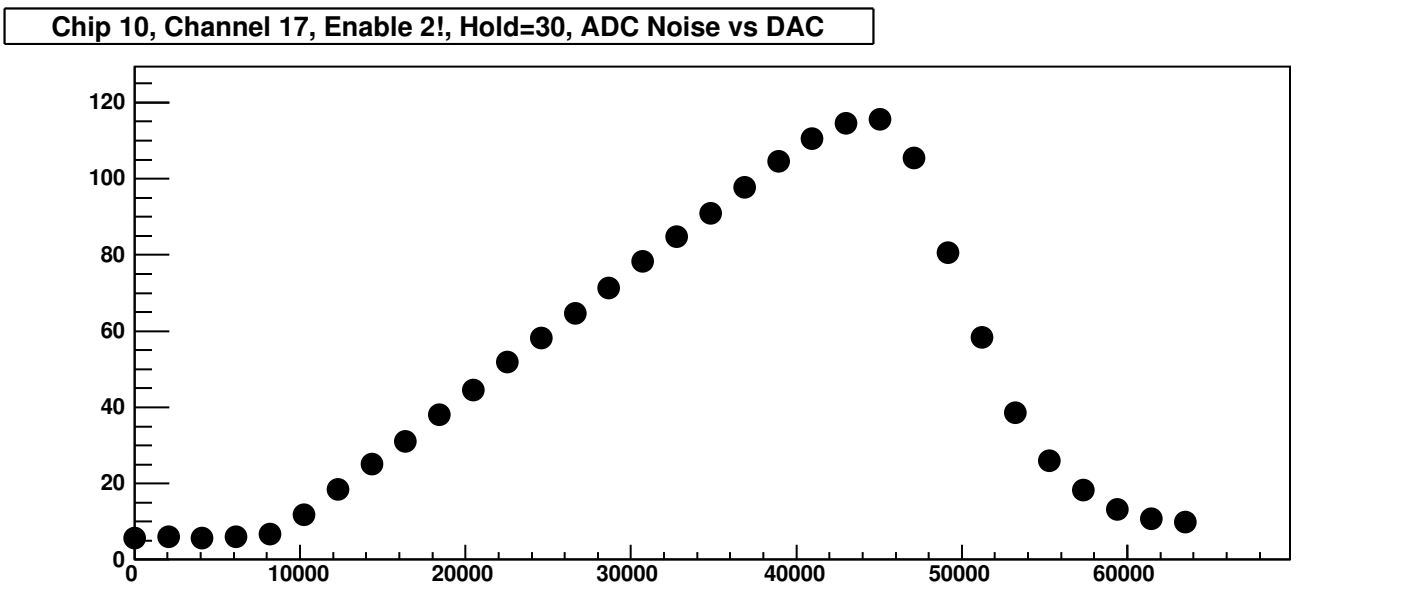


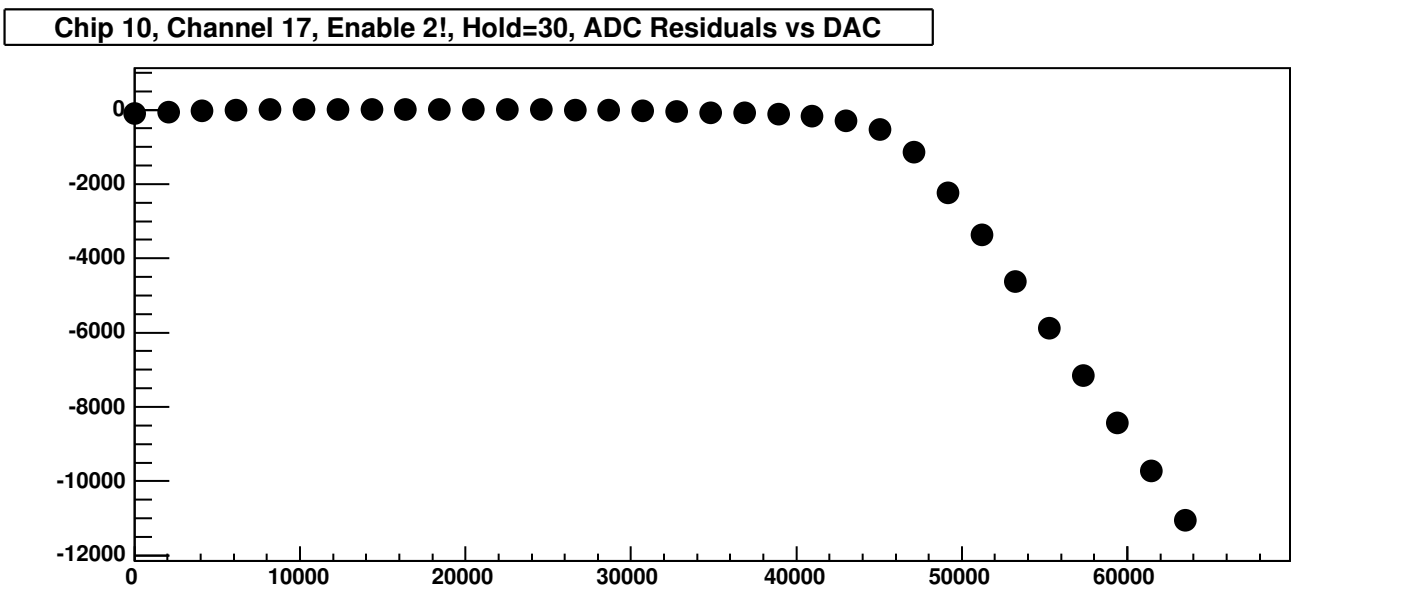

Chip 10, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2083 / 15
p0 1447 ± 0.1077
p1 -0.00091 ± 4.355e-06
1450
1400
1350
1300
1250
1200
1150
1100
0
10000
20000
30000
40000
50000
60000

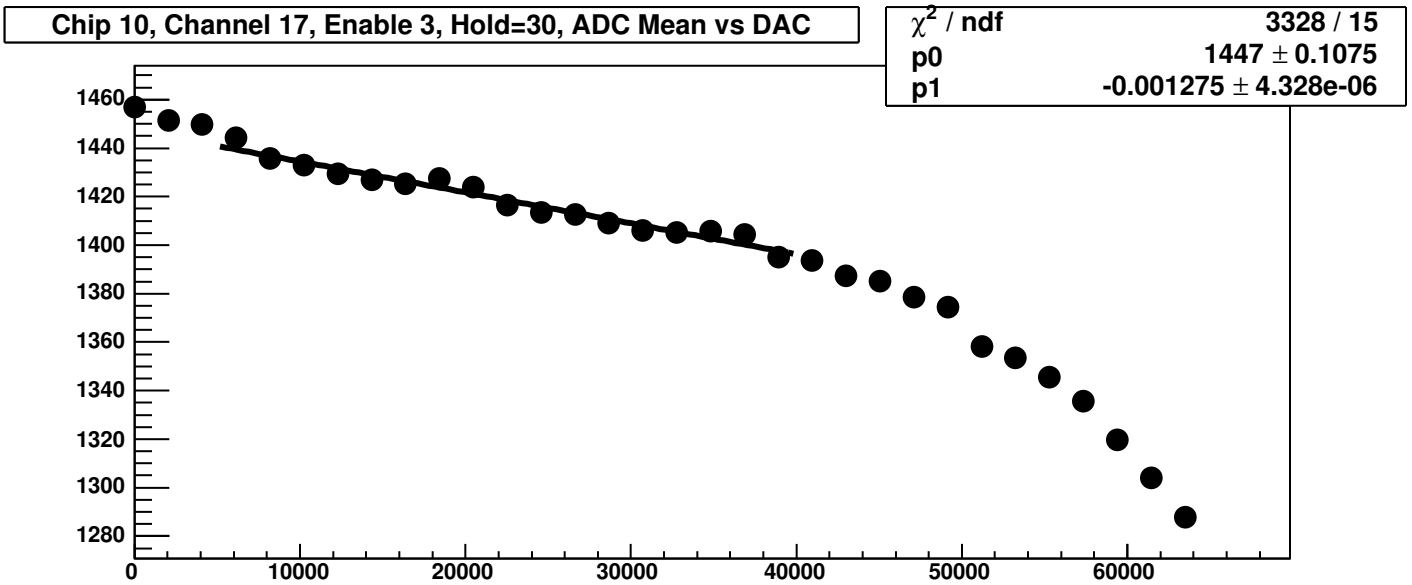

Chip 10, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

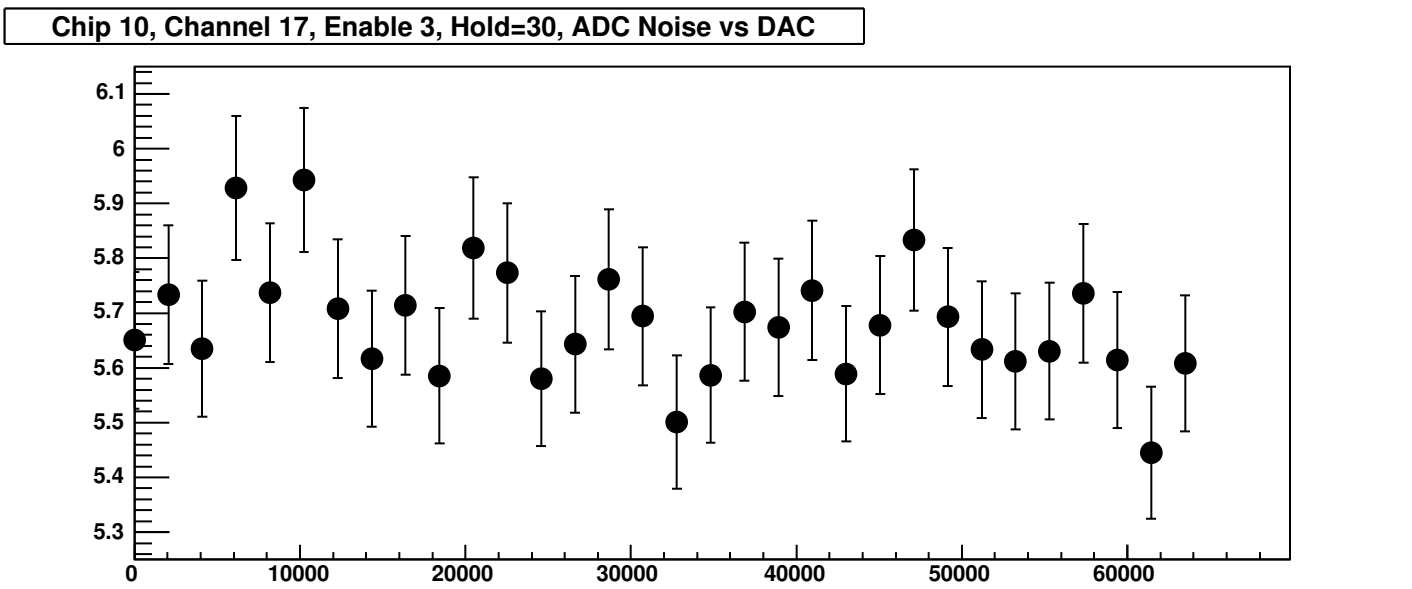

Chip 10, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

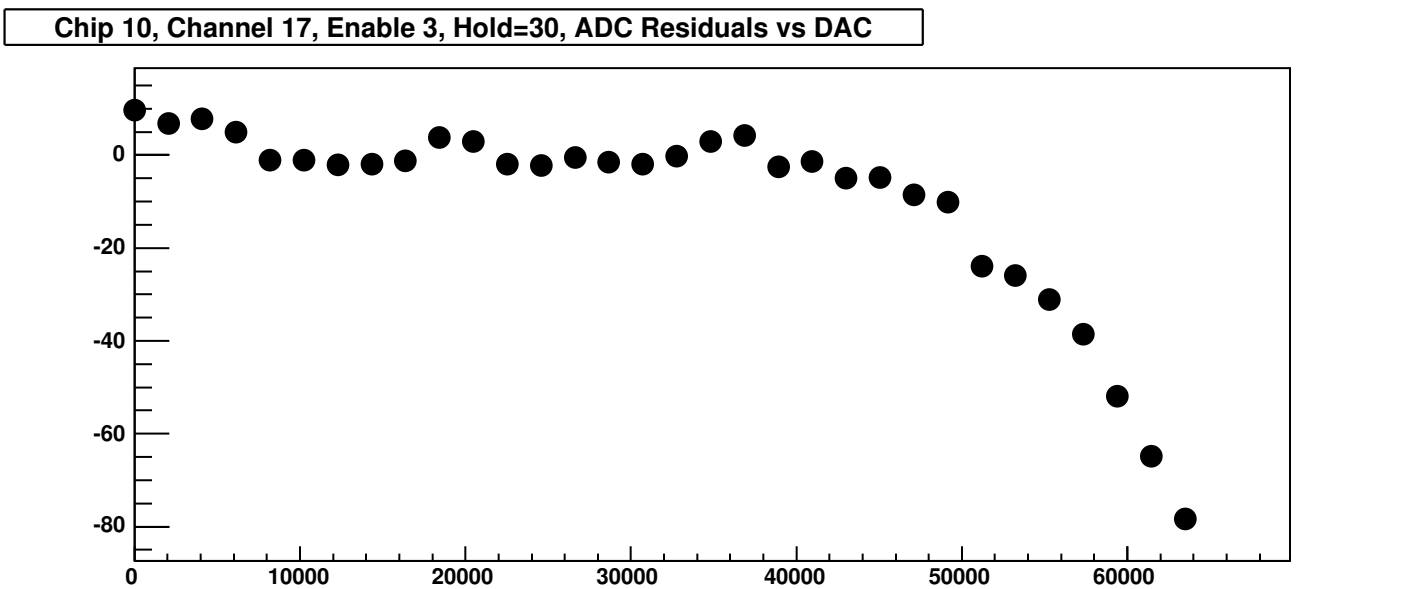

Chip 10, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 6465 / 15
p0 1558 ± 0.2772
p1 0.6229 ± 2.852e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

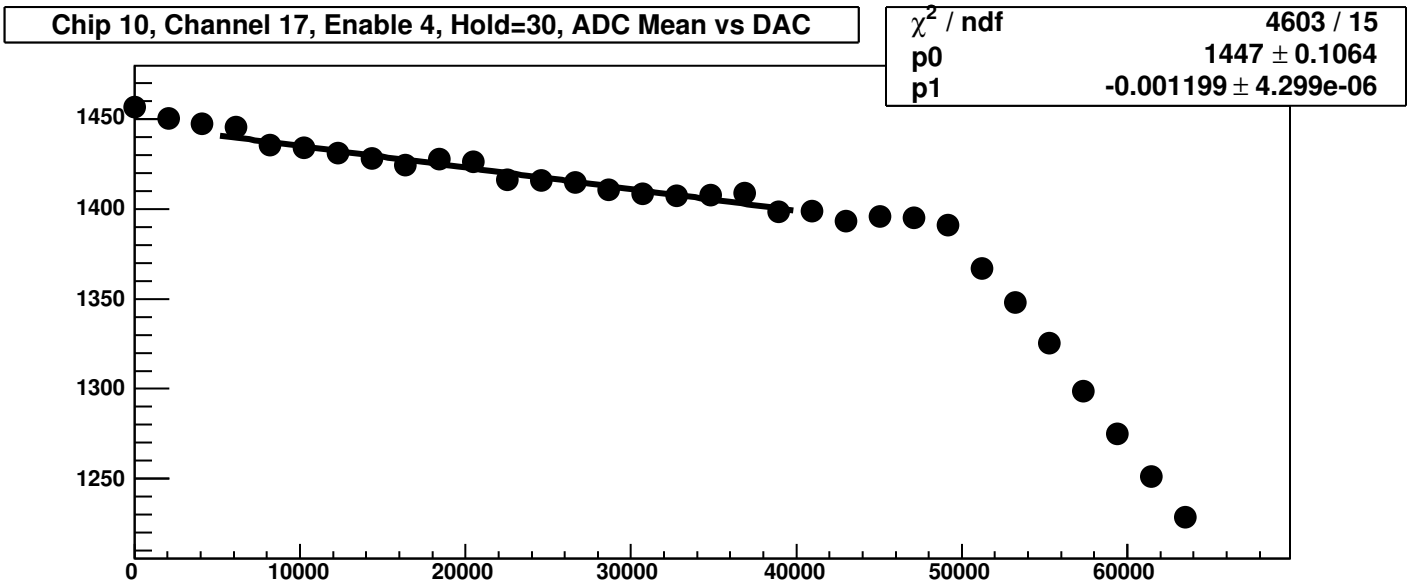

Chip 10, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

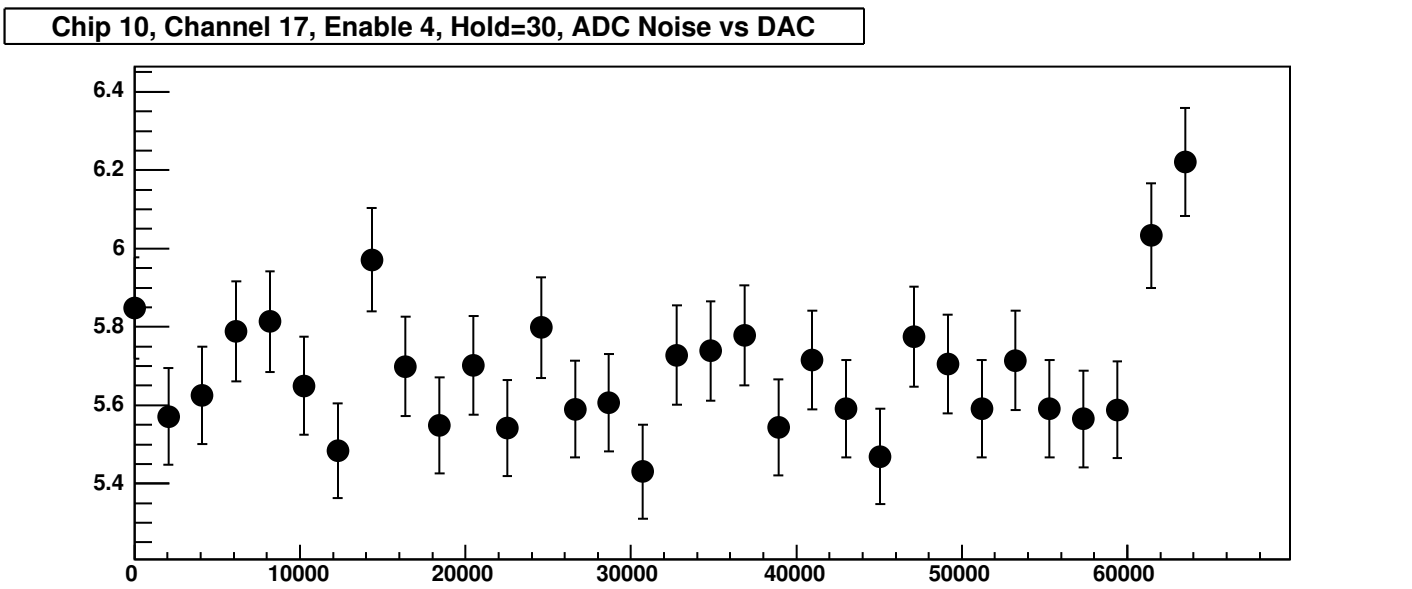

Chip 10, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

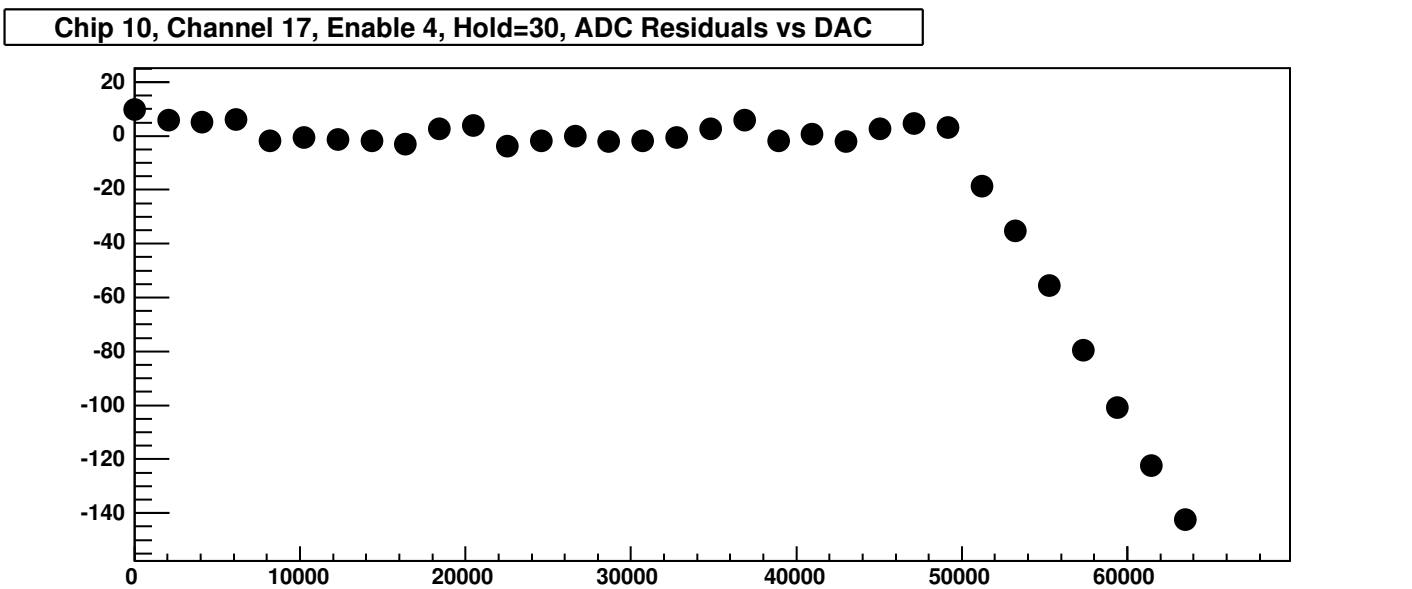

Chip 10, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3328 / 15
p0 1447 ± 0.1075
p1 -0.001275 ± 4.328e-06
1460
1440
1420
1400
1380
1360
1340
1320
1300
1280
0
10000
20000
30000
40000
50000
60000

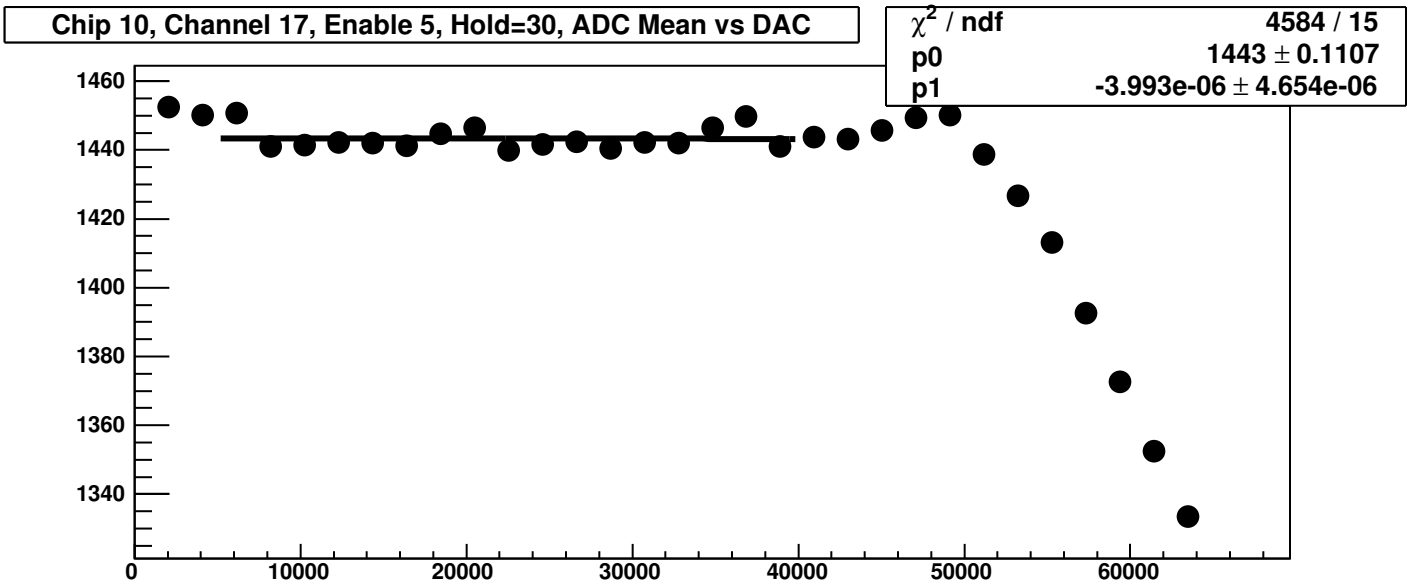

Chip 10, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
5.3
0
10000
20000
30000
40000
50000
60000

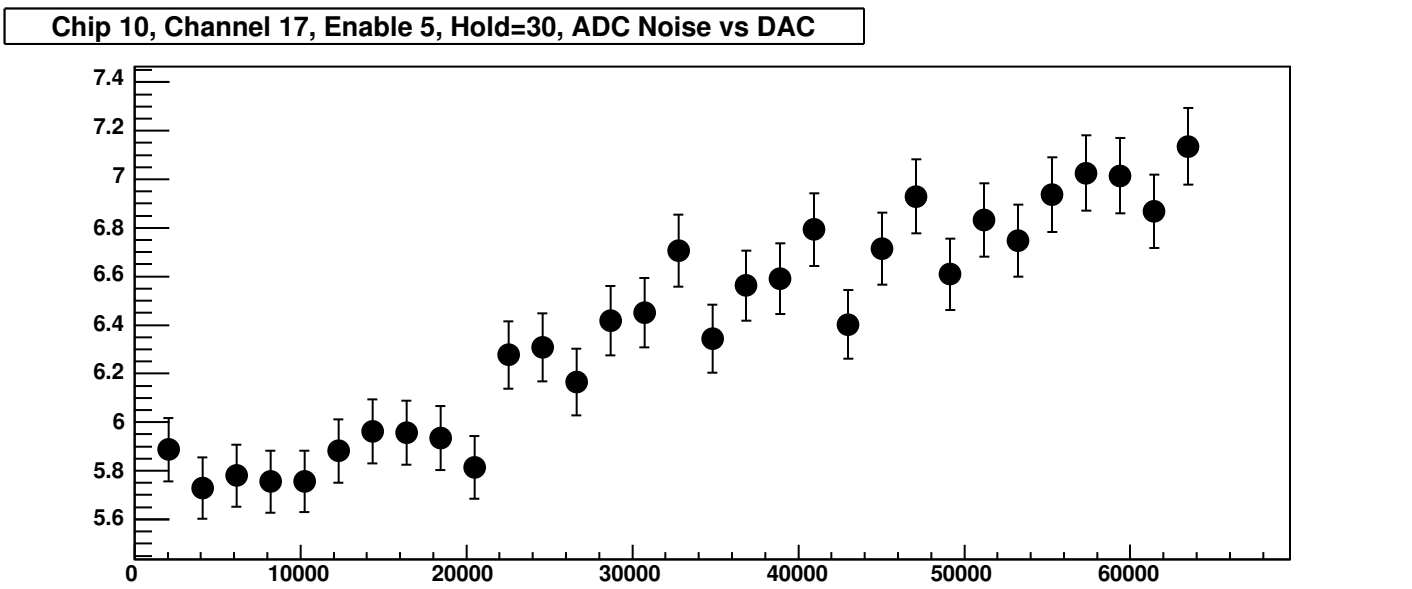

Chip 10, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

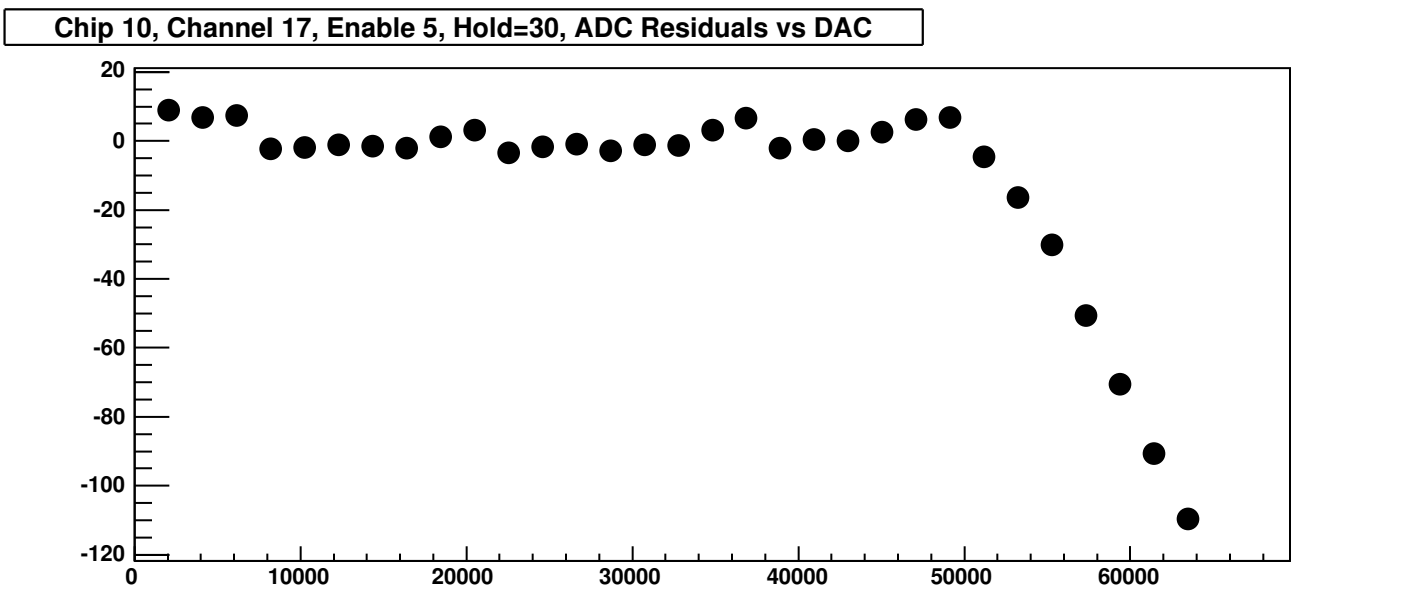

Chip 10, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4603 / 15
p0 1447 ± 0.1064
p1 -0.001199 ± 4.299e-06
1450
1400
1350
1300
1250
0
10000
20000
30000
40000
50000
60000

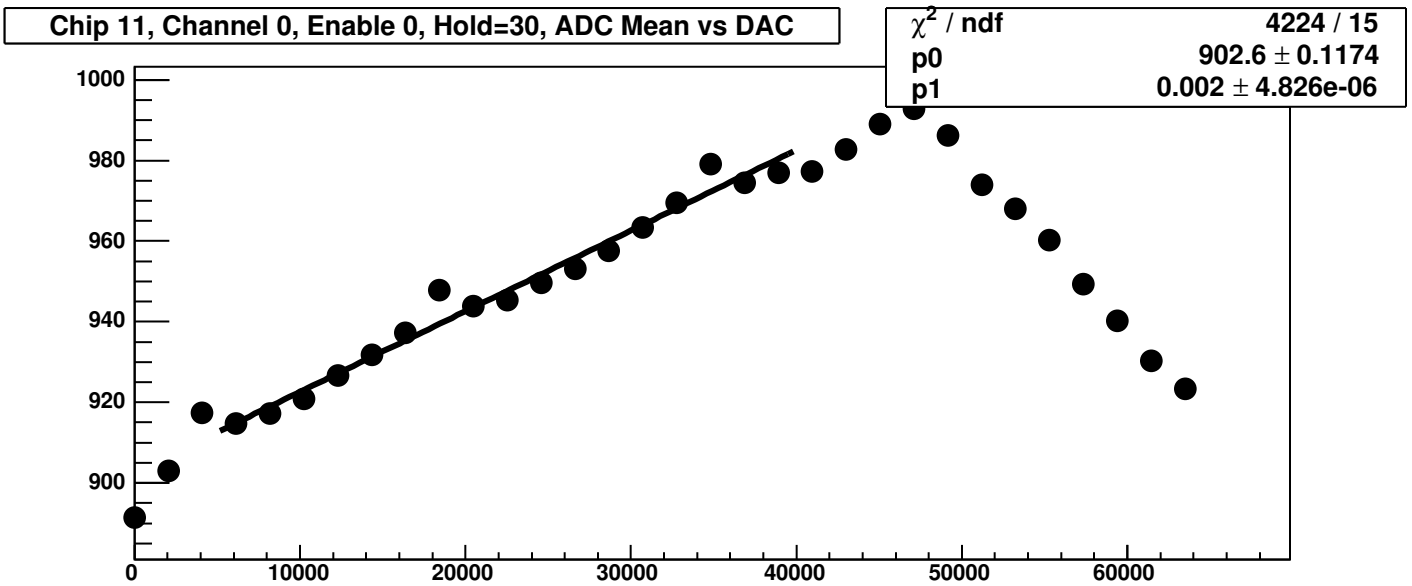

Chip 10, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

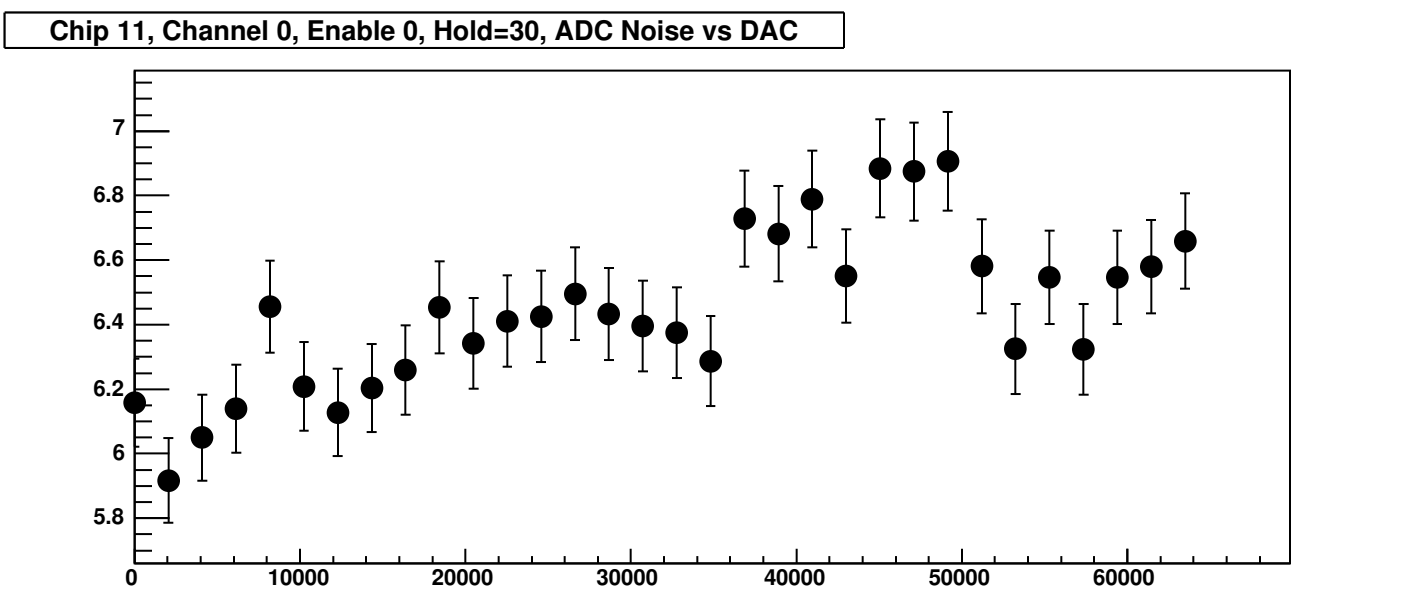

Chip 10, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

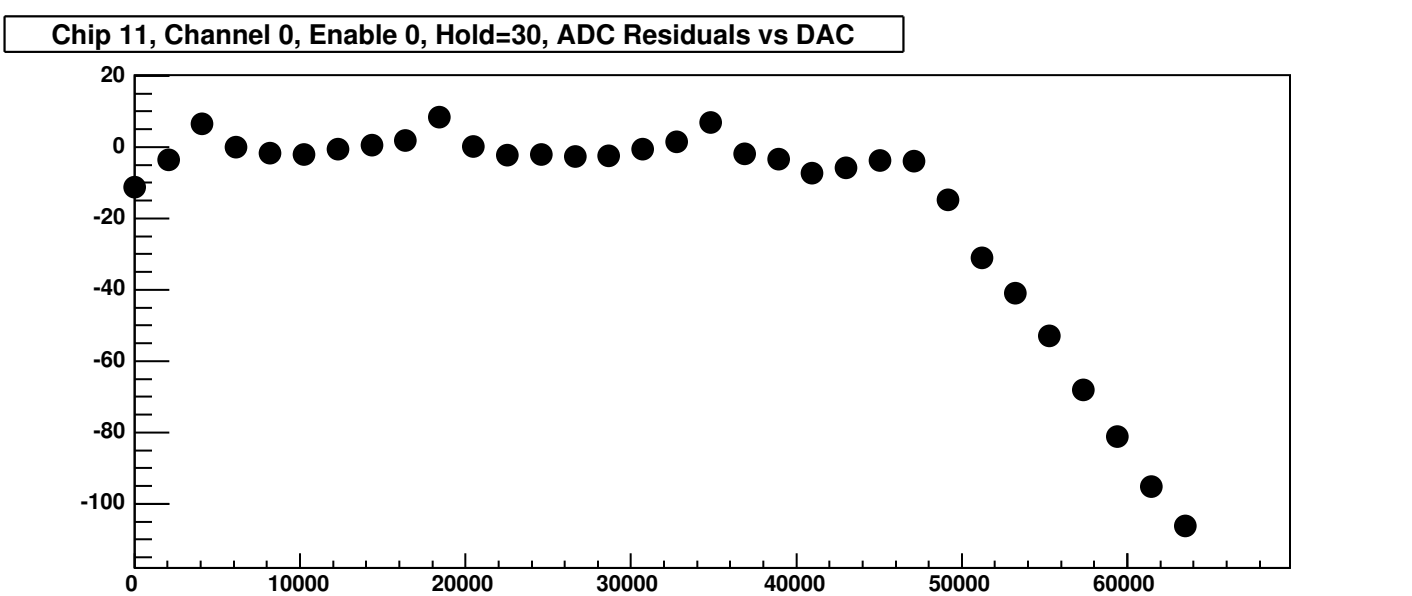

Chip 10, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4584 / 15
p0 1443 ± 0.1107
p1 -3.993e-06 ± 4.654e-06
1460
1440
1420
1400
1380
1360
1340
0
10000
20000
30000
40000
50000
60000

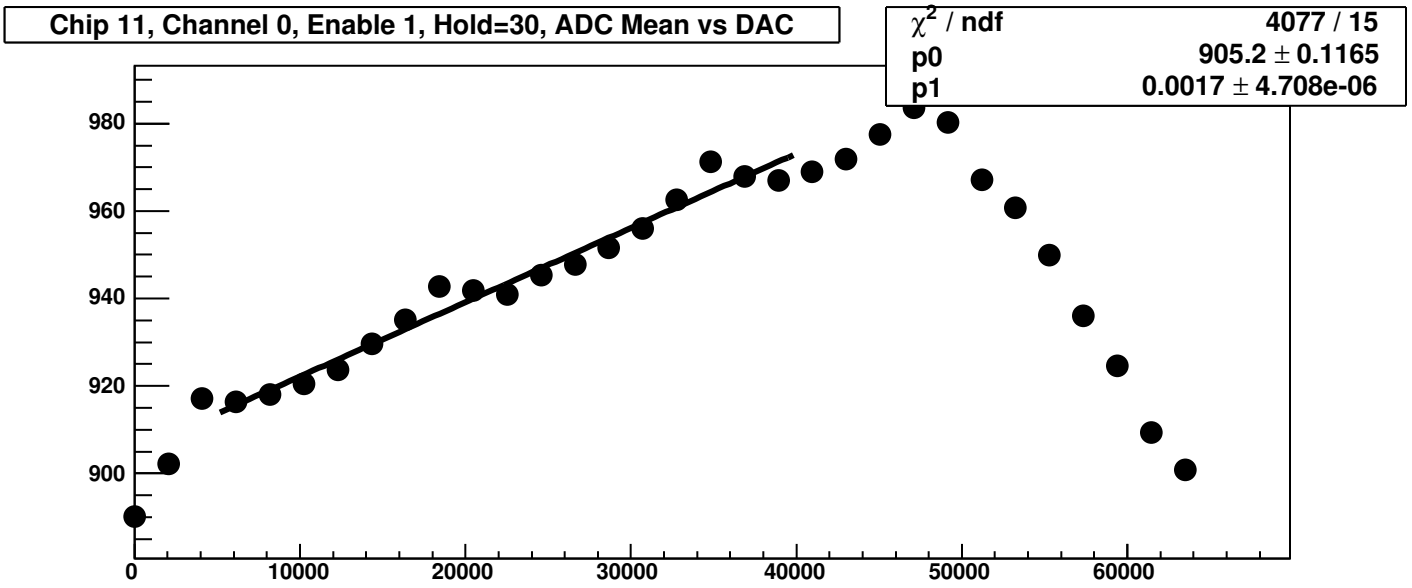

Chip 10, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

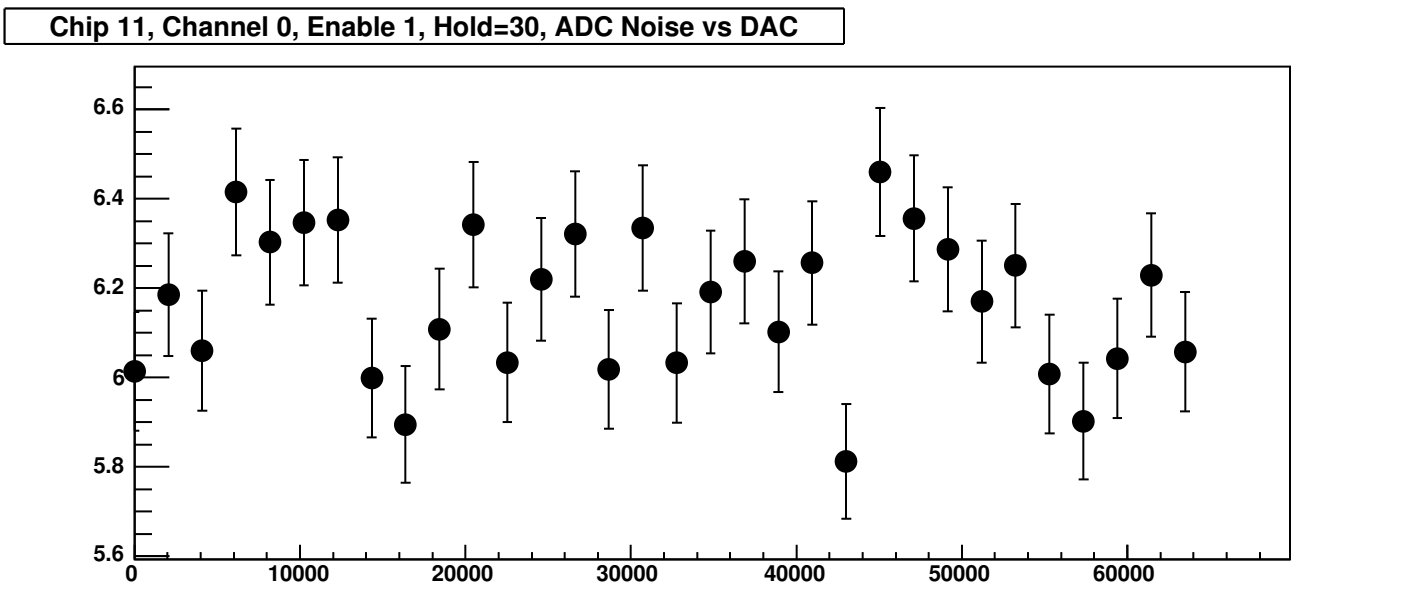

Chip 10, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

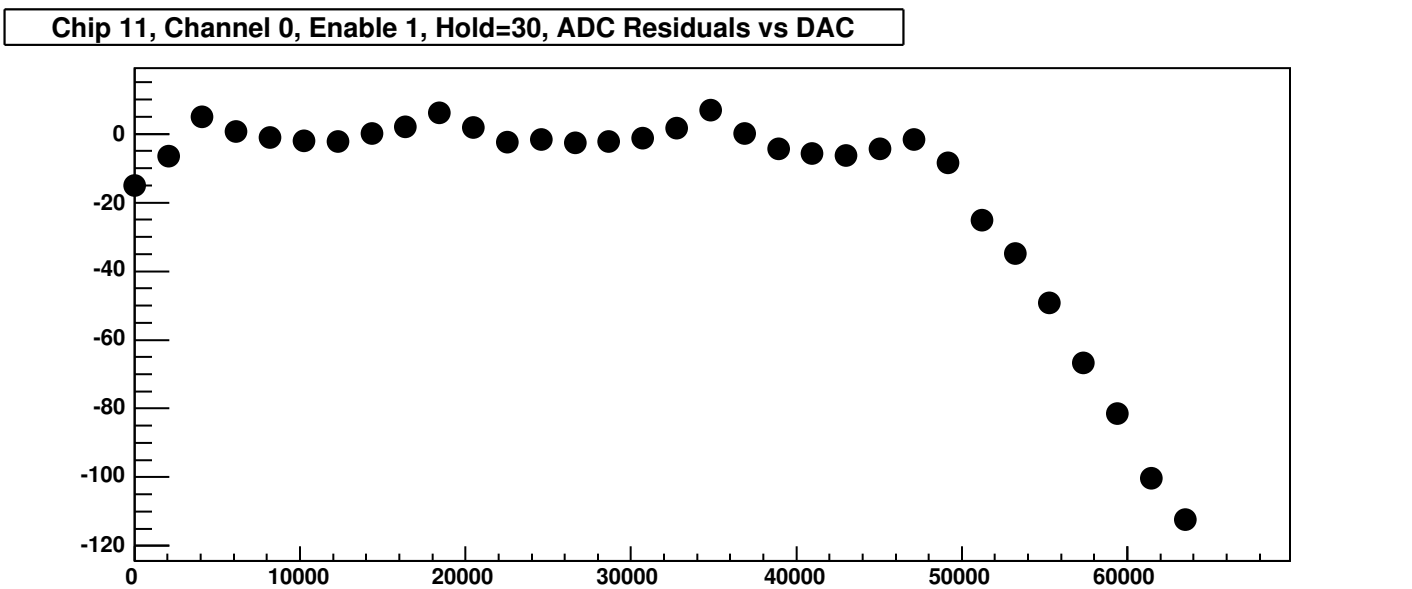

Chip 11, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4224 / 15
p0 902.6 ± 0.1174
p1 0.002 ± 4.826e-06
1000
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000

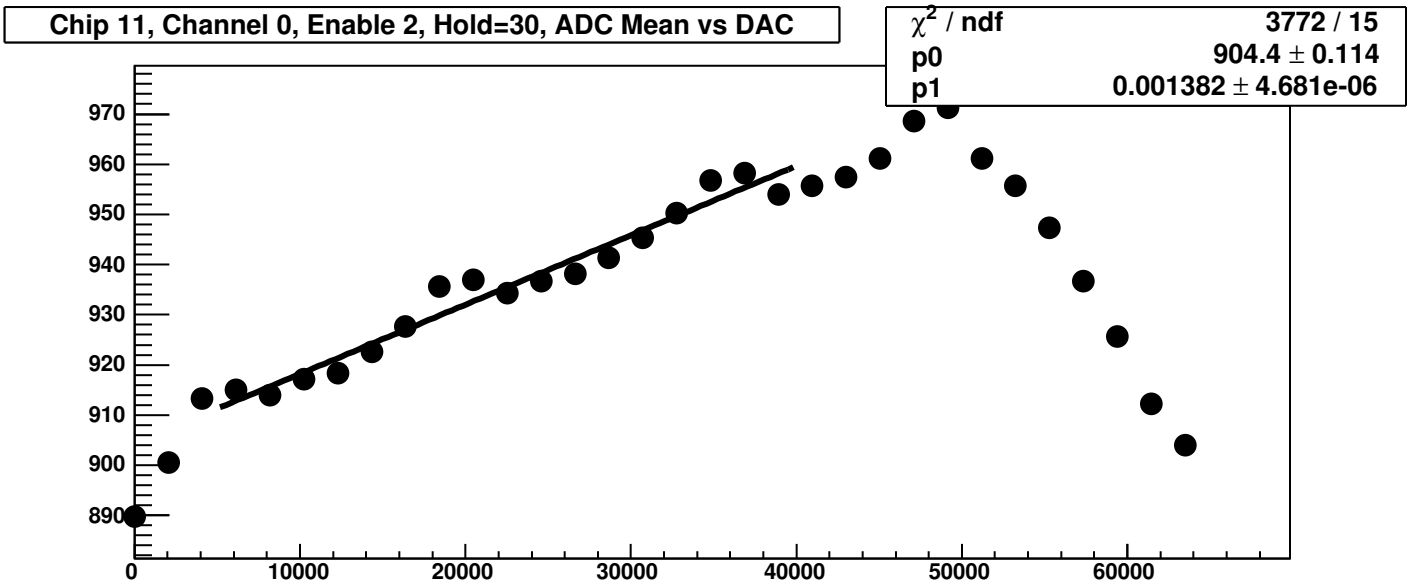

Chip 11, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

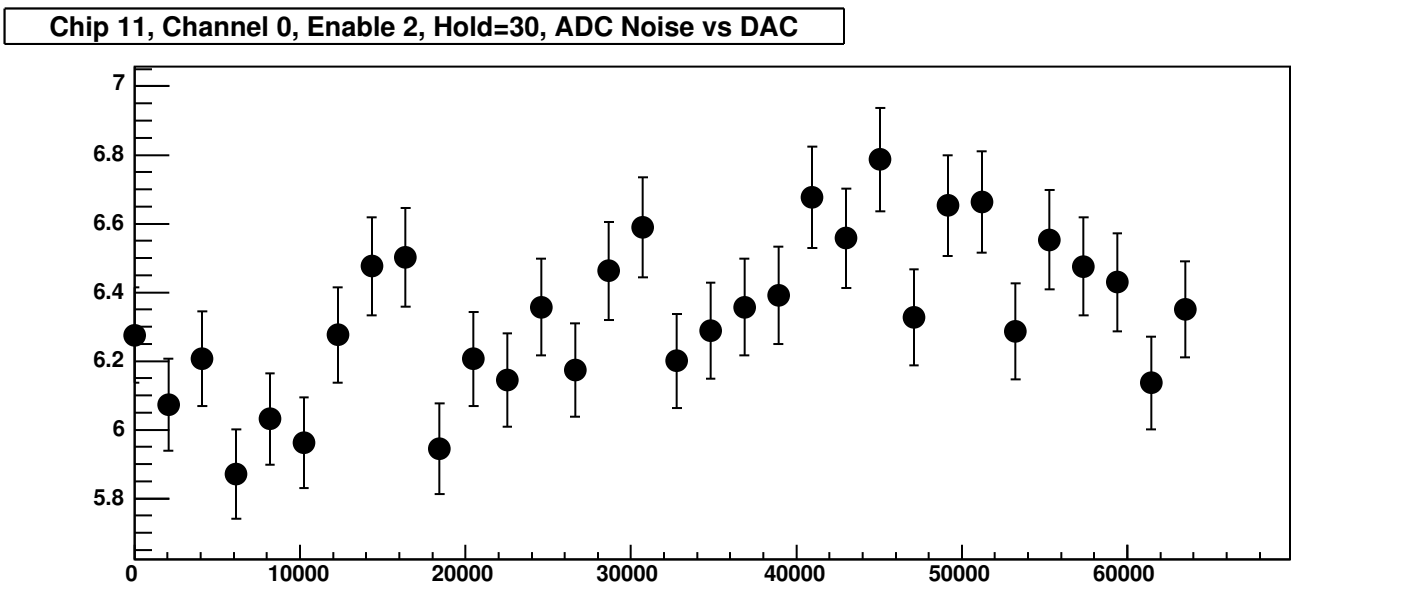

Chip 11, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

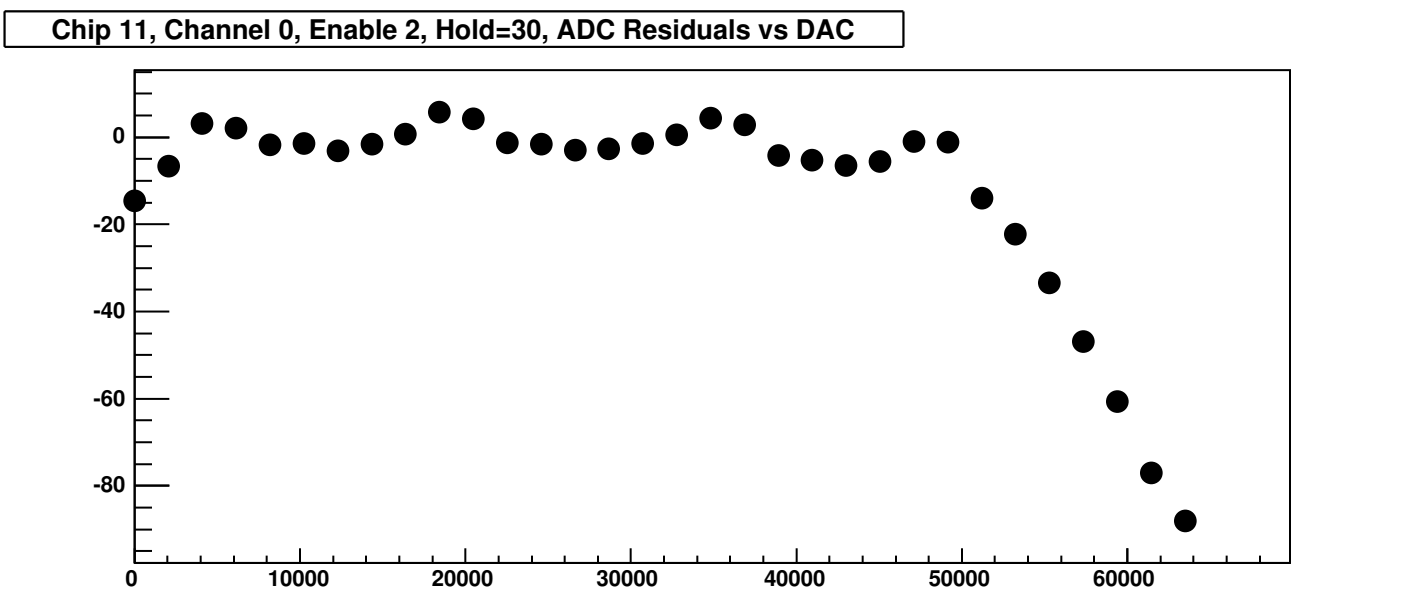

Chip 11, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4077 / 15
p0 905.2 ± 0.1165
p1 0.0017 ± 4.708e-06
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000

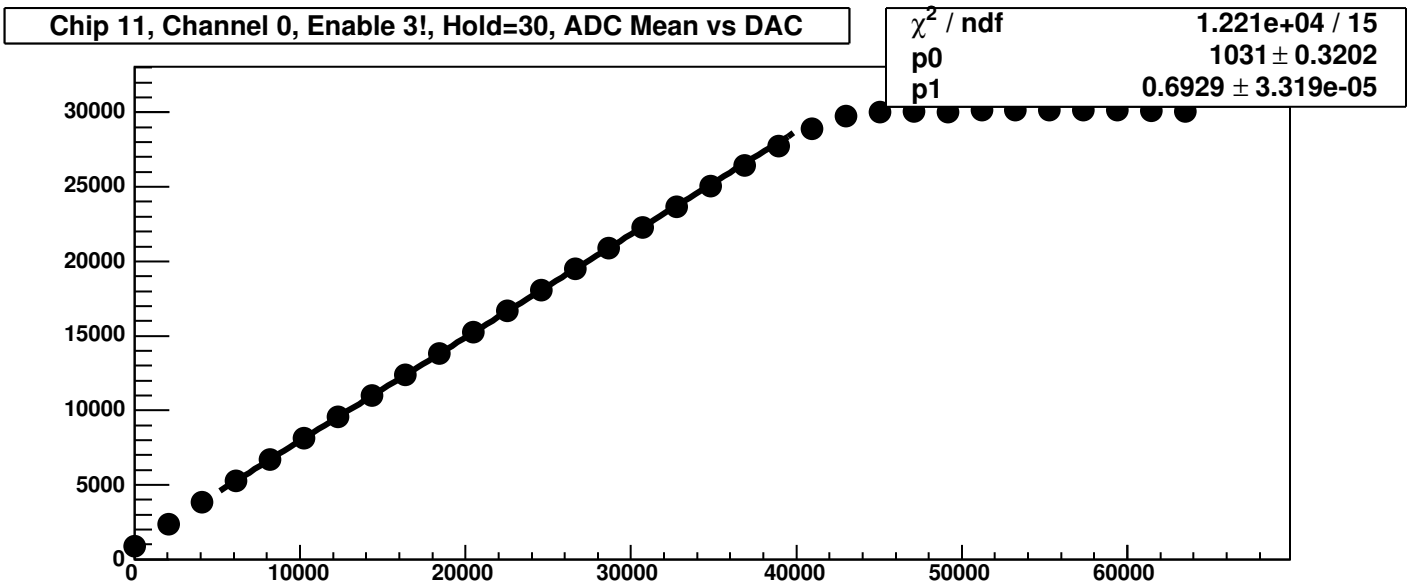

Chip 11, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

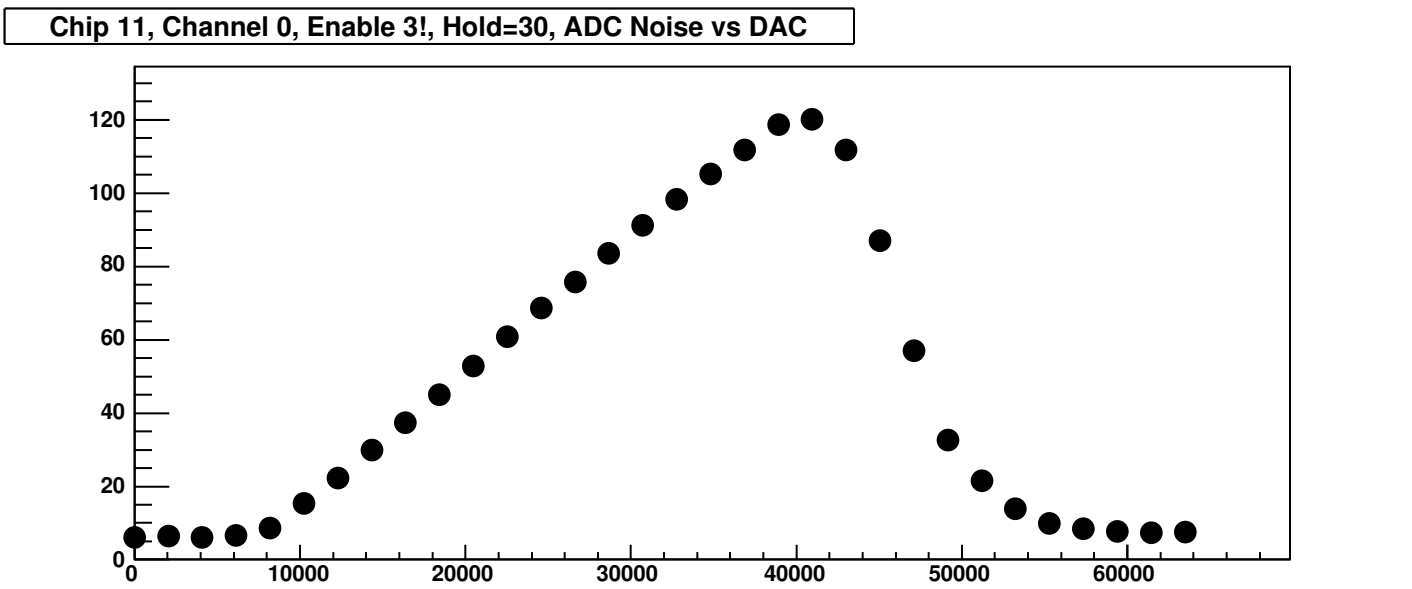

Chip 11, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

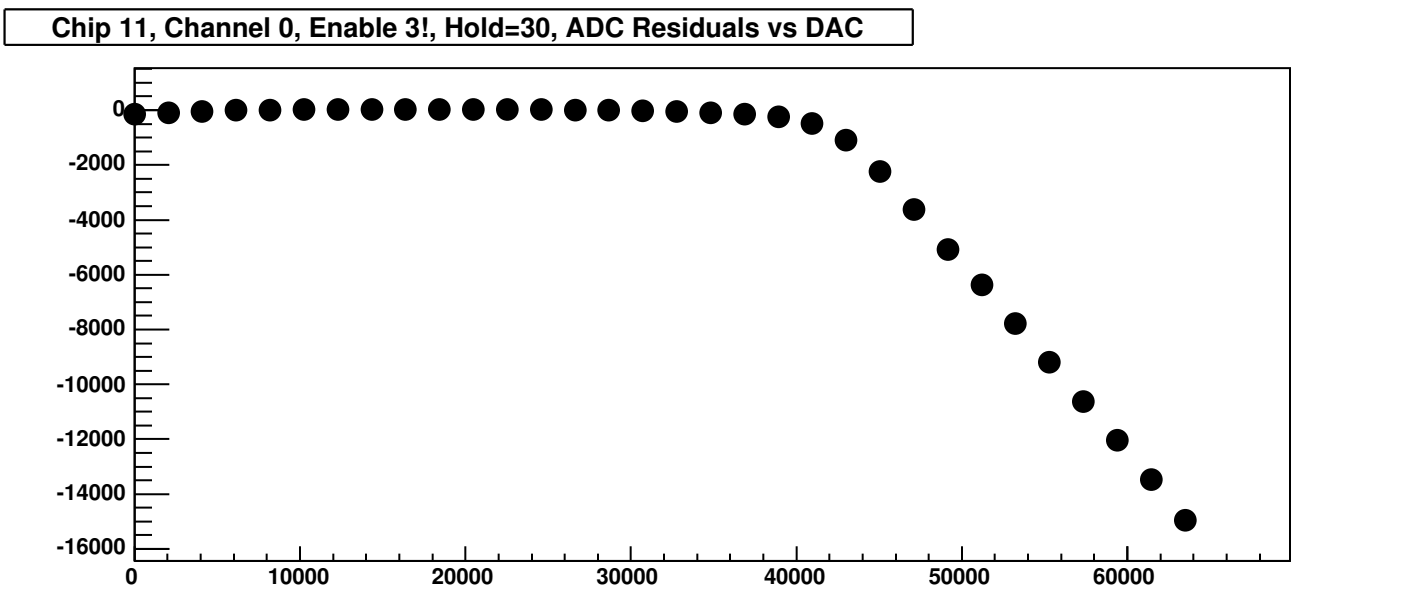

Chip 11, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3772 / 15
p0 904.4 ± 0.114
p1 0.001382 ± 4.681e-06
970
960
950
940
930
920
910
900
890
0
10000
20000
30000
40000
50000
60000

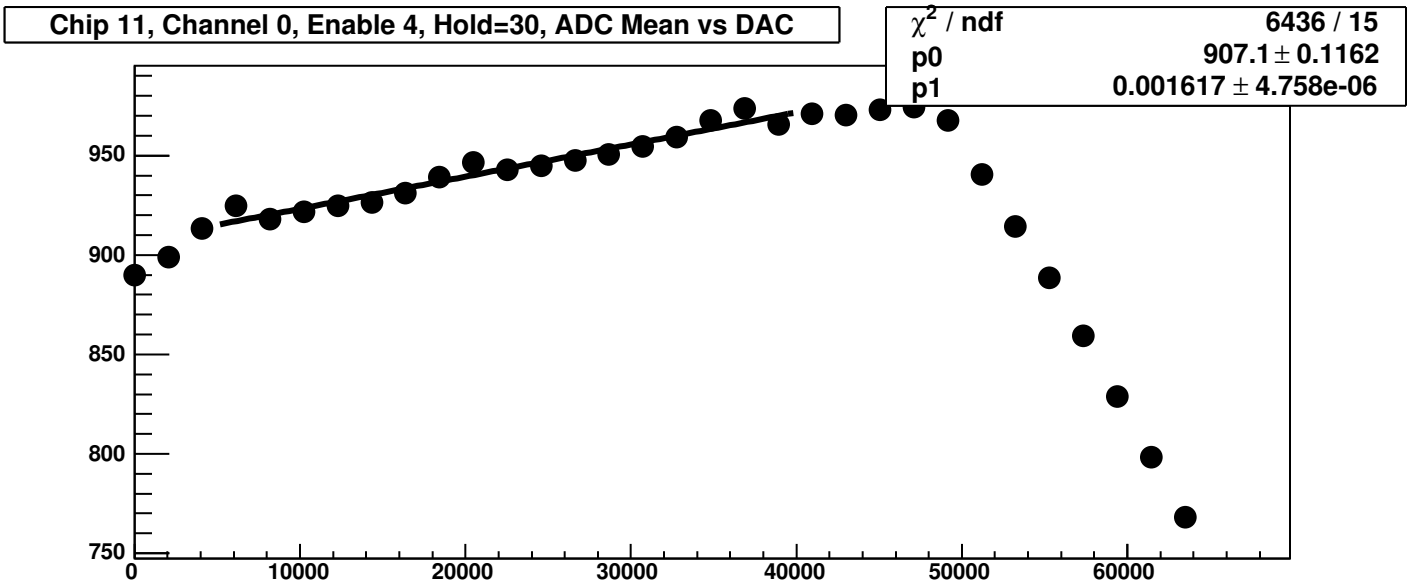

Chip 11, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

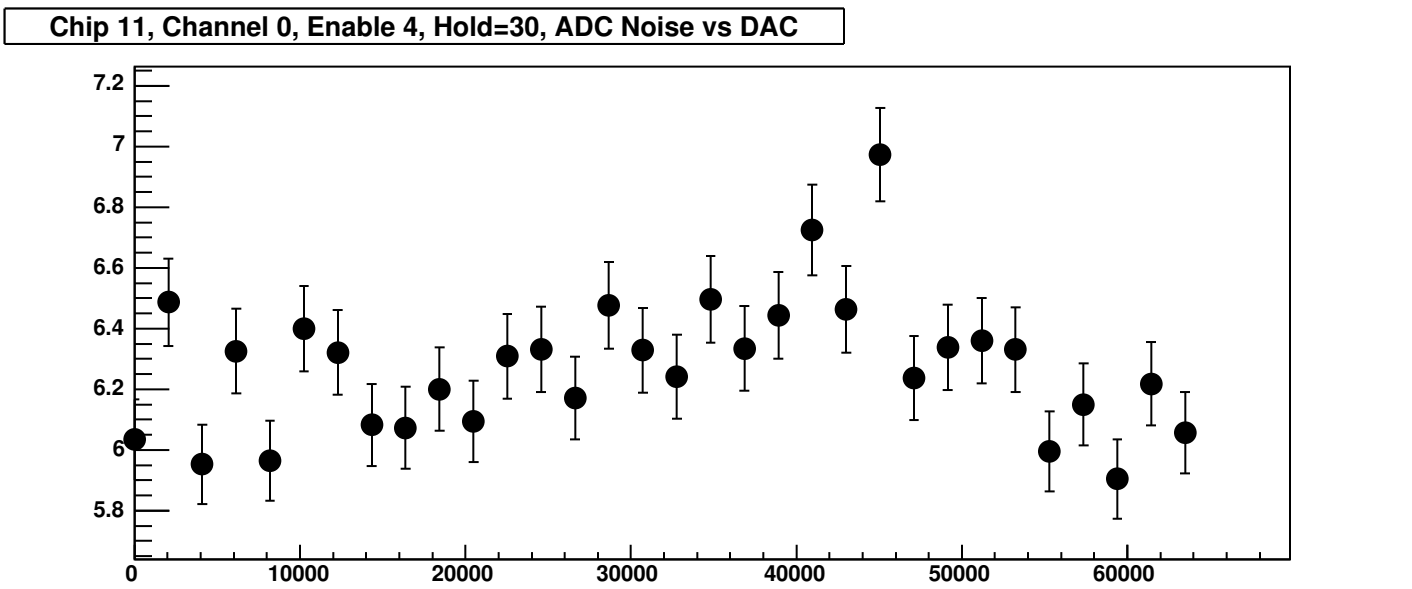

Chip 11, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

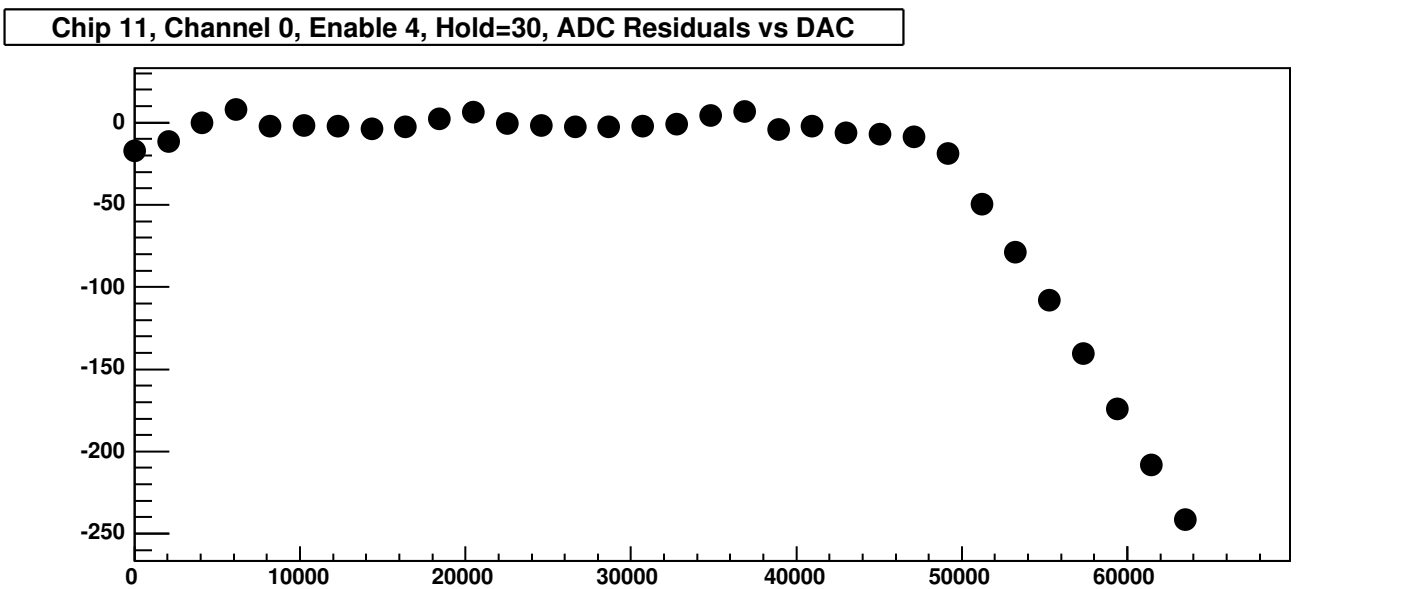

Chip 11, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.221e+04 / 15
p0 1031 ± 0.3202
p1 0.6929 ± 3.319e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

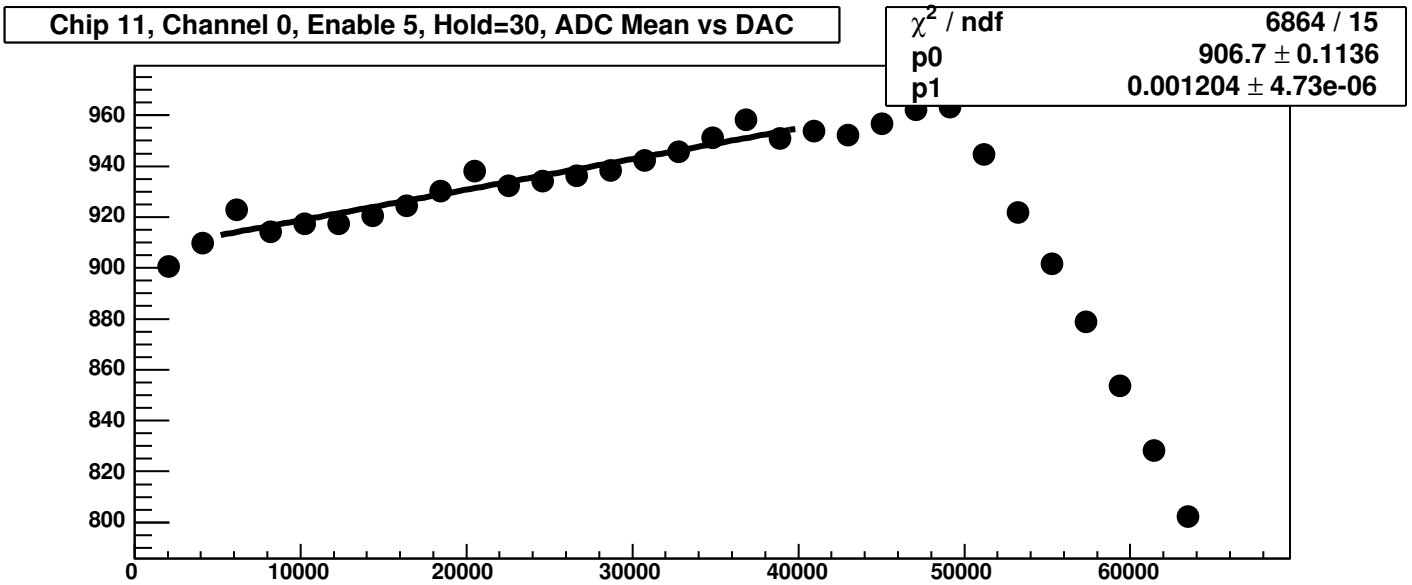

Chip 11, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

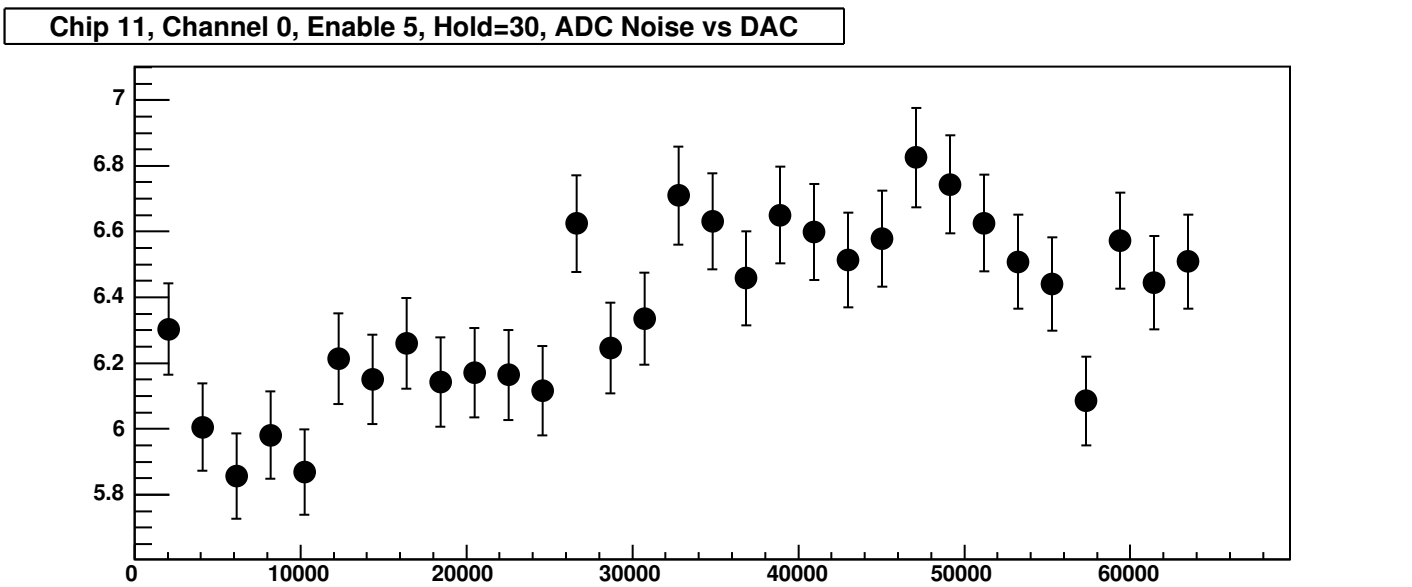

Chip 11, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
10000
20000
30000
40000
50000
60000

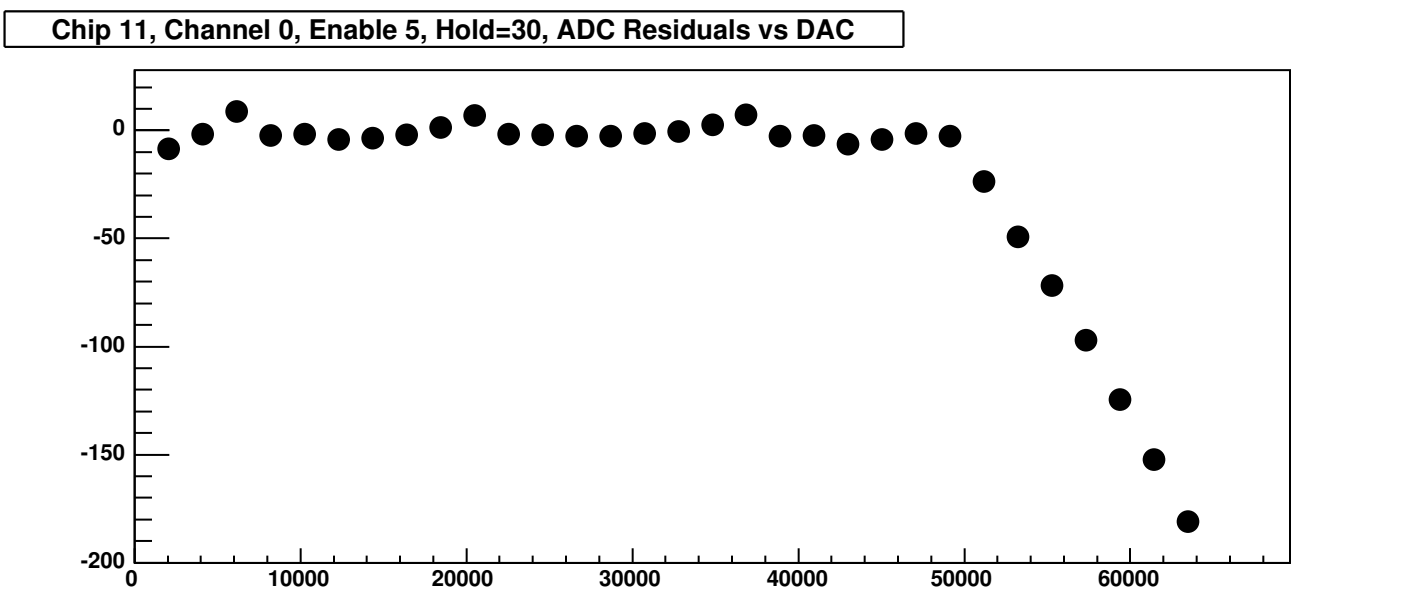

Chip 11, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6436 / 15
p0 907.1 ± 0.1162
p1 0.001617 ± 4.758e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

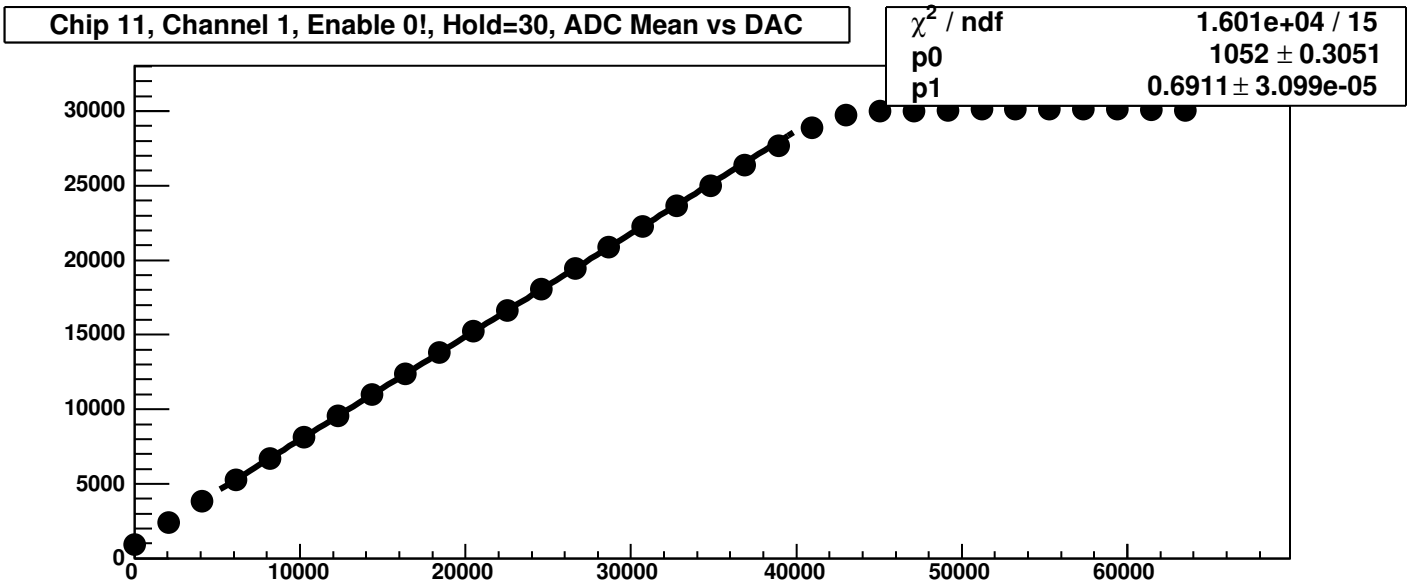

Chip 11, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

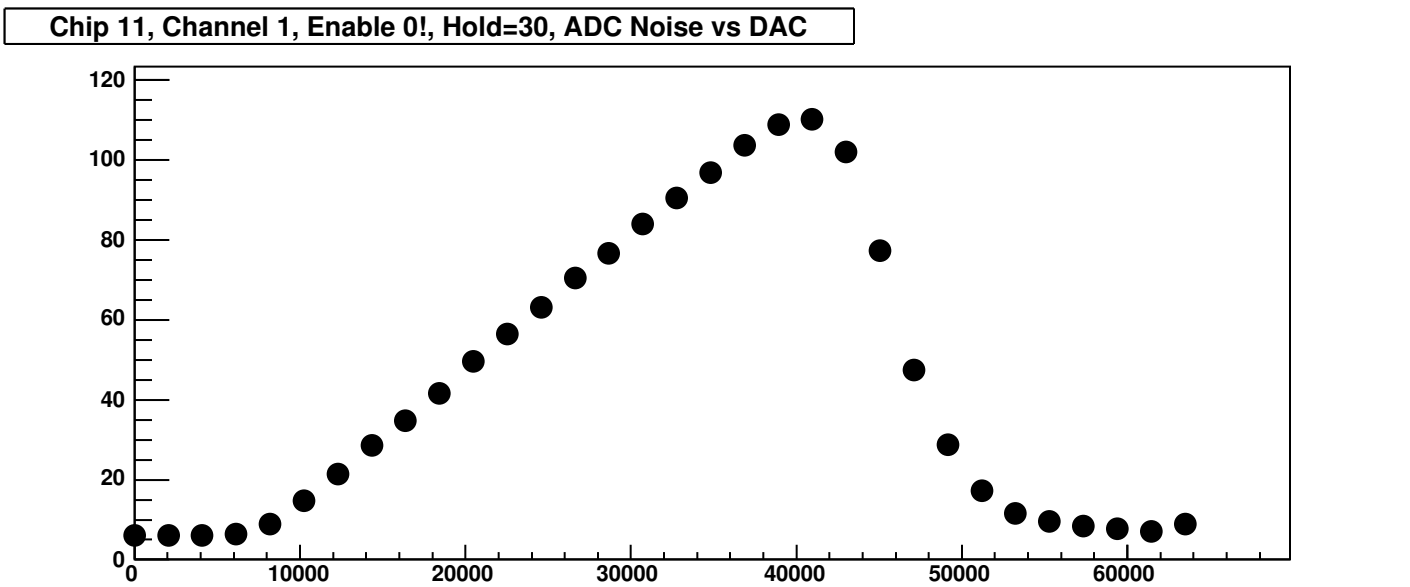

Chip 11, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

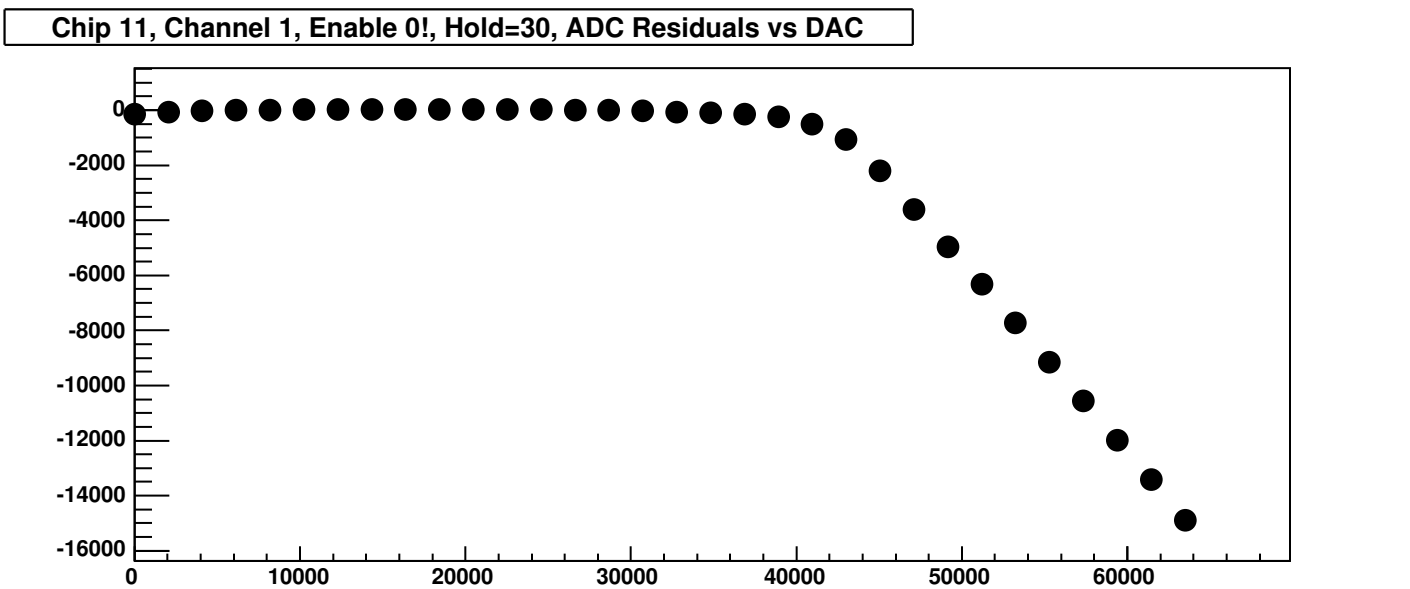

Chip 11, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6864 / 15
p0 906.7 ± 0.1136
p1 0.001204 ± 4.73e-06
960
940
920
900
880
860
840
820
800
0
10000
20000
30000
40000
50000
60000

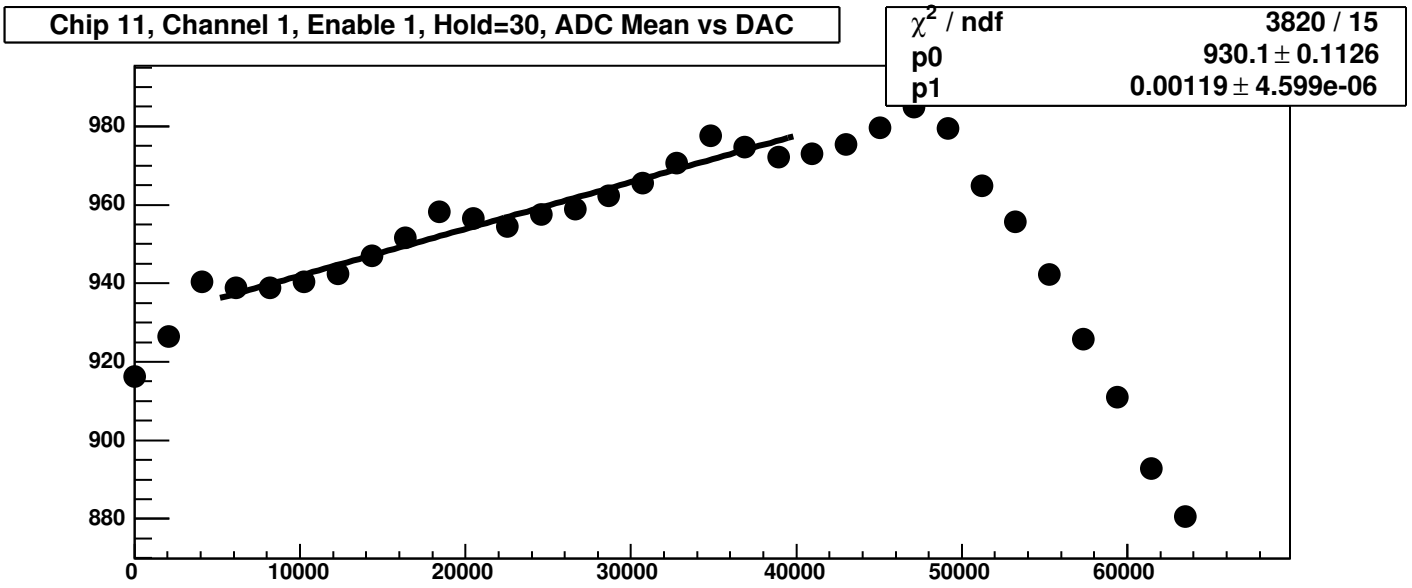

Chip 11, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

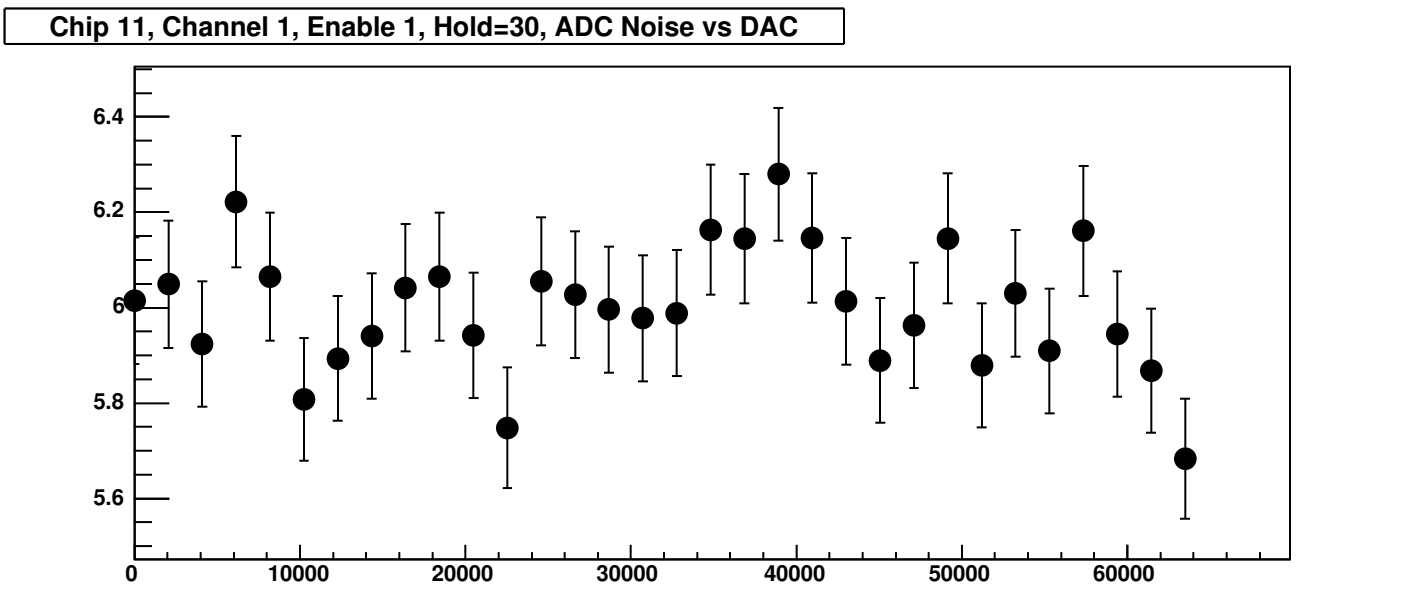

Chip 11, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

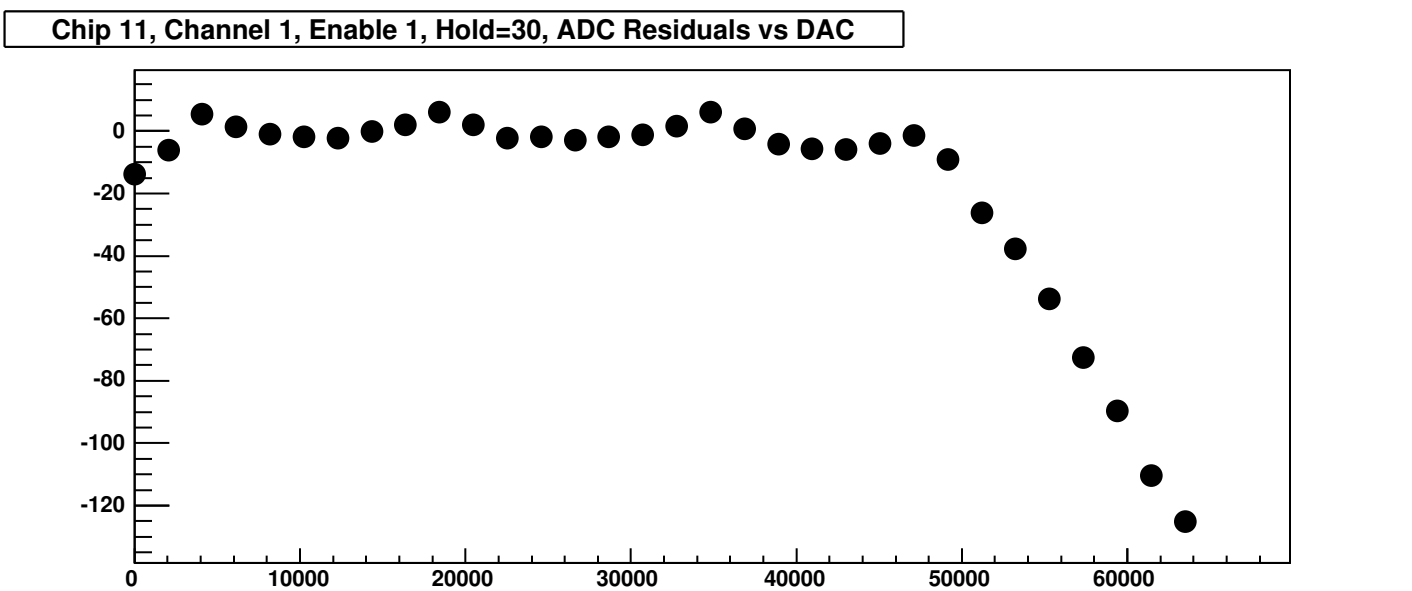

Chip 11, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.601e+04 / 15
p0 1052 ± 0.3051
p1 0.6911 ± 3.099e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

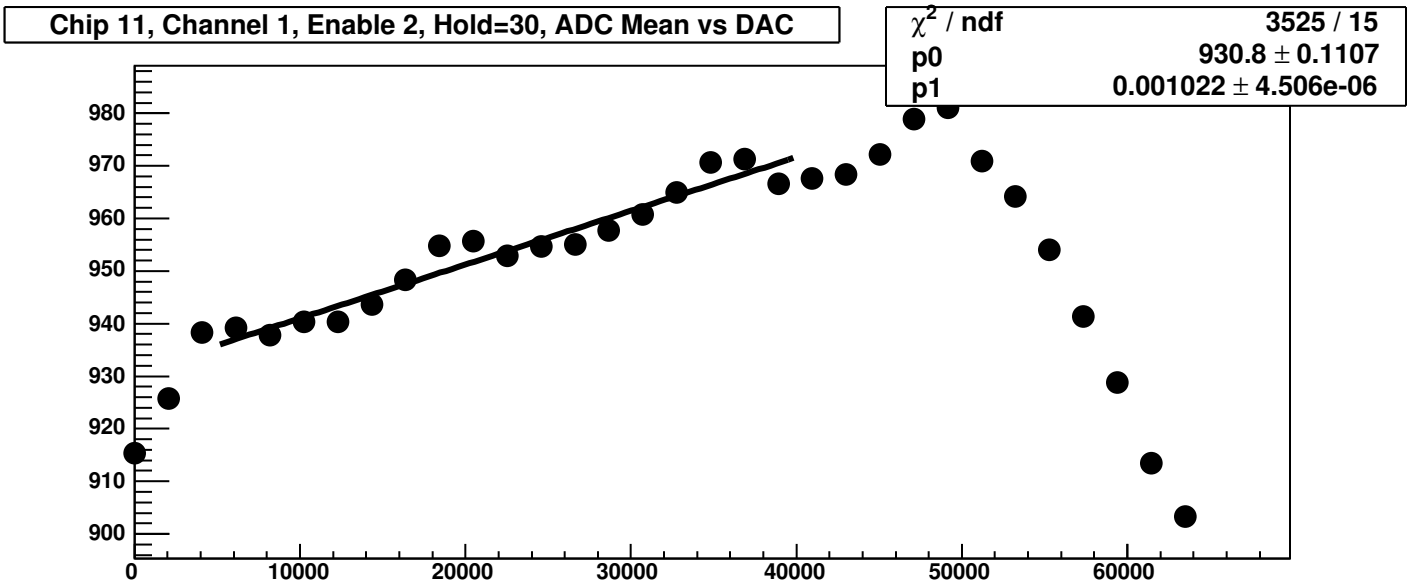

Chip 11, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

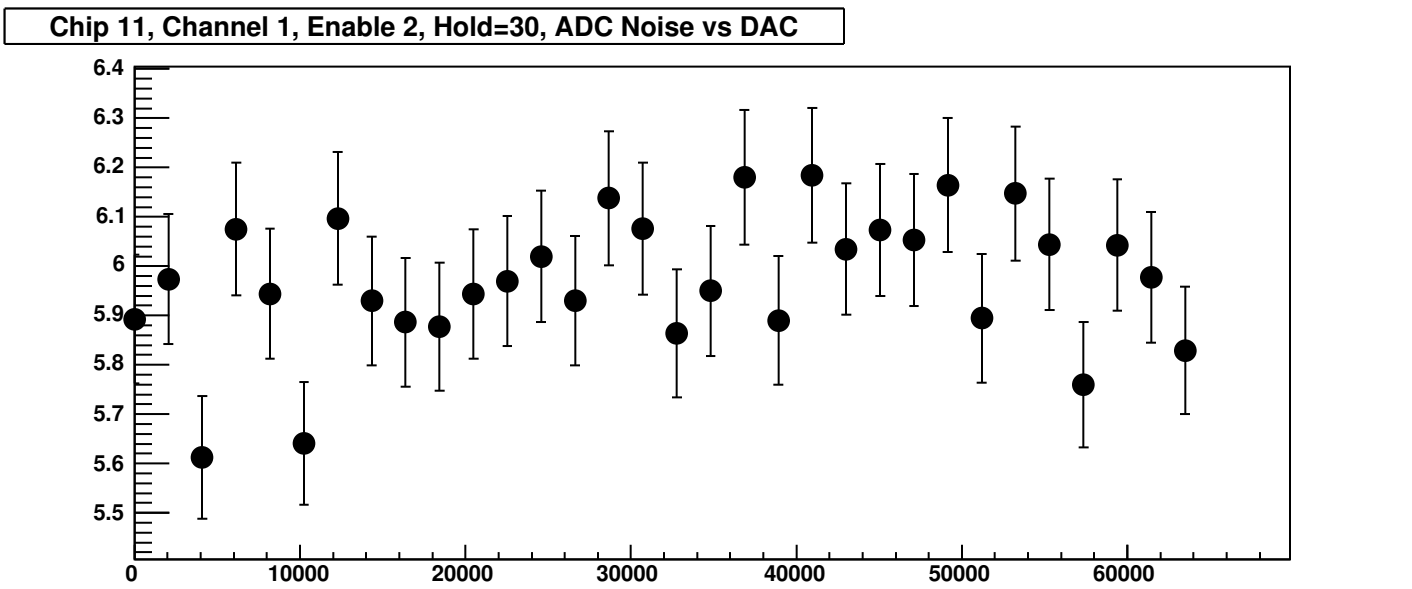

Chip 11, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
10000
20000
30000
40000
50000
60000

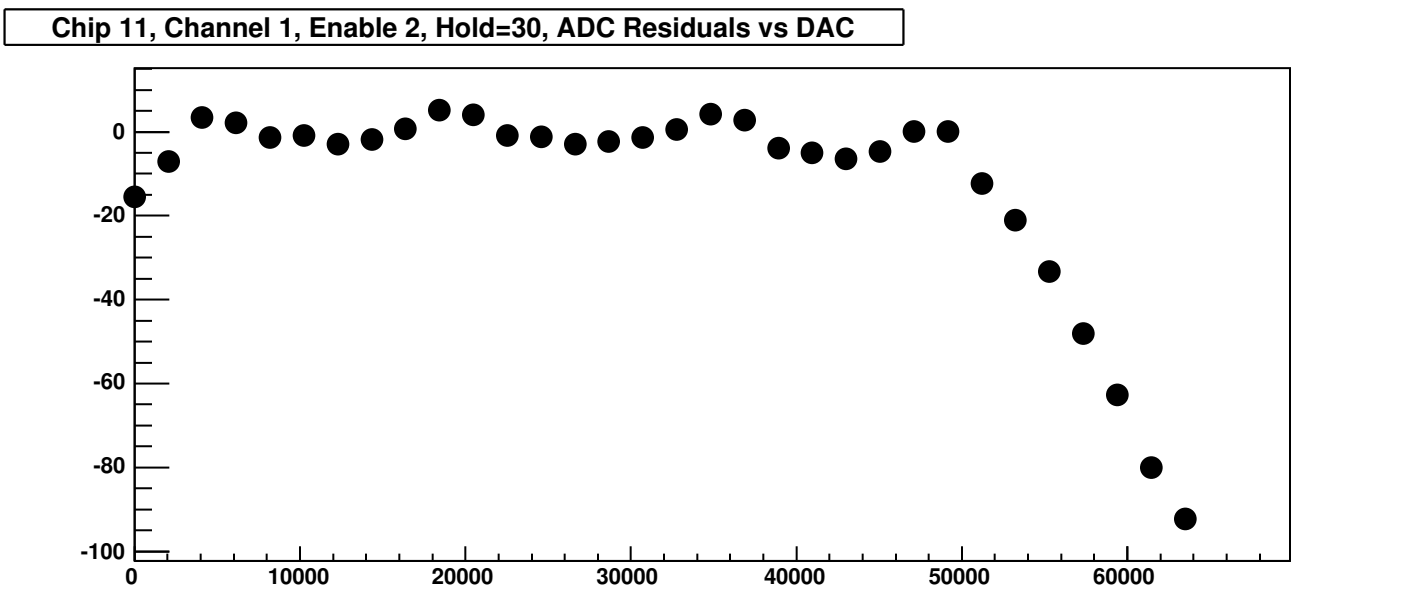

Chip 11, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3820 / 15
p0 930.1 ± 0.1126
p1 0.00119 ± 4.599e-06
980
960
940
920
900
880
0
10000
20000
30000
40000
50000
60000

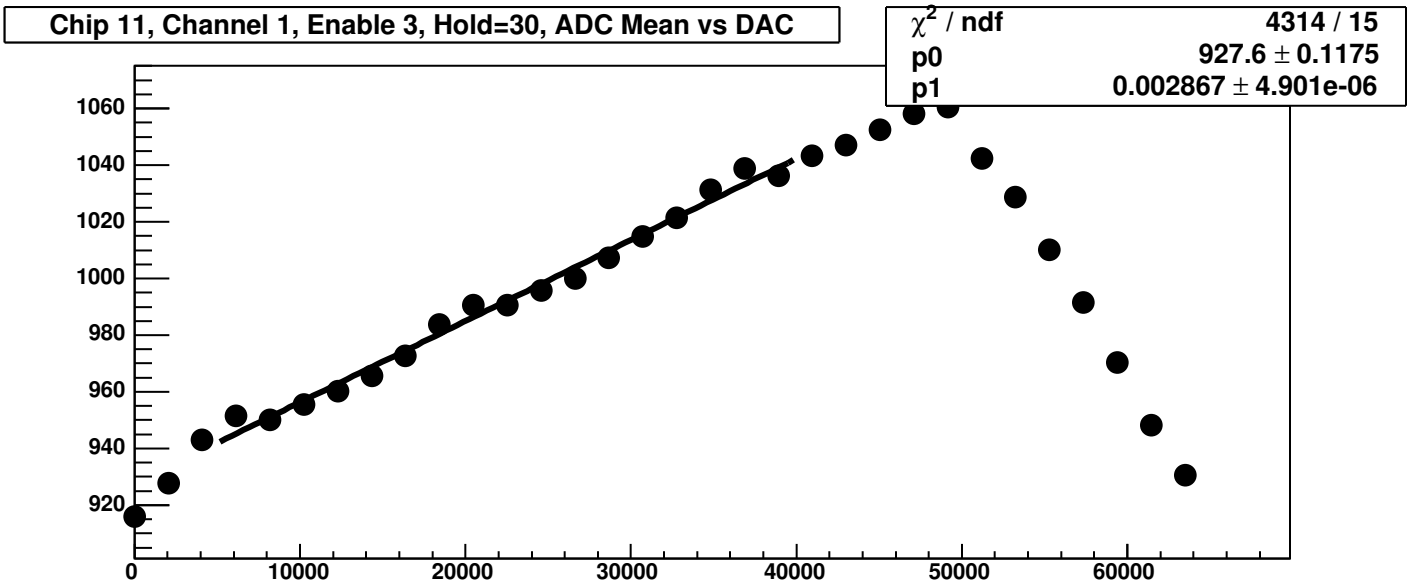

Chip 11, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

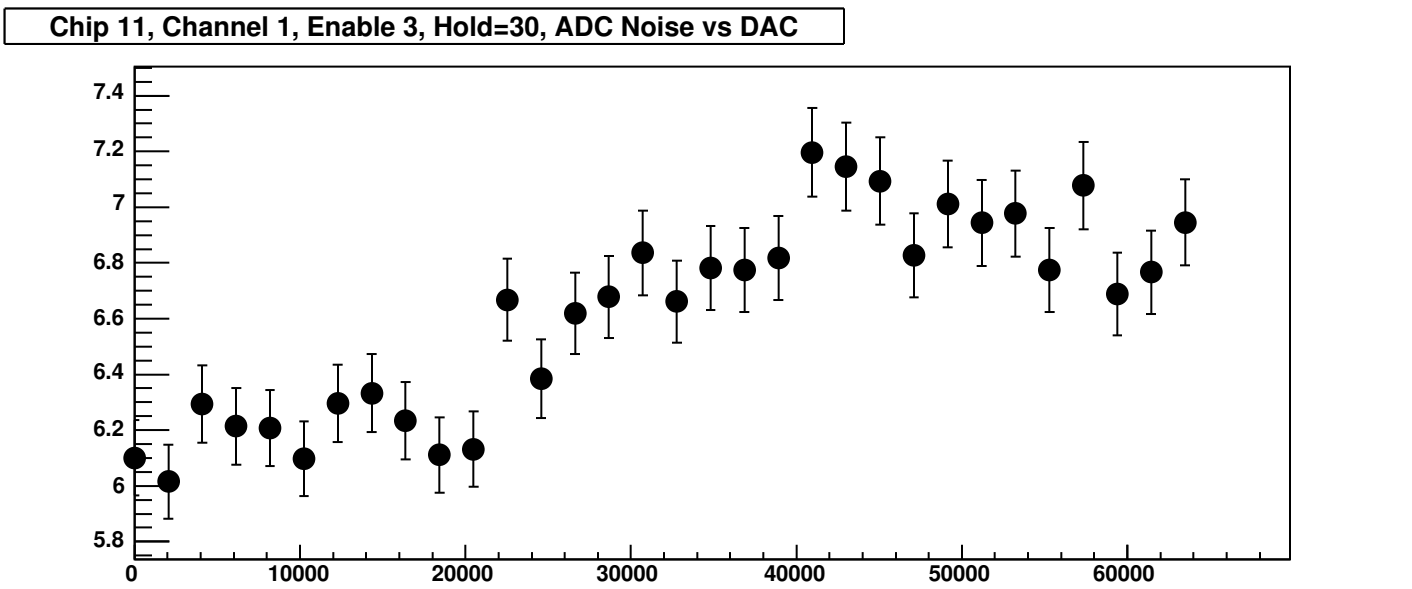

Chip 11, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

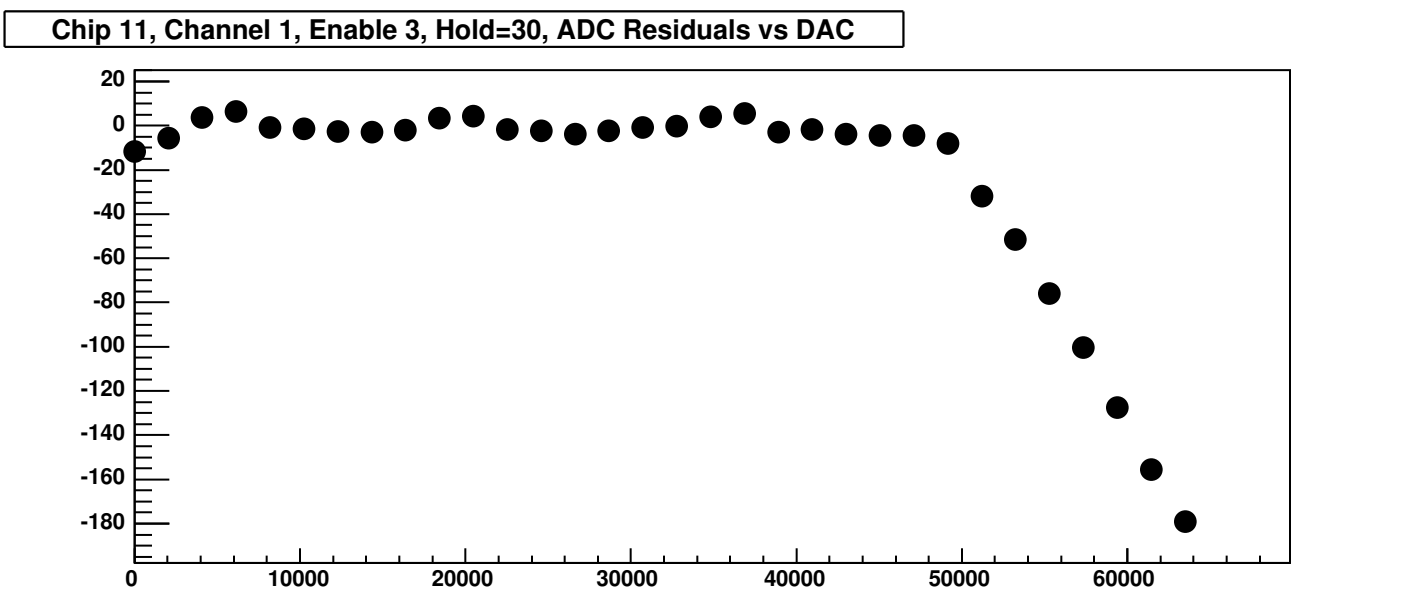

Chip 11, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3525 / 15
p0 930.8 ± 0.1107
p1 0.001022 ± 4.506e-06
980
970
960
950
940
930
920
910
900
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

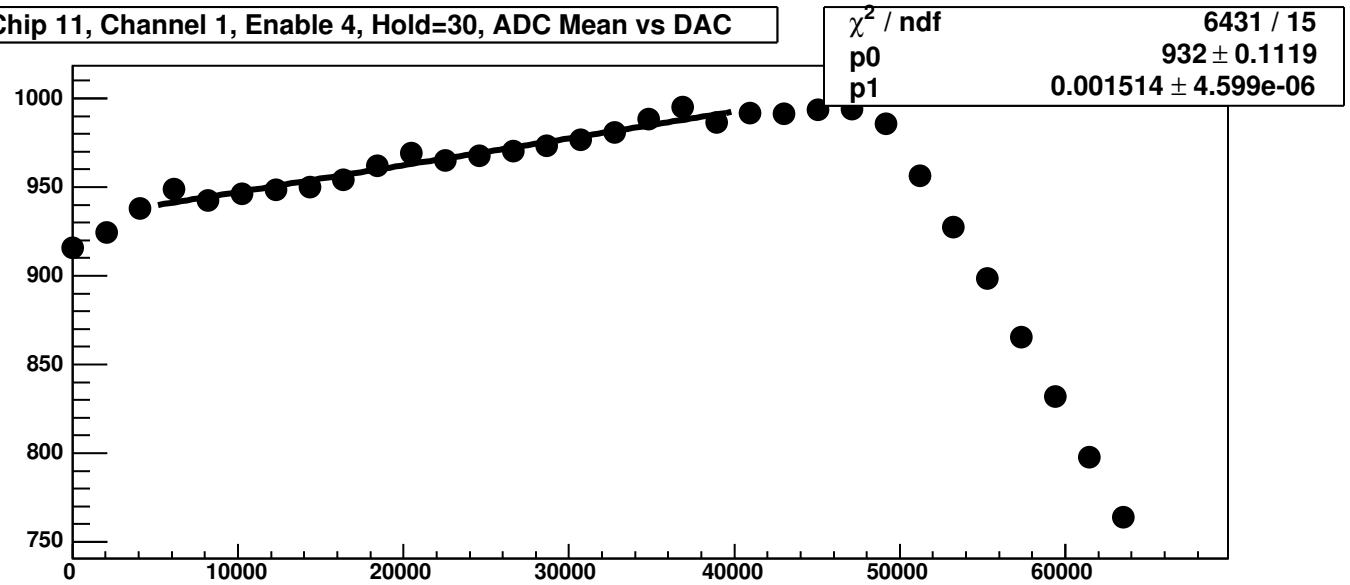

Chip 11, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4314 / 15
p0 927.6 ± 0.1175
p1 0.002867 ± 4.901e-06
1060
1040
1020
1000
980
960
940
920
0
10000
20000
30000
40000
50000
60000

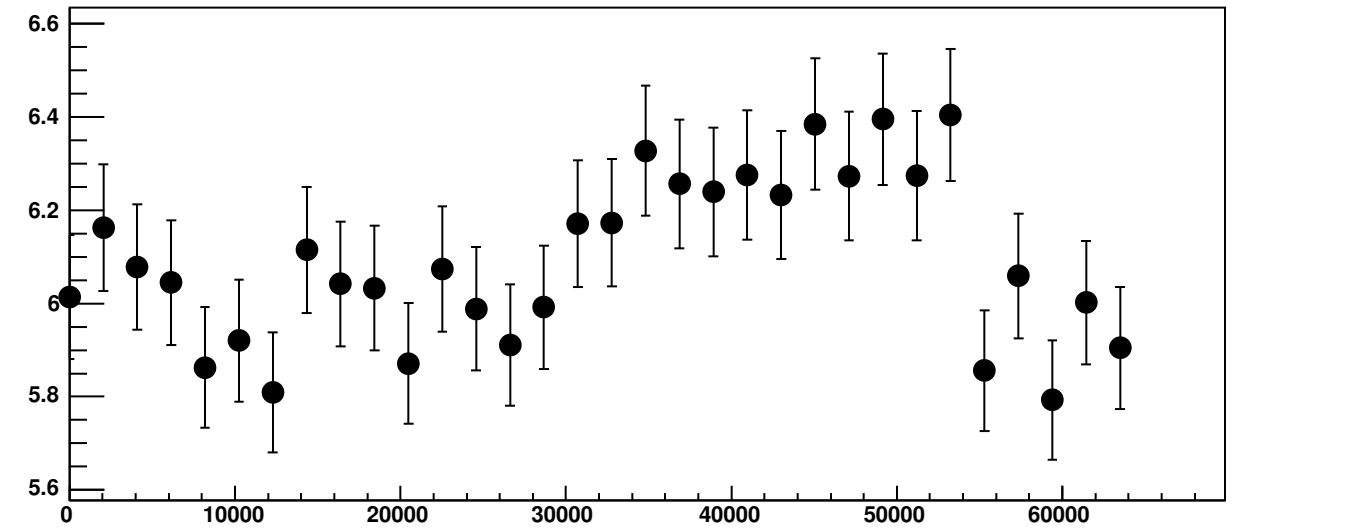

Chip 11, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

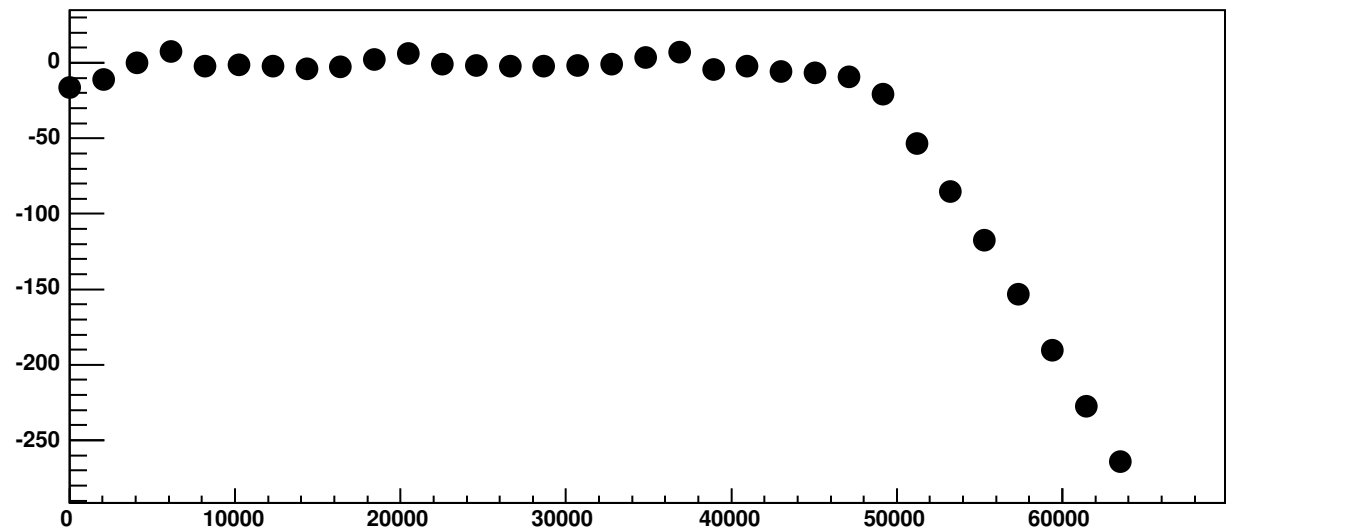
Chip 11, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC

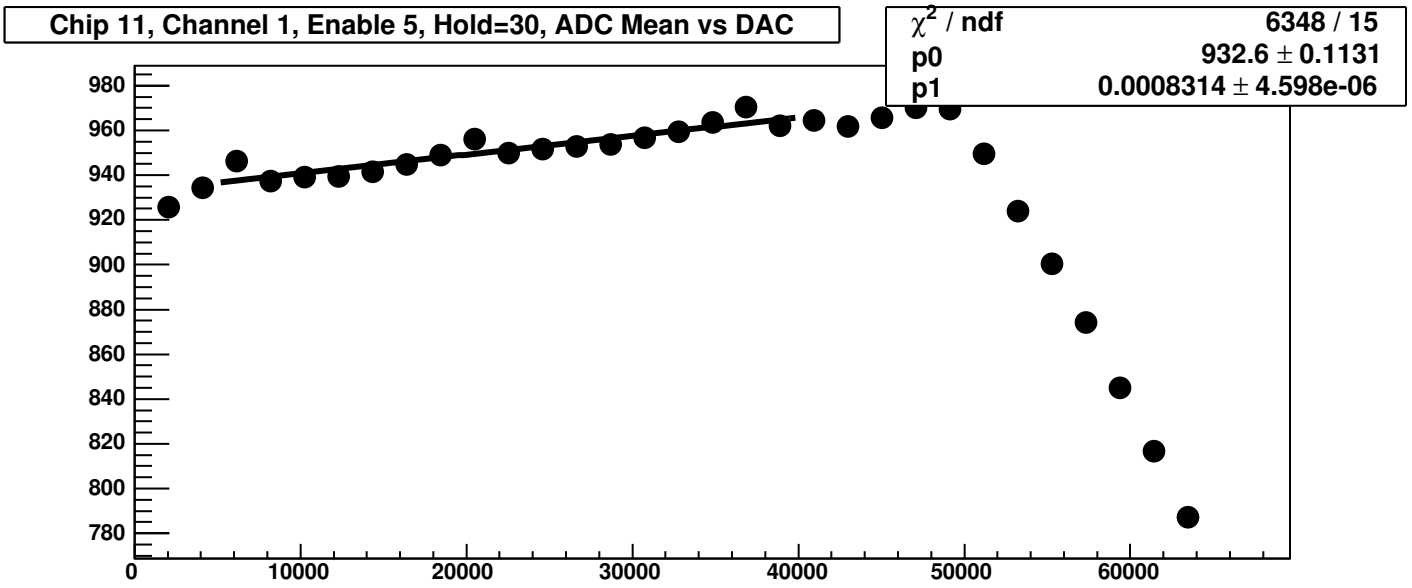


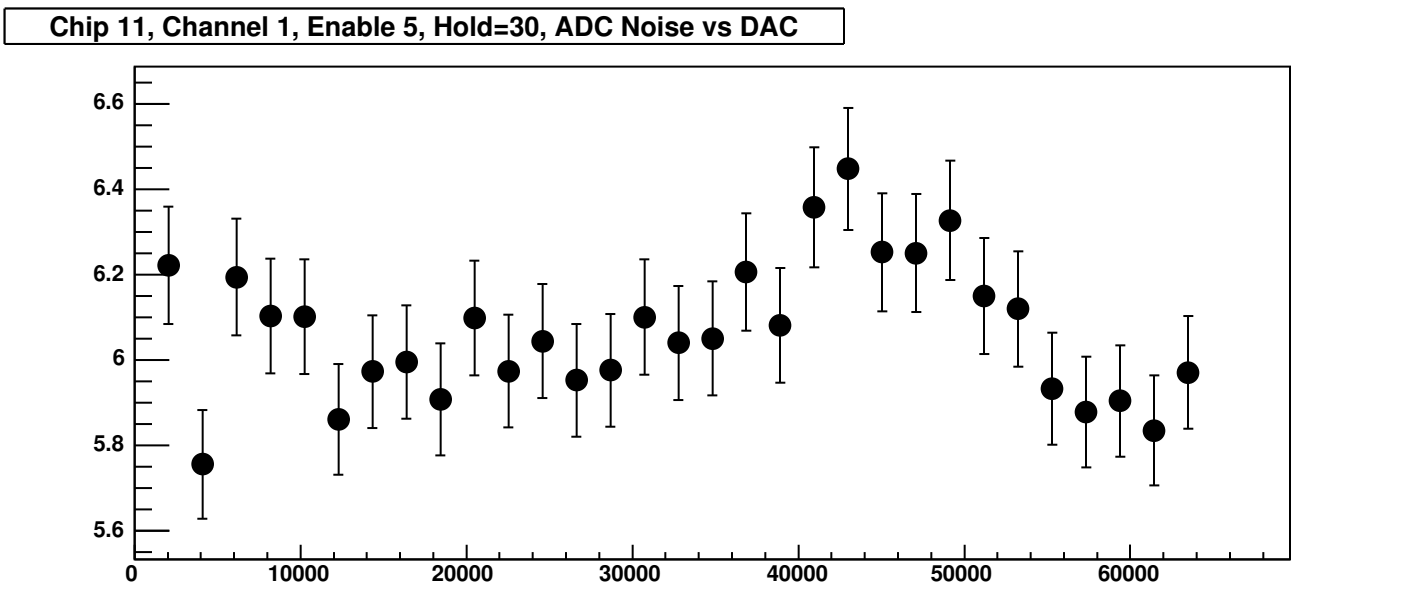
Chip 11, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC

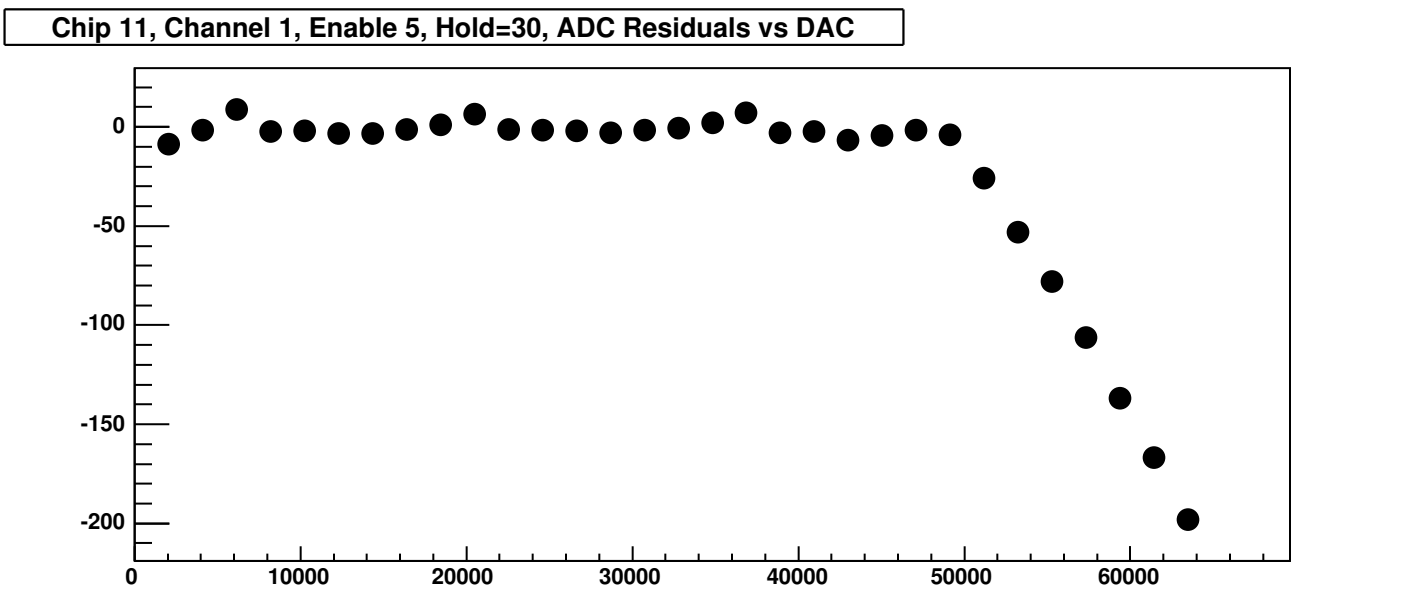


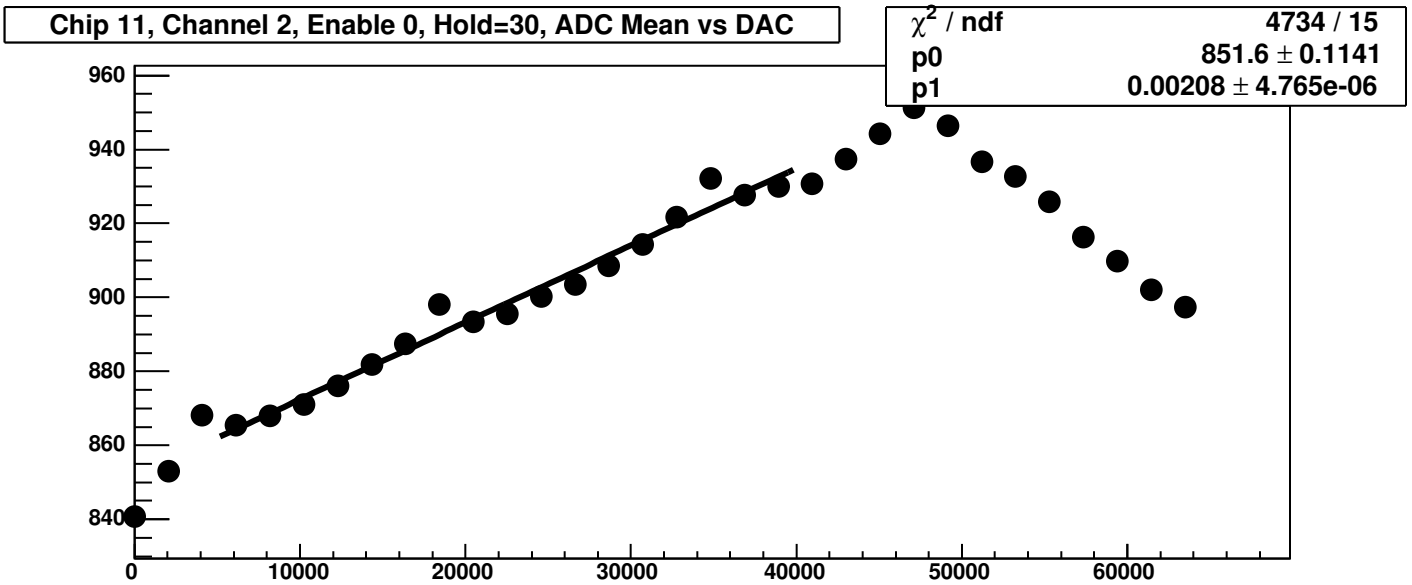
Chip 11, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC

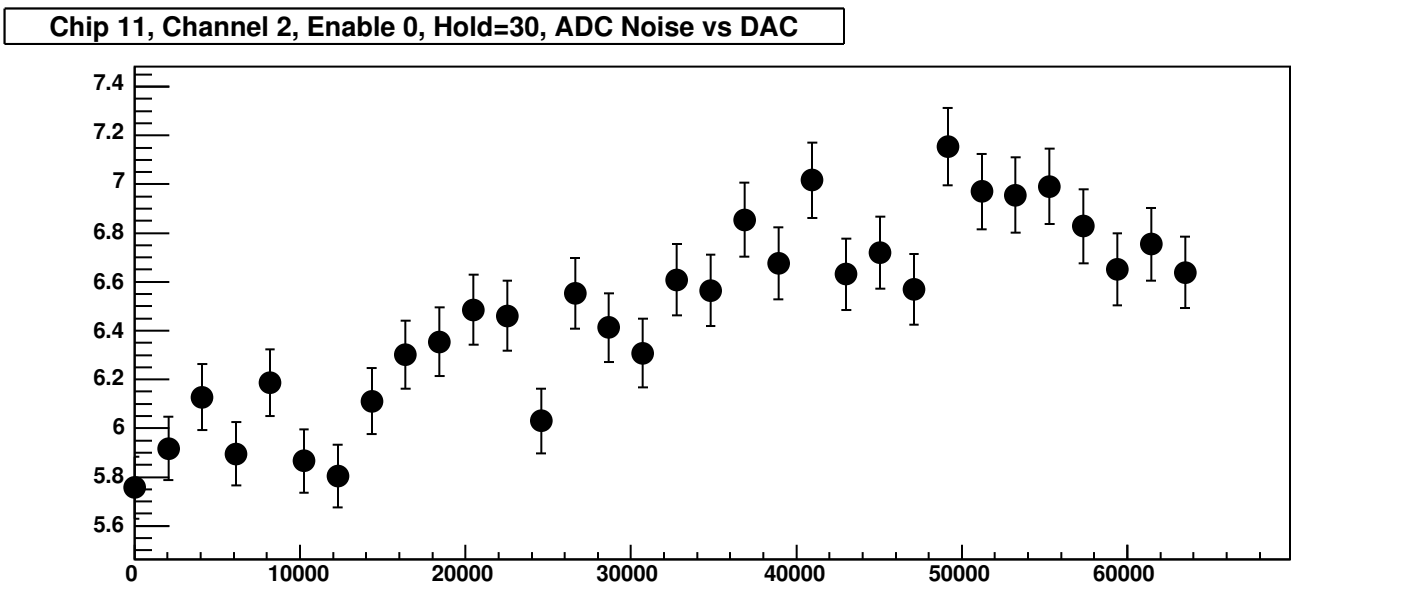


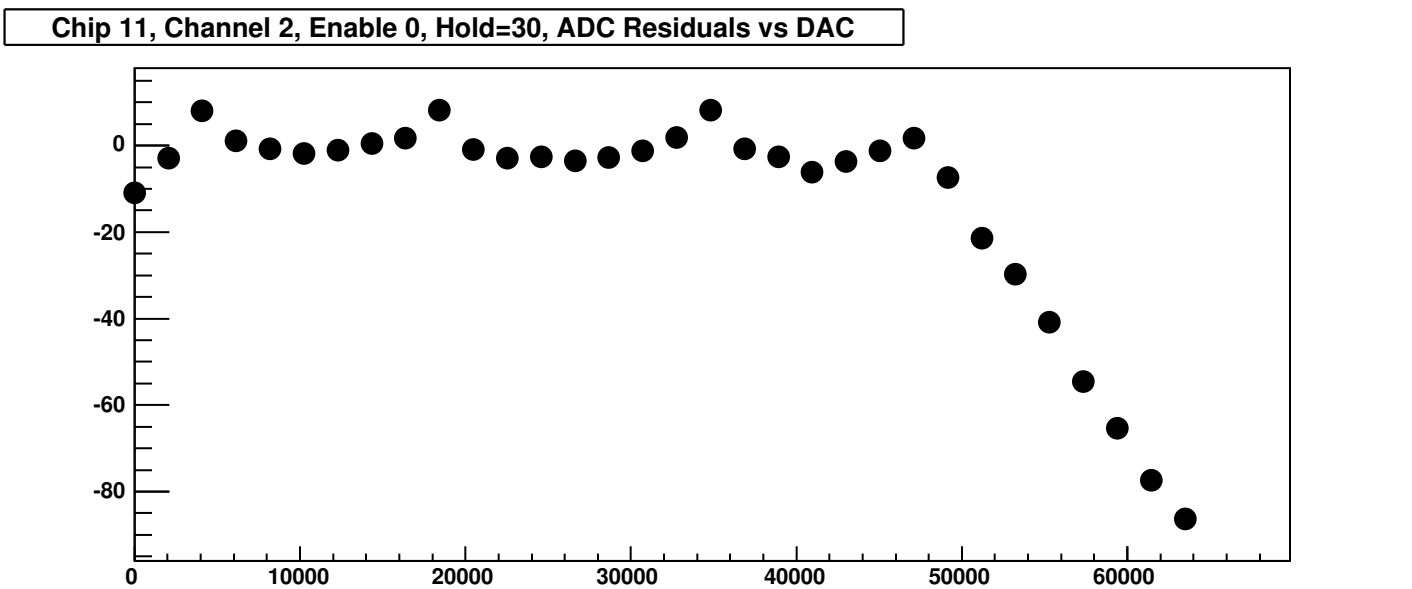

Chip 11, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6348 / 15
p0 932.6 ± 0.1131
p1 0.0008314 ± 4.598e-06
980
960
940
920
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

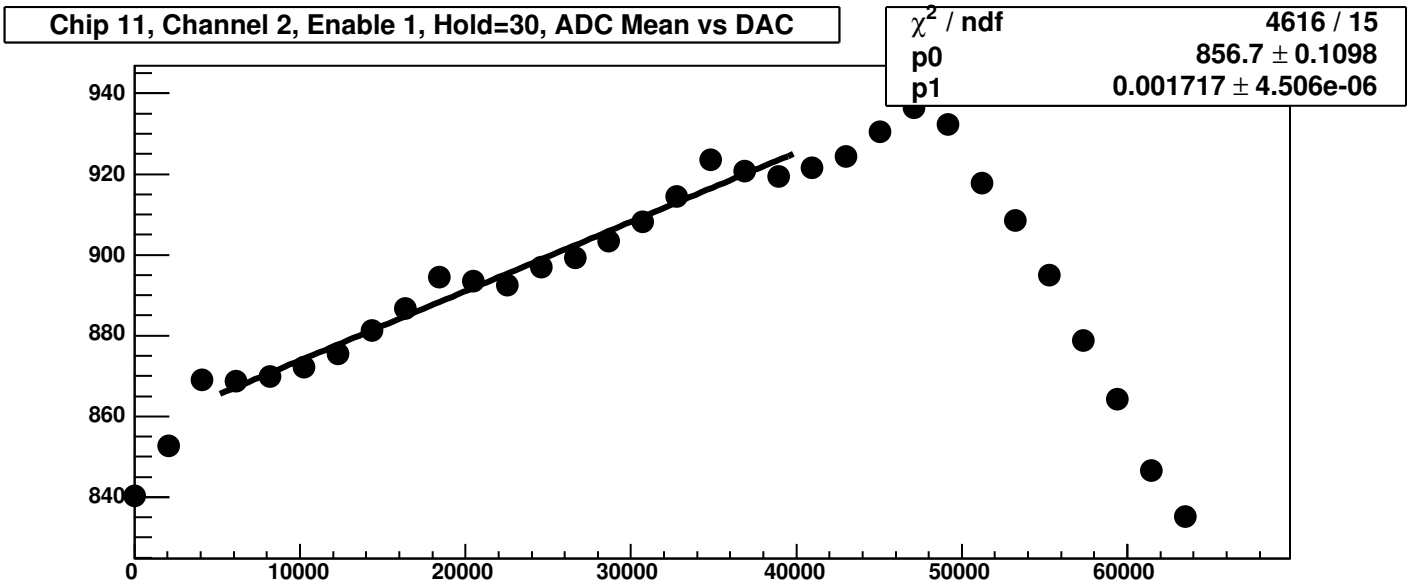

Chip 11, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

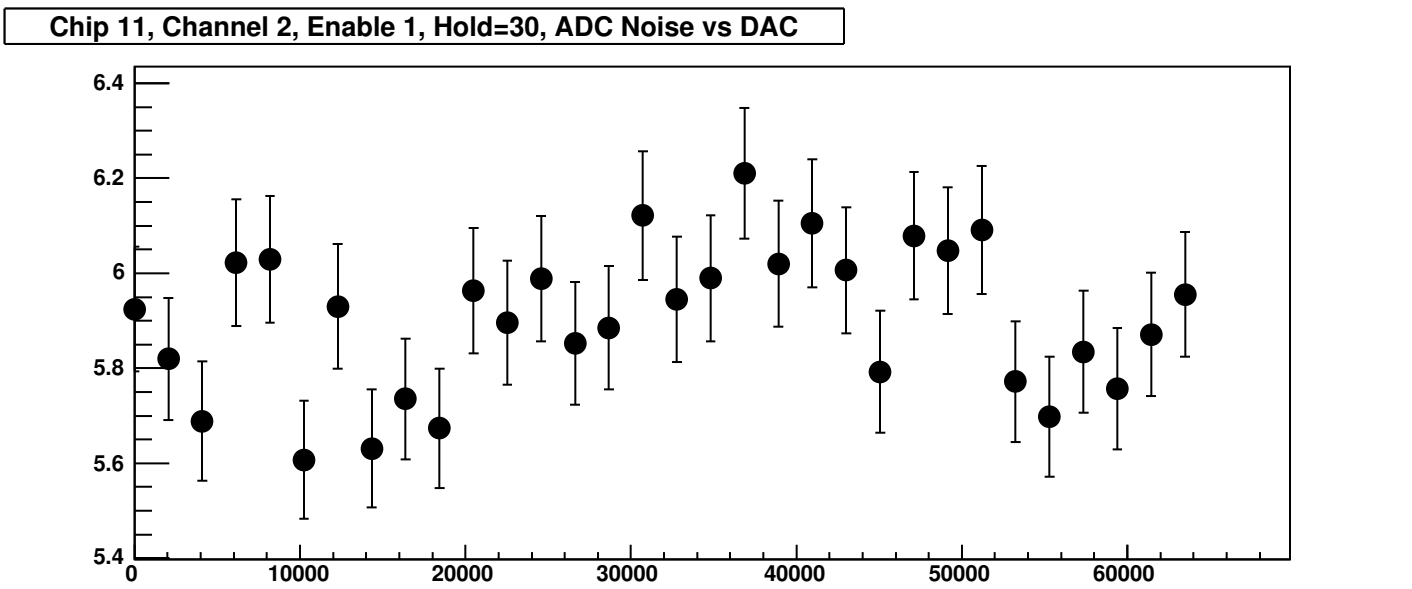

Chip 11, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

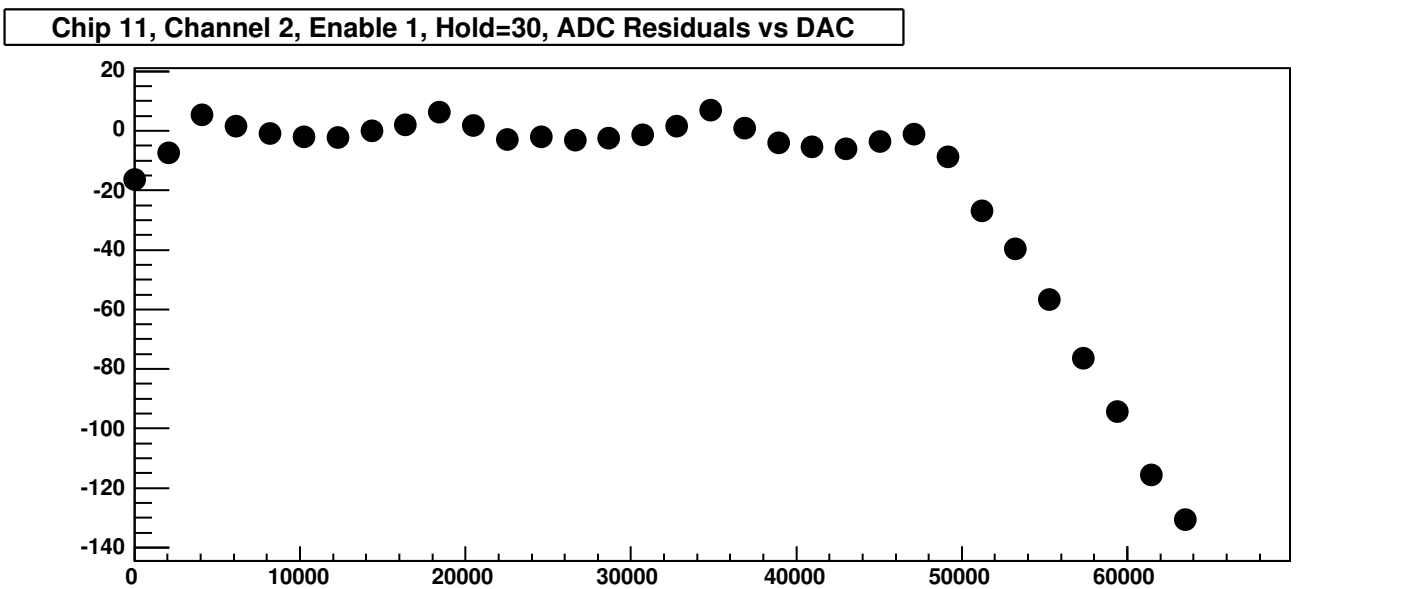

Chip 11, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4734 / 15
p0 851.6 ± 0.1141
p1 0.00208 ± 4.765e-06
960
940
920
900
880
860
840
0
10000
20000
30000
40000
50000
60000

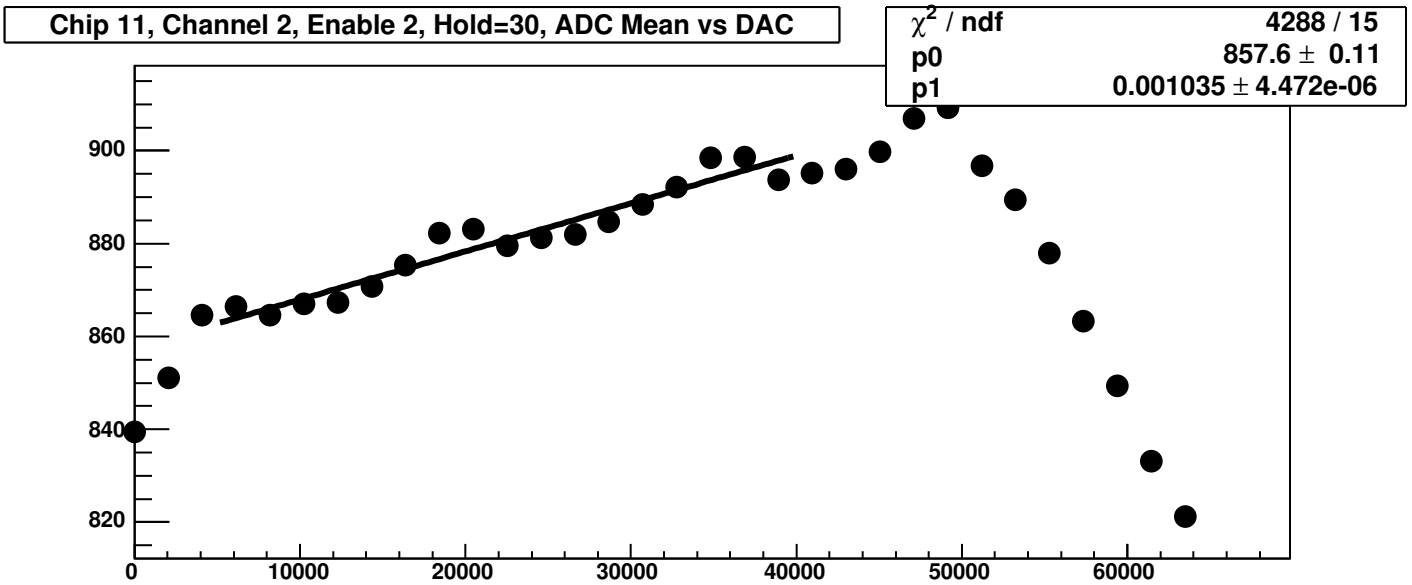

Chip 11, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

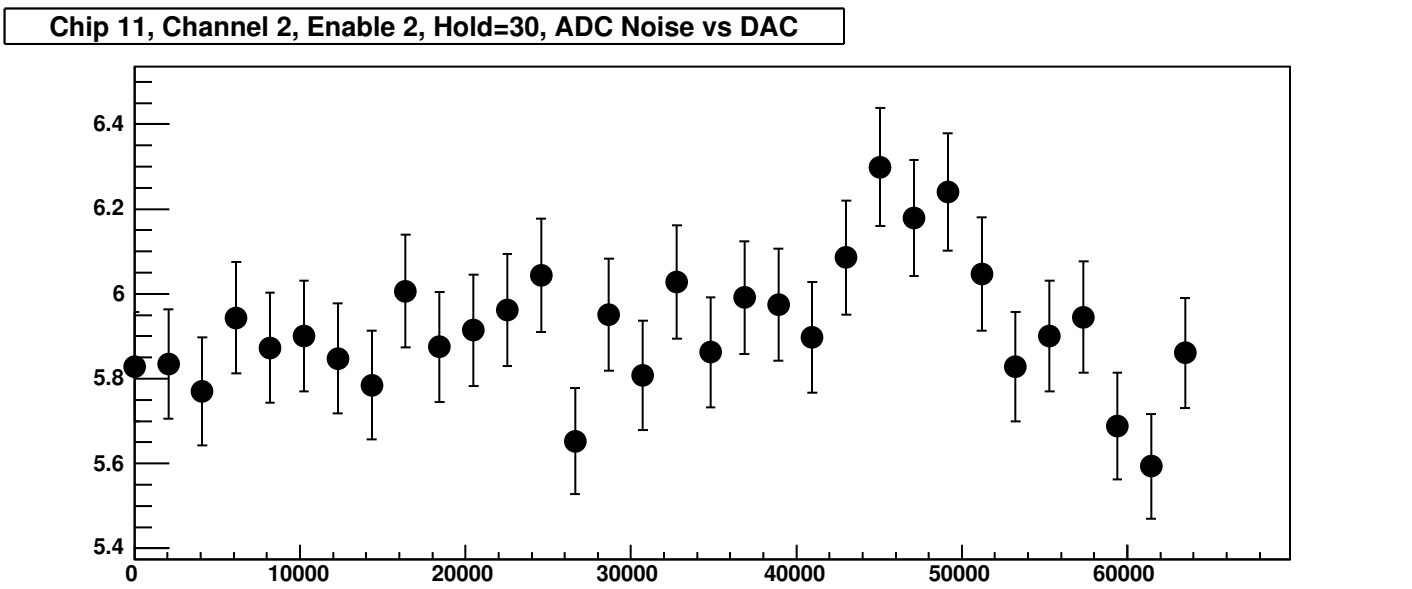

Chip 11, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

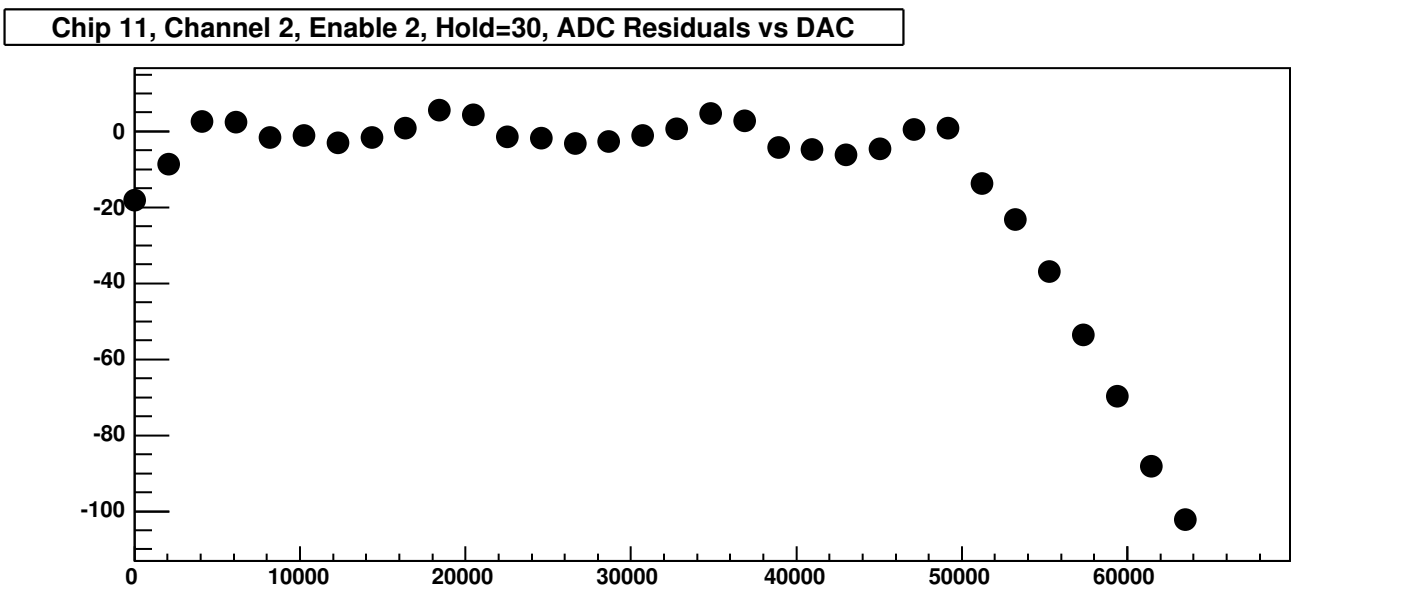

Chip 11, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4616 / 15
p0 856.7 ± 0.1098
p1 0.001717 ± 4.506e-06
940
920
900
880
860
840
0
10000
20000
30000
40000
50000
60000

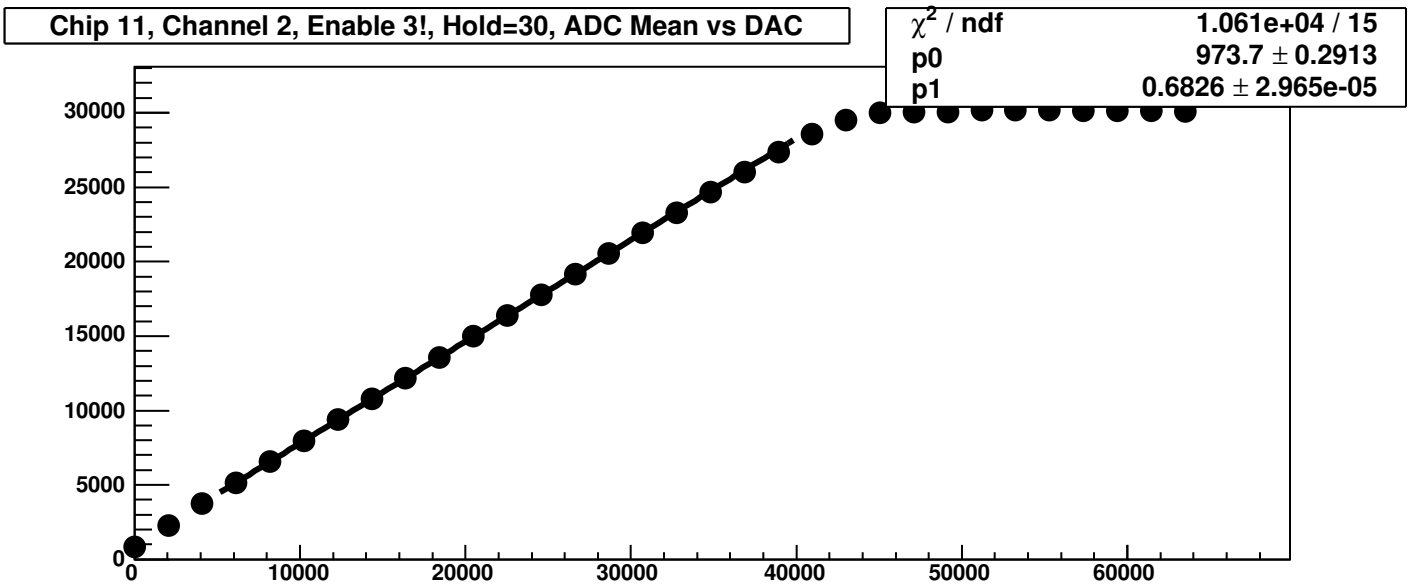

Chip 11, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

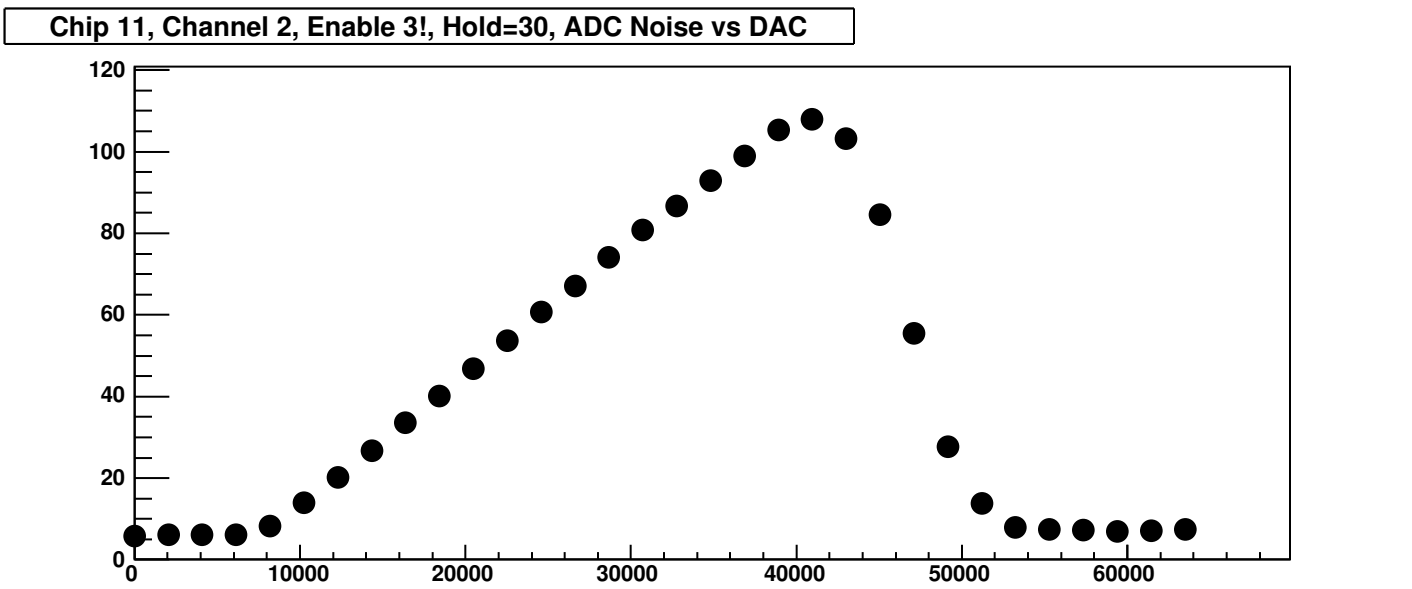

Chip 11, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

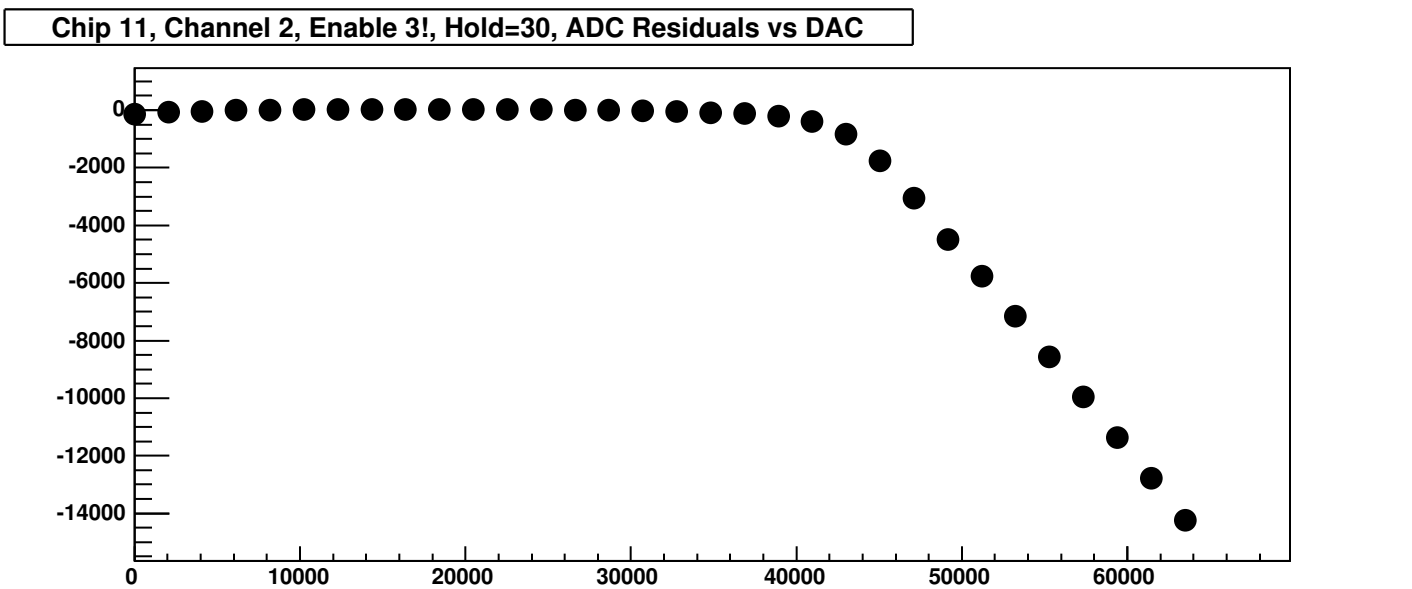

Chip 11, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4288 / 15
p0 857.6 ± 0.11
p1 0.001035 ± 4.472e-06
900
880
860
840
820
0
10000
20000
30000
40000
50000
60000

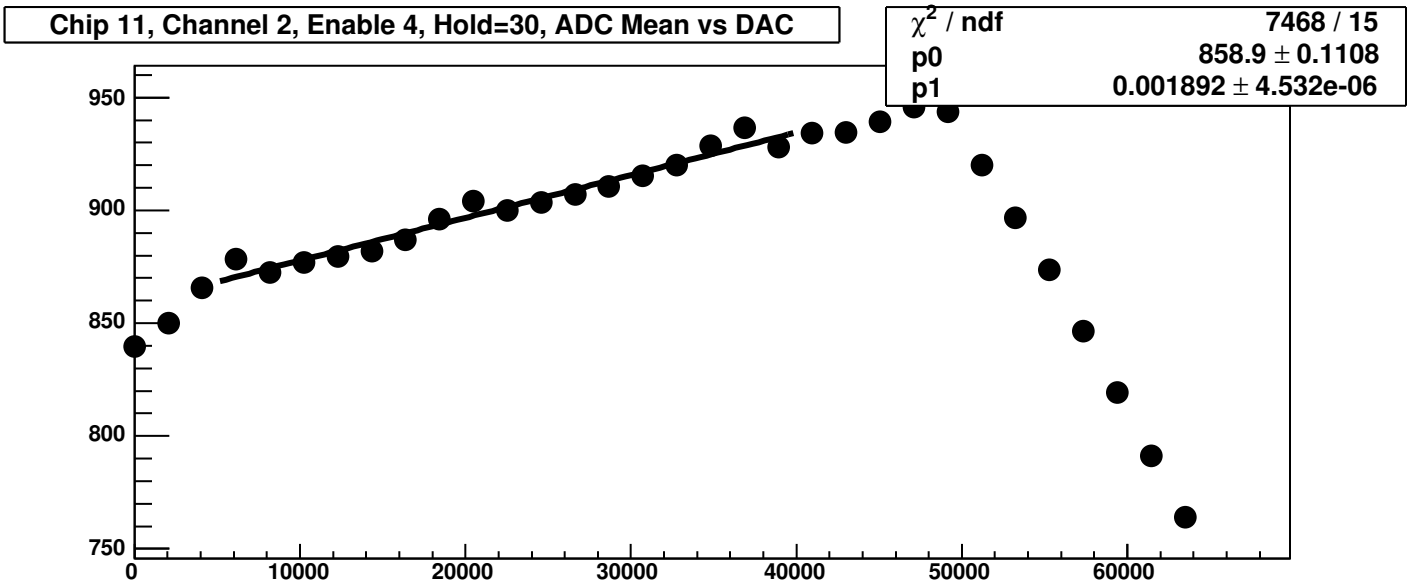

Chip 11, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

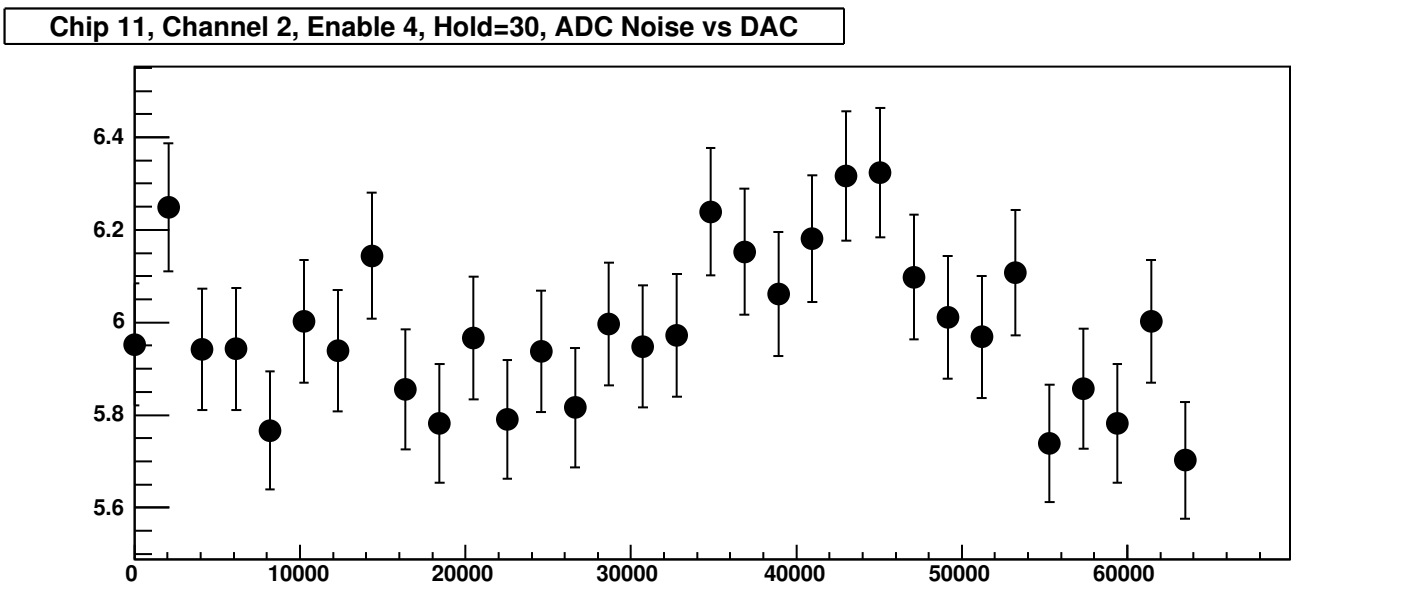

Chip 11, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

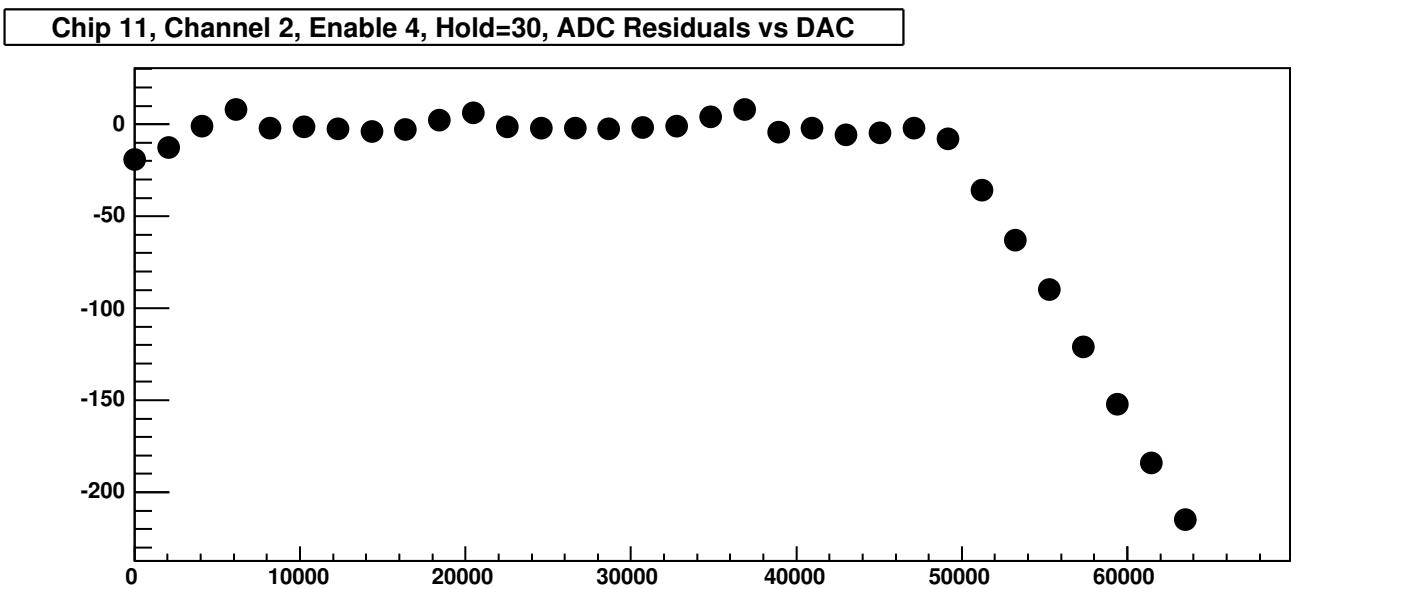

Chip 11, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.061e+04 / 15
p0 973.7 ± 0.2913
p1 0.6826 ± 2.965e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

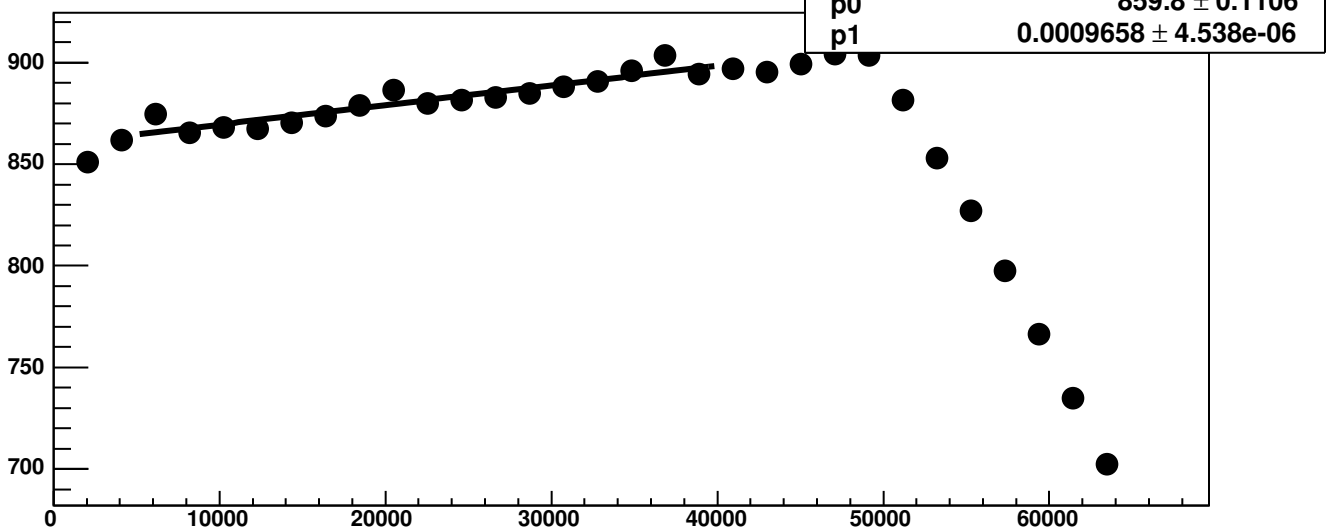

Chip 11, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7468 / 15
p0 858.9 ± 0.1108
p1 0.001892 ± 4.532e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

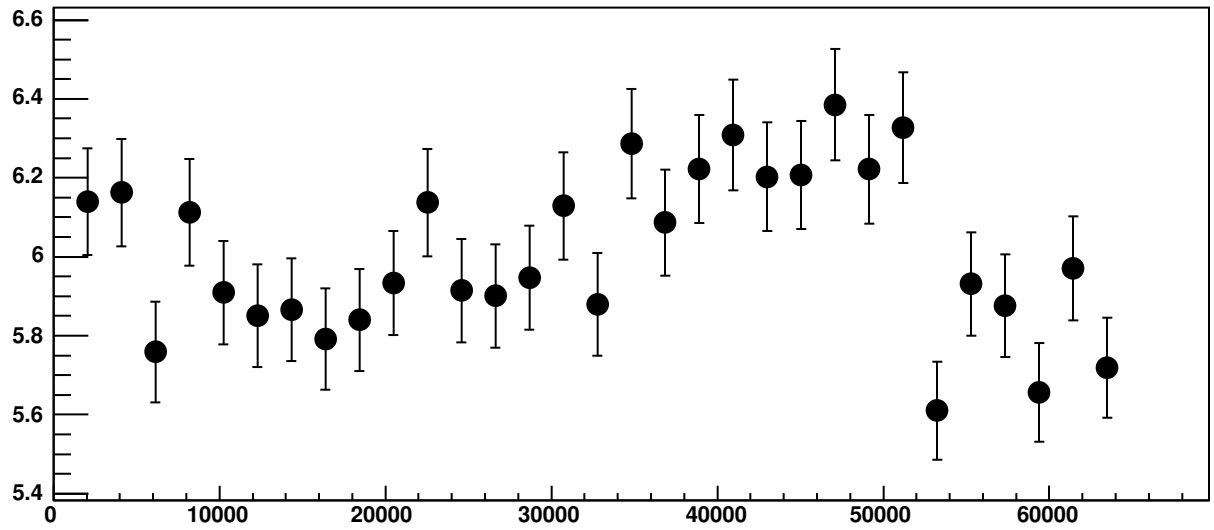

Chip 11, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

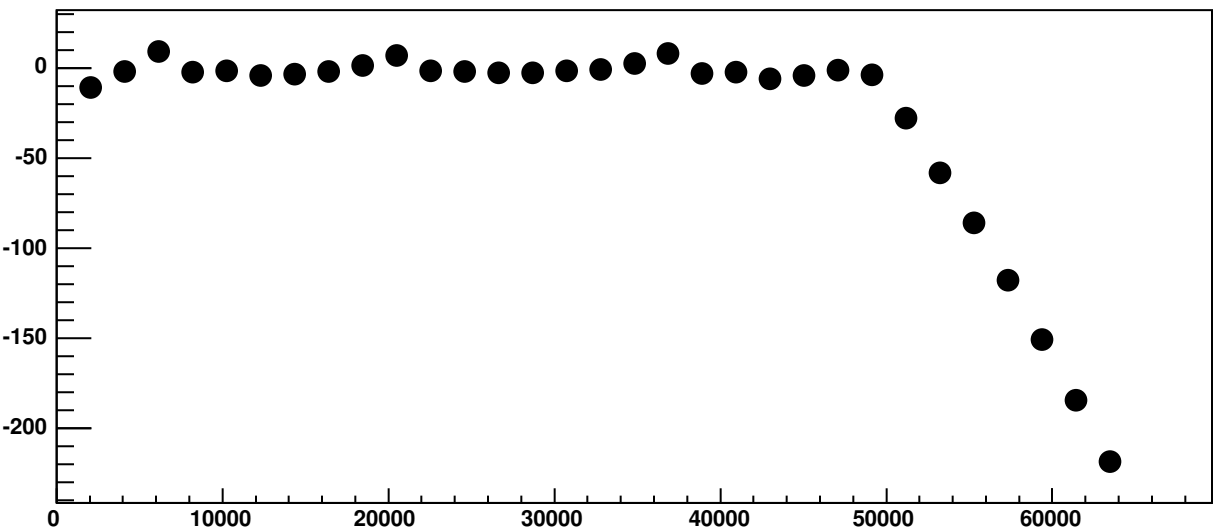
Chip 11, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC

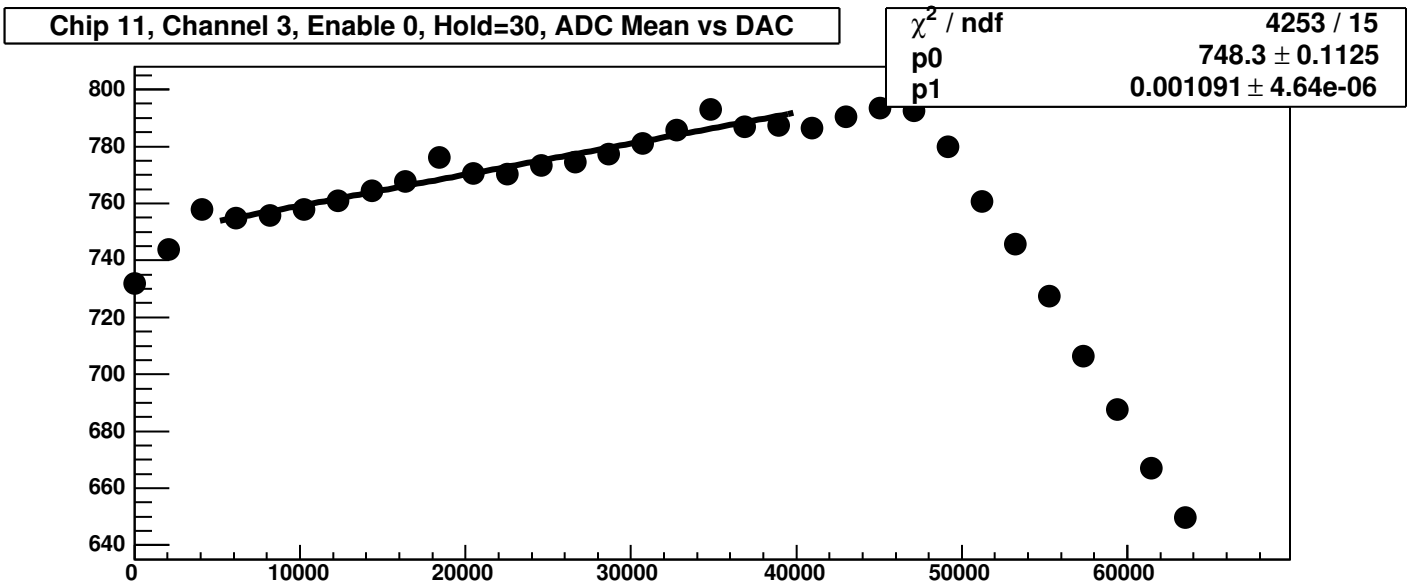


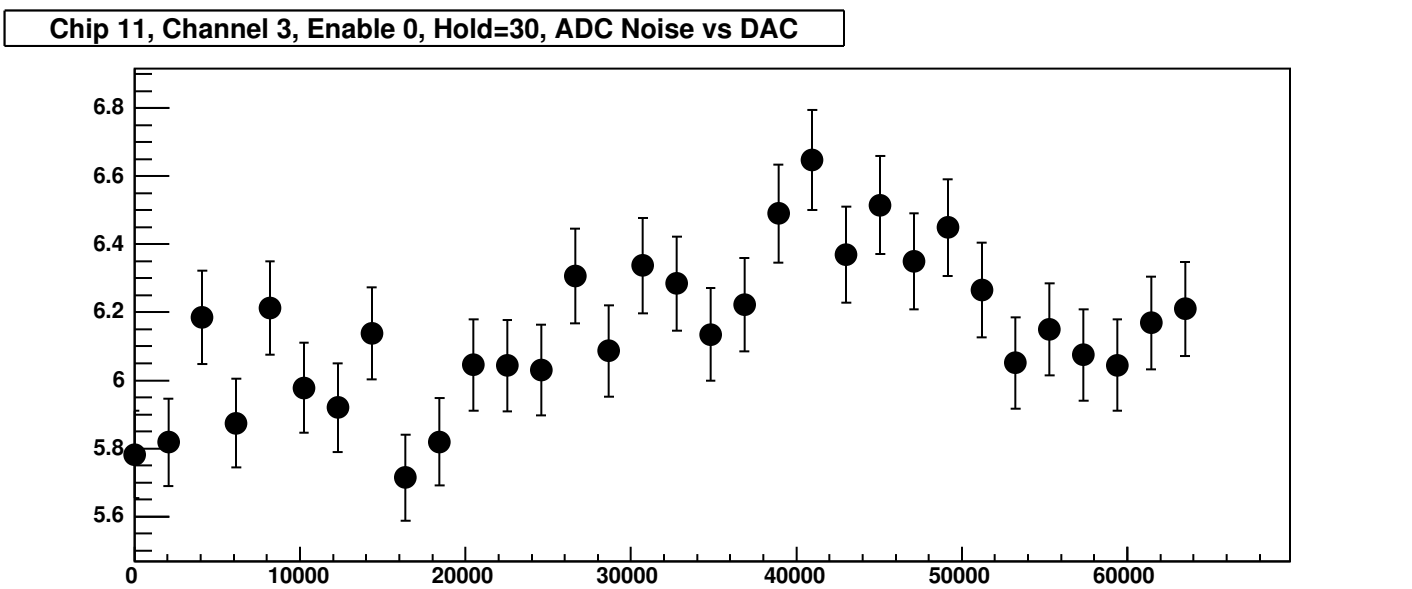
Chip 11, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

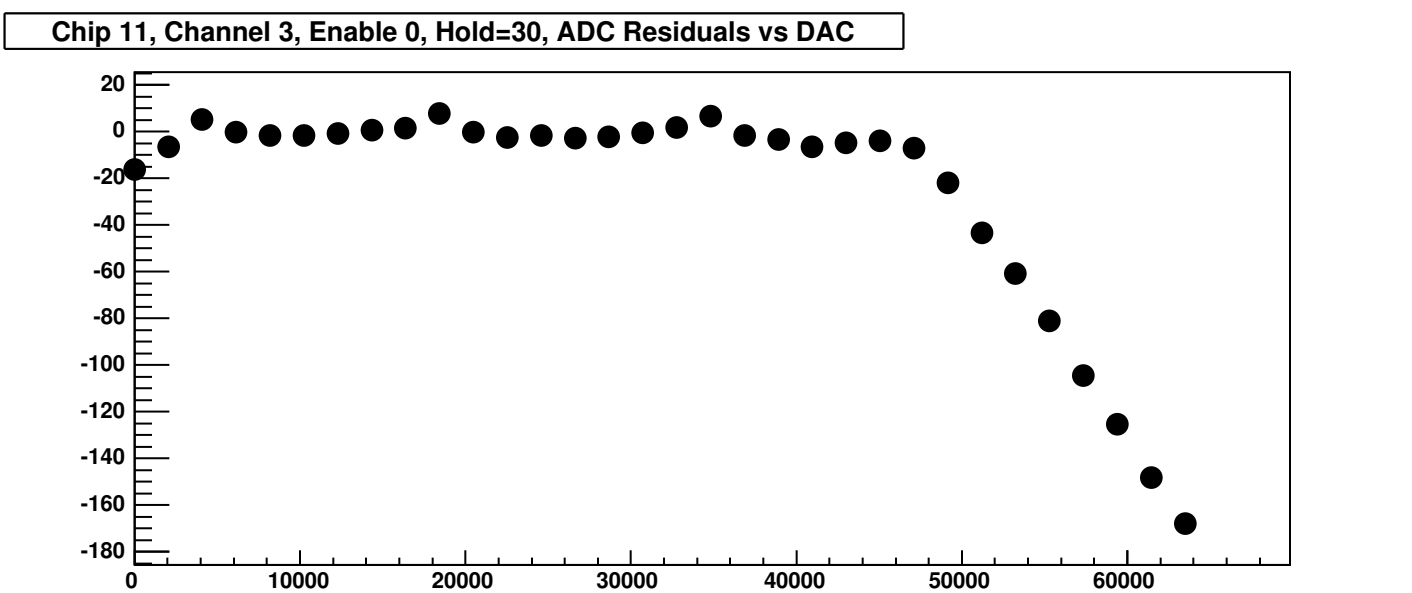


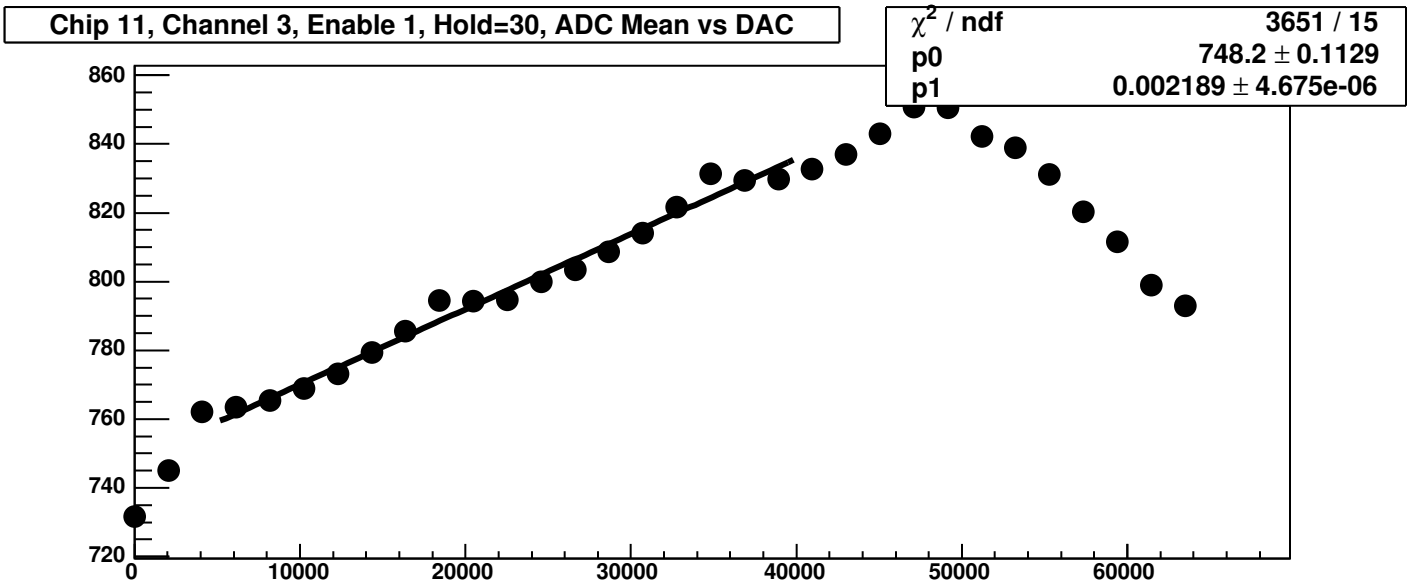
Chip 11, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC

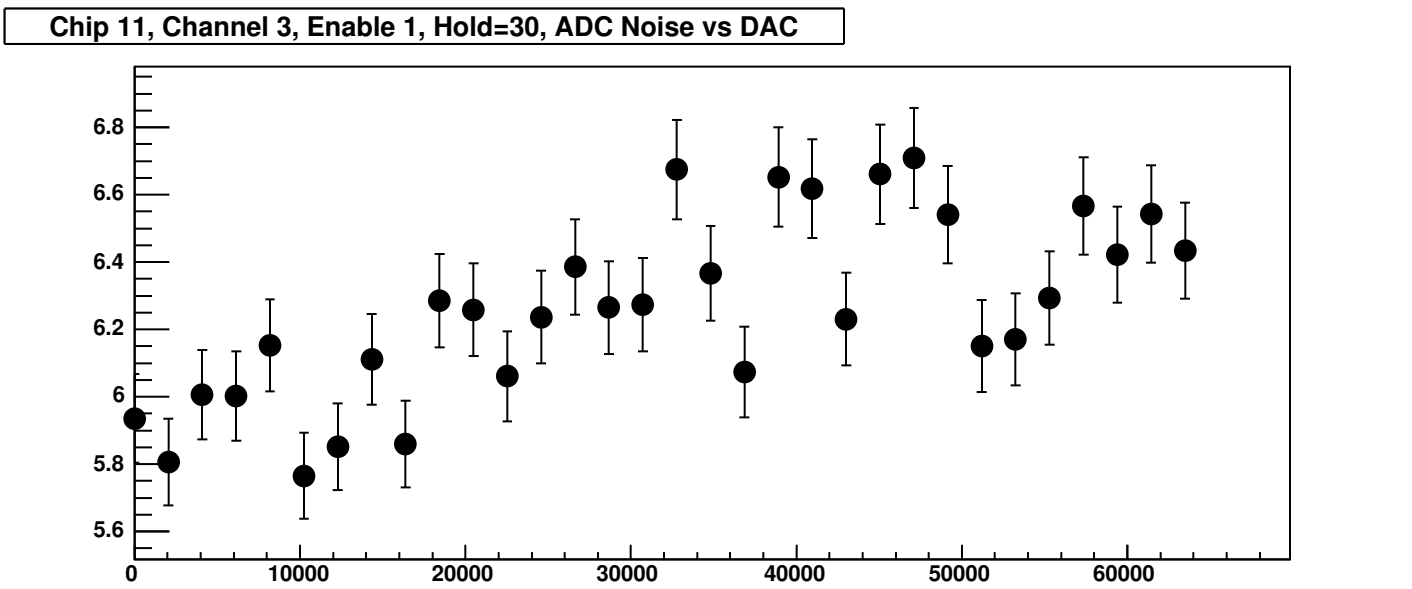


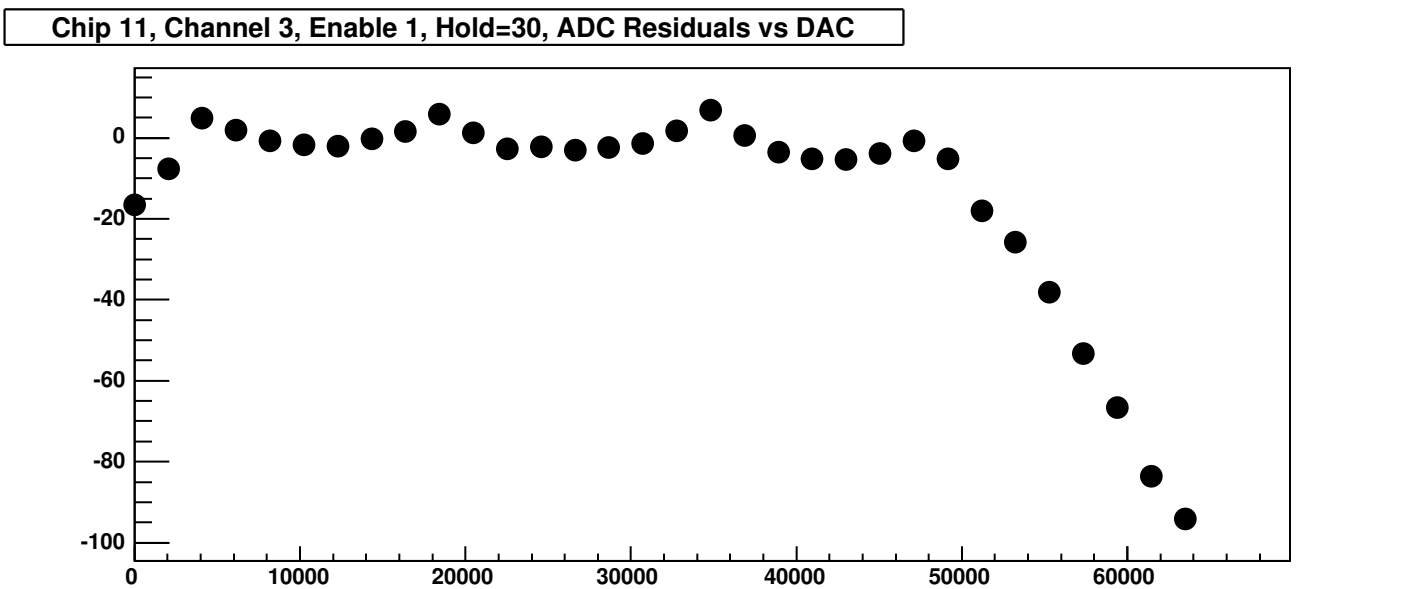

Chip 11, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4253 / 15
p0 748.3 ± 0.1125
p1 0.001091 ± 4.64e-06
800
780
760
740
720
700
680
660
640
0
10000
20000
30000
40000
50000
60000

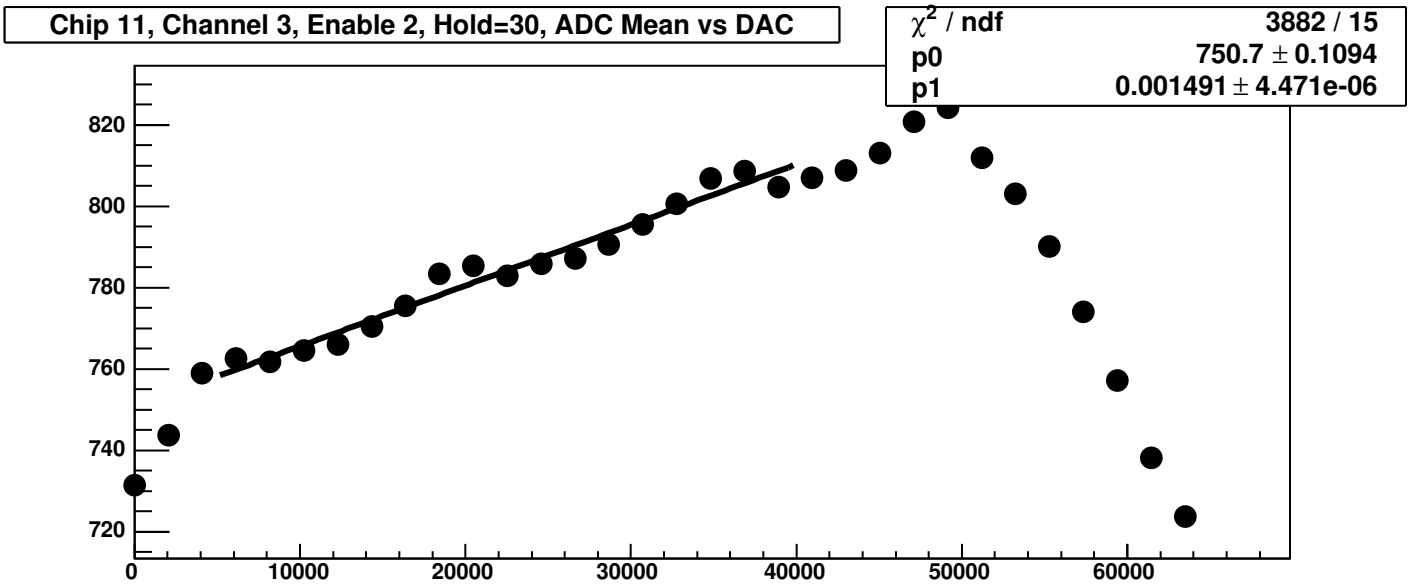

Chip 11, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

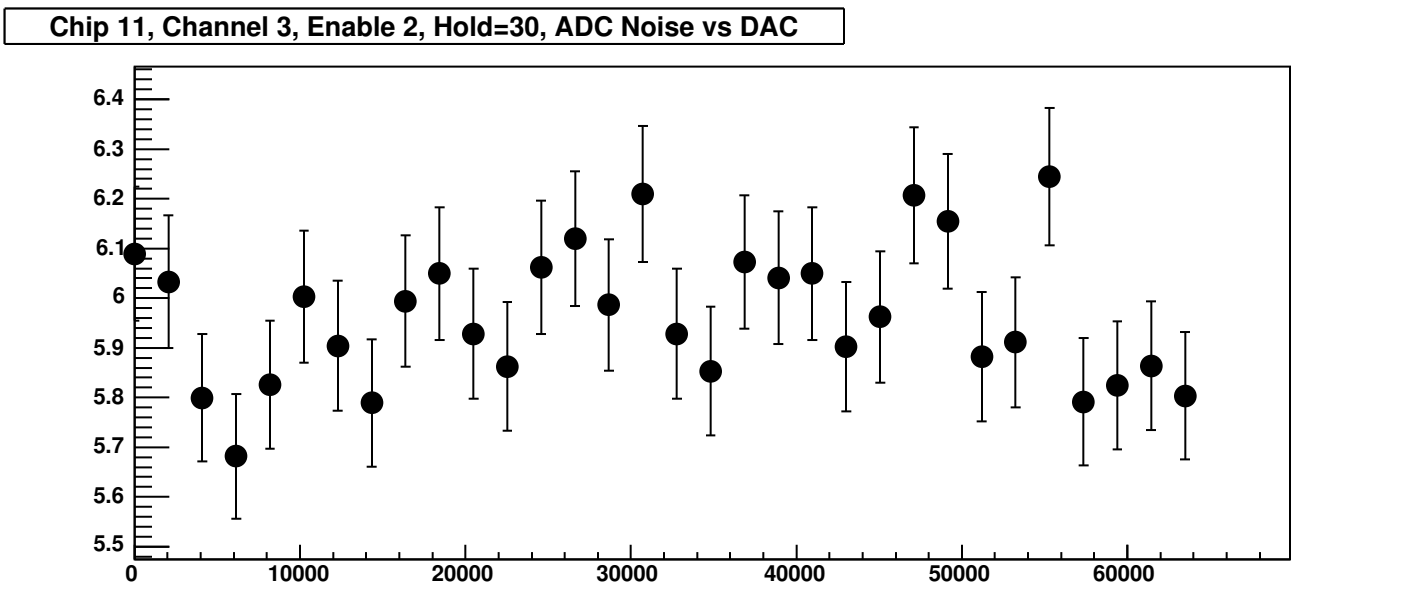

Chip 11, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

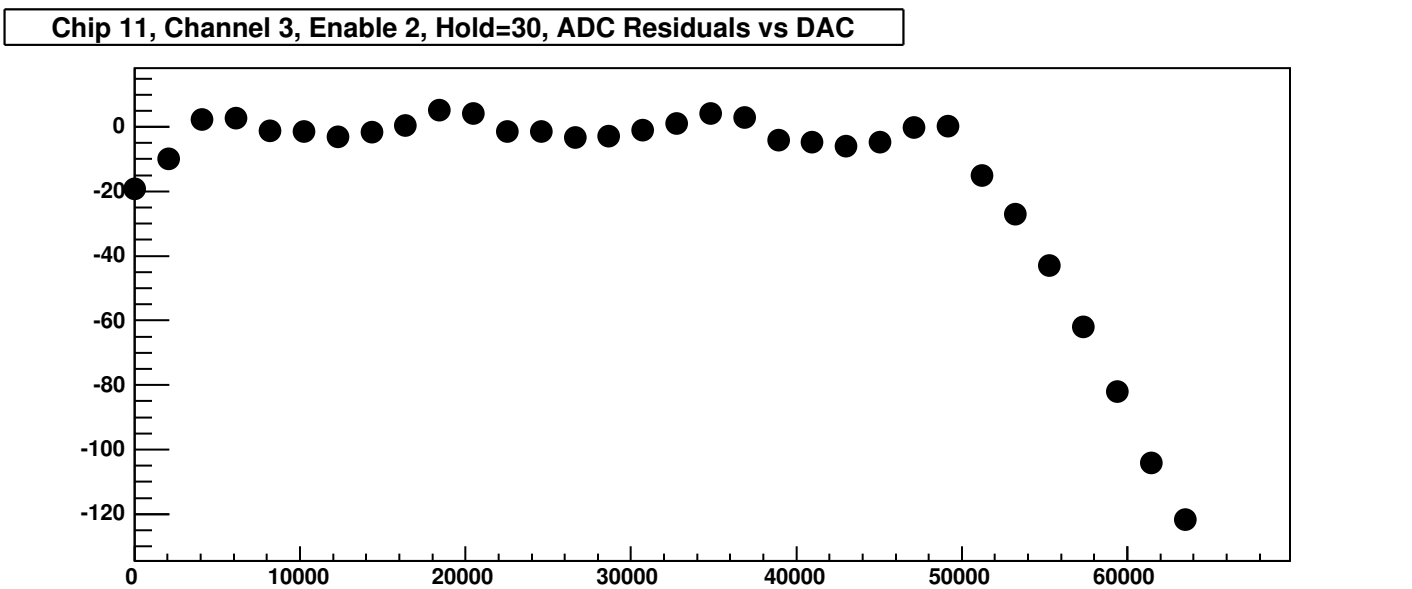

Chip 11, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3651 / 15
p0 748.2 ± 0.1129
p1 0.002189 ± 4.675e-06
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

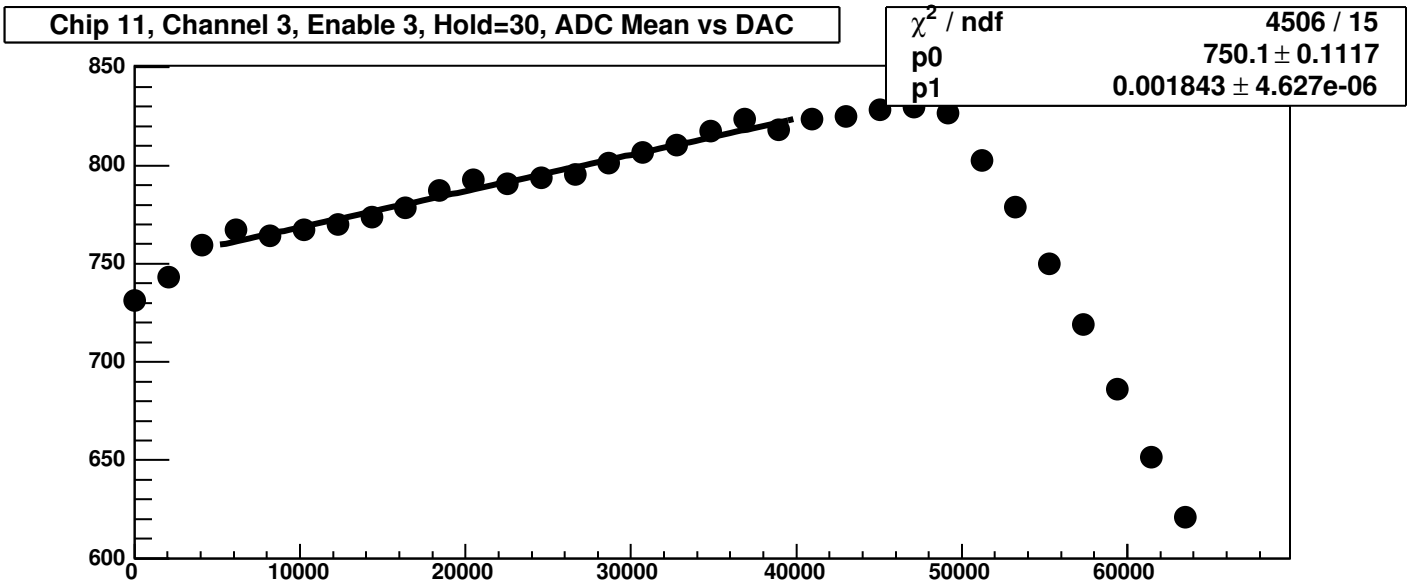

Chip 11, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

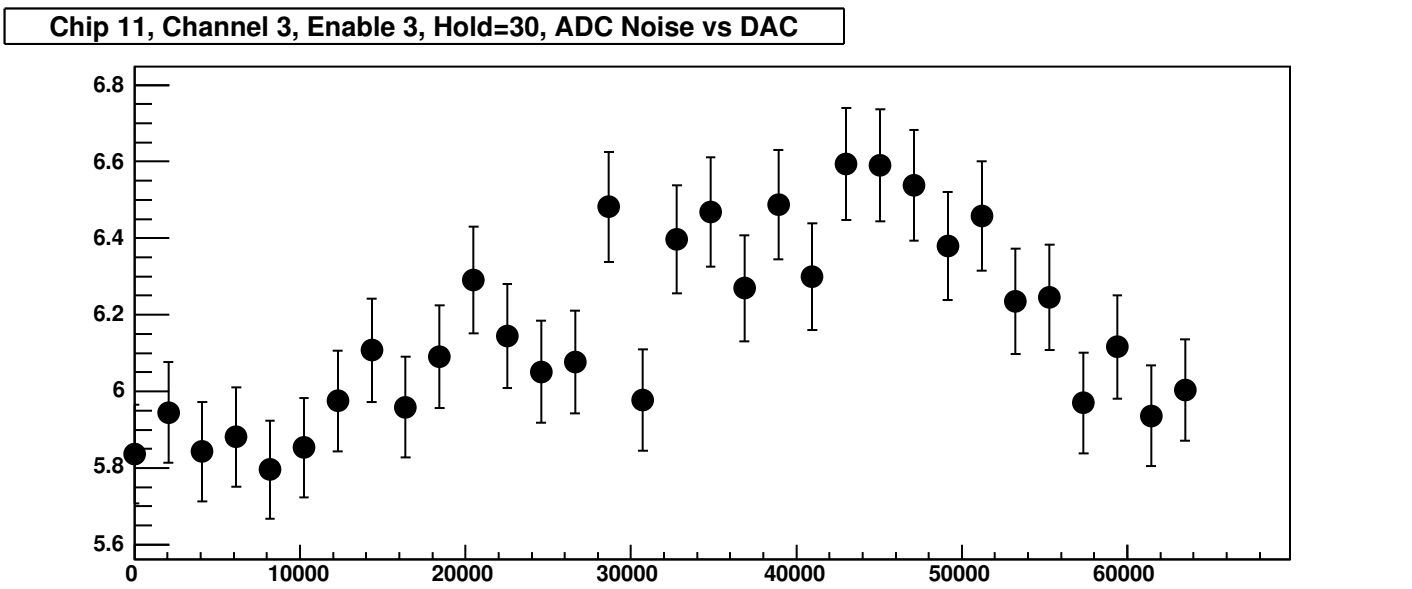

Chip 11, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
0
10000
20000
30000
40000
50000
60000

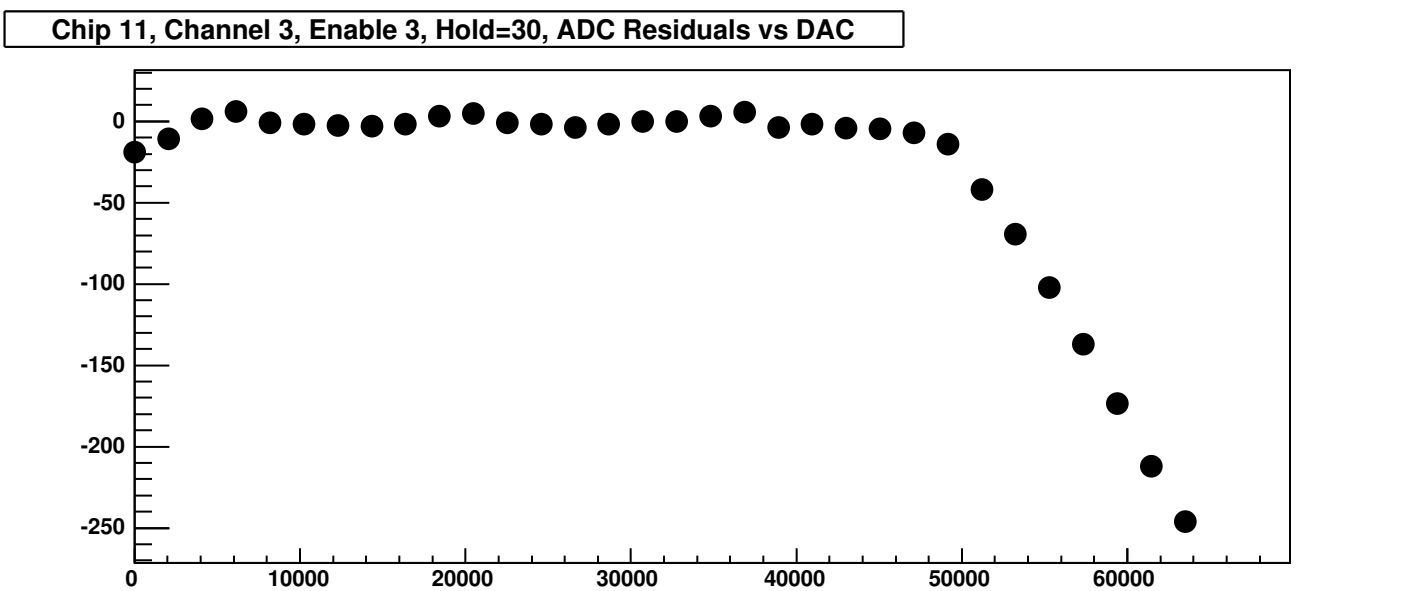

Chip 11, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3882 / 15
p0 750.7 ± 0.1094
p1 0.001491 ± 4.471e-06
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

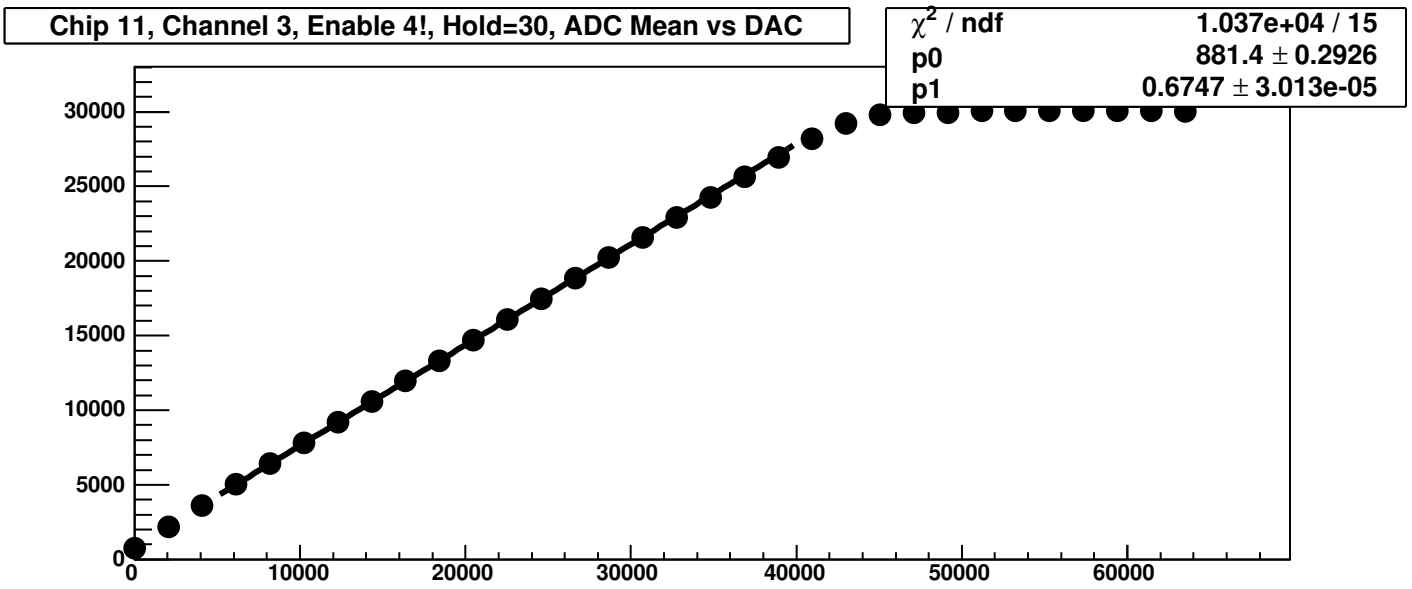

Chip 11, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

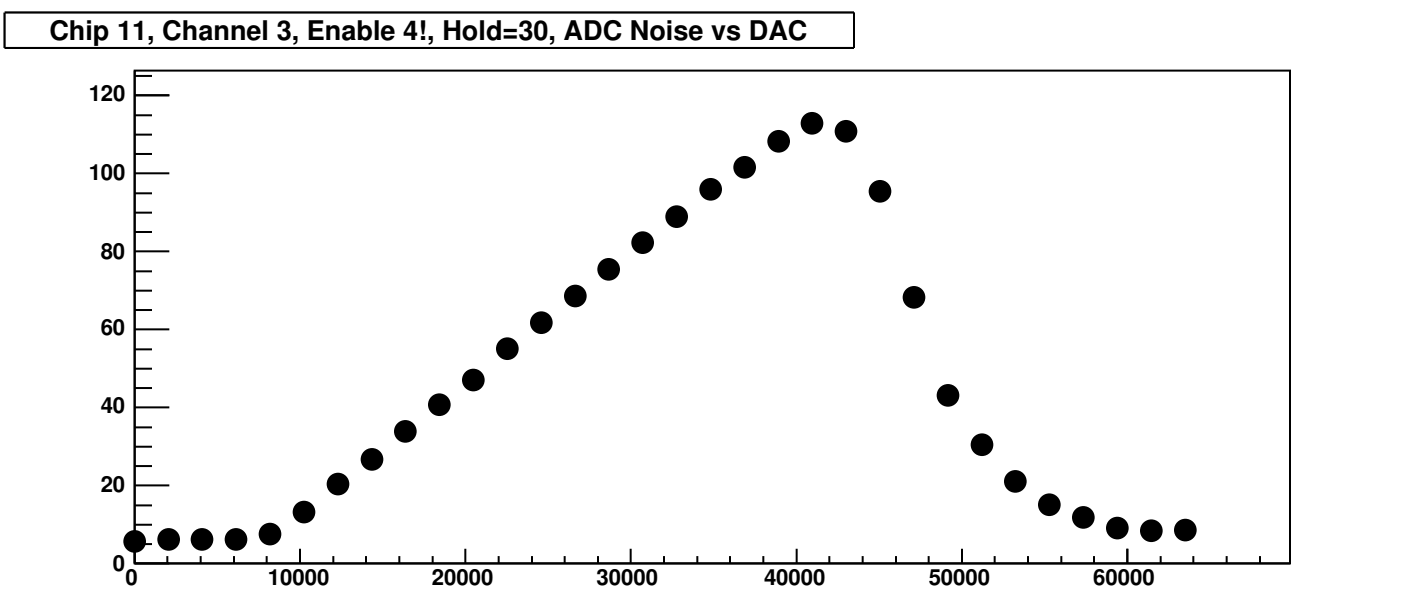

Chip 11, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

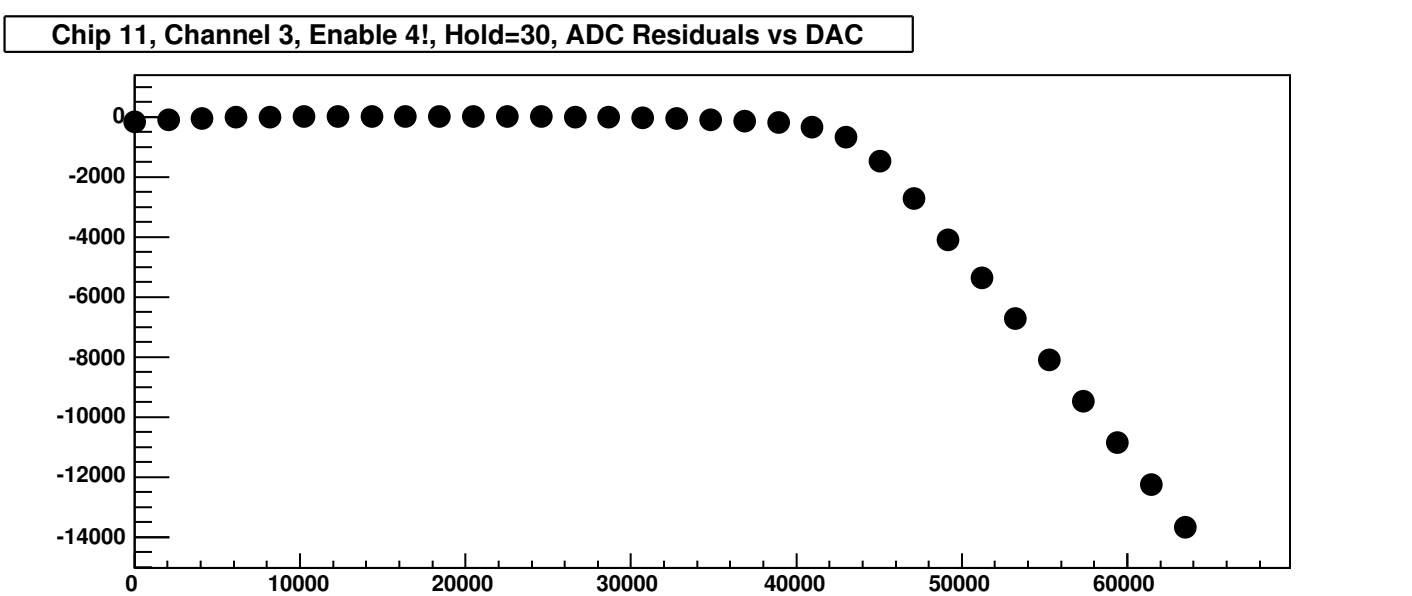

Chip 11, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4506 / 15
p0 750.1 ± 0.1117
p1 0.001843 ± 4.627e-06
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

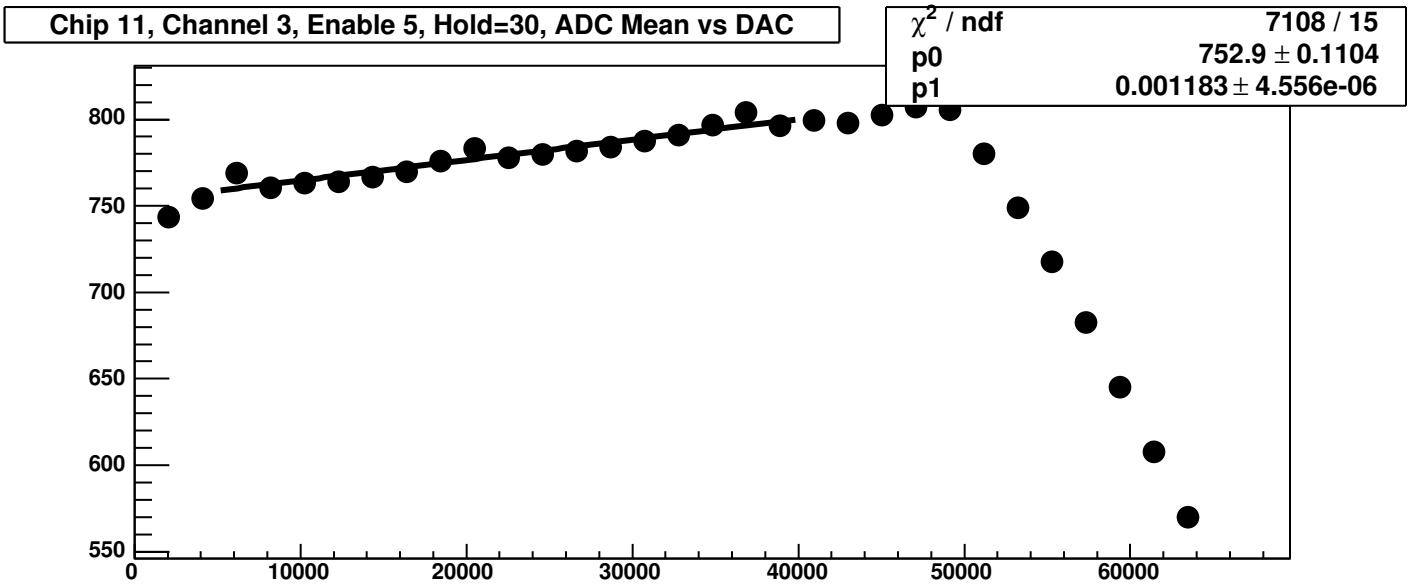

Chip 11, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

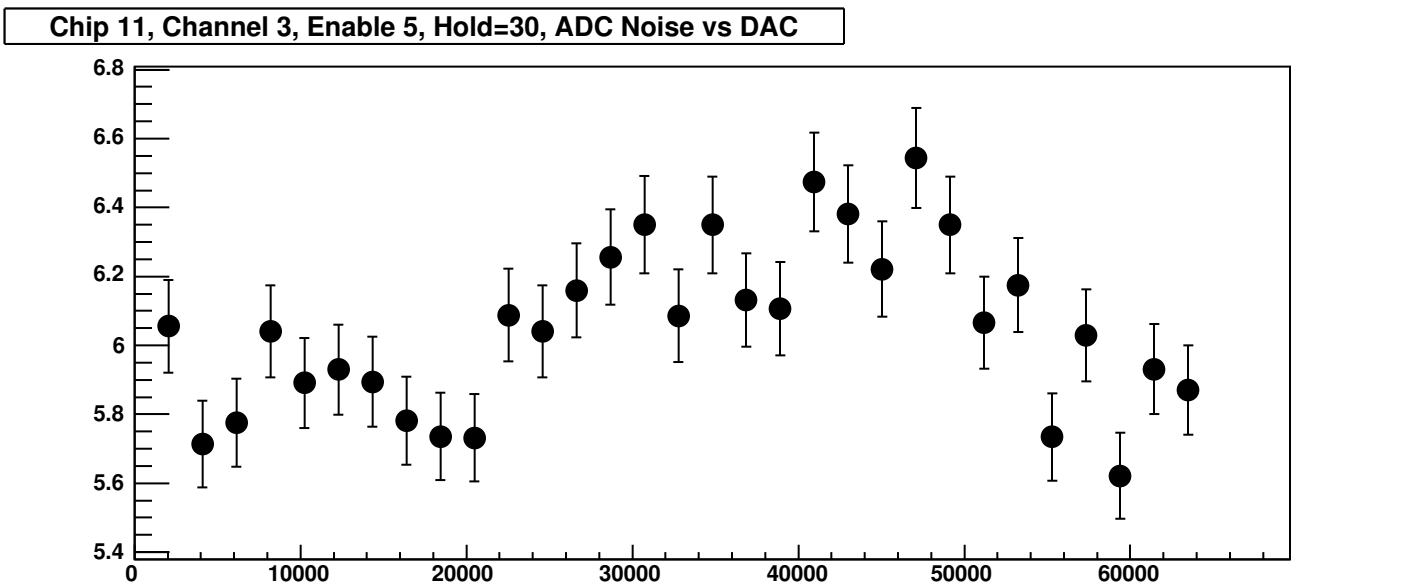

Chip 11, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

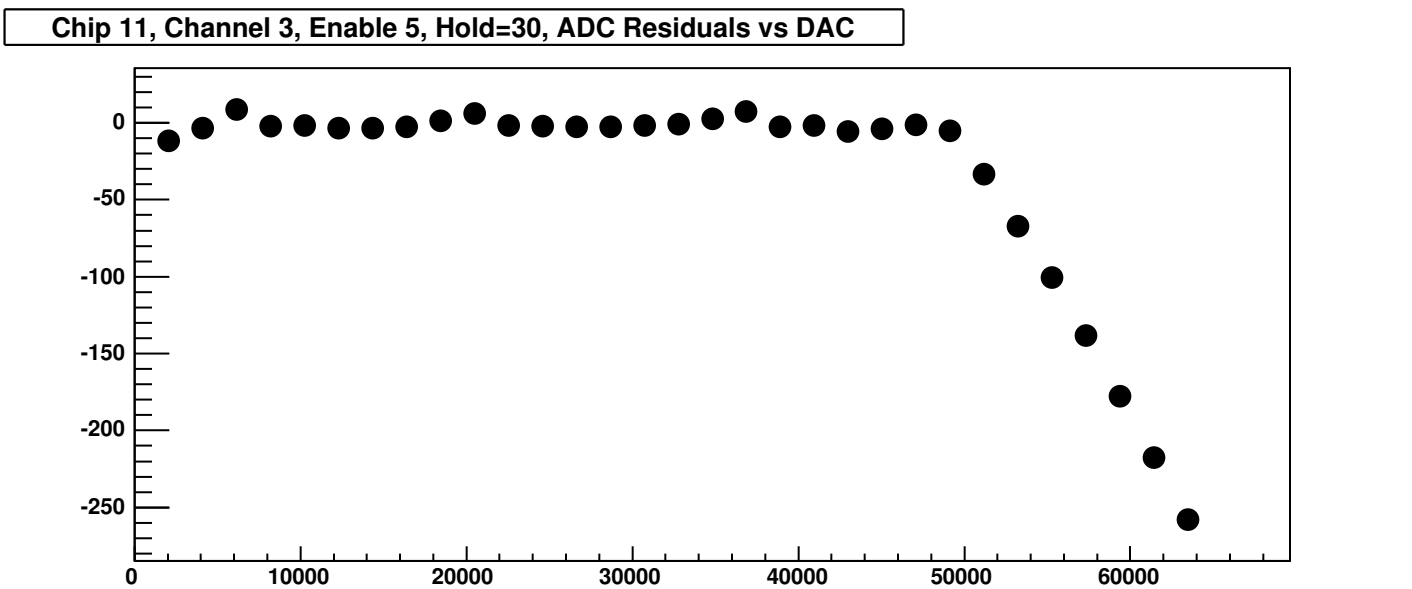

Chip 11, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.037e+04 / 15
p0 881.4 ± 0.2926
p1 0.6747 ± 3.013e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

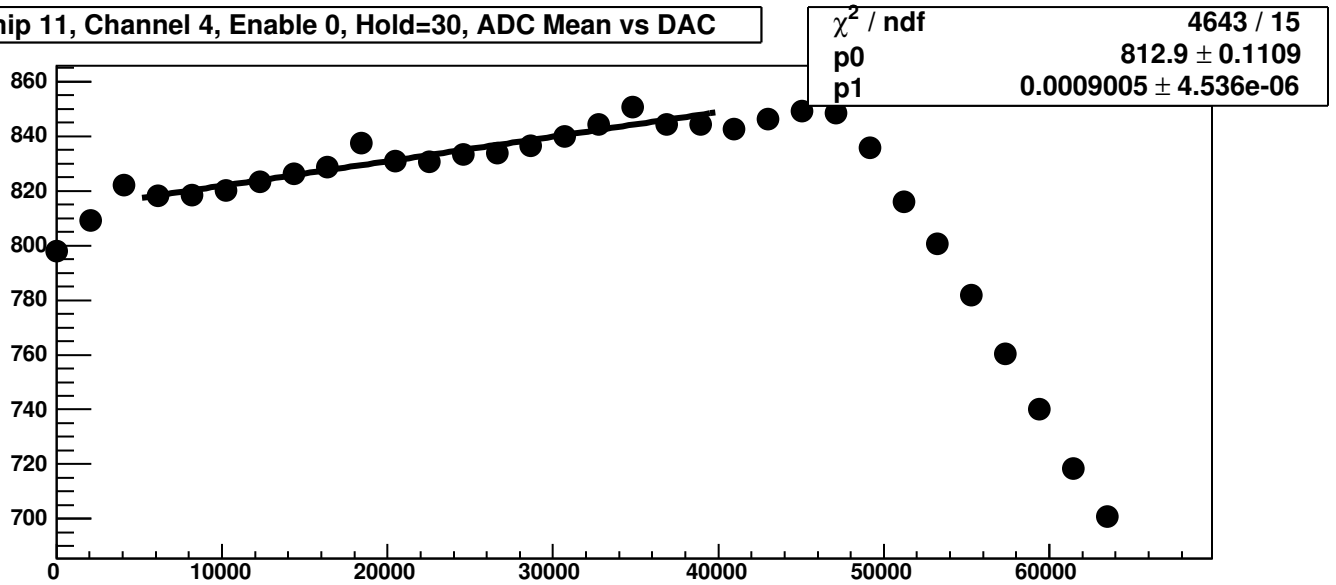

Chip 11, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7108 / 15
p0 752.9 ± 0.1104
p1 0.001183 ± 4.556e-06
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

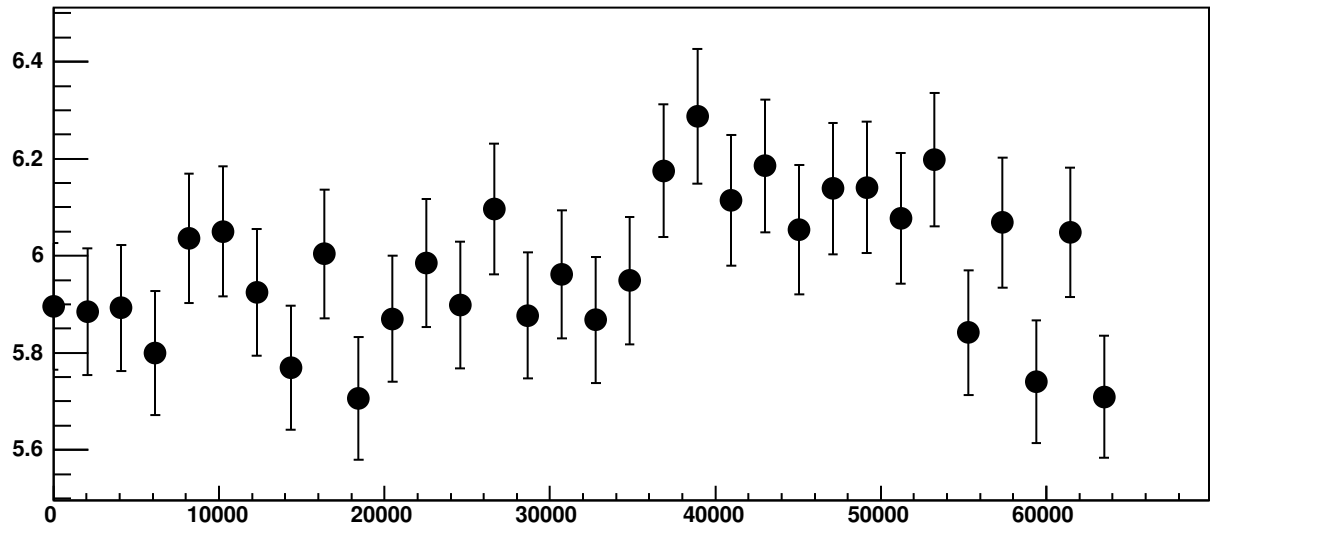

Chip 11, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

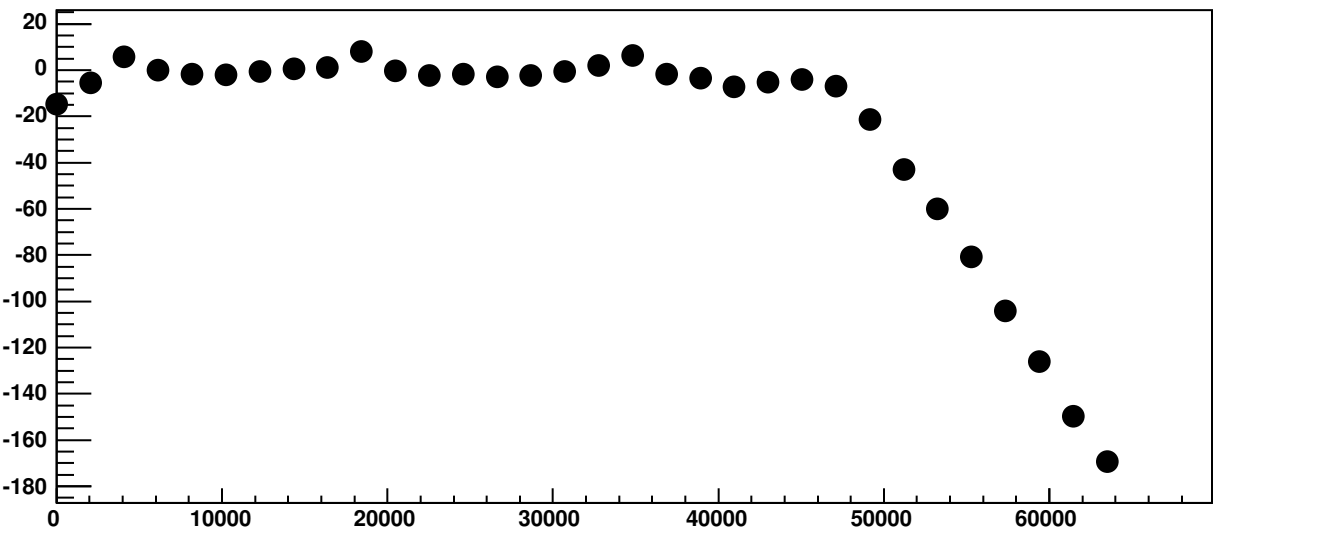
Chip 11, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

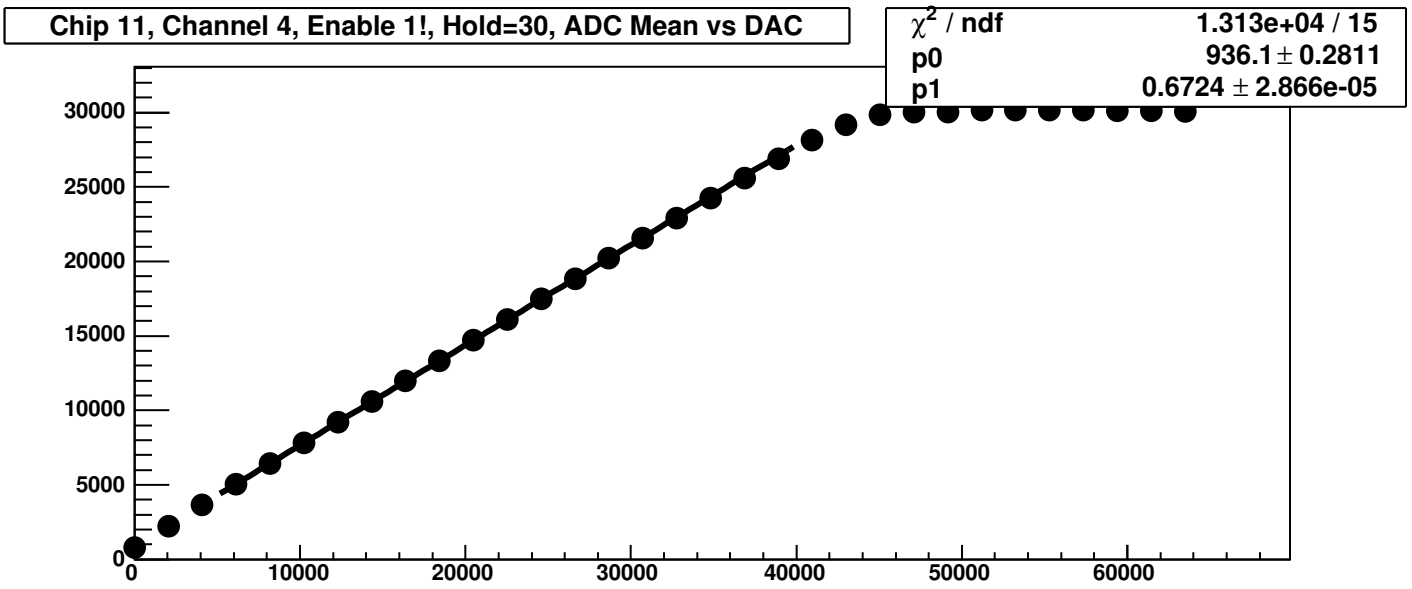


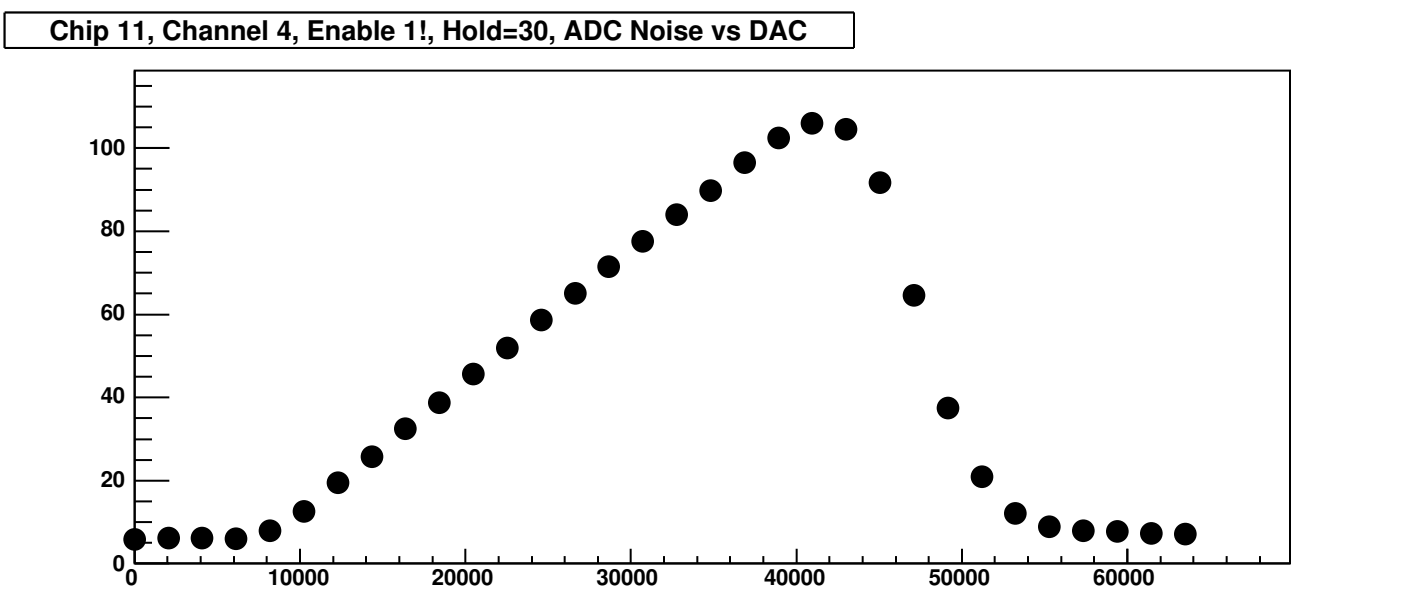
Chip 11, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC

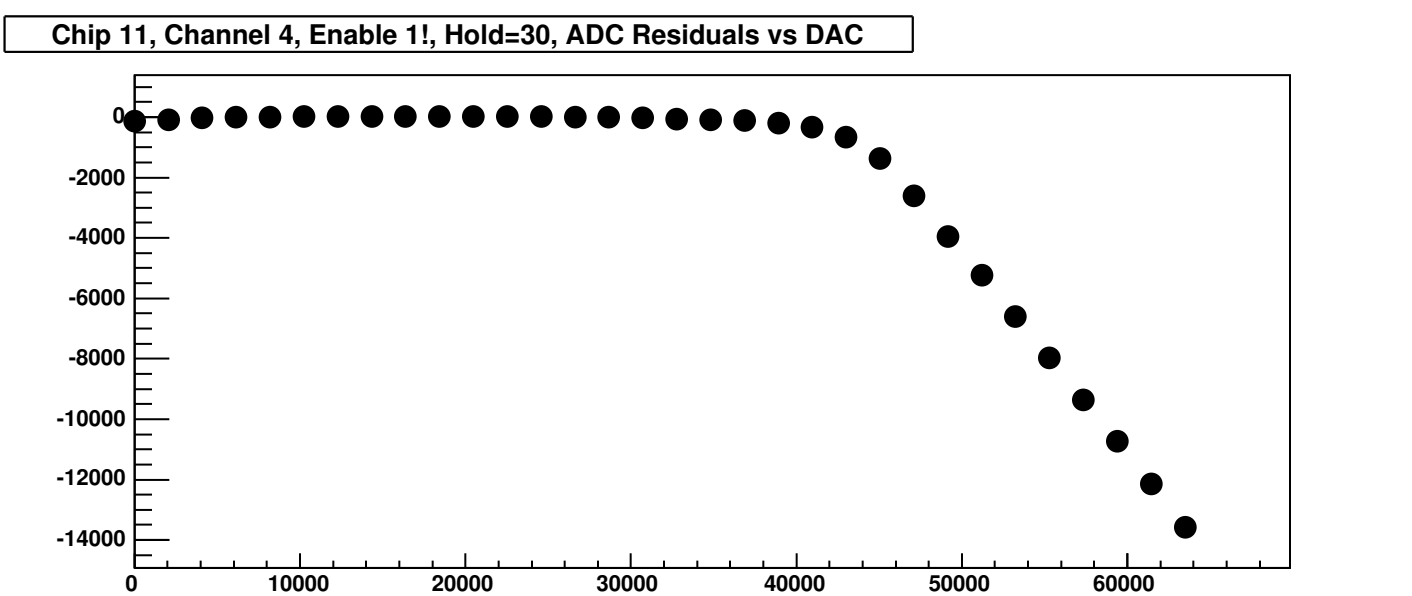


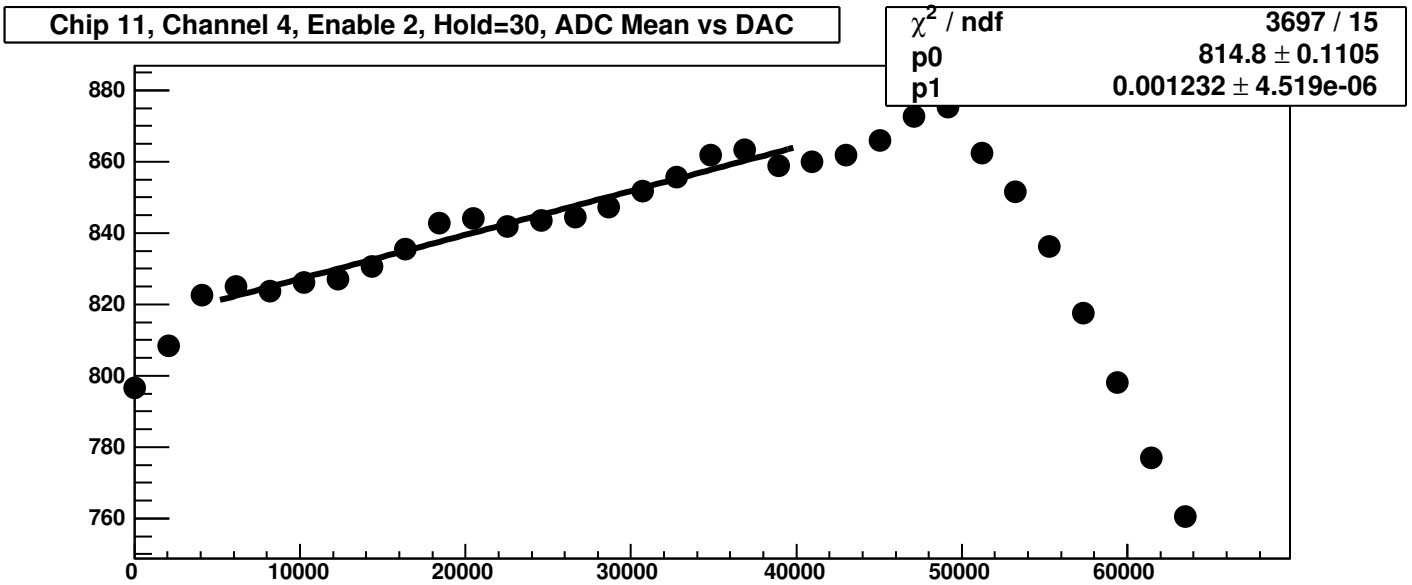
Chip 11, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

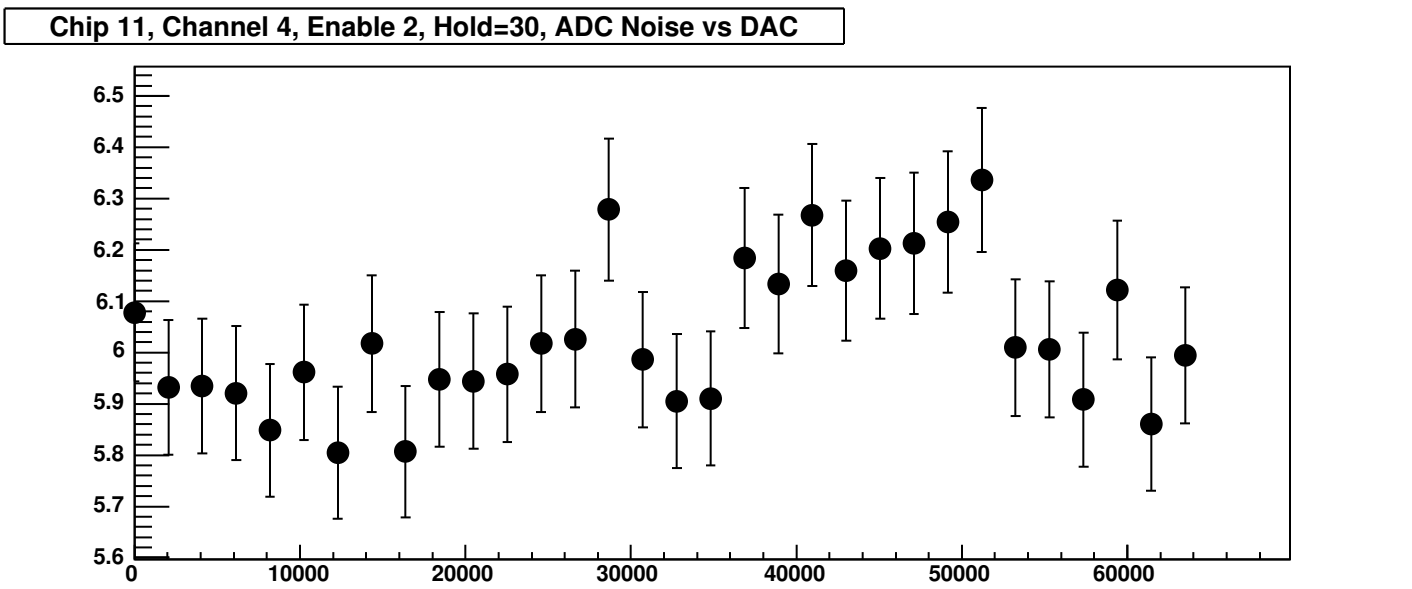


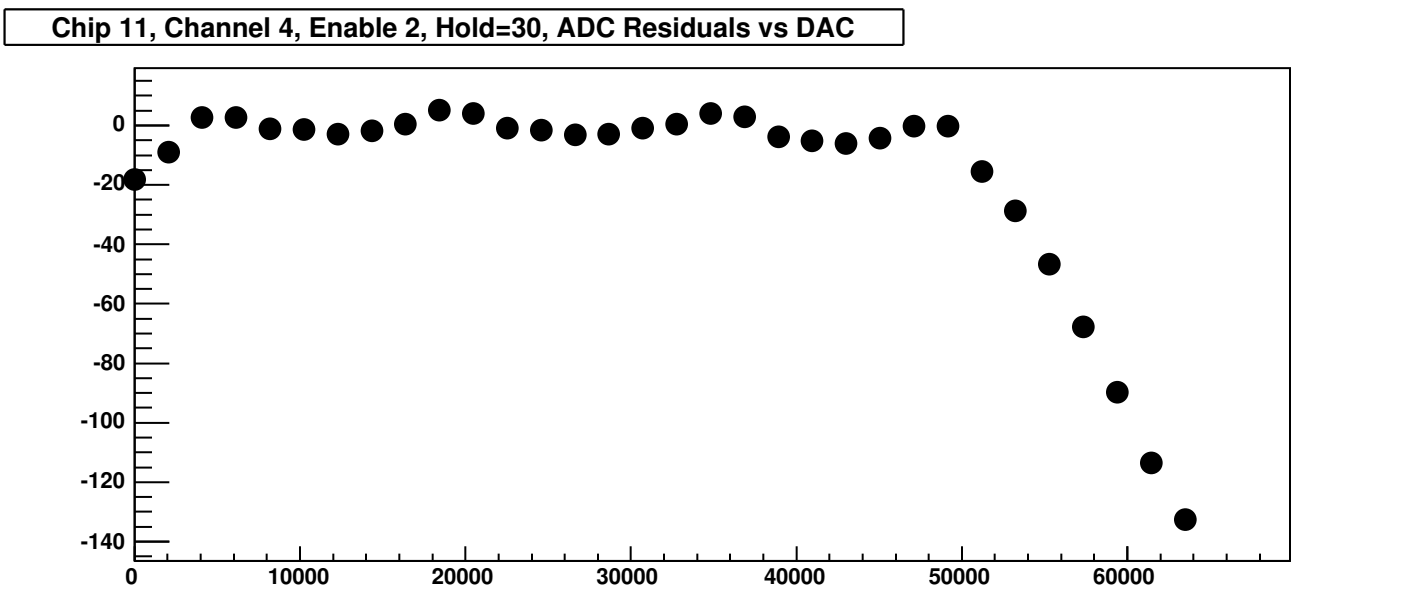

Chip 11, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.313e+04 / 15
p0 936.1 ± 0.2811
p1 0.6724 ± 2.866e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

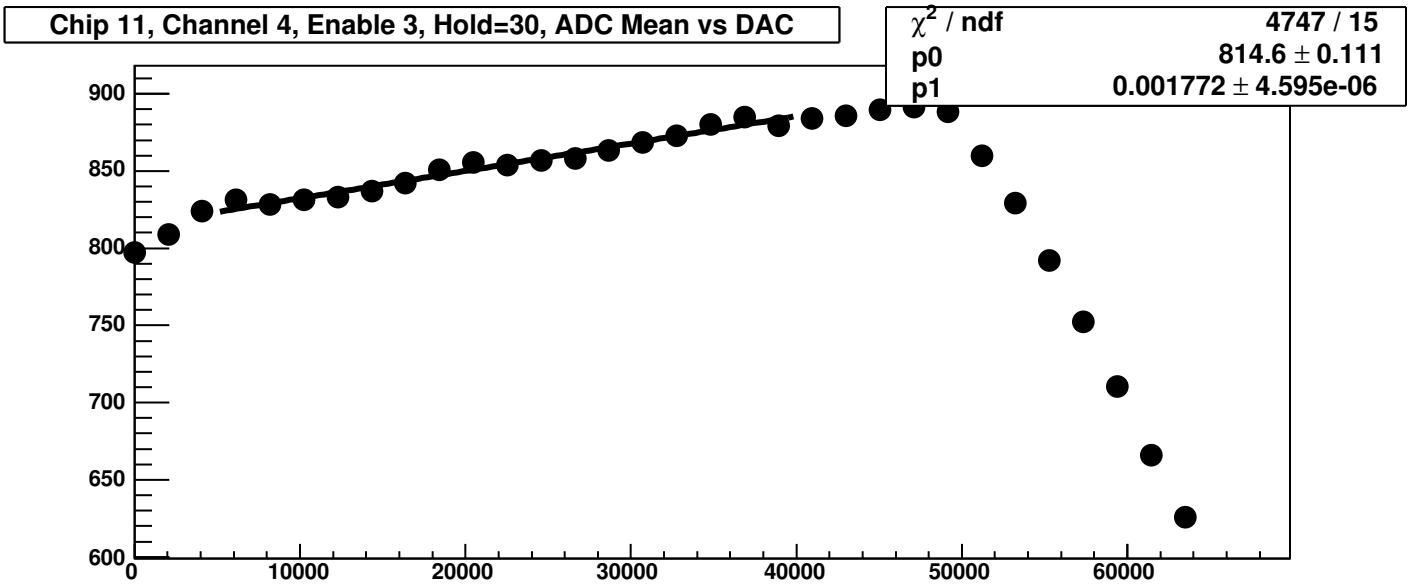

Chip 11, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

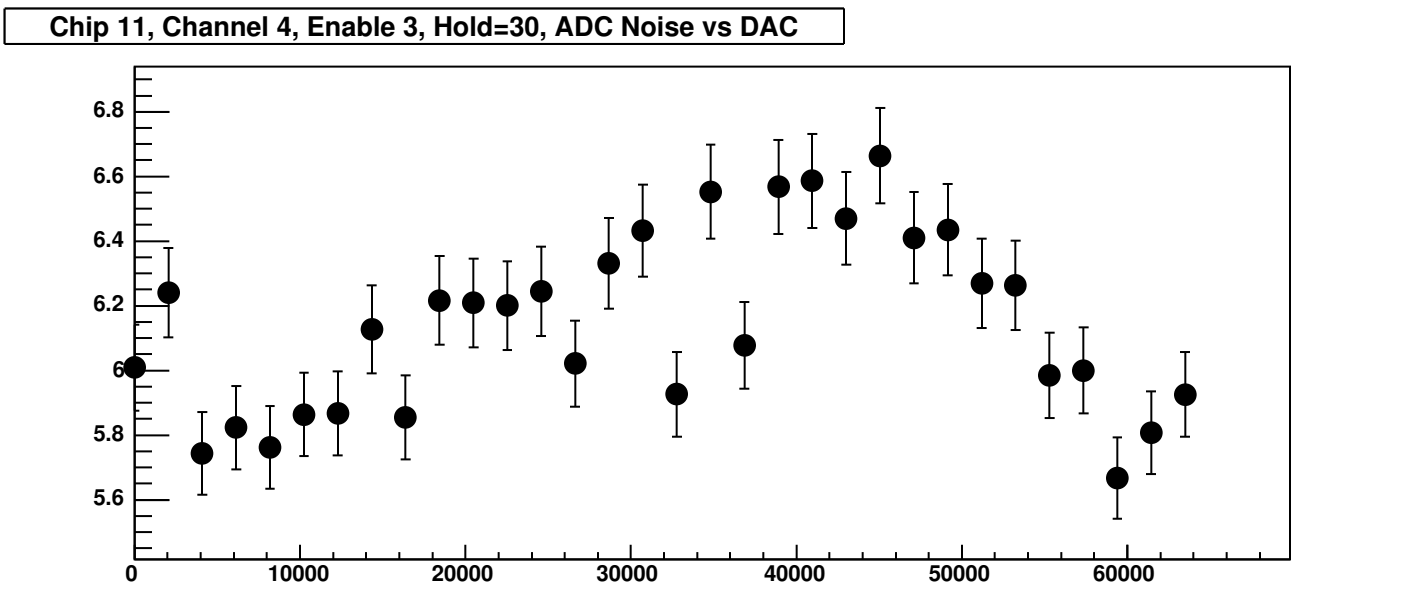

Chip 11, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

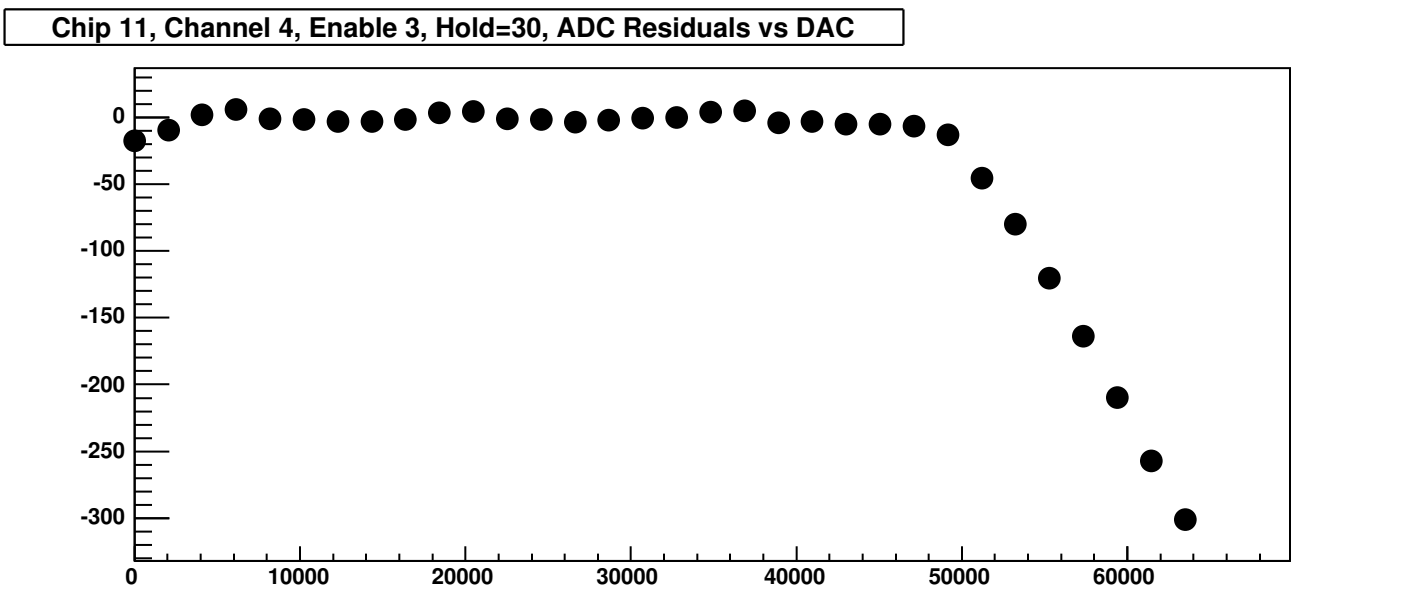

Chip 11, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3697 / 15
p0 814.8 ± 0.1105
p1 0.001232 ± 4.519e-06
880
860
840
820
800
780
760
0
10000
20000
30000
40000
50000
60000

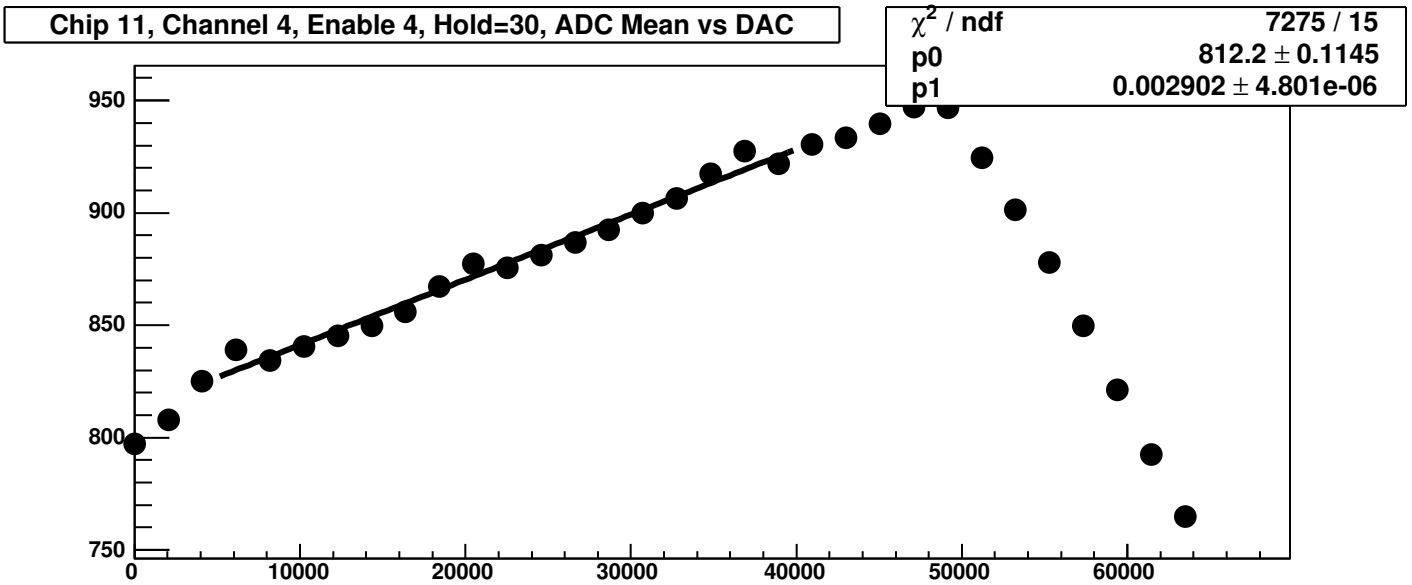

Chip 11, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
0
10000
20000
30000
40000
50000
60000

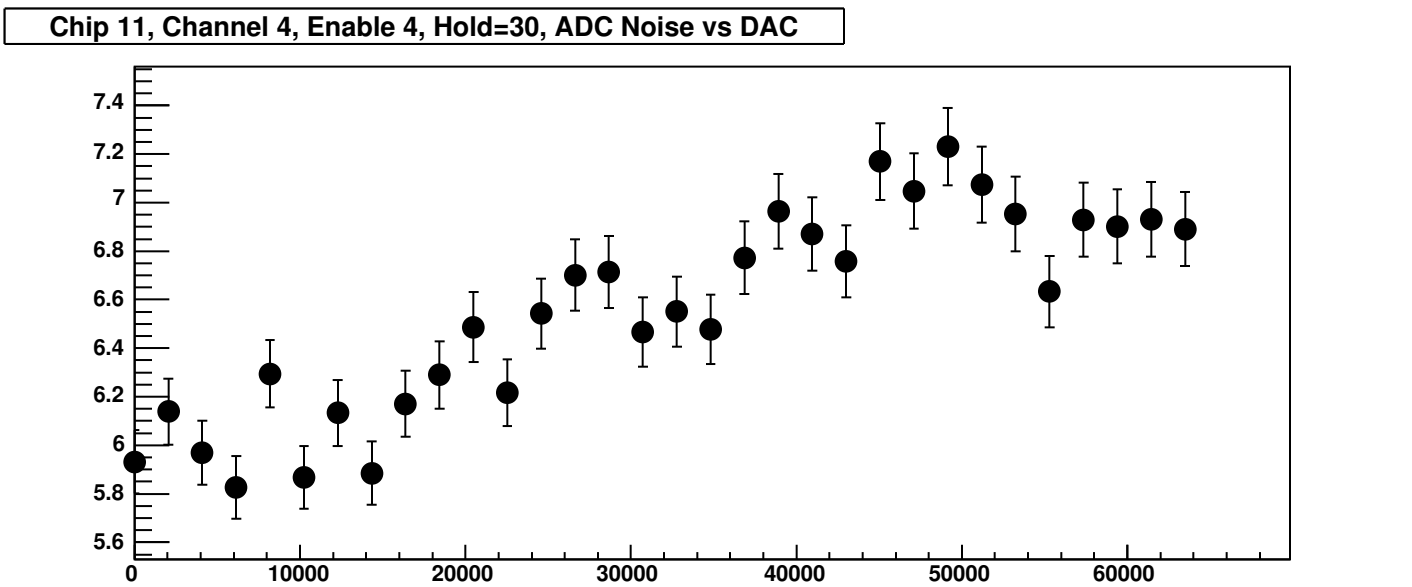

Chip 11, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

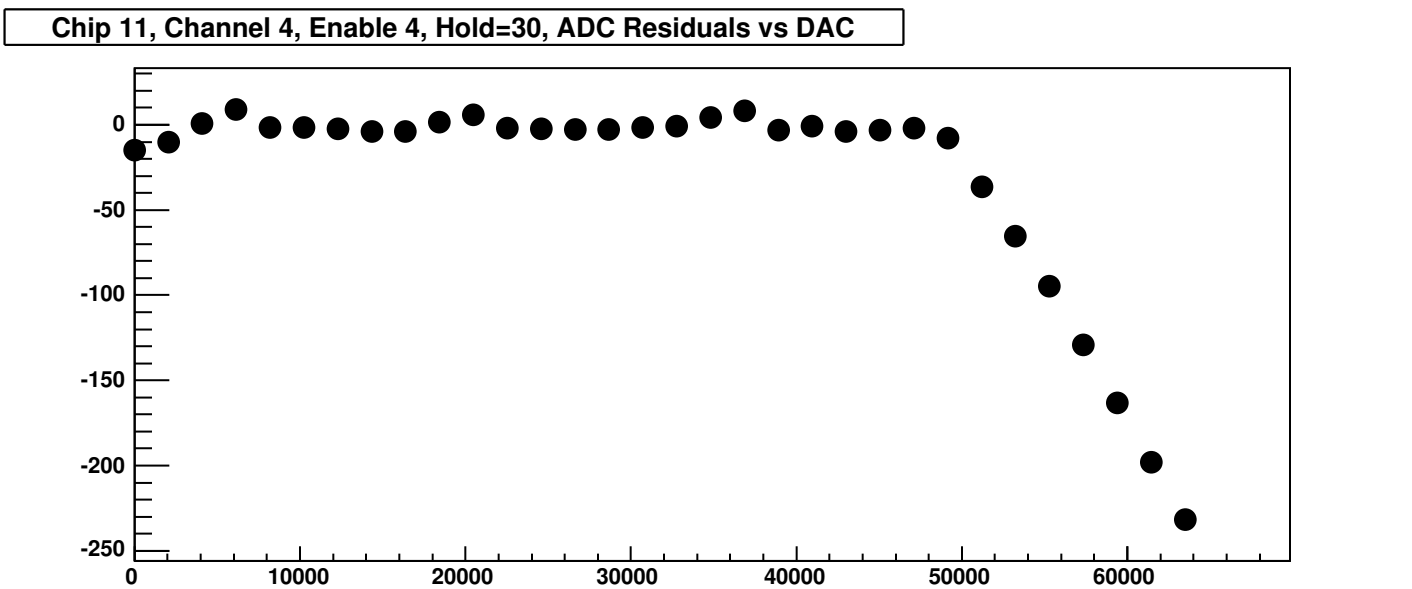

Chip 11, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4747 / 15
p0 814.6 ± 0.111
p1 0.001772 ± 4.595e-06
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

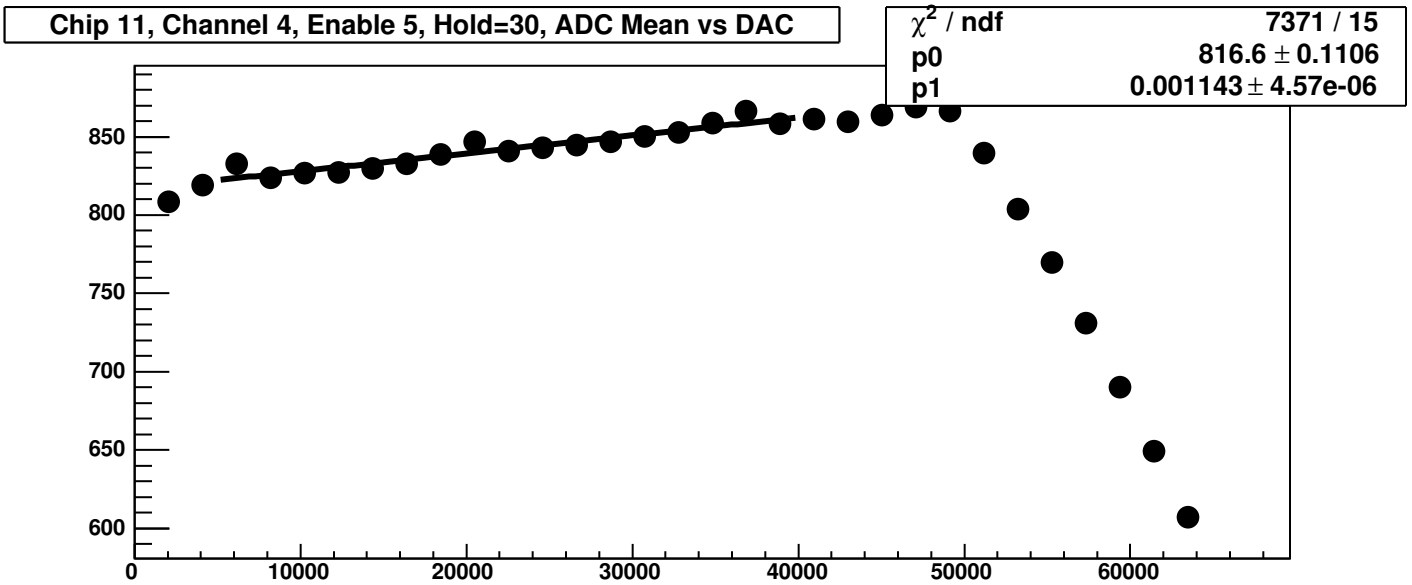

Chip 11, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

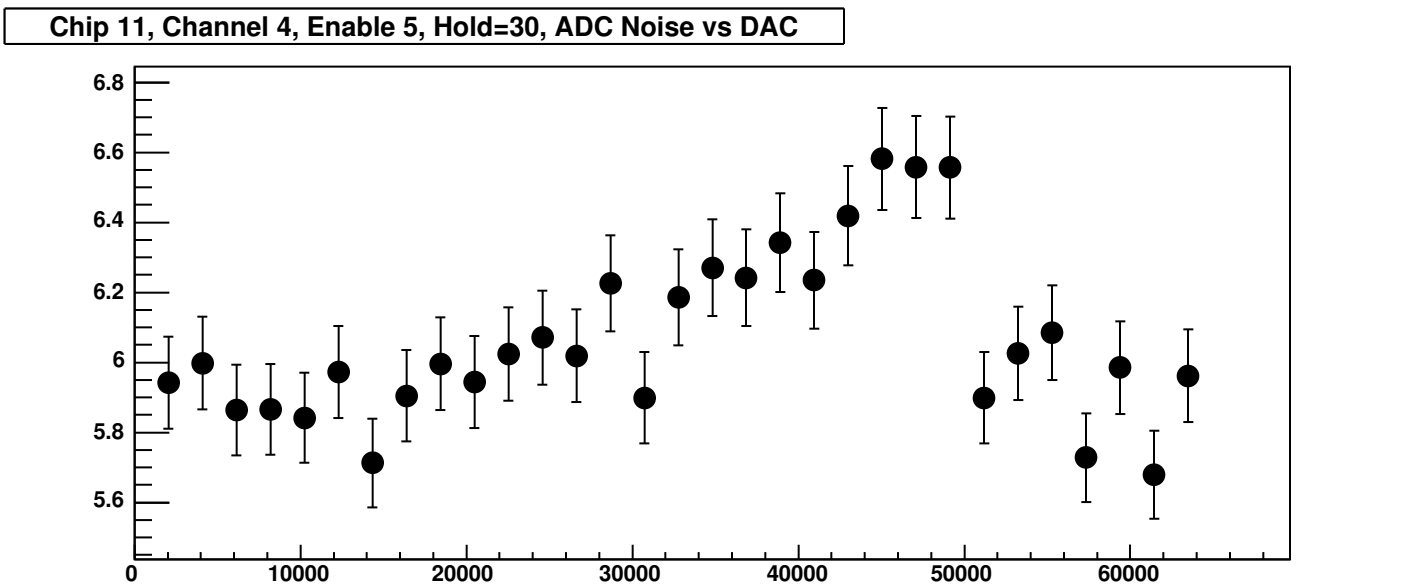

Chip 11, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

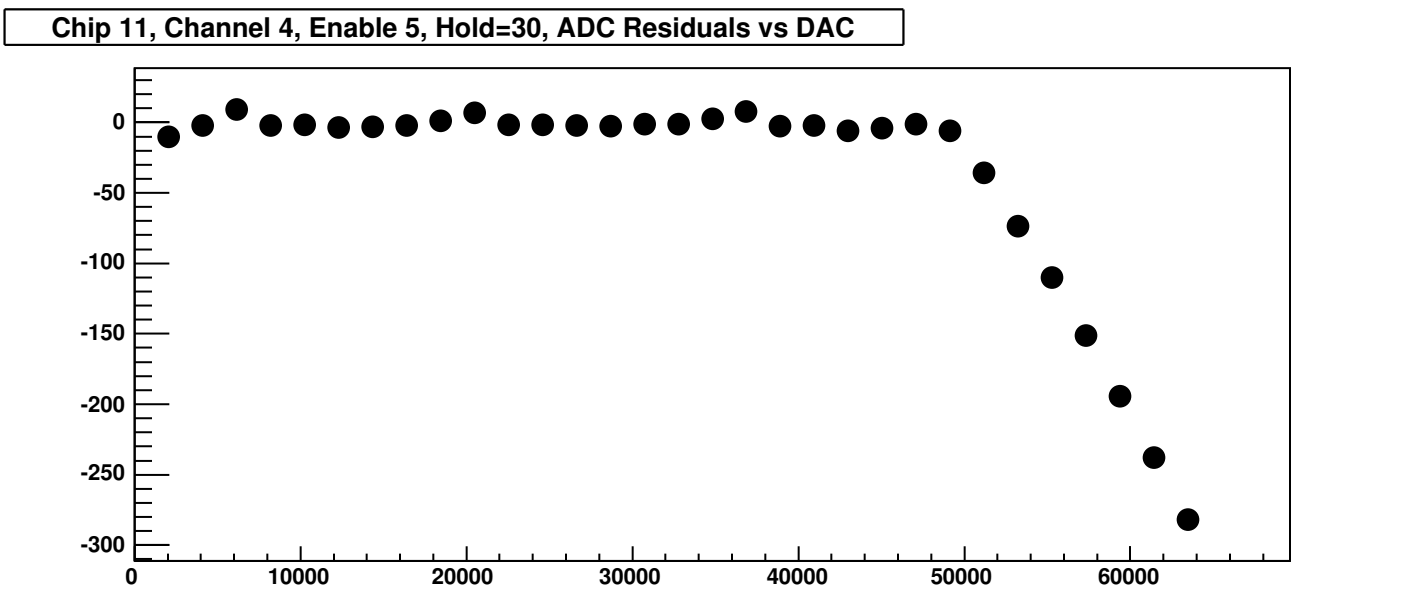

Chip 11, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7275 / 15
p0 812.2 ± 0.1145
p1 0.002902 ± 4.801e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

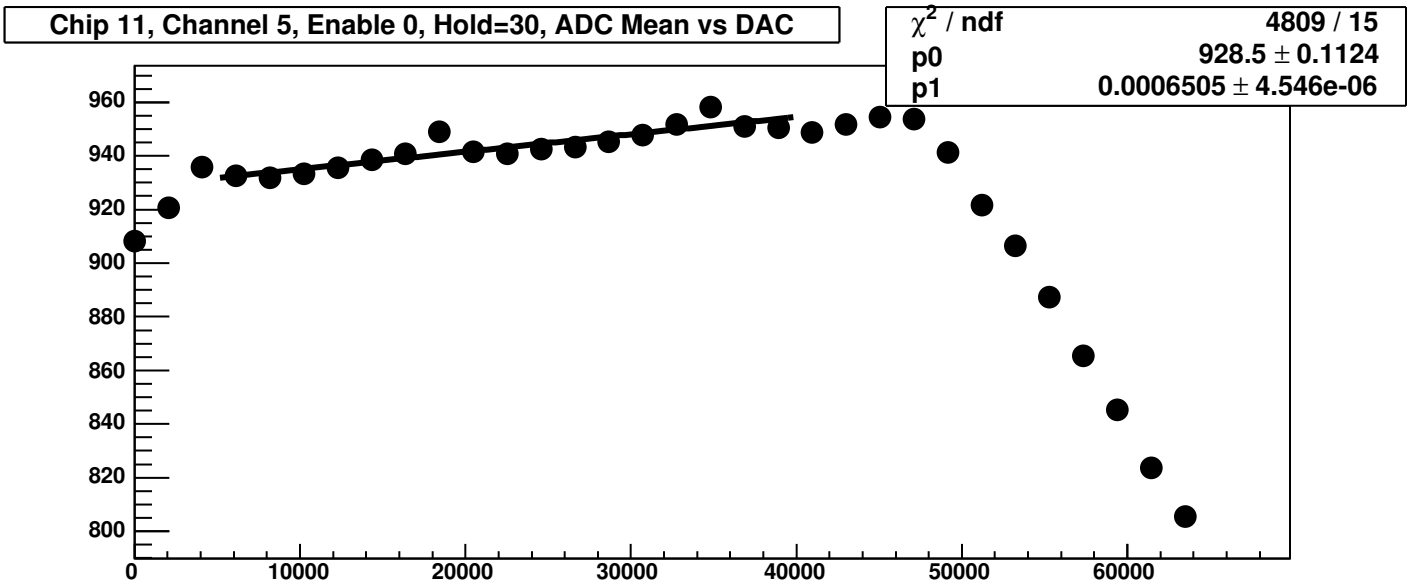

Chip 11, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

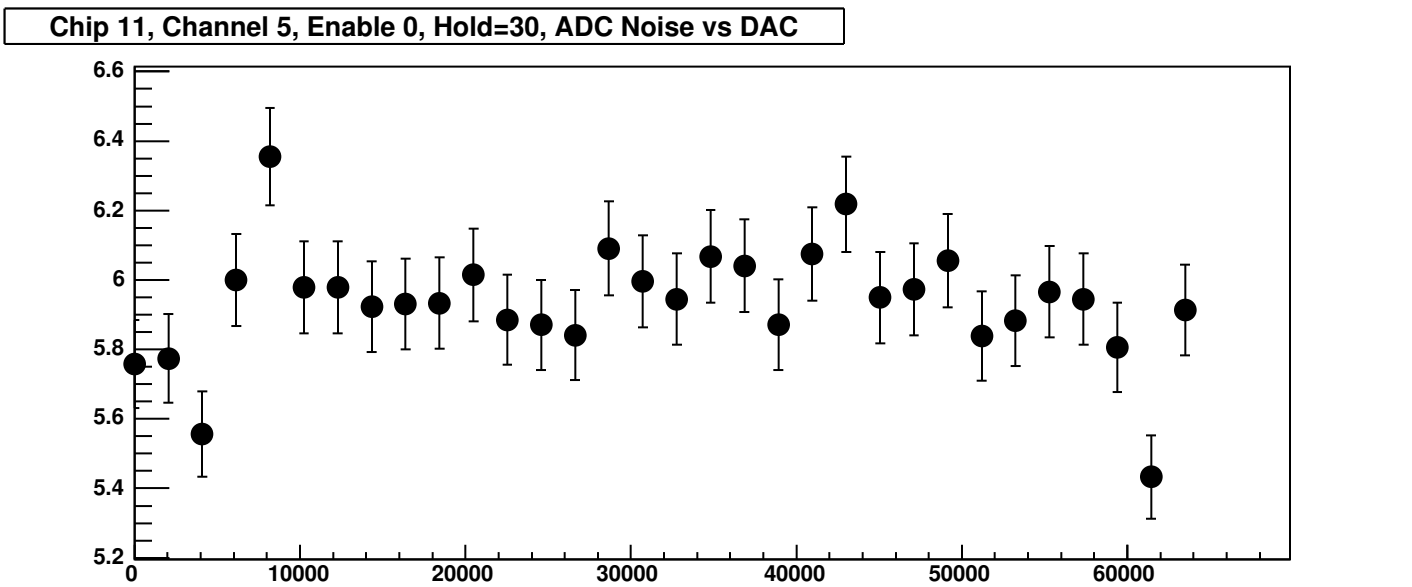

Chip 11, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

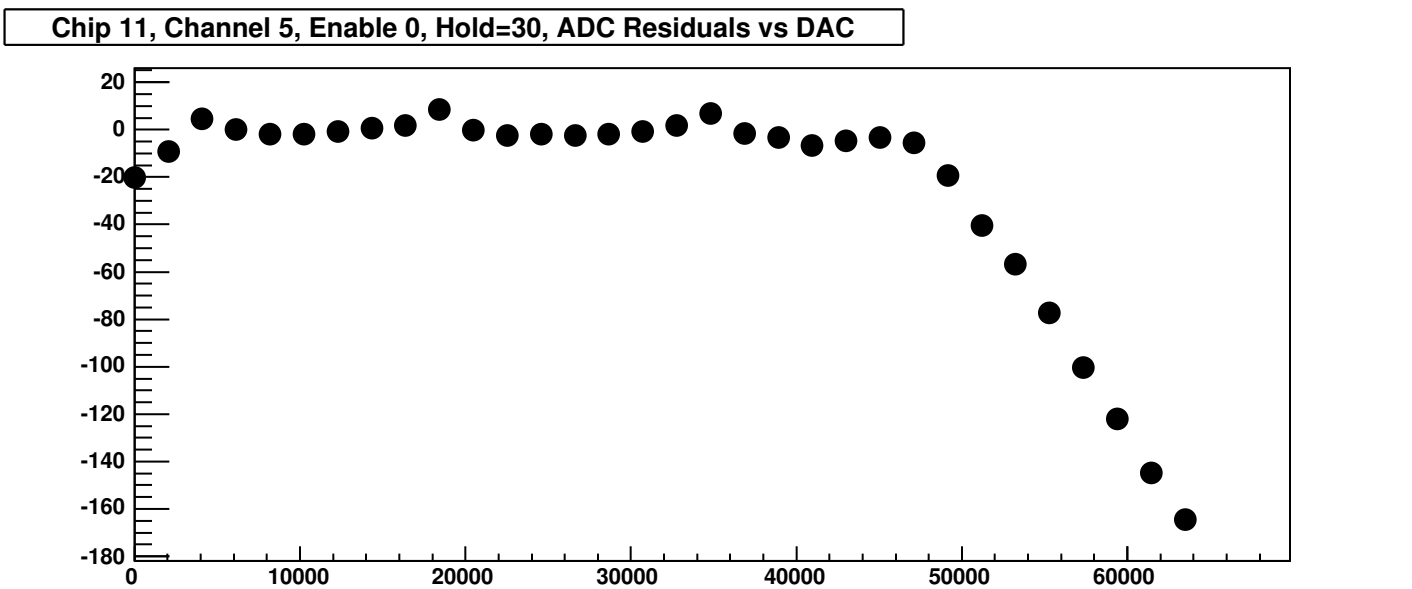

Chip 11, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7371 / 15
p0 816.6 ± 0.1106
p1 0.001143 ± 4.57e-06
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

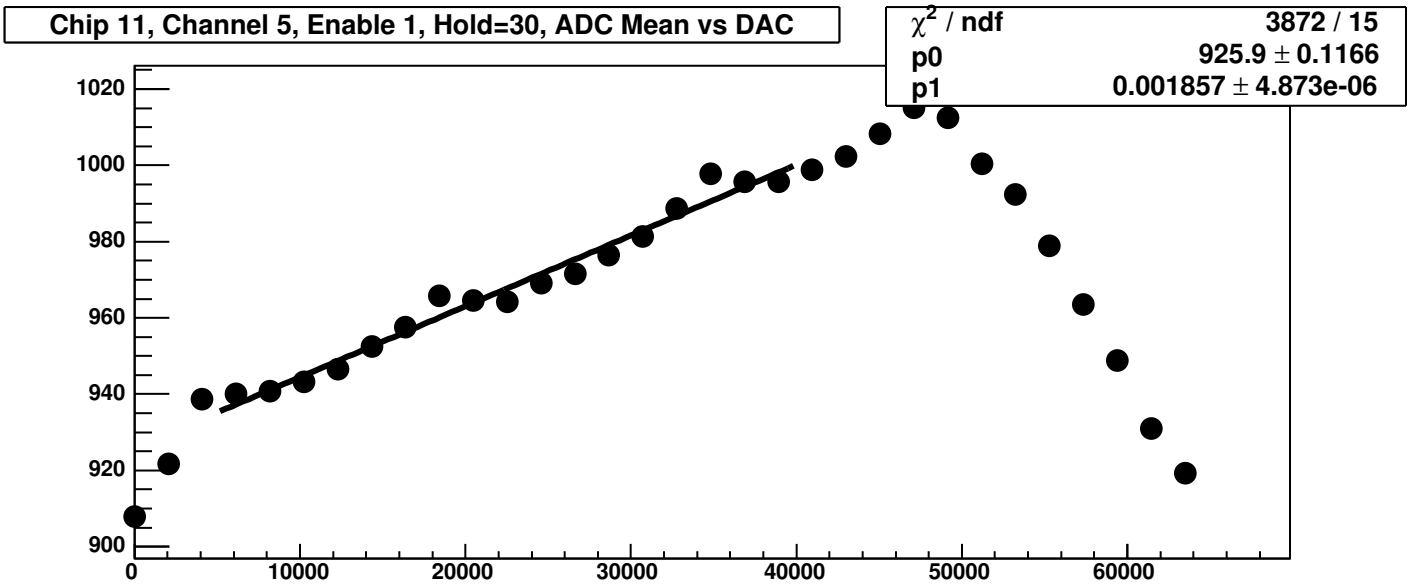

Chip 11, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

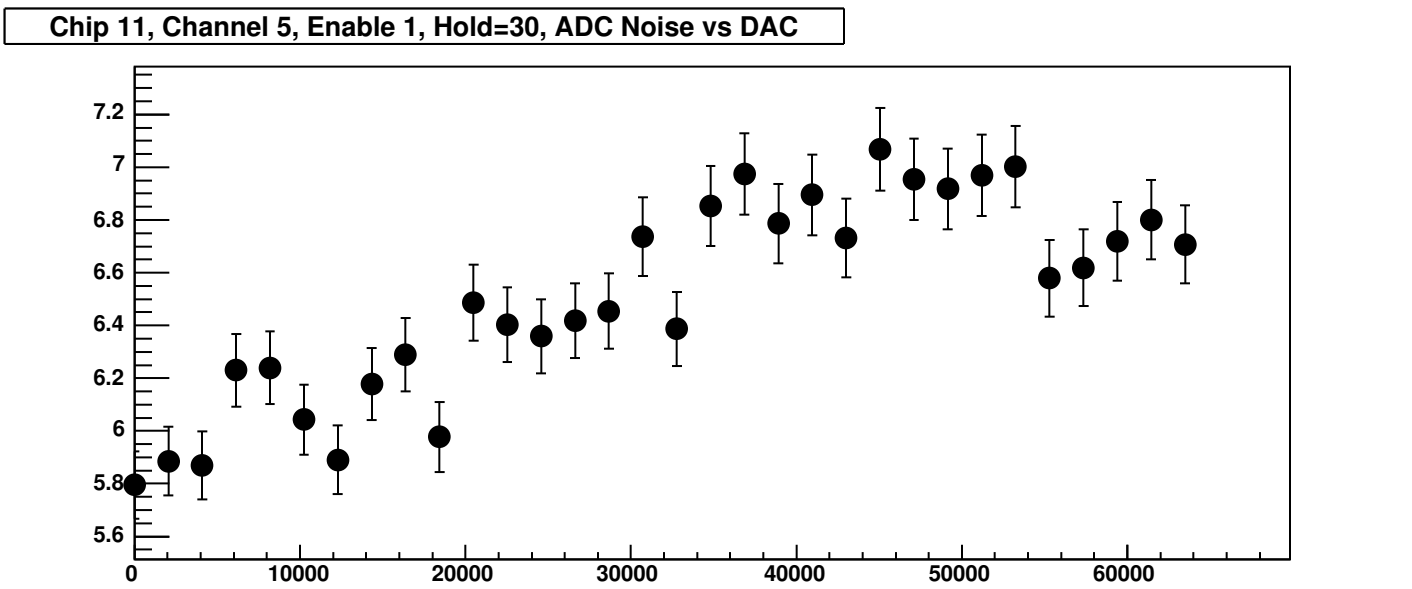

Chip 11, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

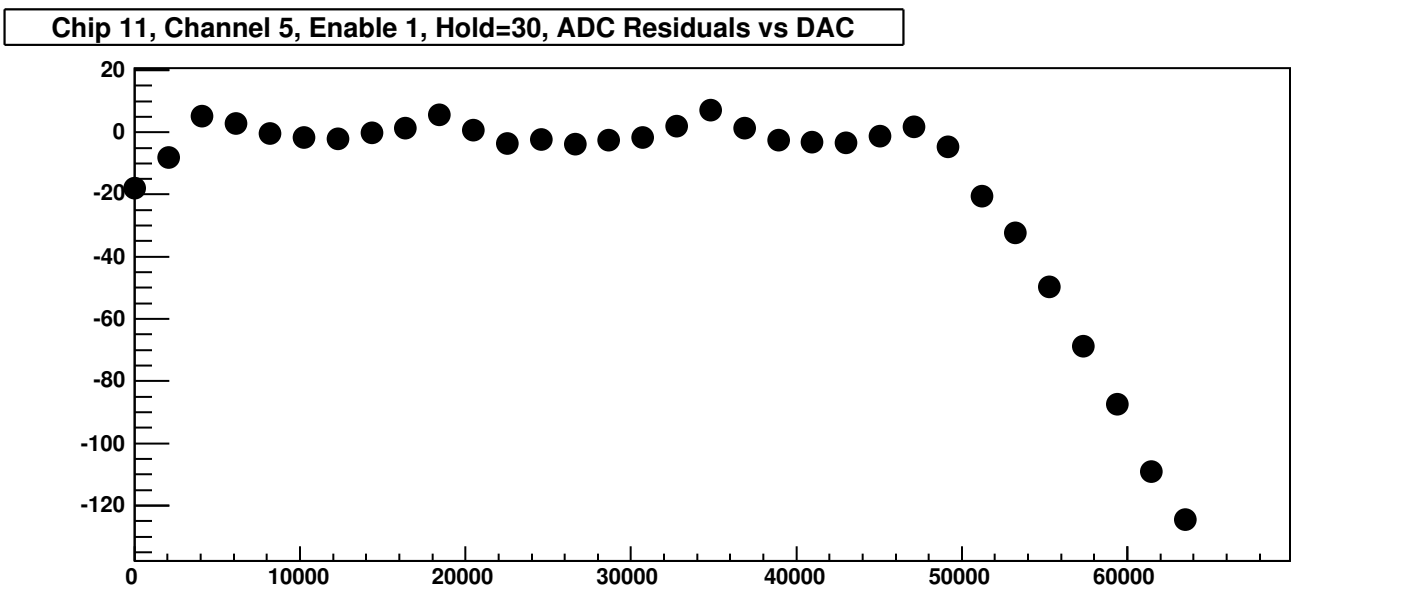

Chip 11, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4809 / 15
p0 928.5 ± 0.1124
p1 0.0006505 ± 4.546e-06
960
940
920
900
880
860
840
820
800
0
10000
20000
30000
40000
50000
60000

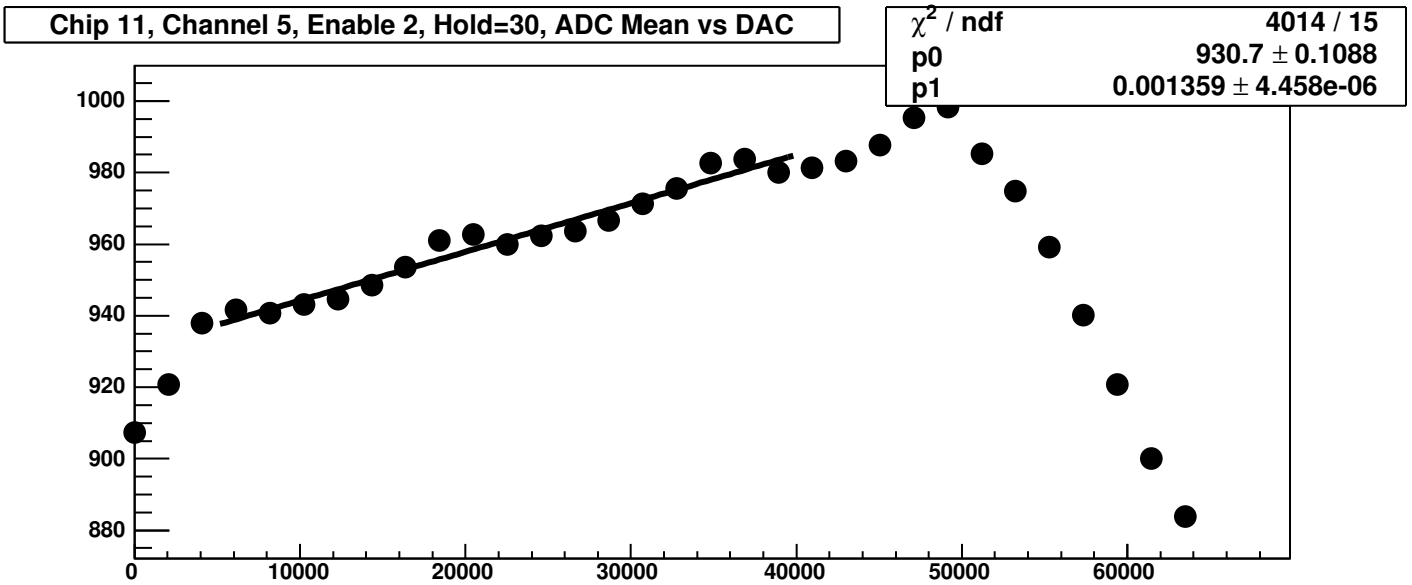

Chip 11, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

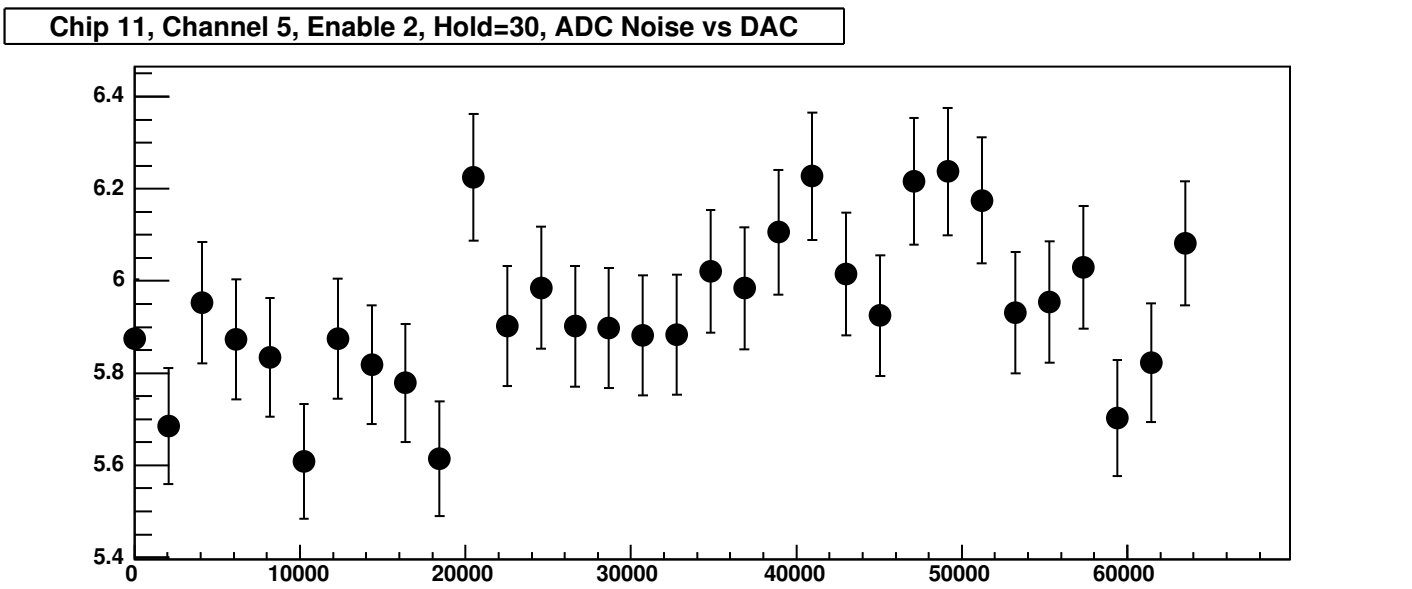

Chip 11, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

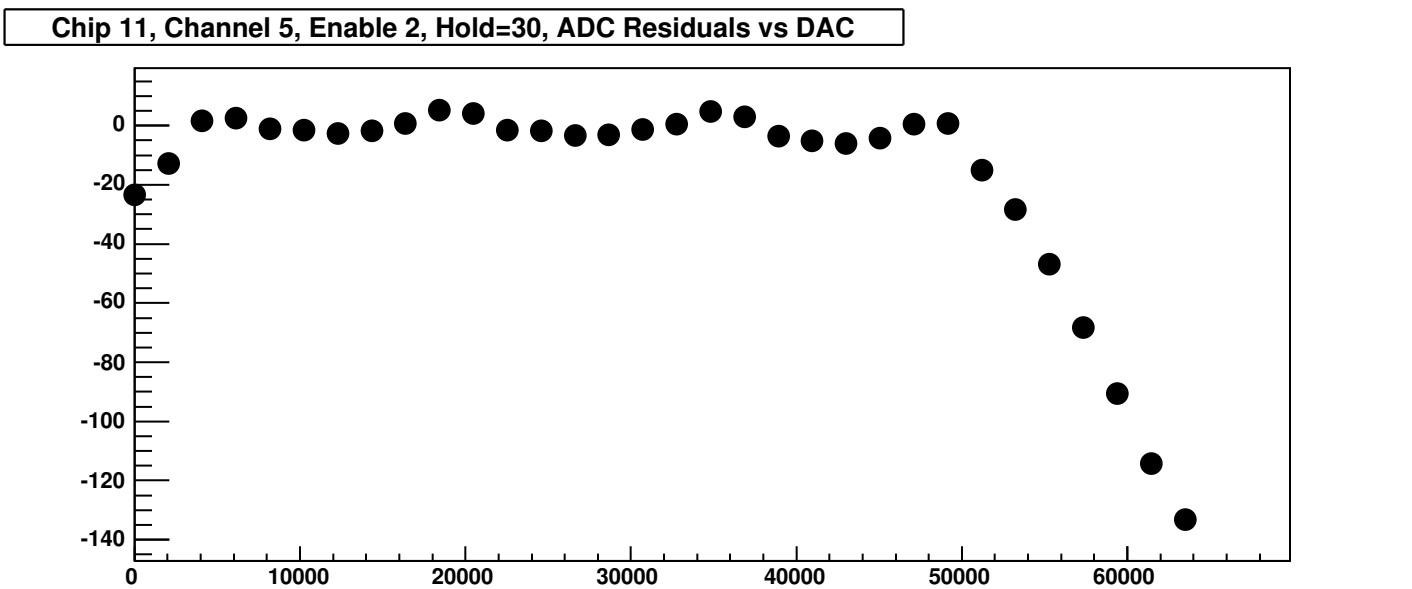

Chip 11, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3872 / 15
p0 925.9 ± 0.1166
p1 0.001857 ± 4.873e-06
1020
1000
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

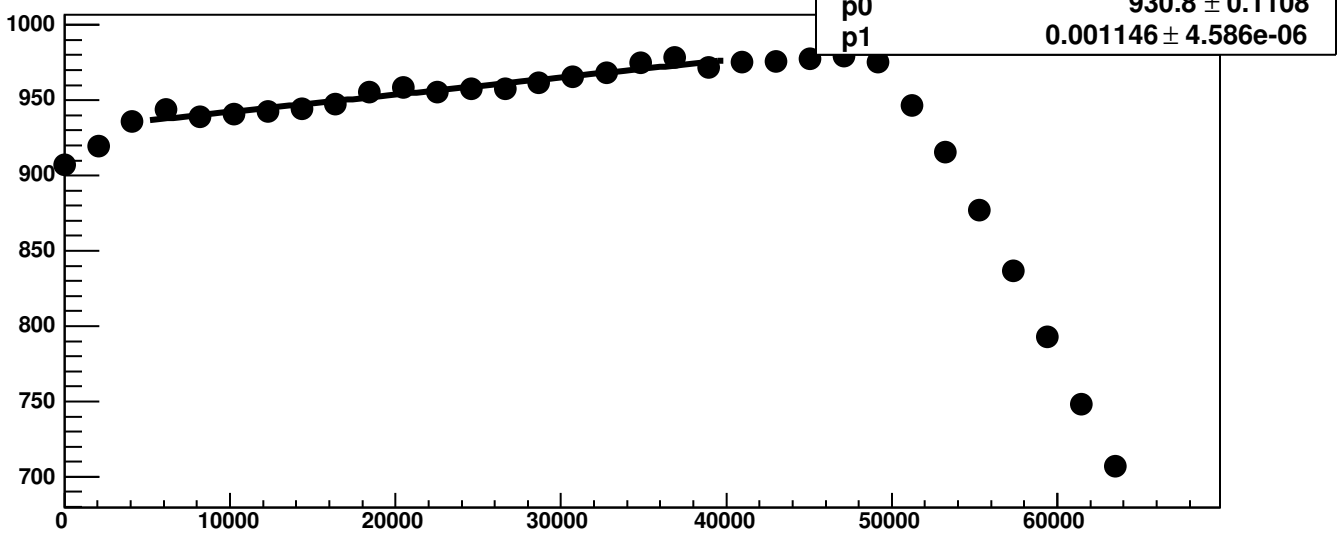

Chip 11, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4014 / 15
p0 930.7 ± 0.1088
p1 0.001359 ± 4.458e-06
1000
980
960
940
920
900
880
0
10000
20000
30000
40000
50000
60000

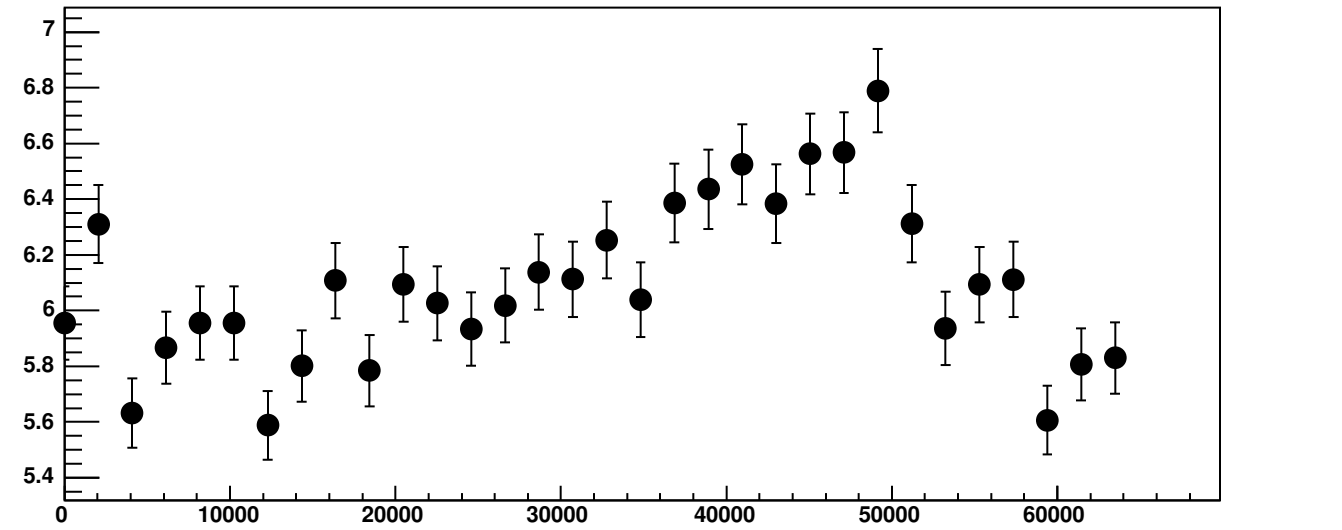

Chip 11, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

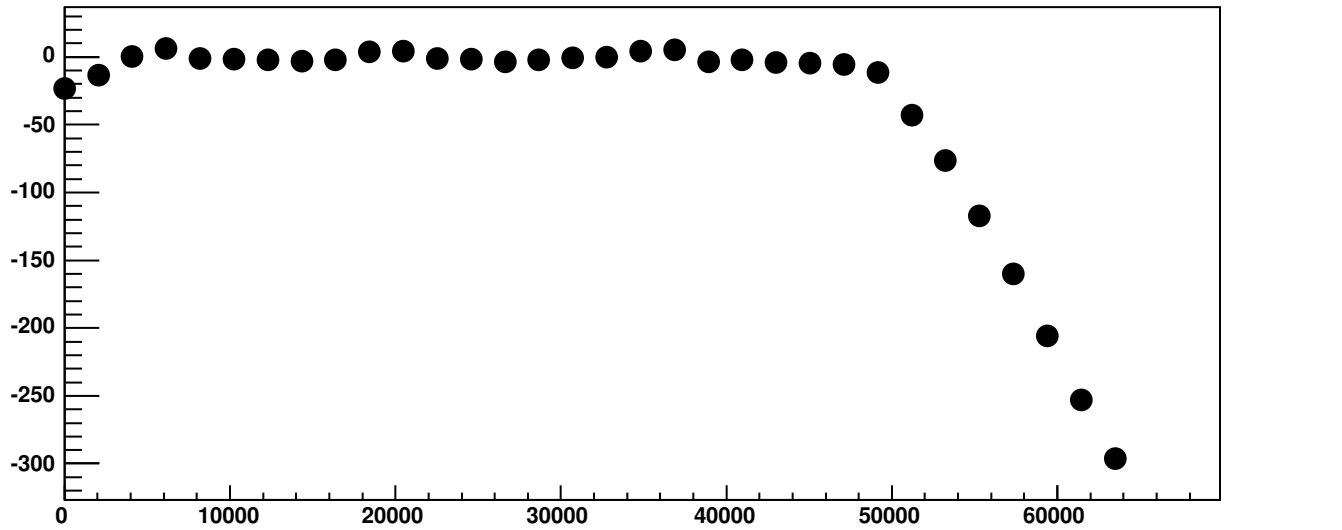
Chip 11, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

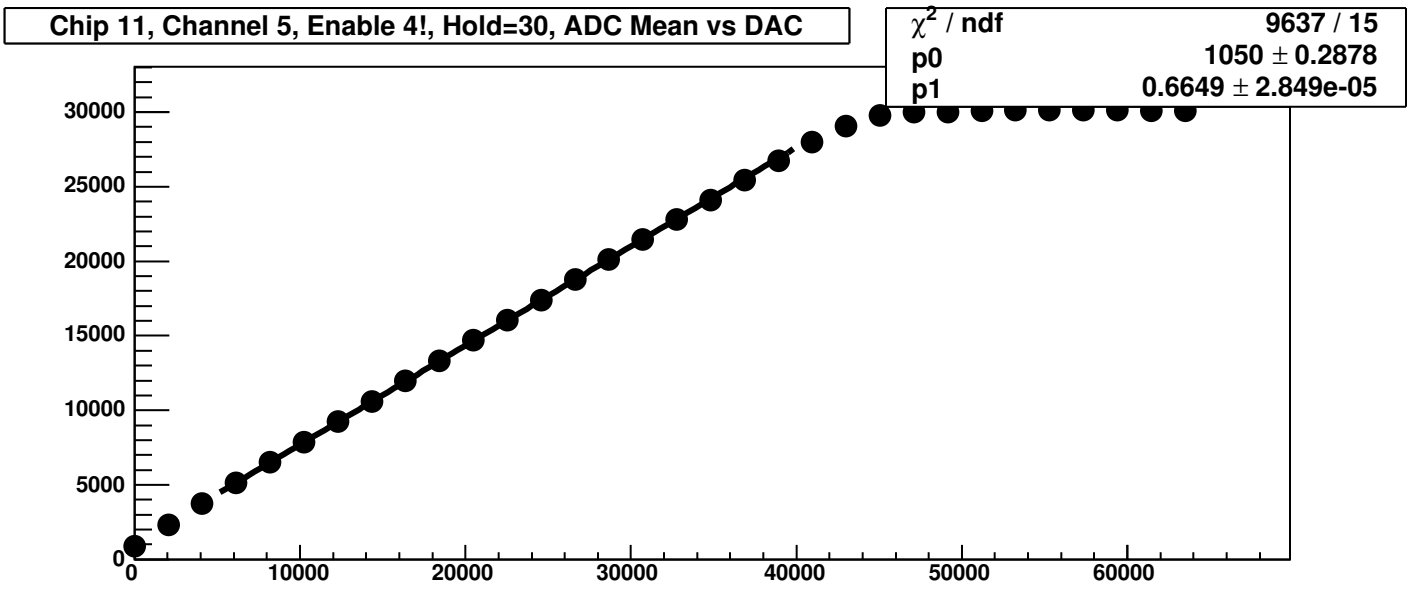


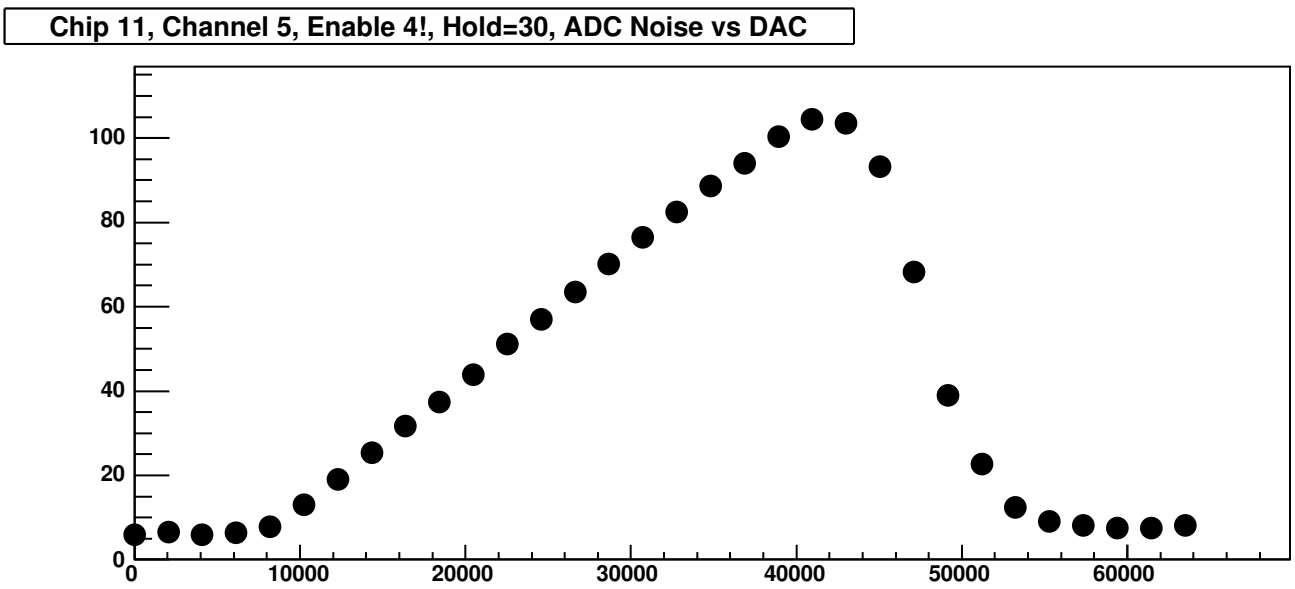
Chip 11, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

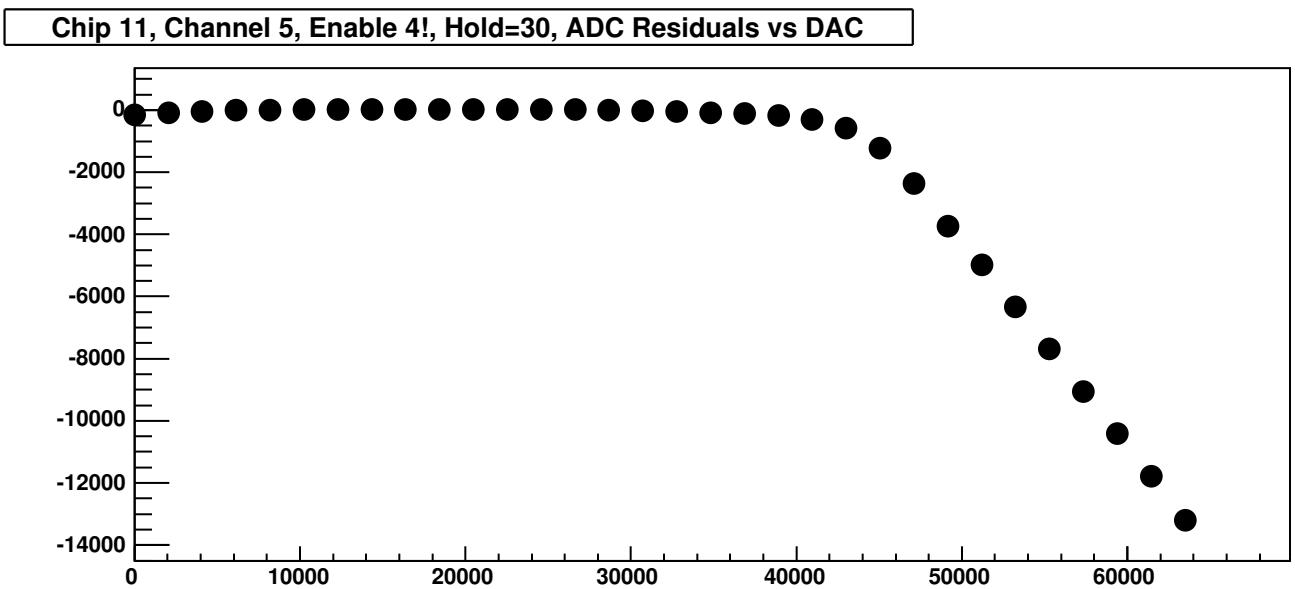


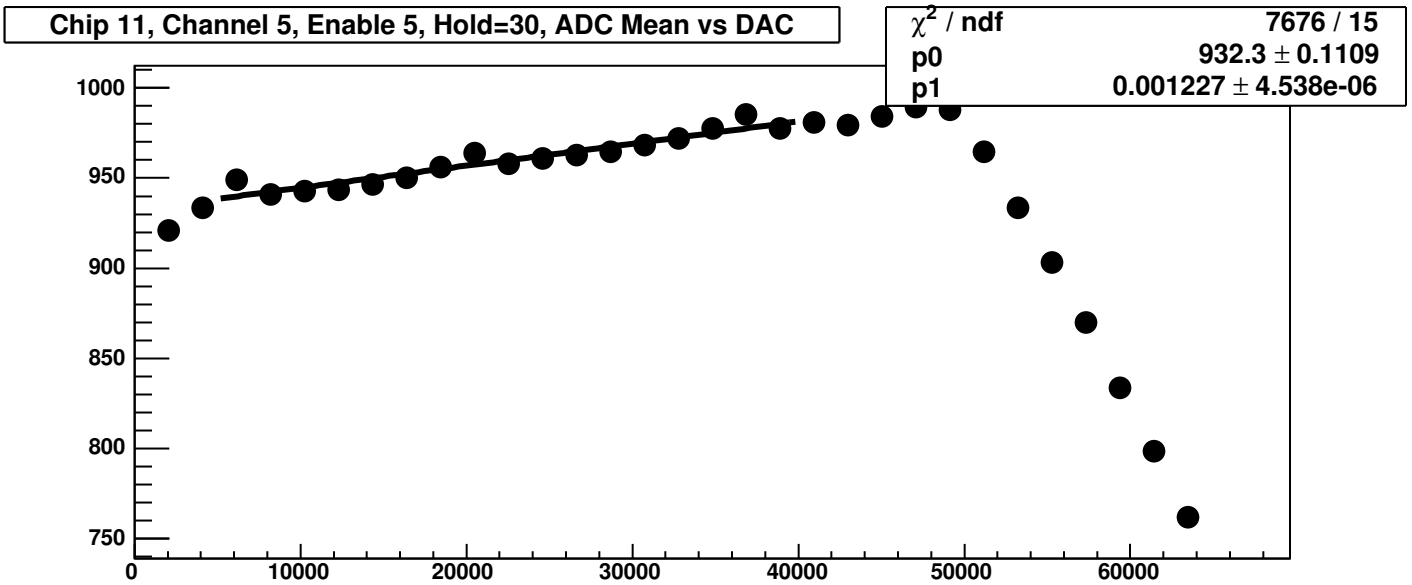
Chip 11, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

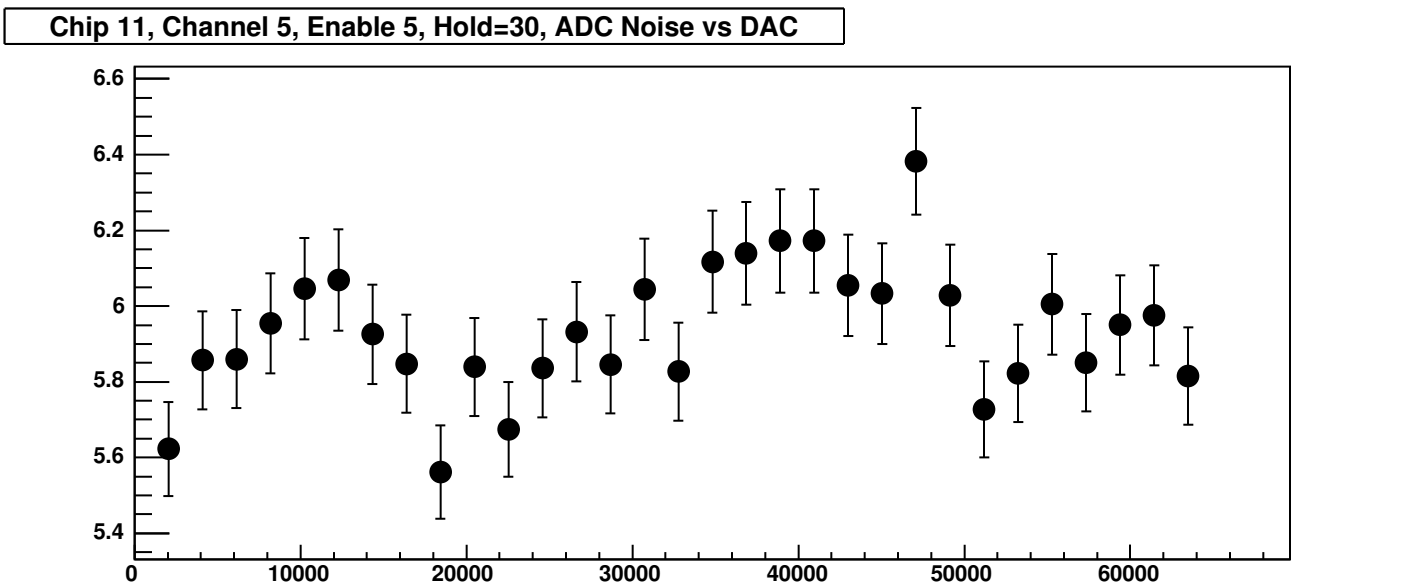


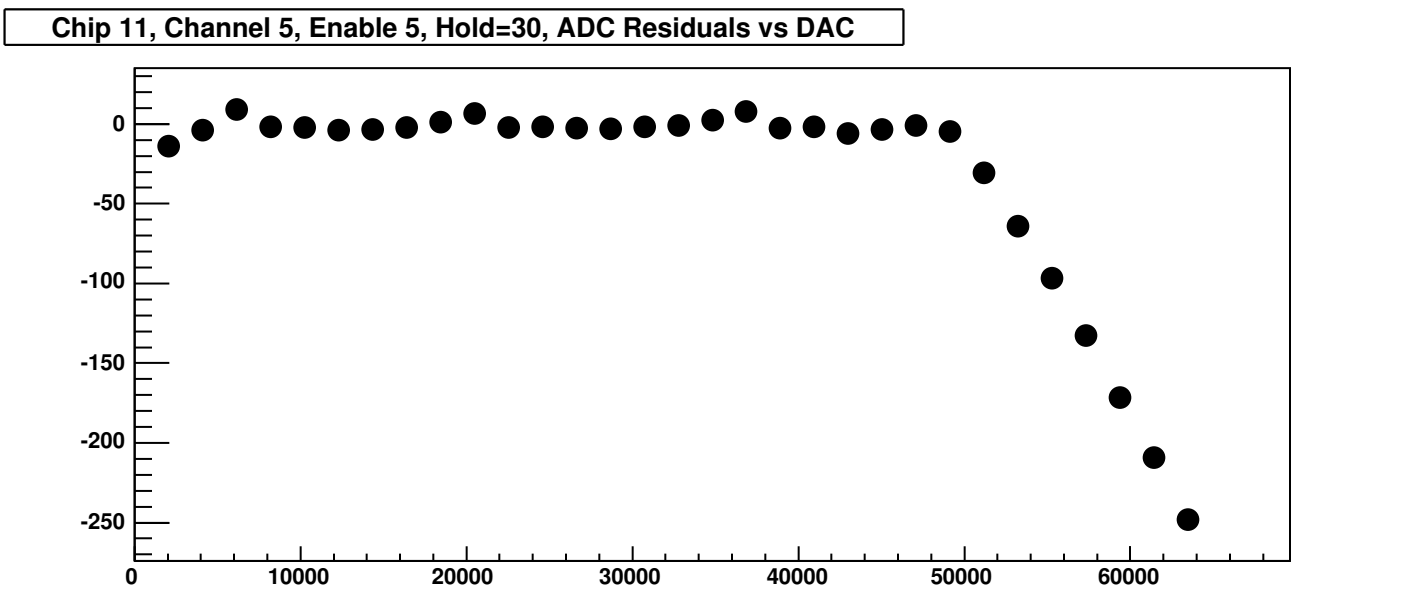

Chip 11, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 9637 / 15
p0 1050 ± 0.2878
p1 0.6649 ± 2.849e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

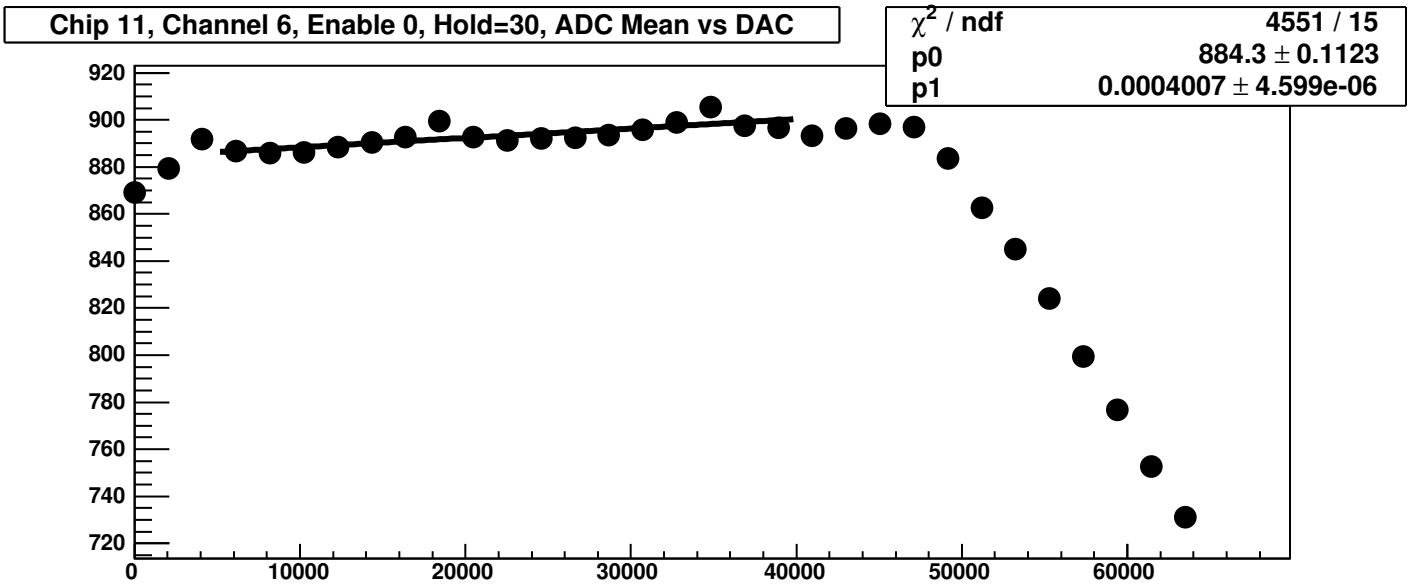

Chip 11, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

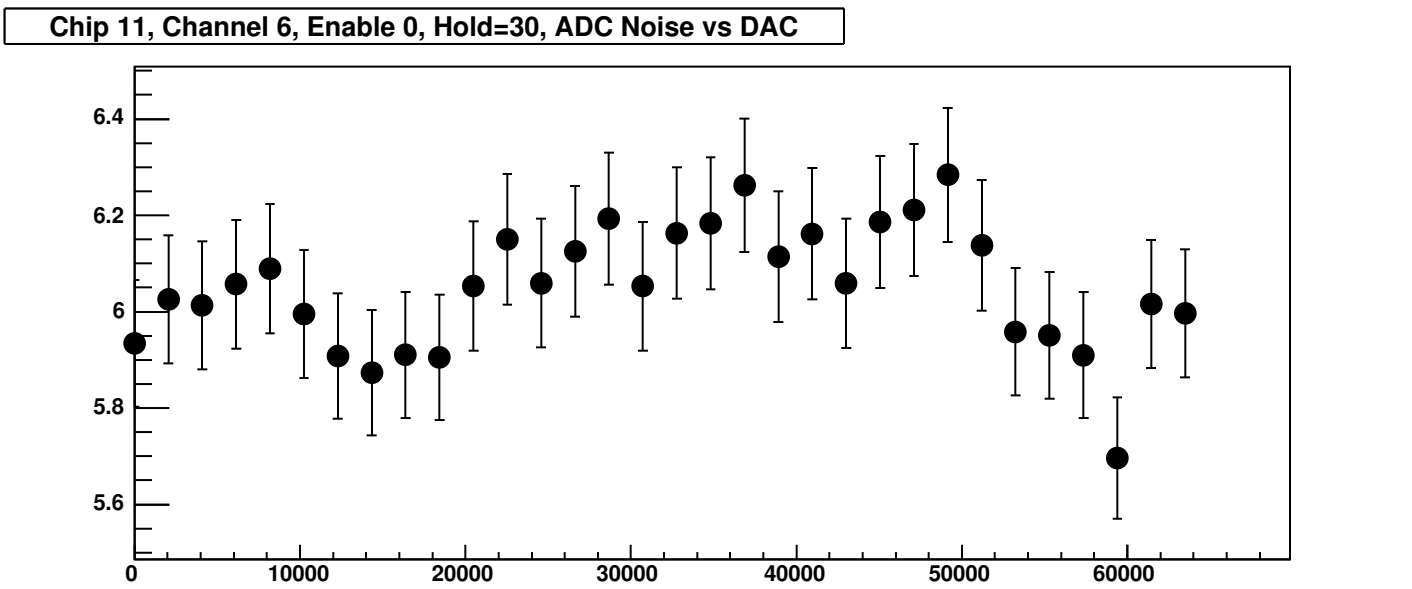

Chip 11, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
10000
20000
30000
40000
50000
60000

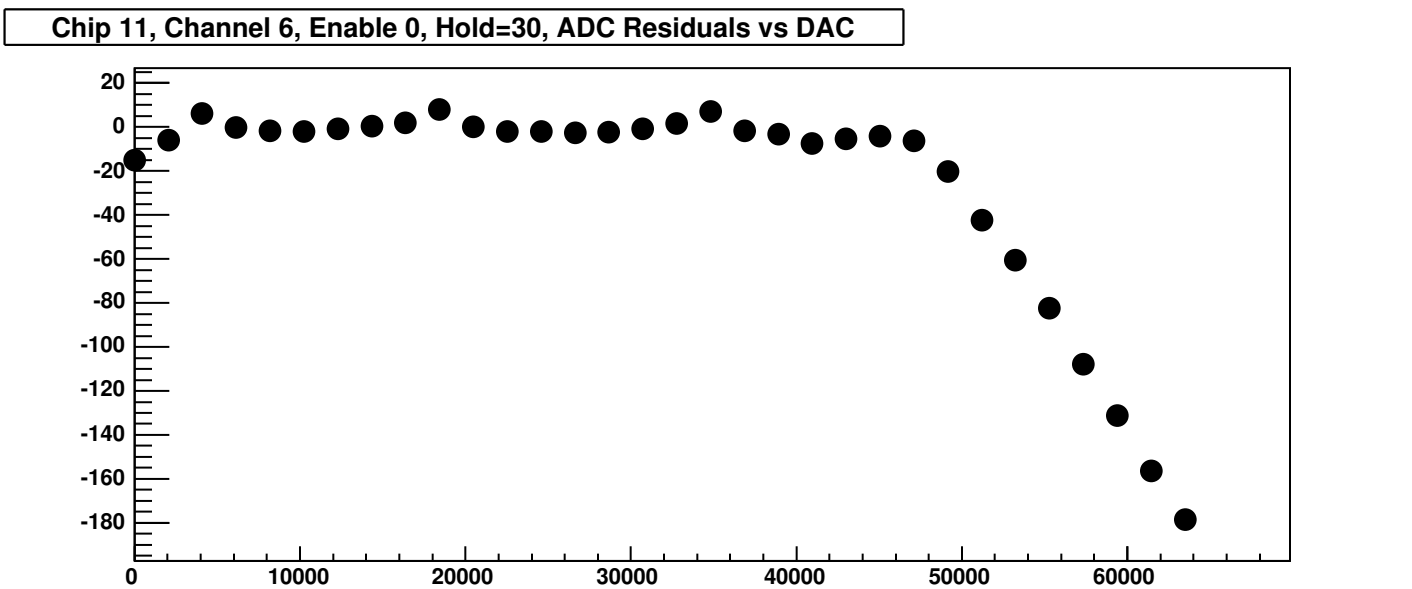

Chip 11, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7676 / 15
p0 932.3 ± 0.1109
p1 0.001227 ± 4.538e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

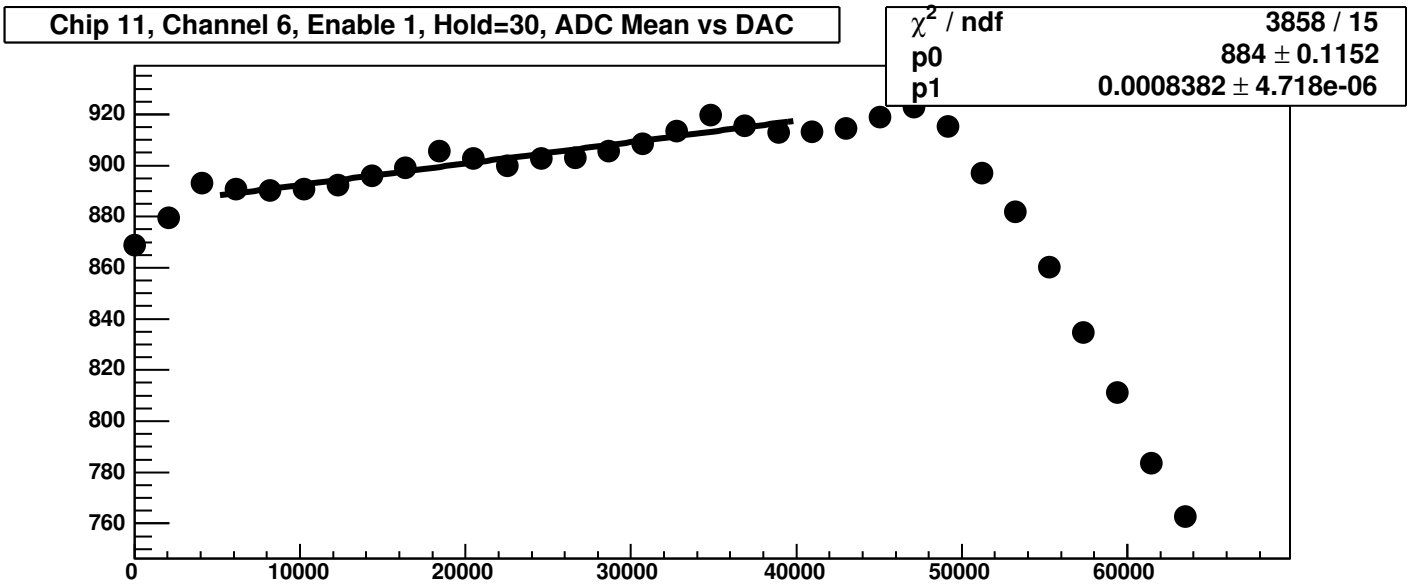

Chip 11, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

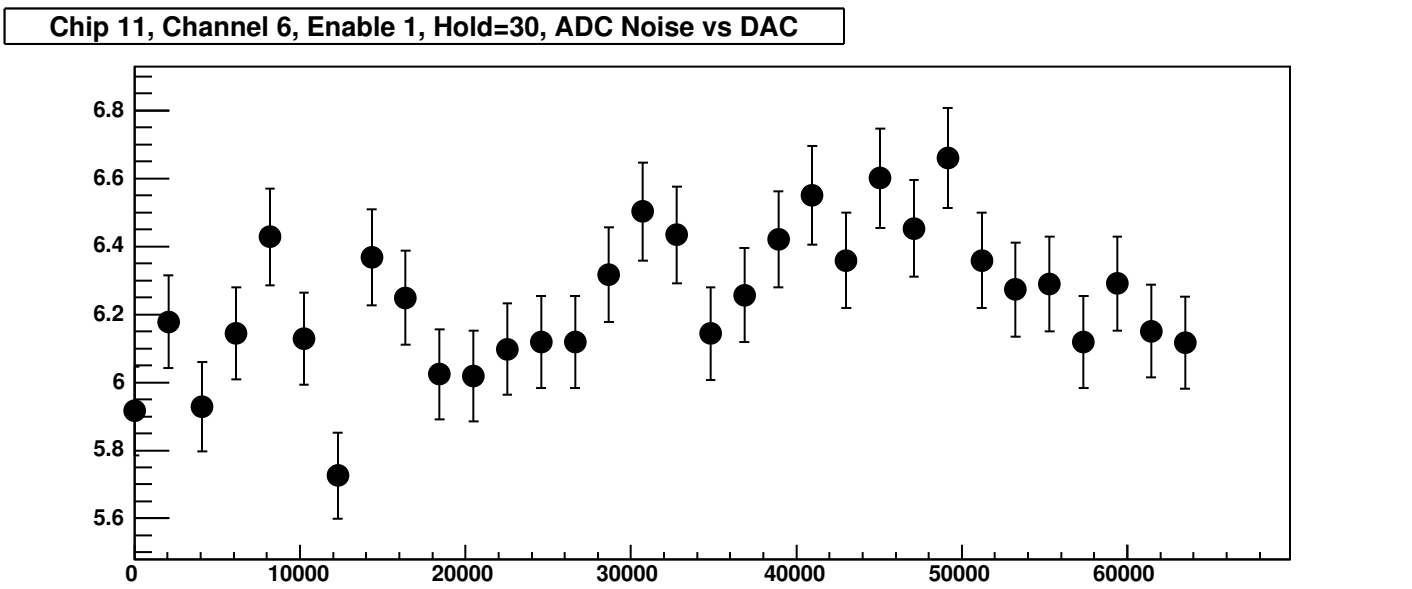

Chip 11, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

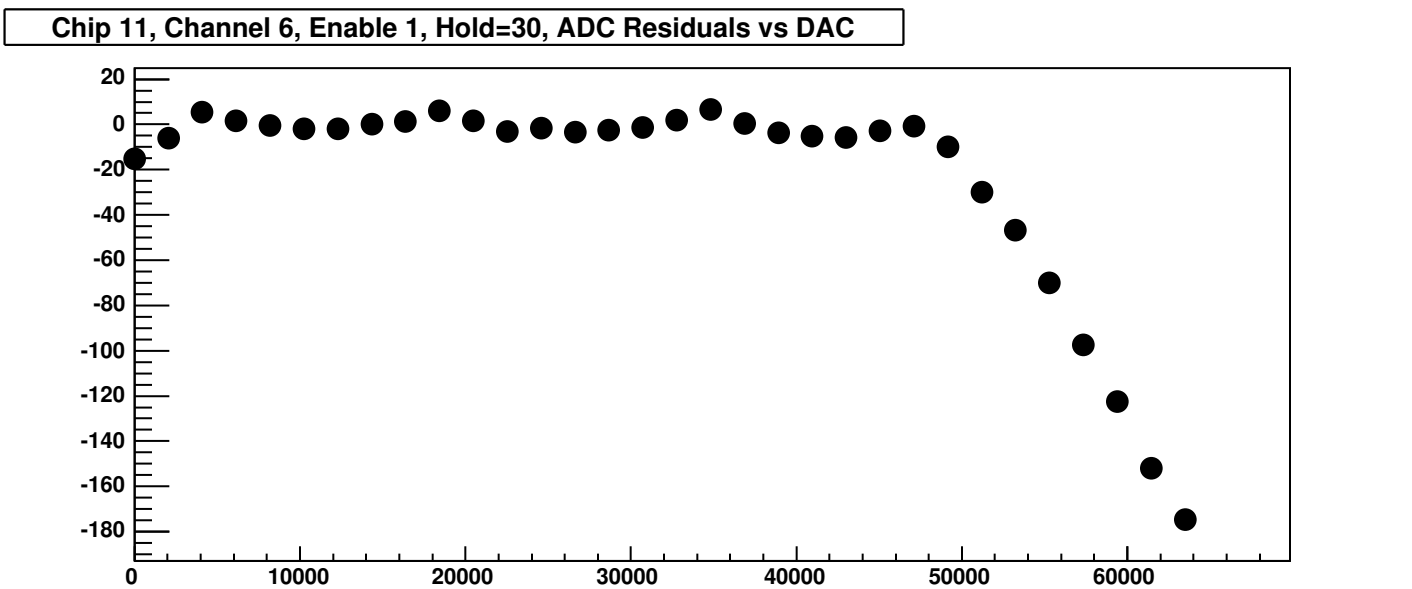

Chip 11, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4551 / 15
p0 884.3 ± 0.1123
p1 0.0004007 ± 4.599e-06
920
900
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

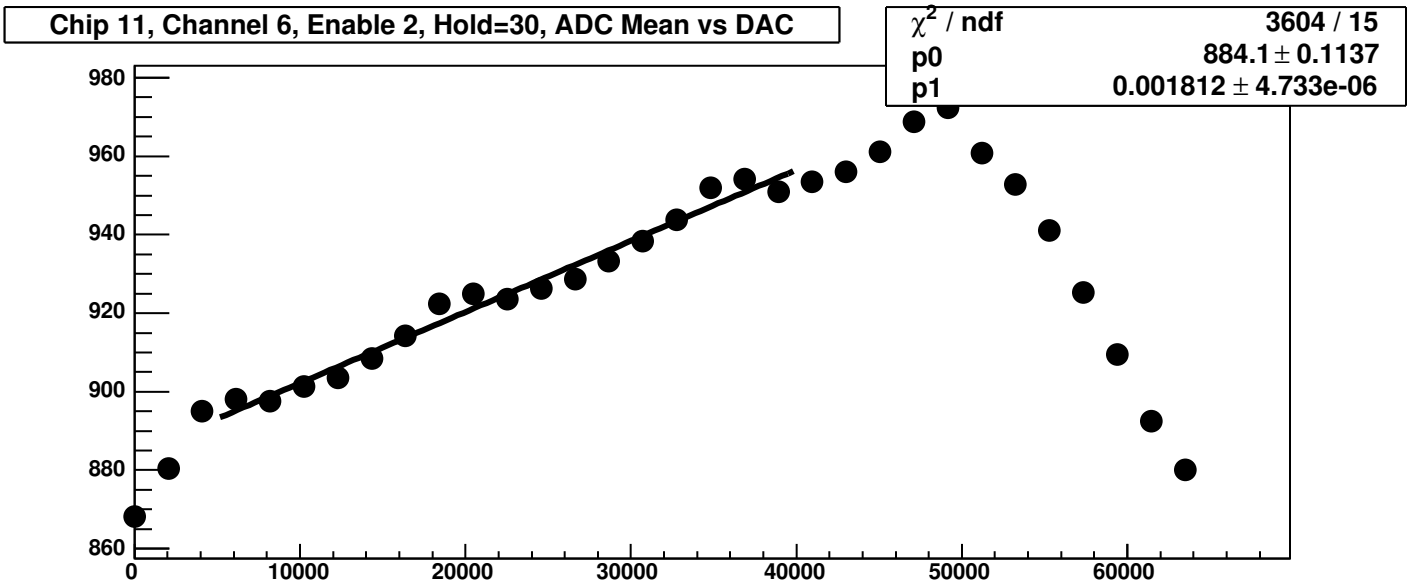

Chip 11, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

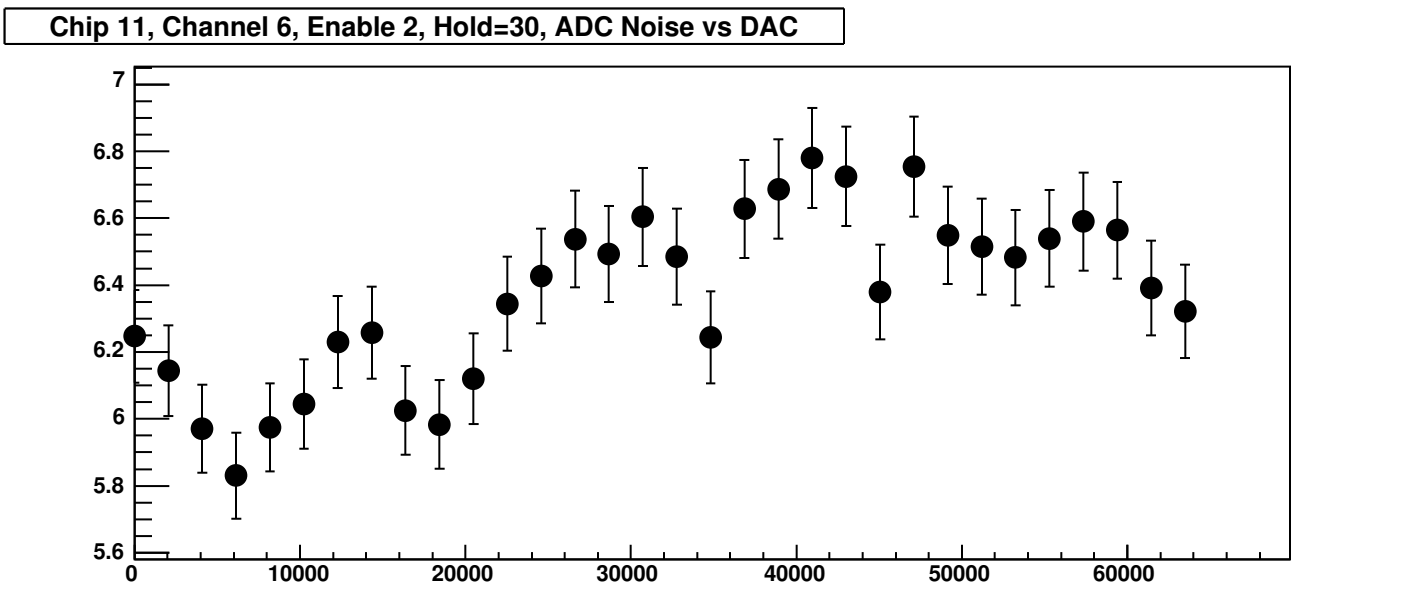

Chip 11, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

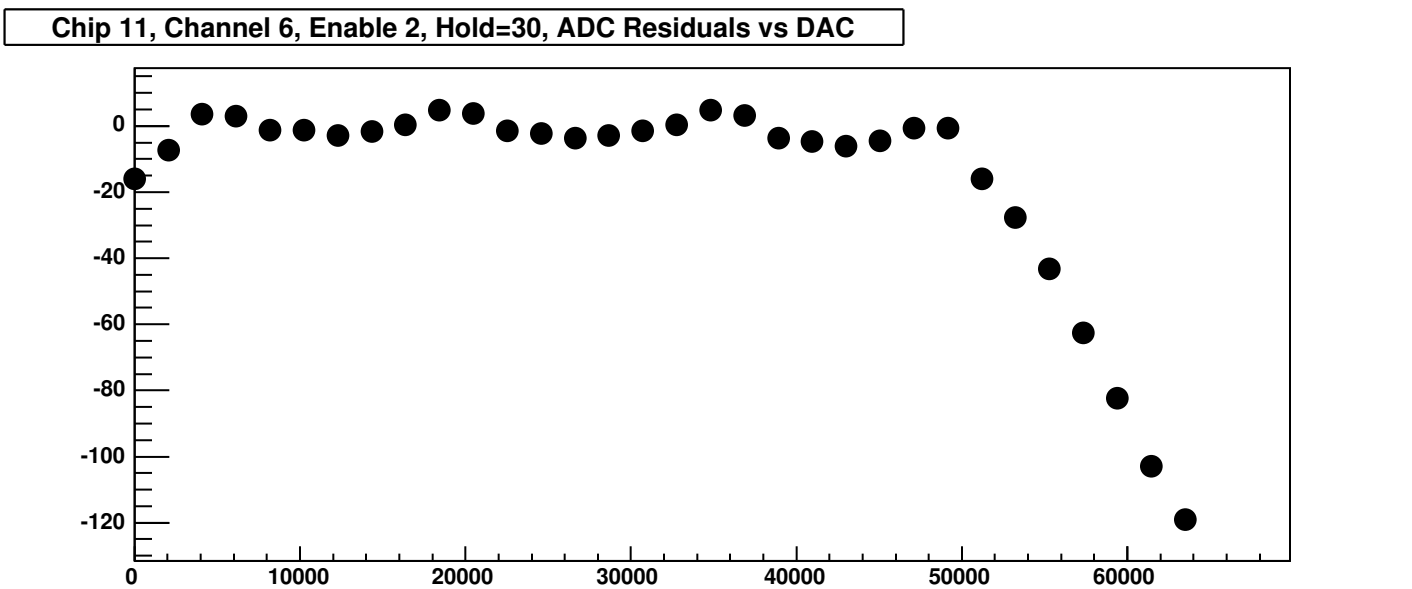

Chip 11, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3858 / 15
p0 884 ± 0.1152
p1 0.0008382 ± 4.718e-06
920
900
880
860
840
820
800
780
760
0
10000
20000
30000
40000
50000
60000

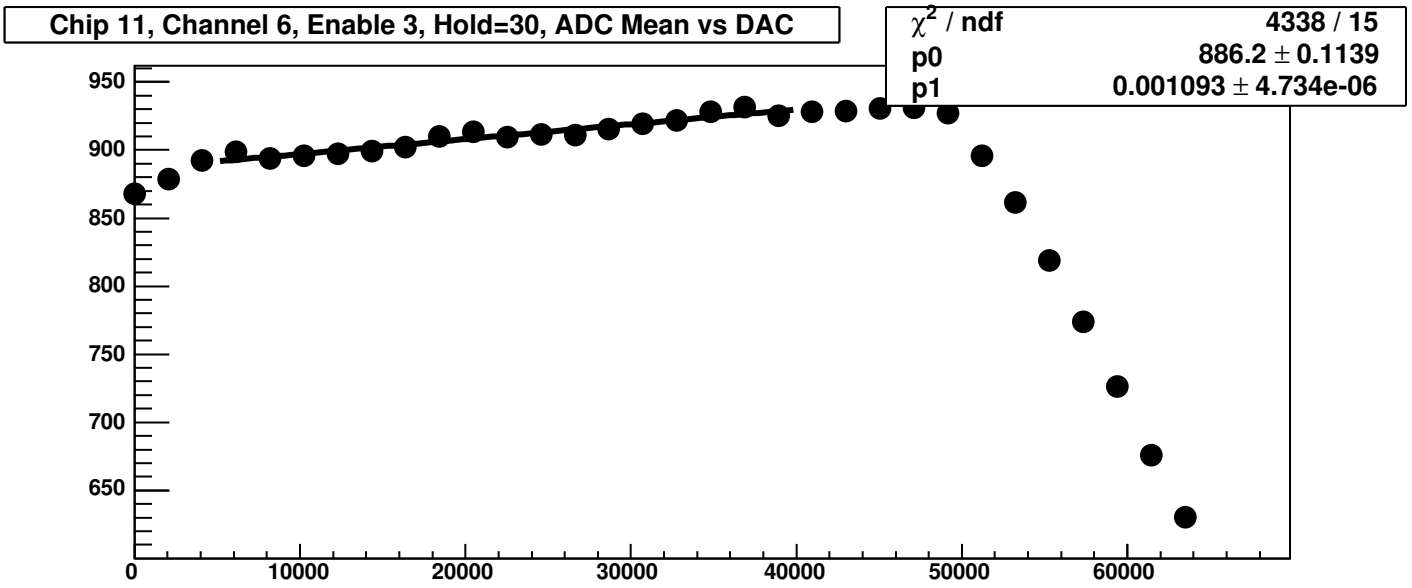

Chip 11, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

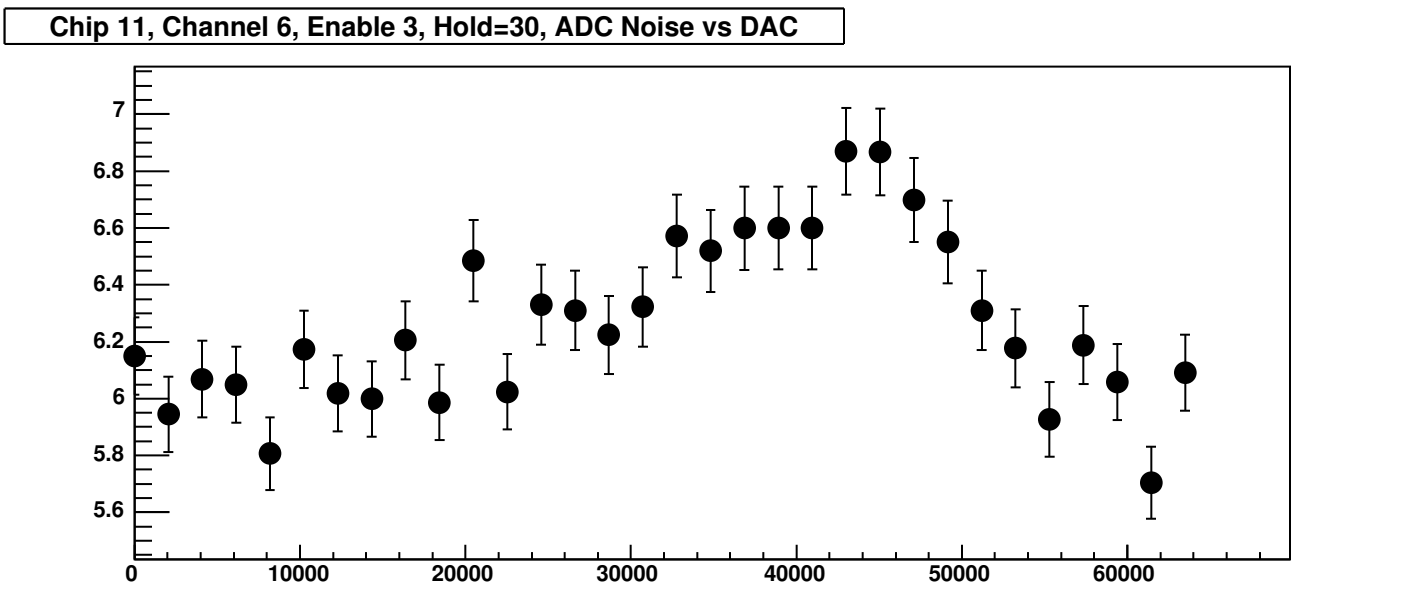

Chip 11, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

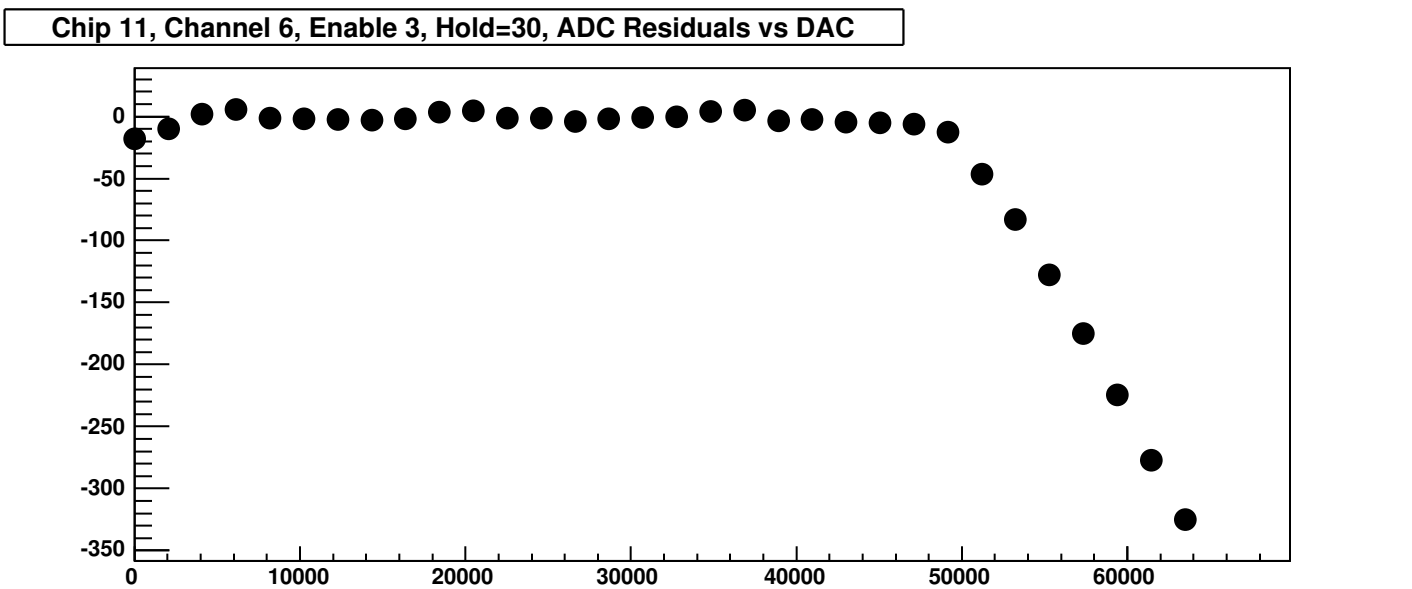

Chip 11, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3604 / 15
p0 884.1 ± 0.1137
p1 0.001812 ± 4.733e-06
980
960
940
920
900
880
860
0
10000
20000
30000
40000
50000
60000

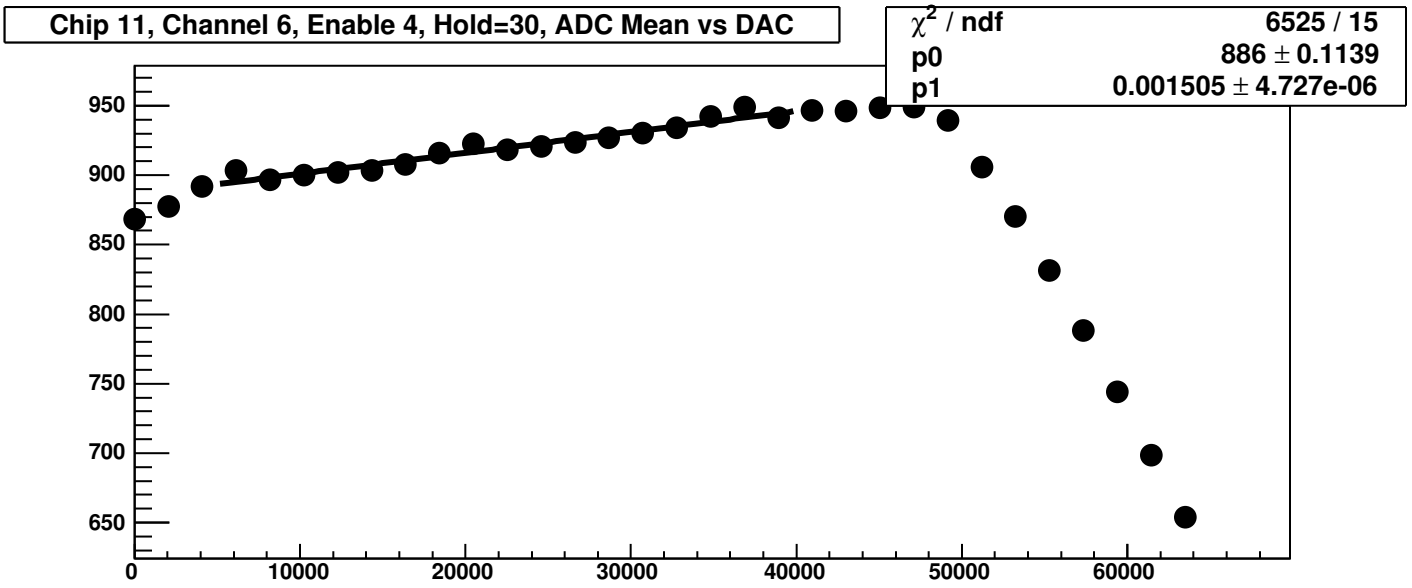

Chip 11, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

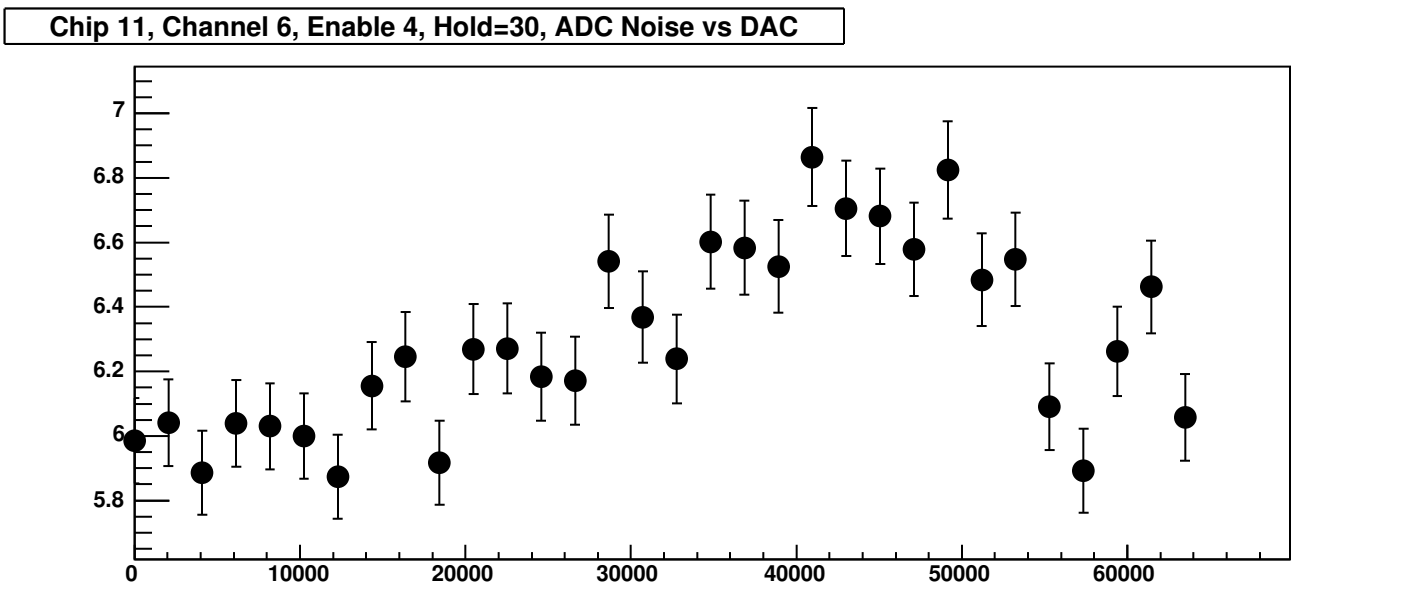

Chip 11, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

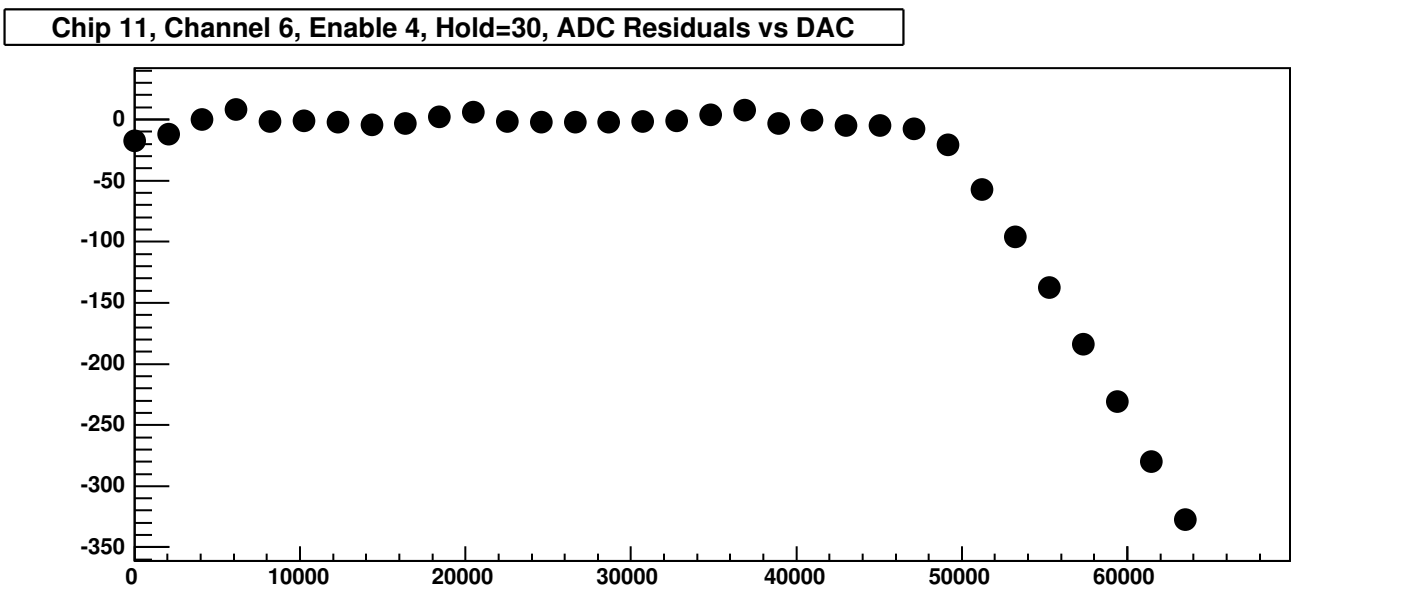

Chip 11, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4338 / 15
p0 886.2 ± 0.1139
p1 0.001093 ± 4.734e-06
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

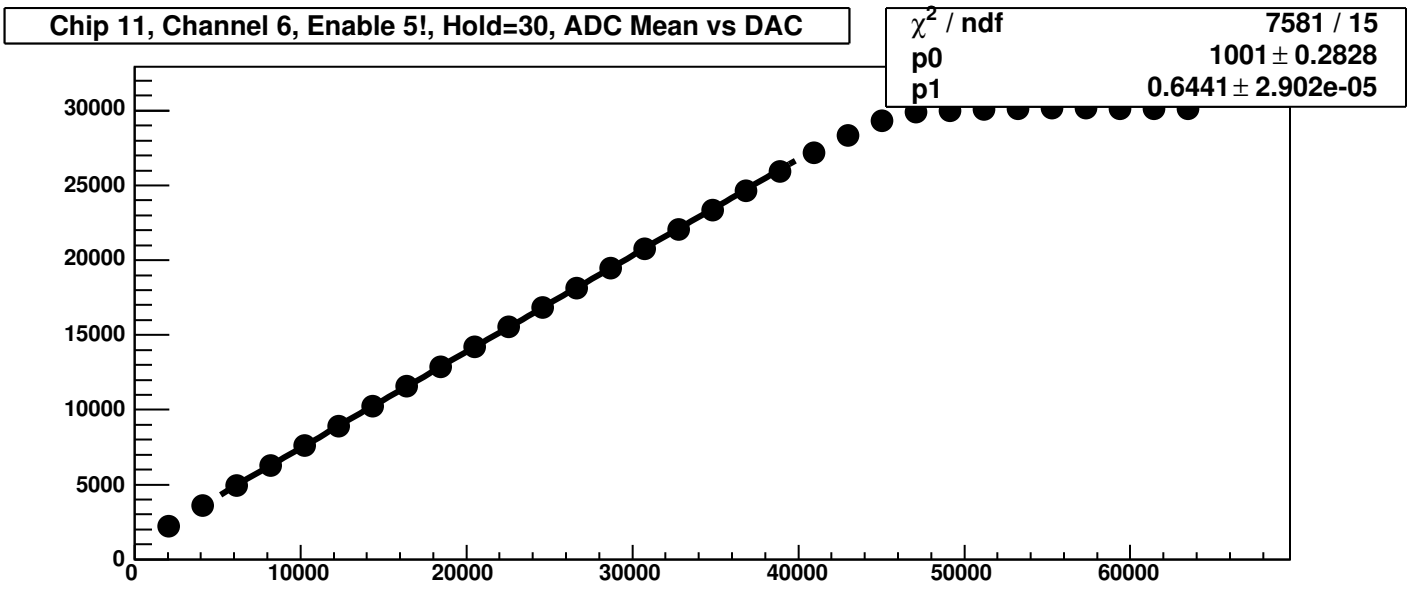

Chip 11, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

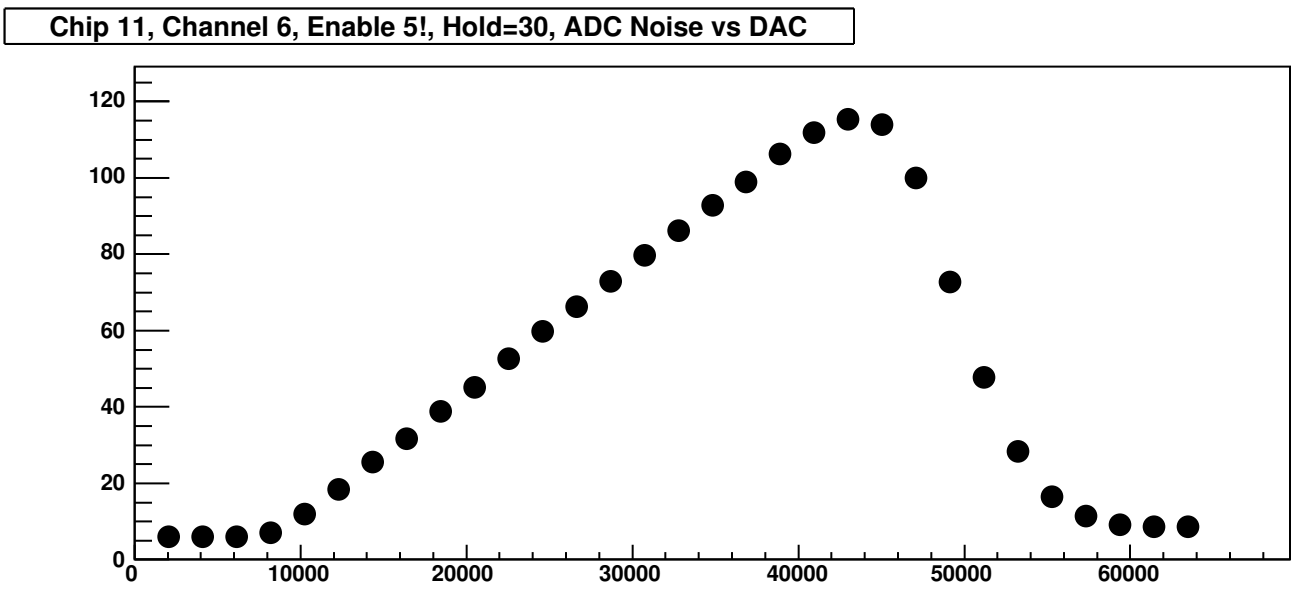

Chip 11, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

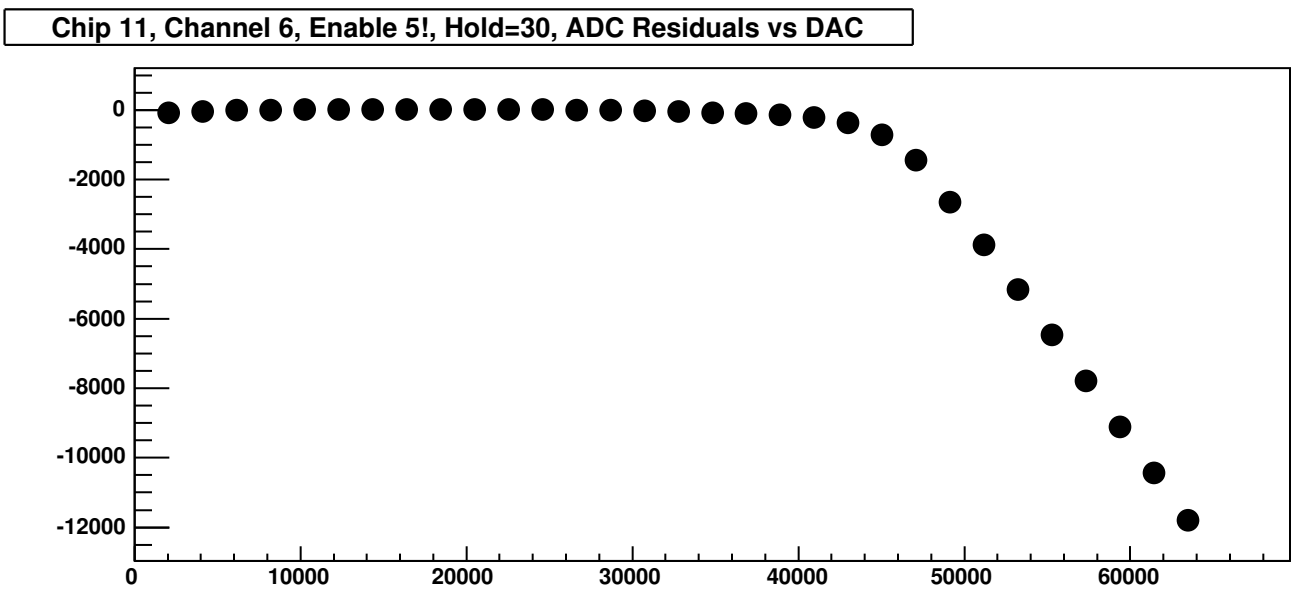

Chip 11, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6525 / 15
p0 886 ± 0.1139
p1 0.001505 ± 4.727e-06
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

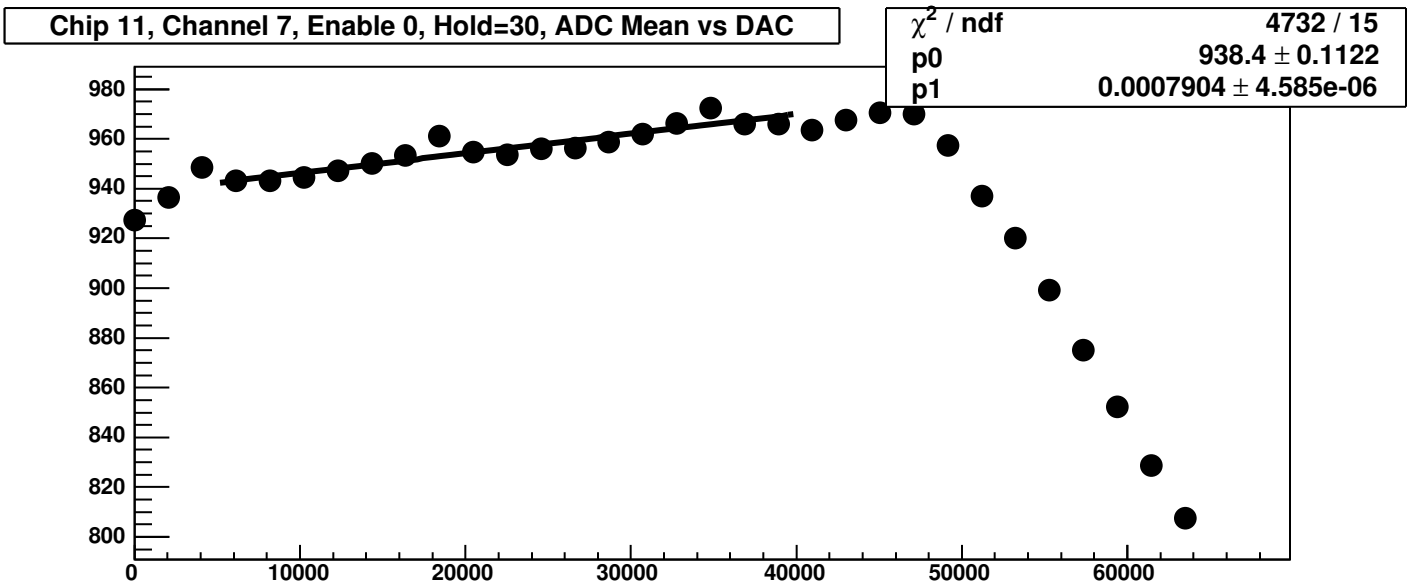

Chip 11, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

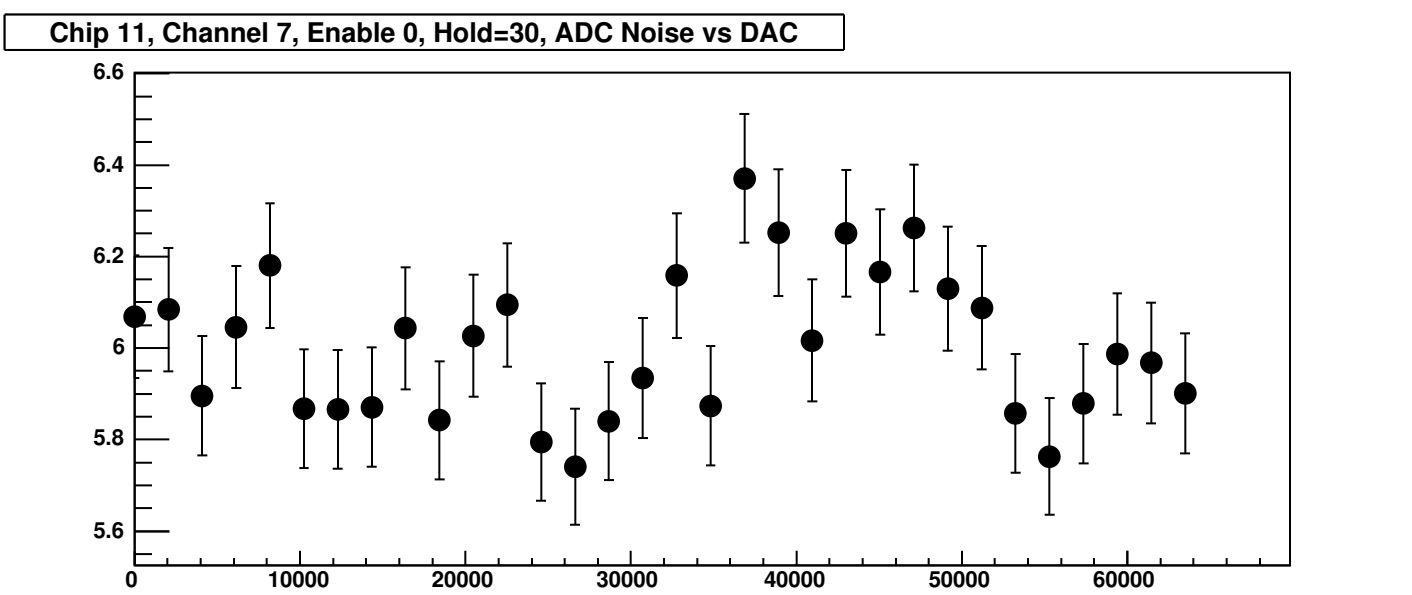

Chip 11, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

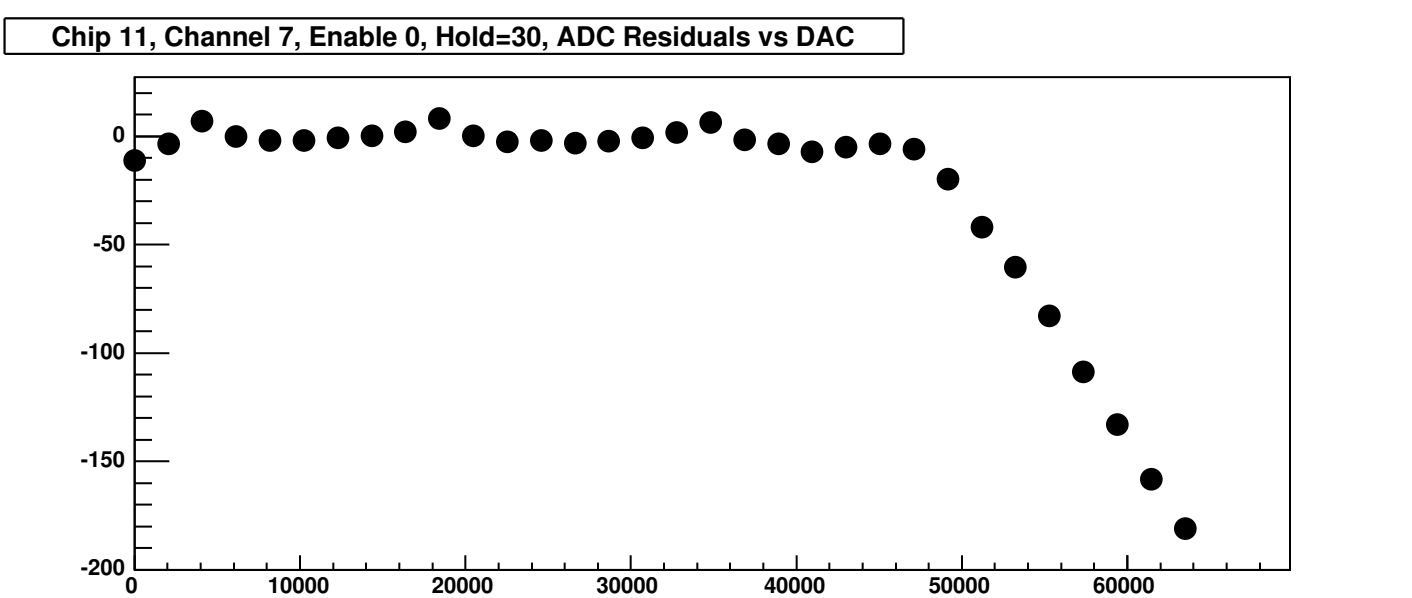

Chip 11, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7581 / 15
p0 1001 ± 0.2828
p1 0.6441 ± 2.902e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

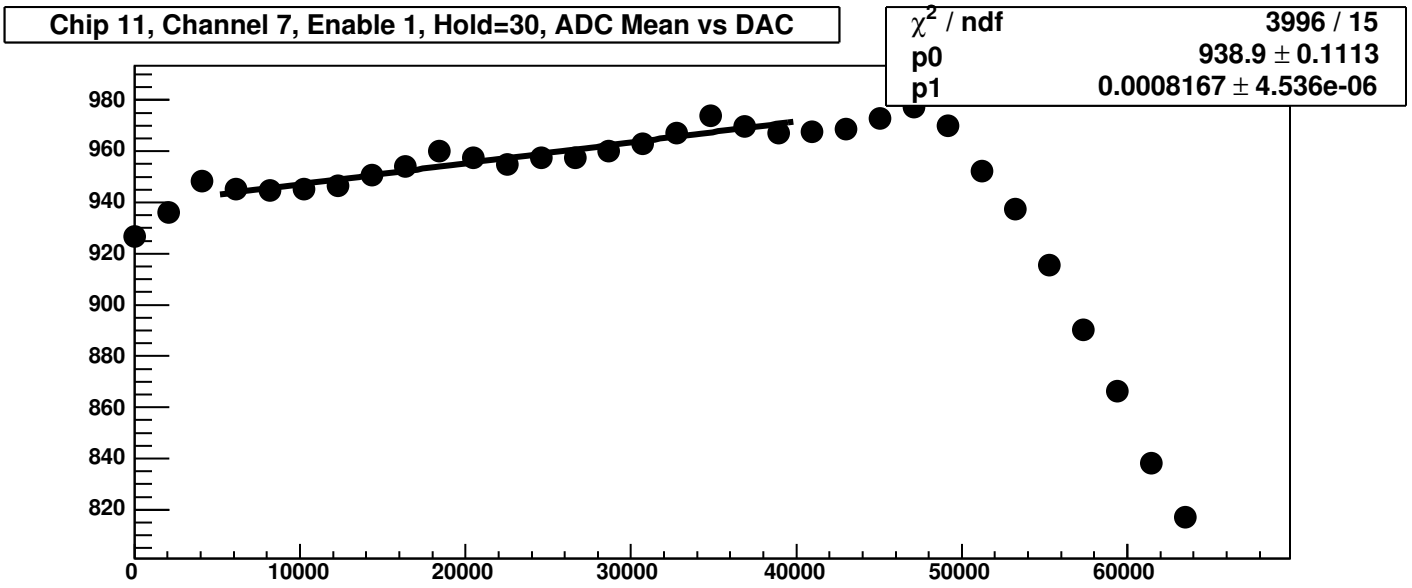

Chip 11, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

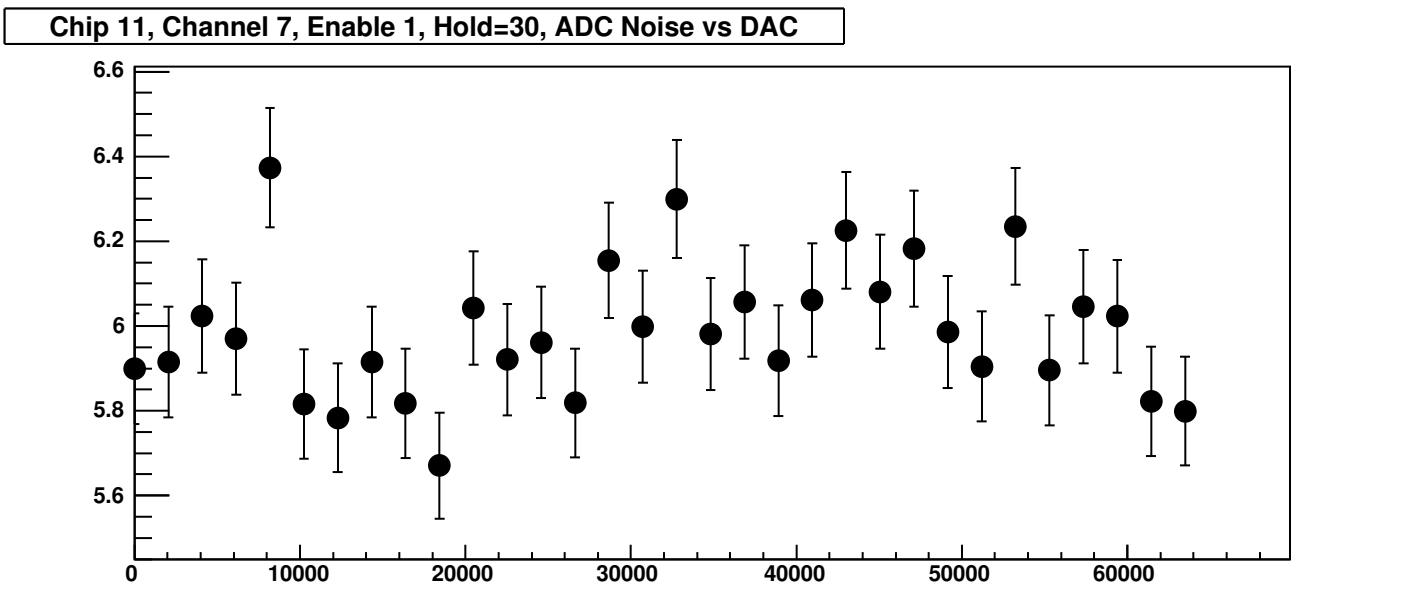

Chip 11, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

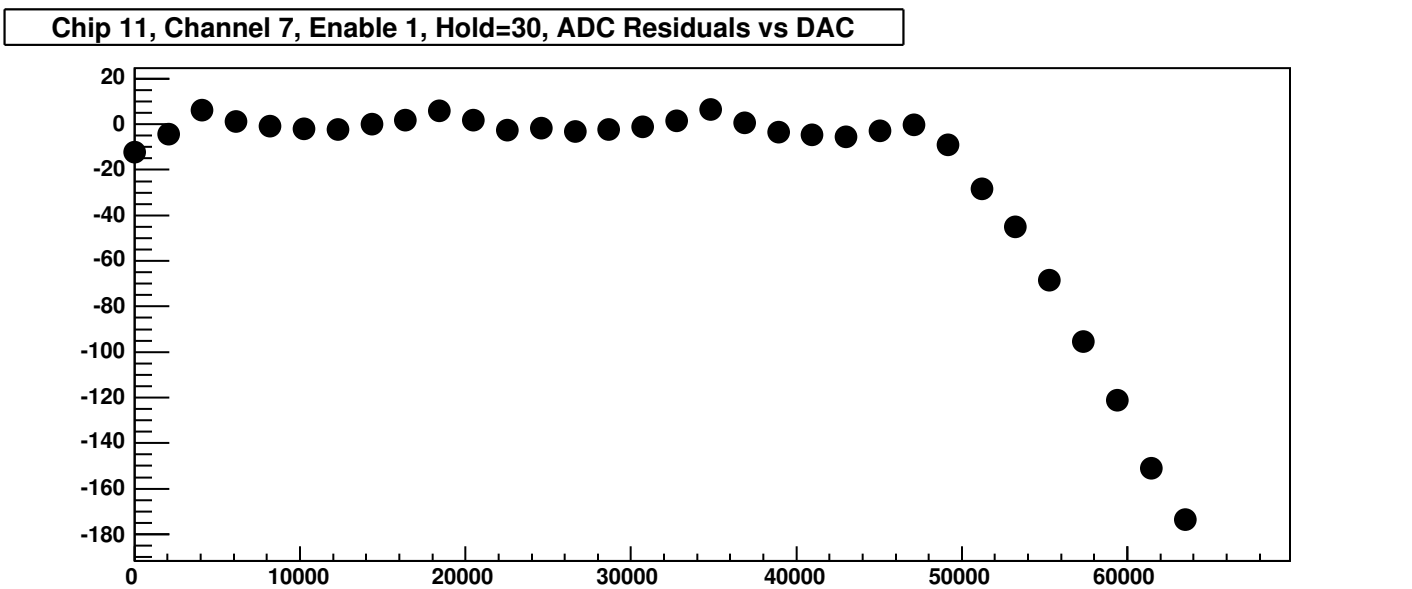

Chip 11, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4732 / 15
p0 938.4 ± 0.1122
p1 0.0007904 ± 4.585e-06
980
960
940
920
900
880
860
840
820
800
0
10000
20000
30000
40000
50000
60000

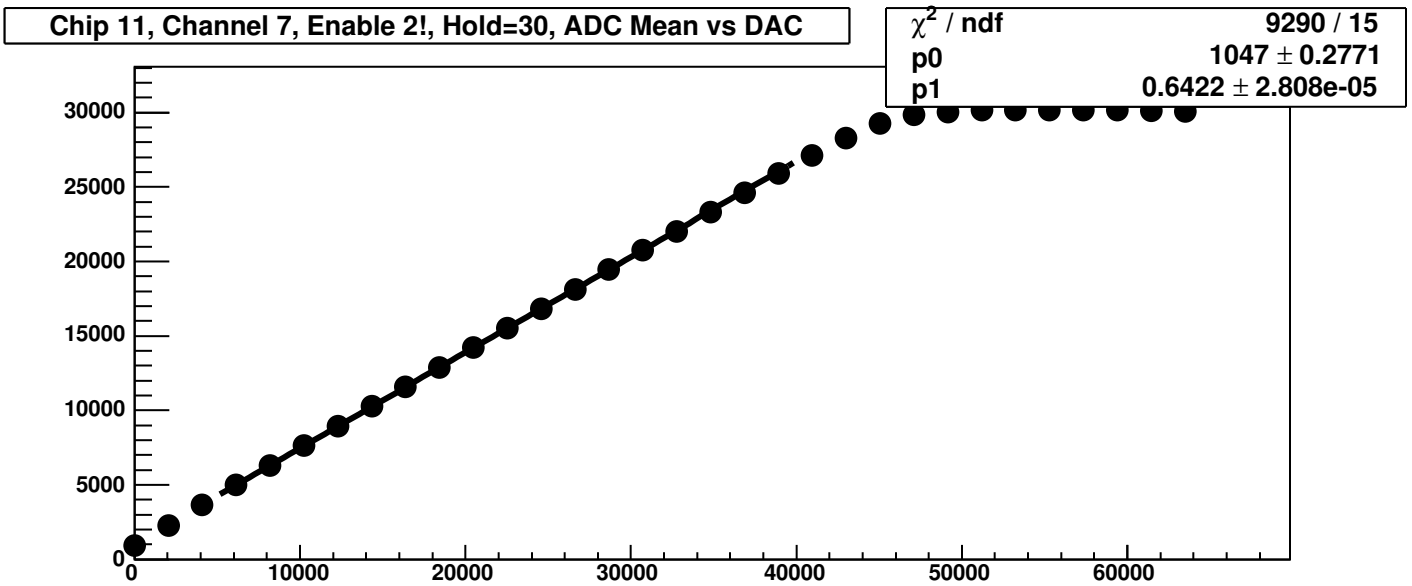

Chip 11, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

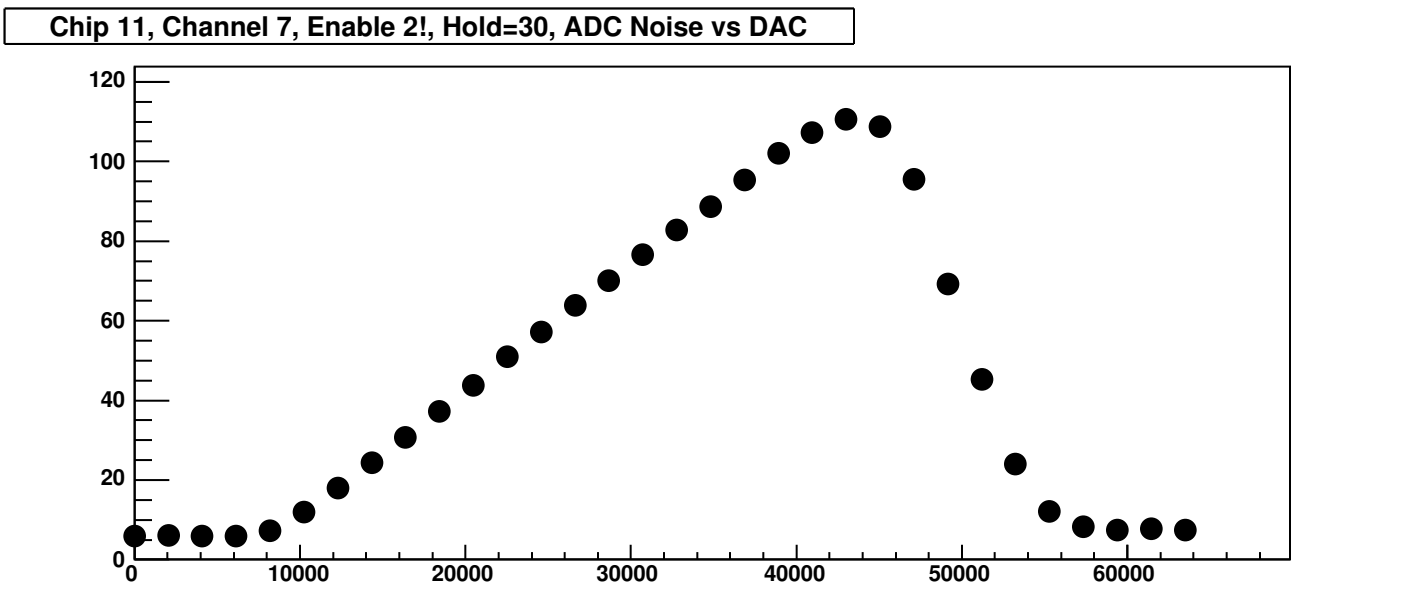

Chip 11, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
0
10000
20000
30000
40000
50000
60000

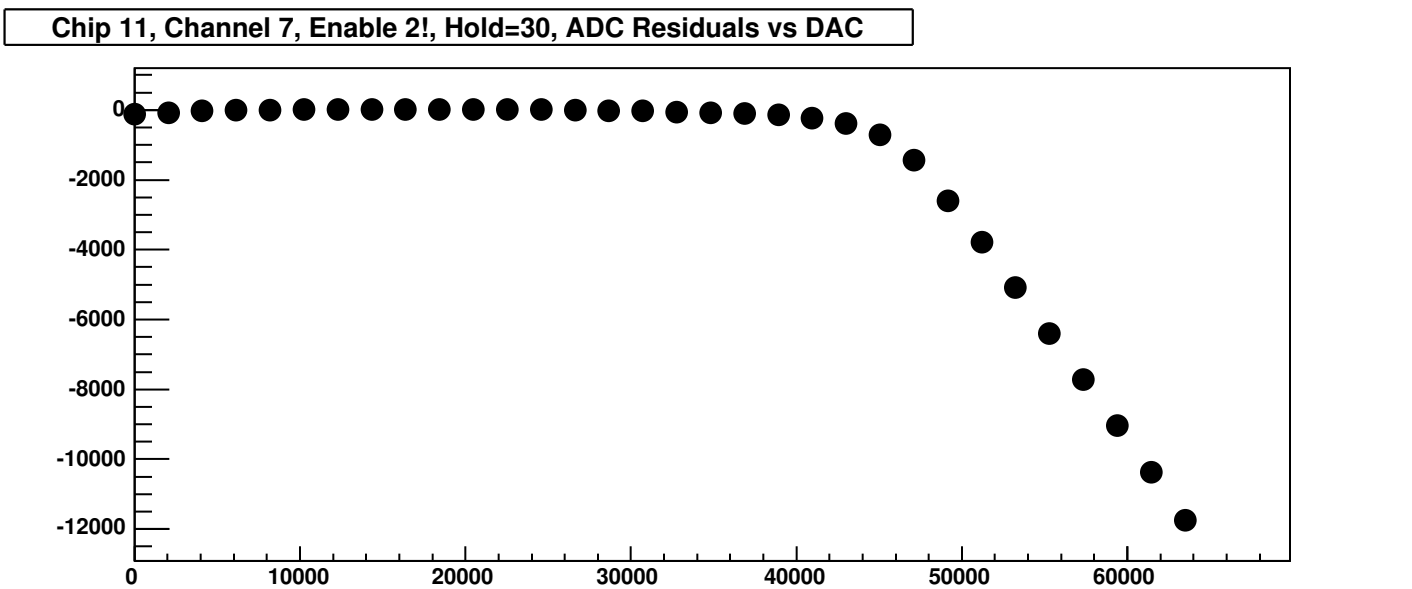

Chip 11, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3996 / 15
p0 938.9 ± 0.1113
p1 0.0008167 ± 4.536e-06
980
960
940
920
900
880
860
840
820
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

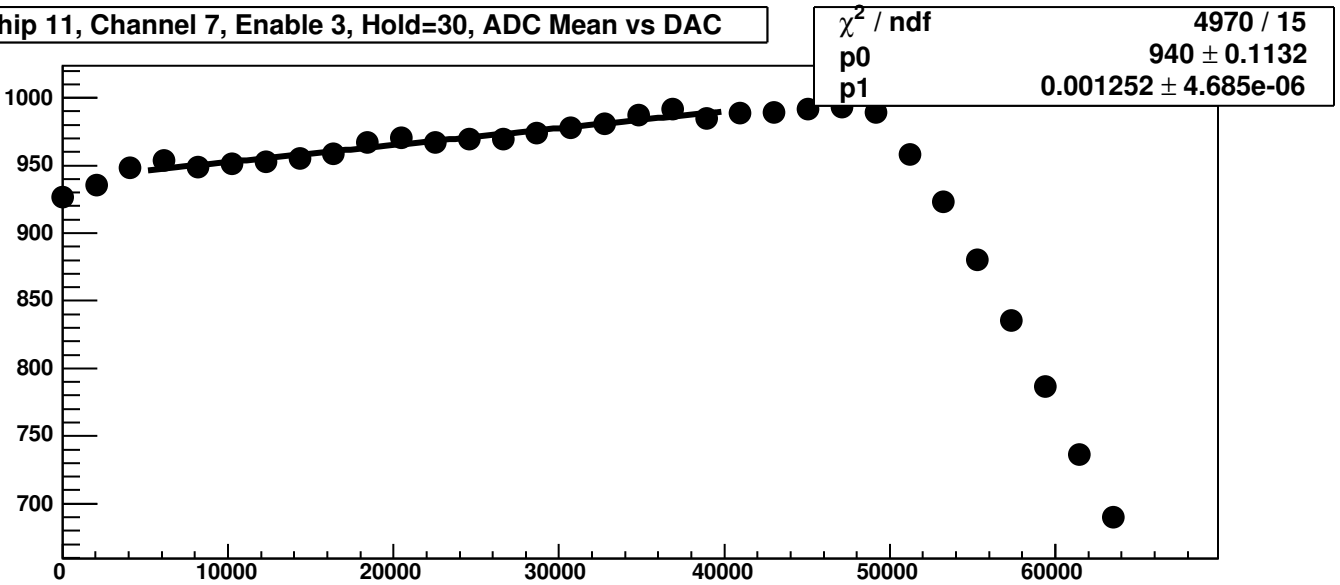

Chip 11, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 9290 / 15
p0 1047 ± 0.2771
p1 0.6422 ± 2.808e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

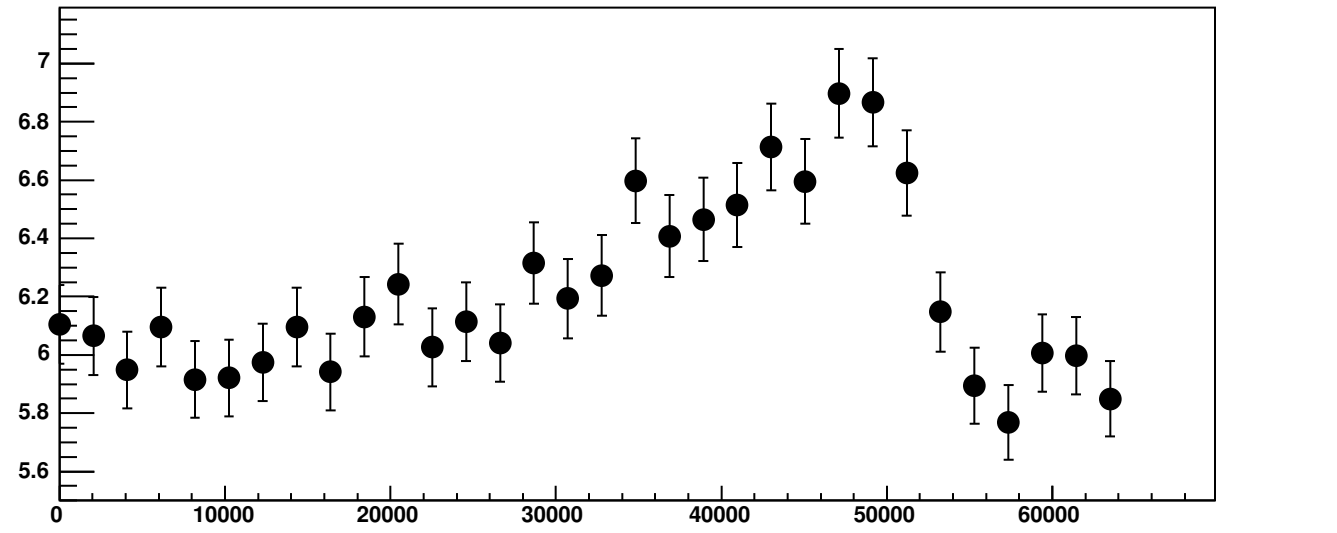

Chip 11, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

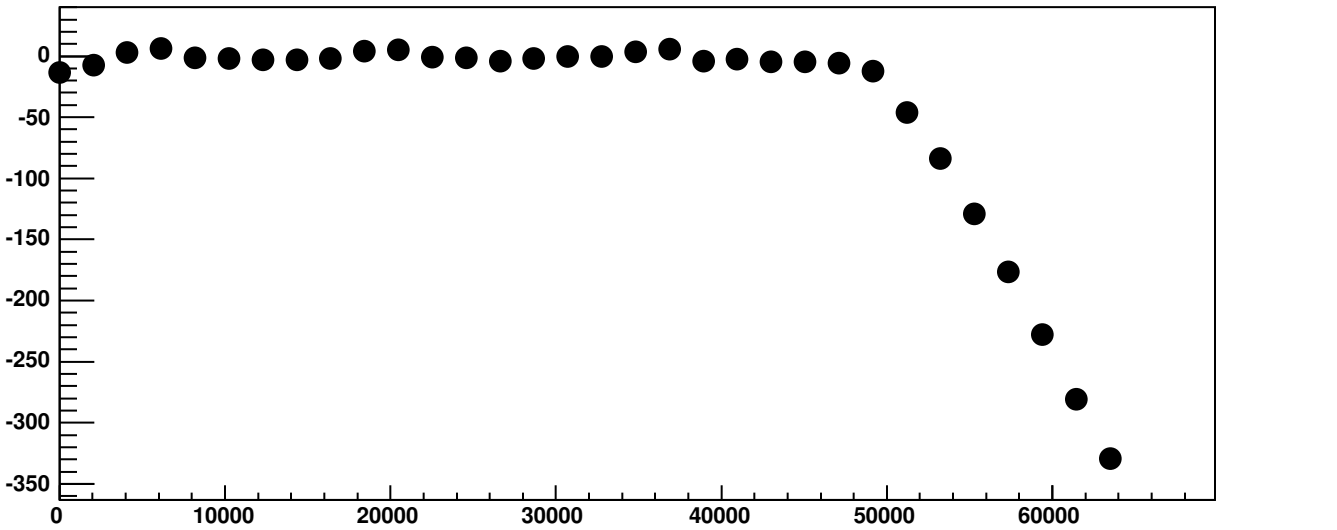
Chip 11, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

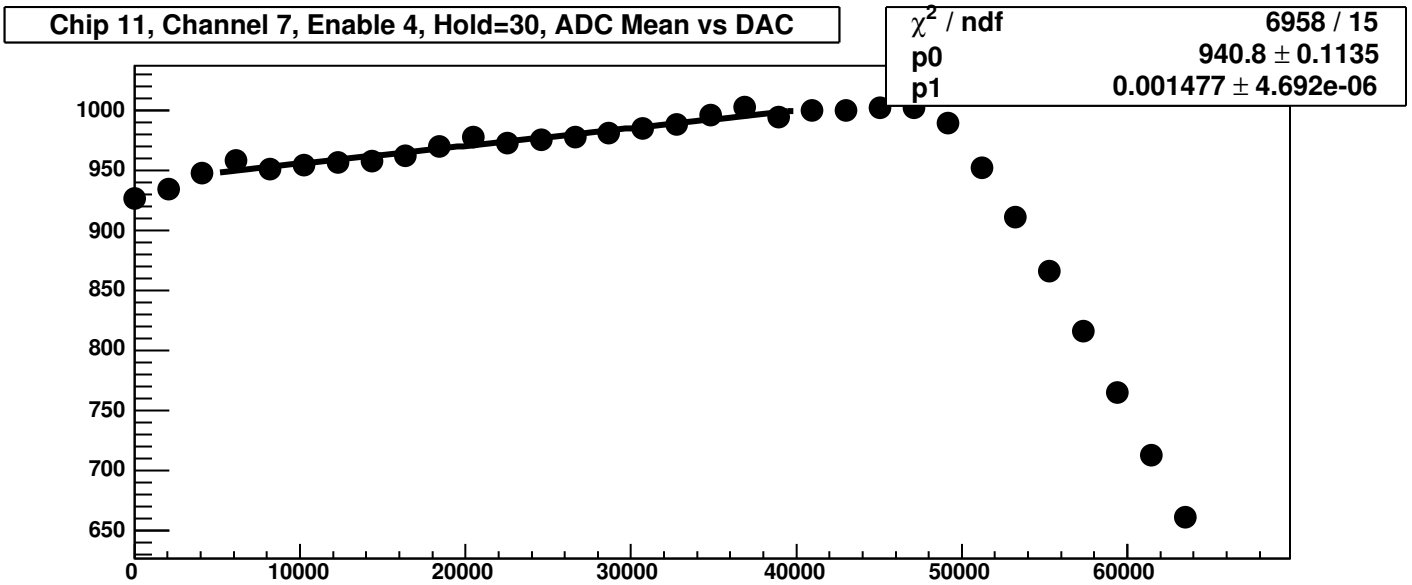


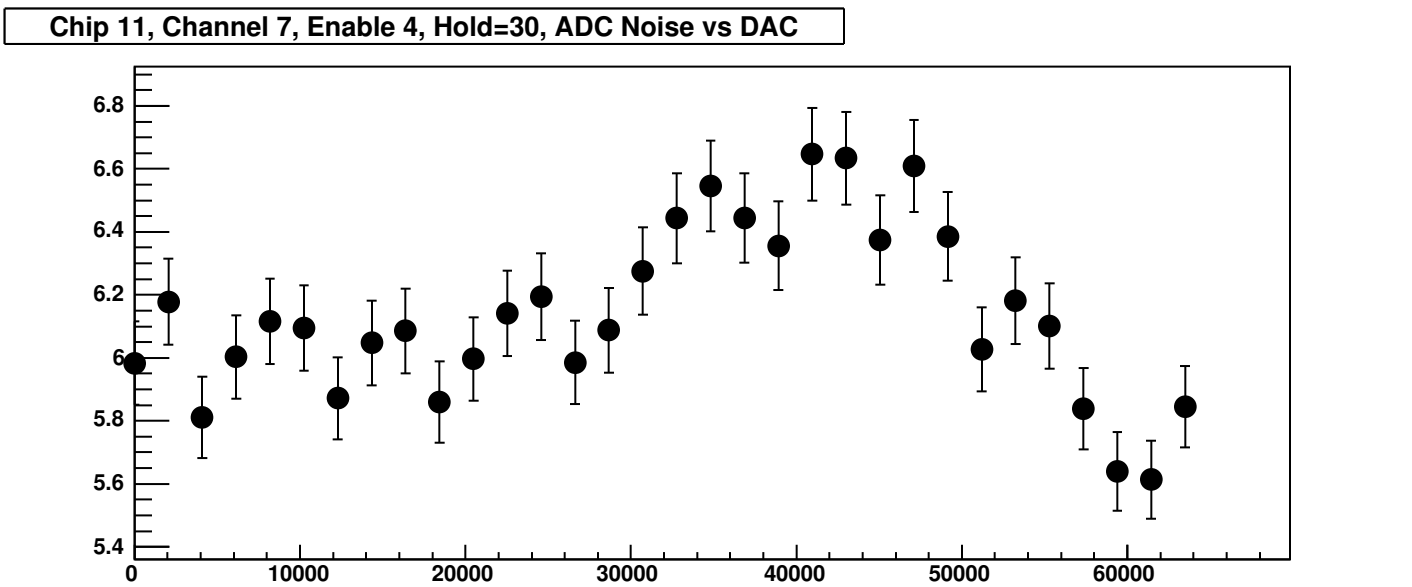
Chip 11, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

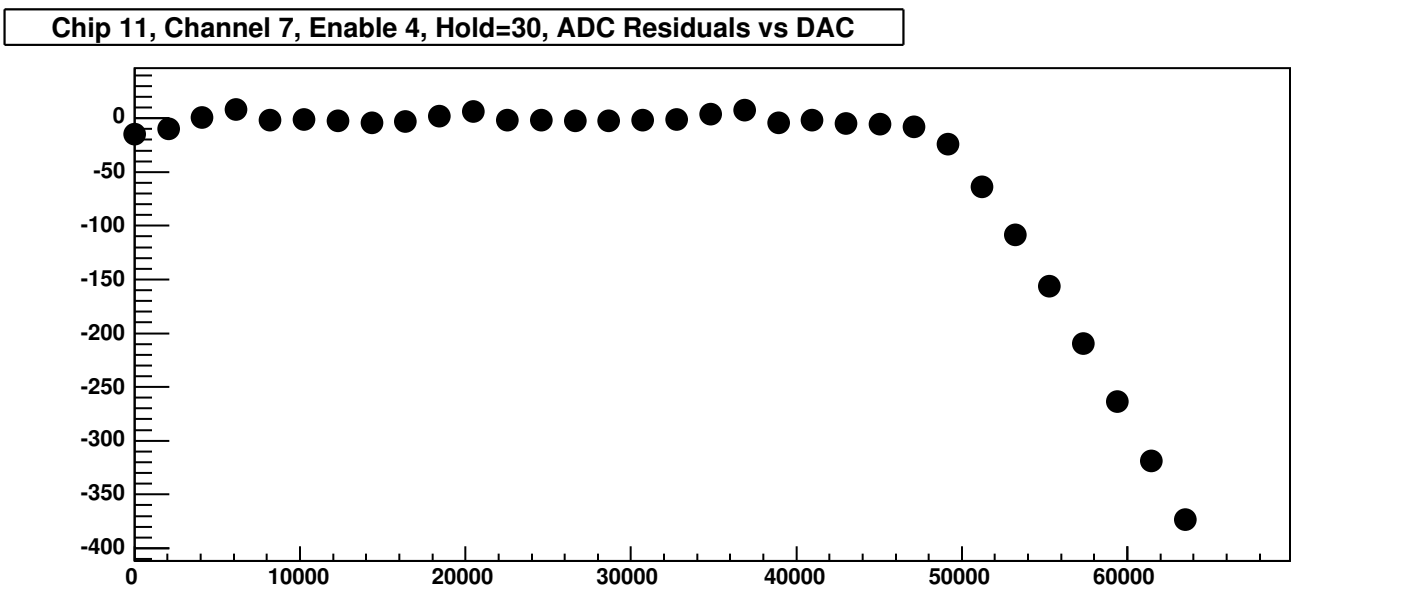


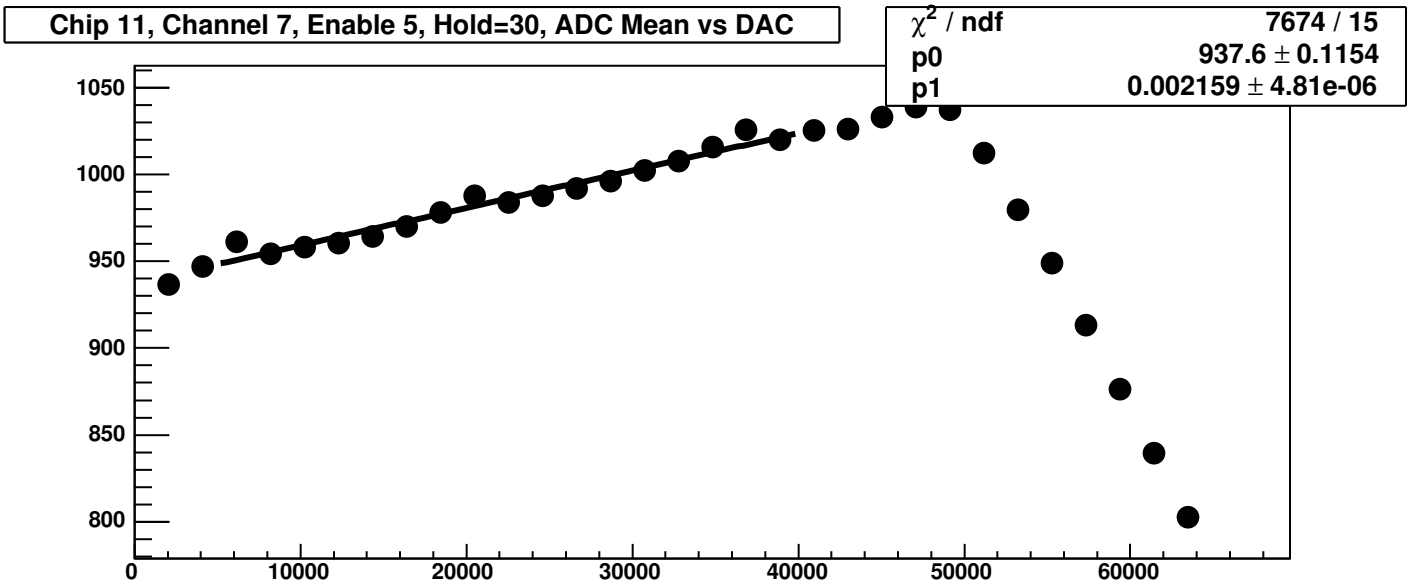
Chip 11, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

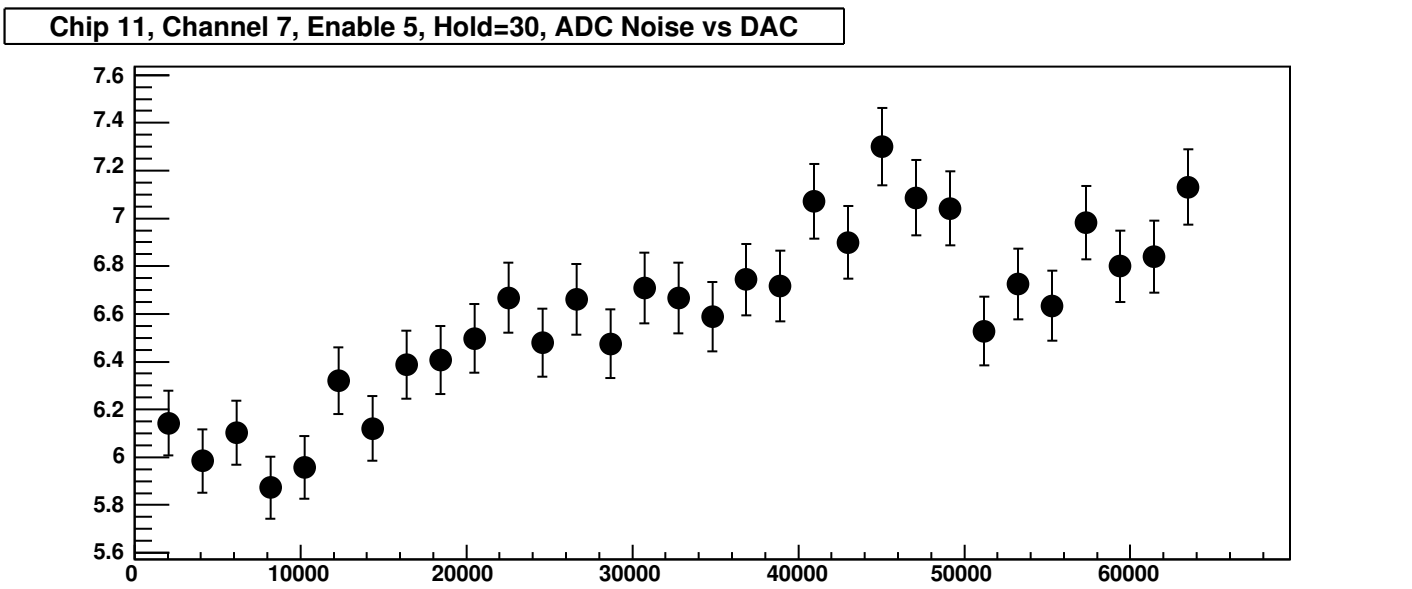


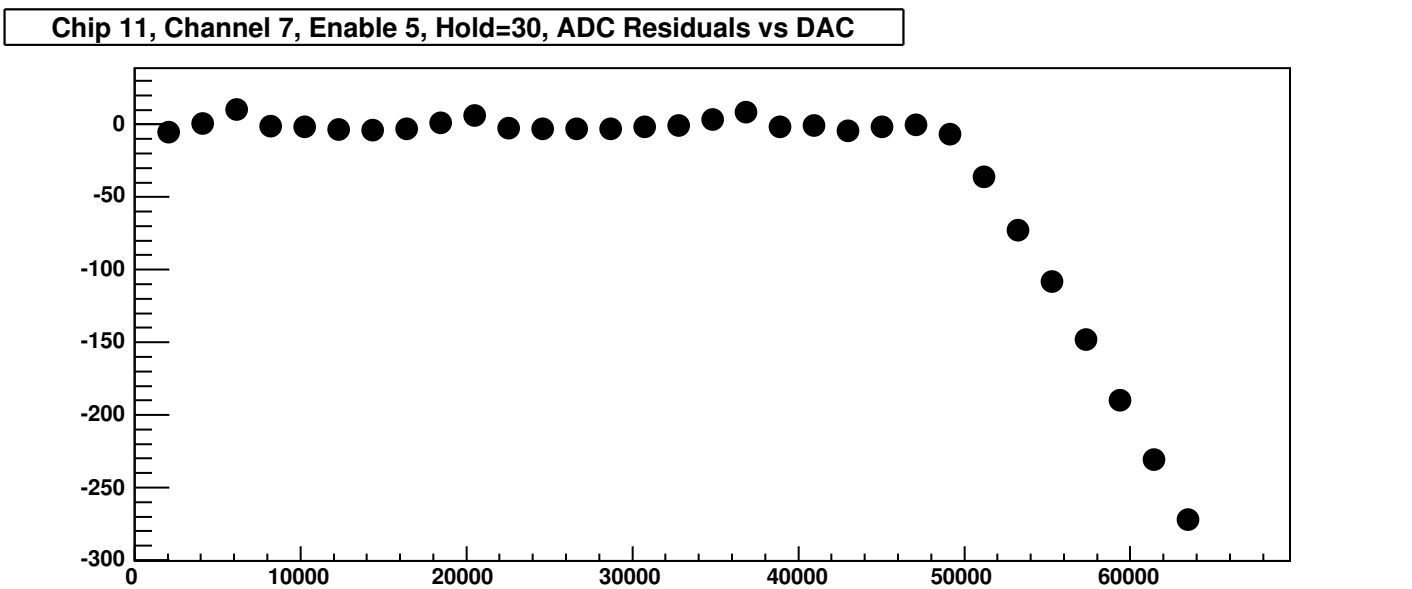

Chip 11, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6958 / 15
p0 940.8 ± 0.1135
p1 0.001477 ± 4.692e-06
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

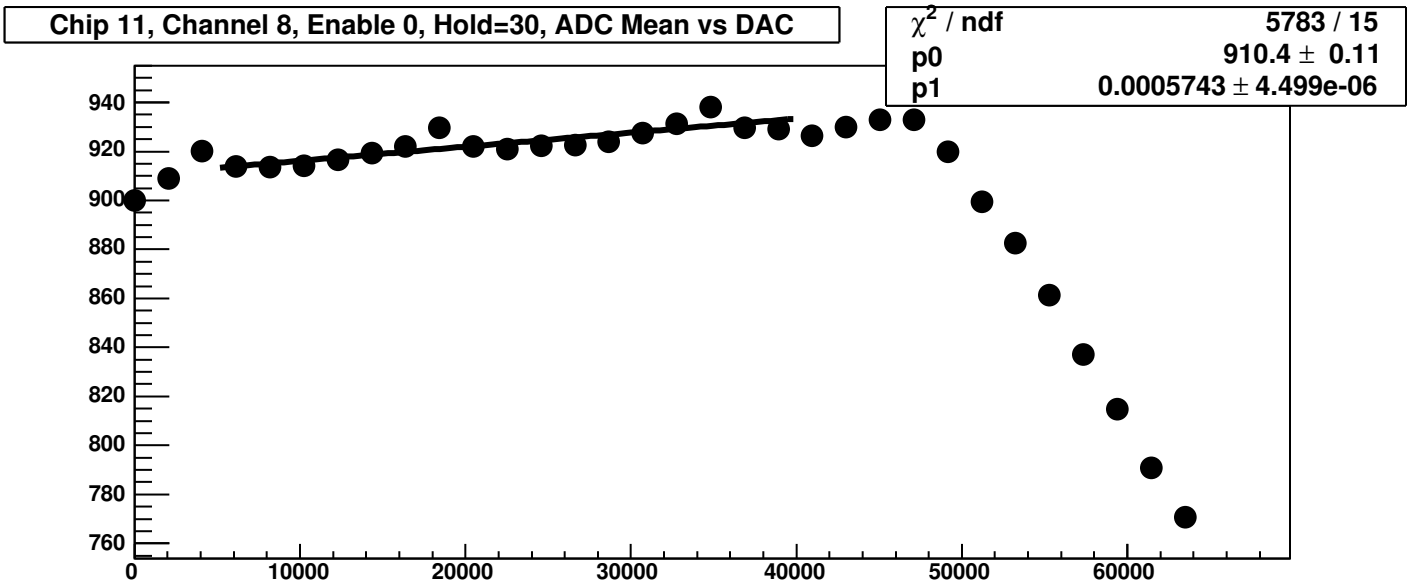

Chip 11, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

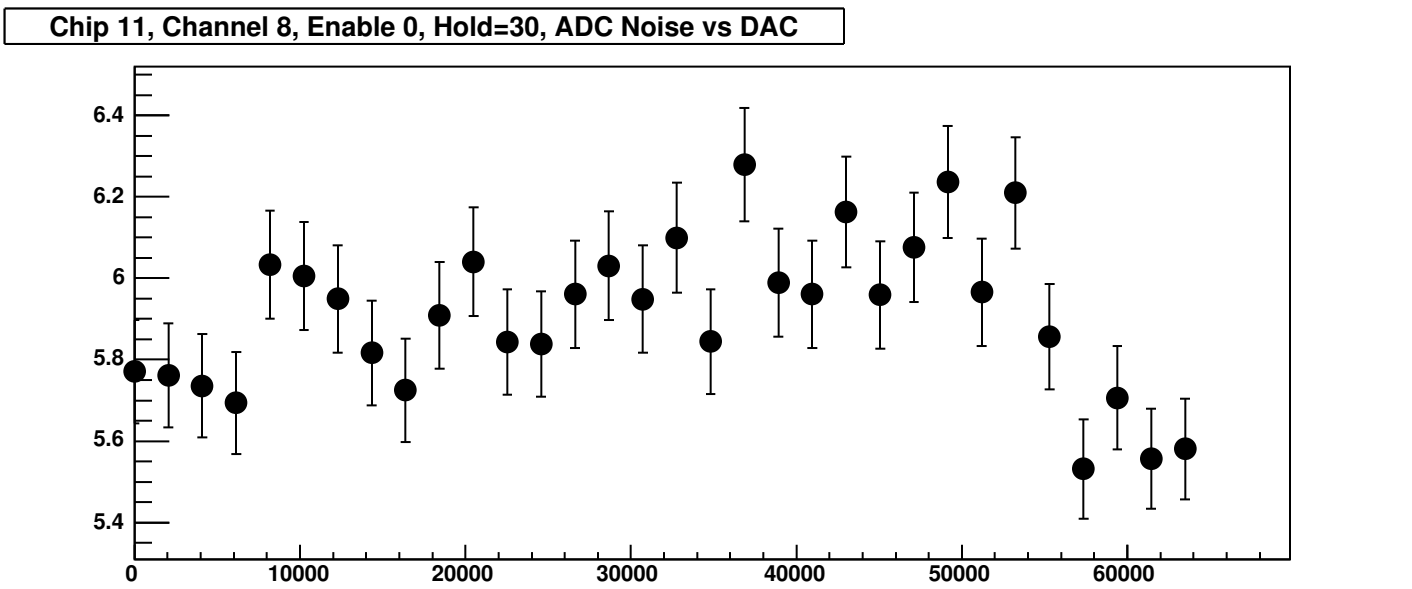

Chip 11, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

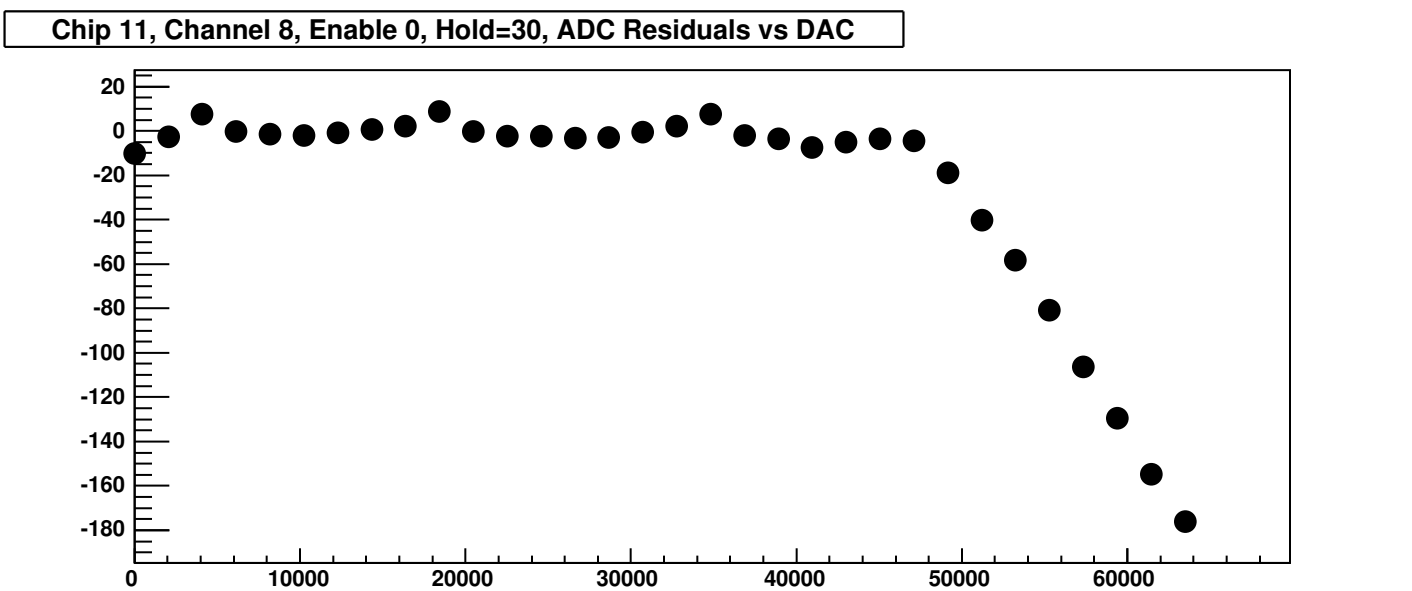

Chip 11, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7674 / 15
p0 937.6 ± 0.1154
p1 0.002159 ± 4.81e-06
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

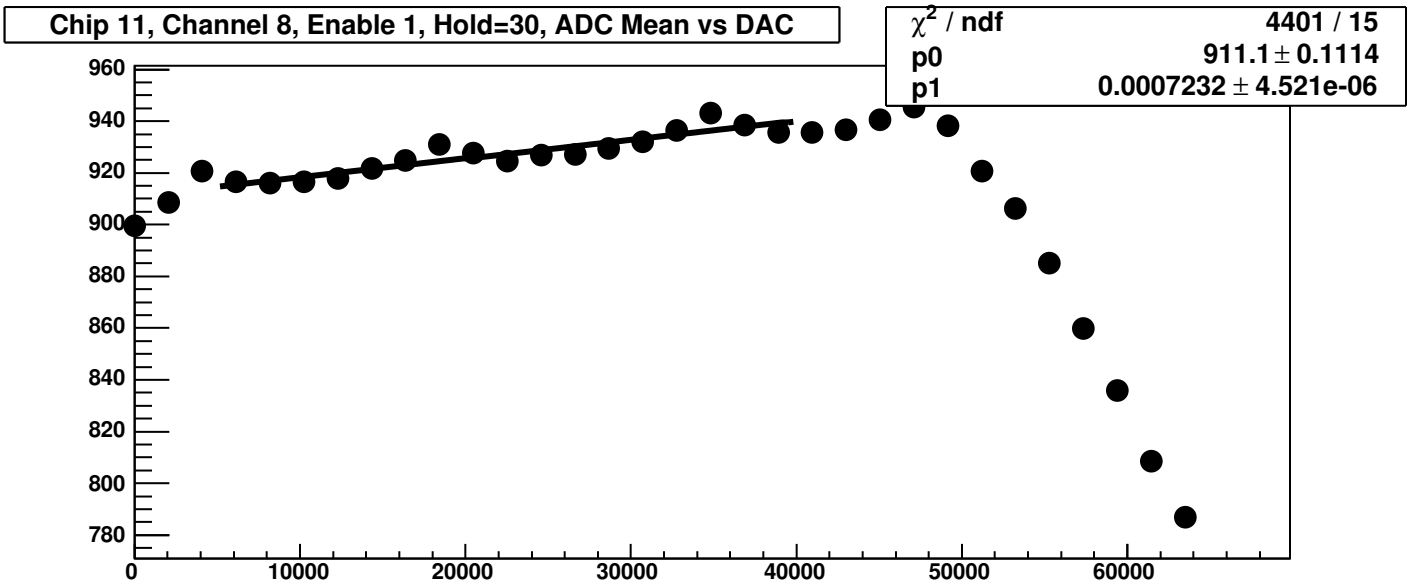

Chip 11, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

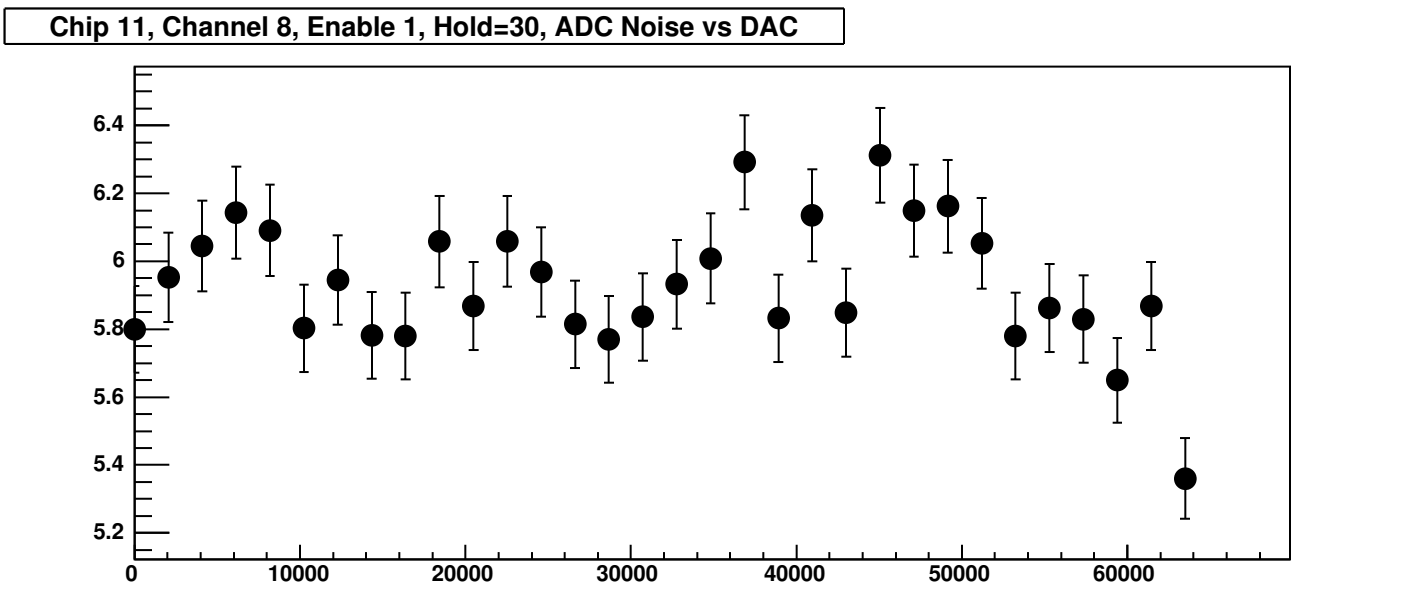

Chip 11, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

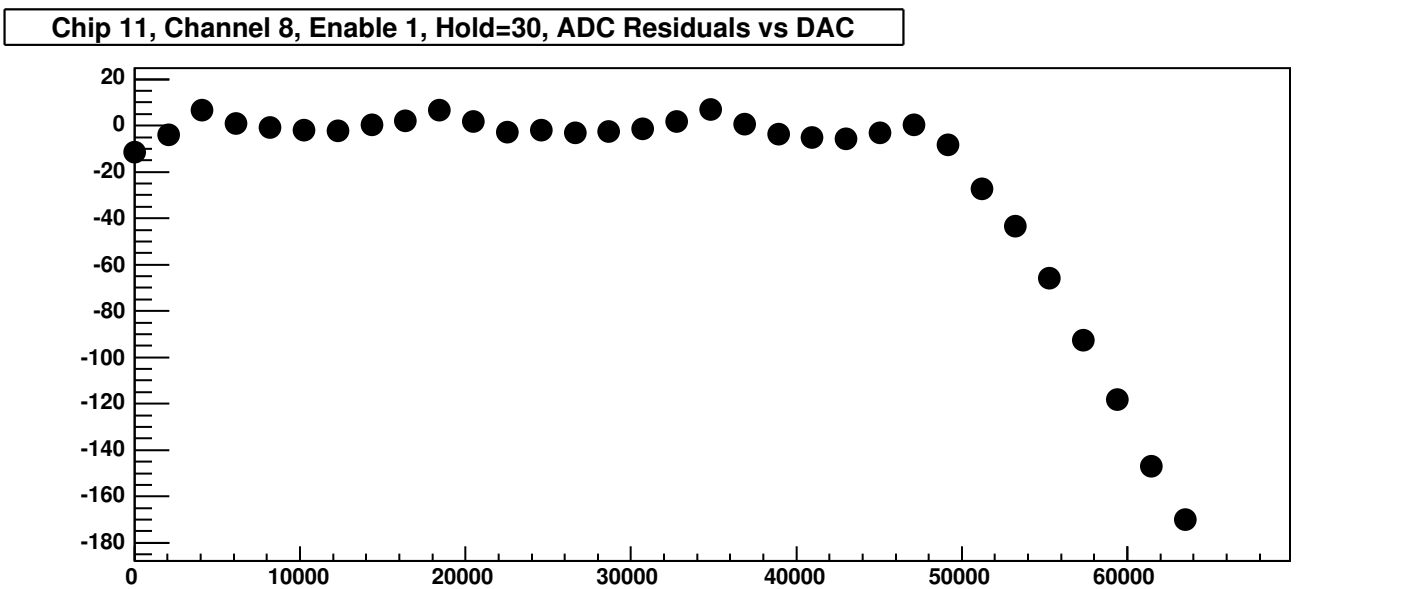

Chip 11, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 5783 / 15
p0 910.4 ± 0.11
p1 0.0005743 ± 4.499e-06
940
920
900
880
860
840
820
800
780
760
0
10000
20000
30000
40000
50000
60000

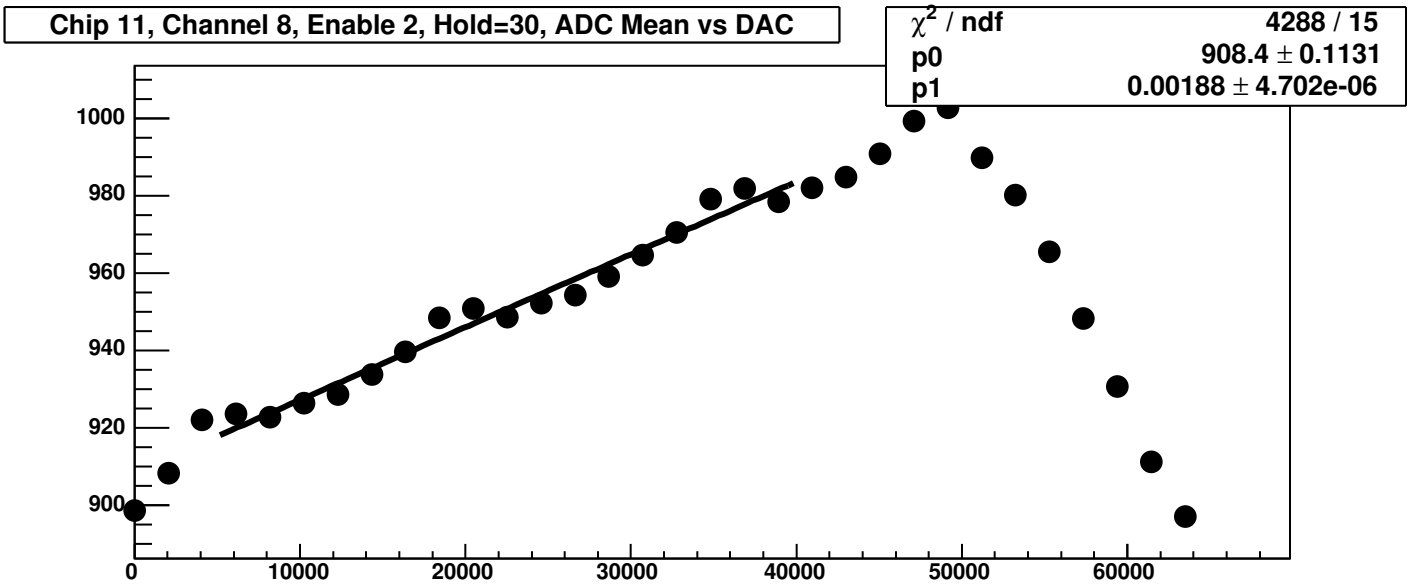

Chip 11, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

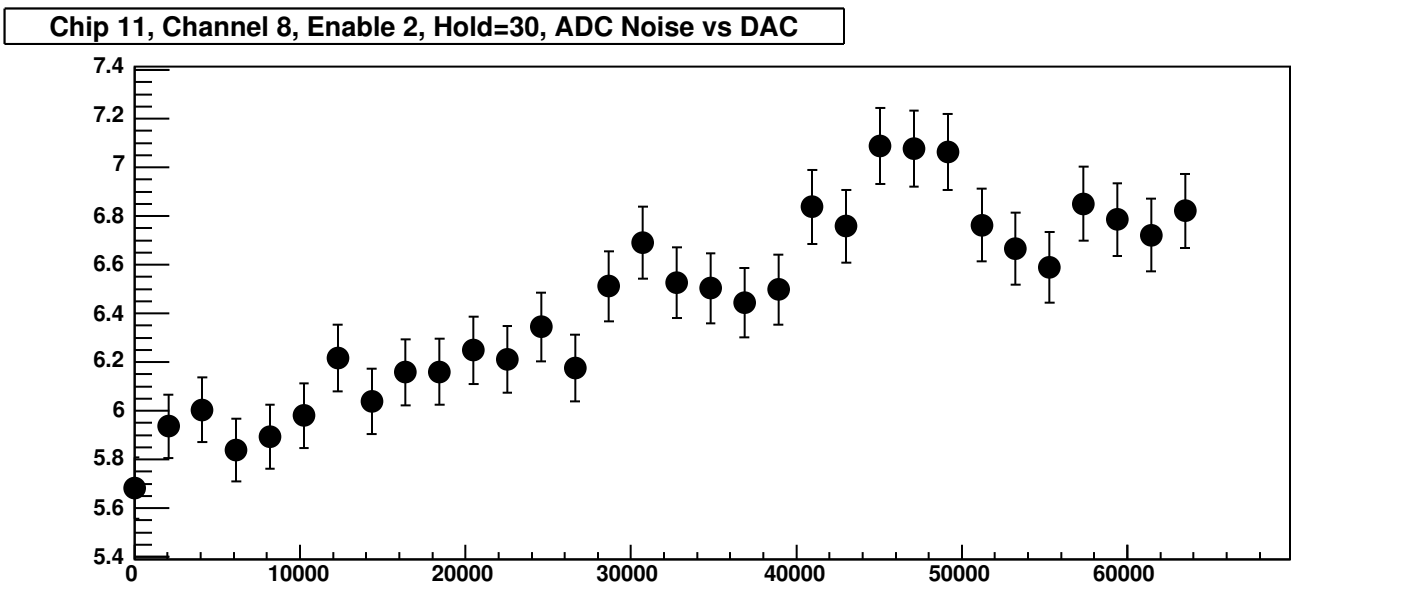

Chip 11, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

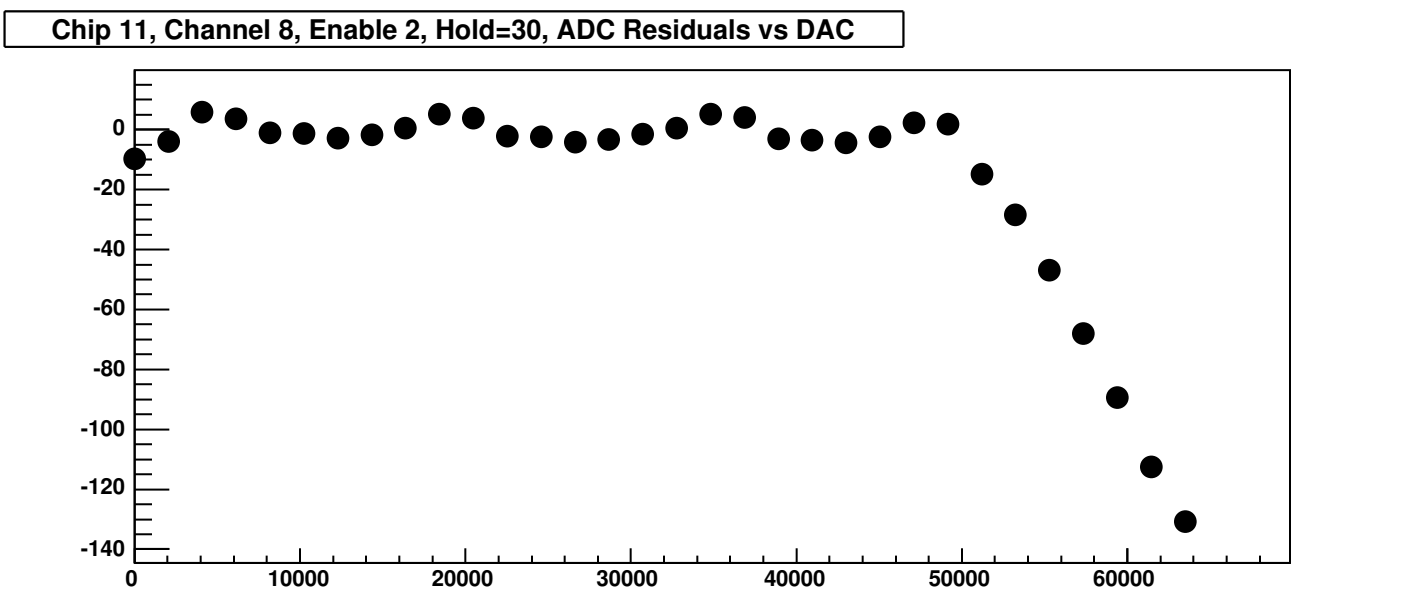

Chip 11, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4401 / 15
p0 911.1 ± 0.1114
p1 0.0007232 ± 4.521e-06
960
940
920
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

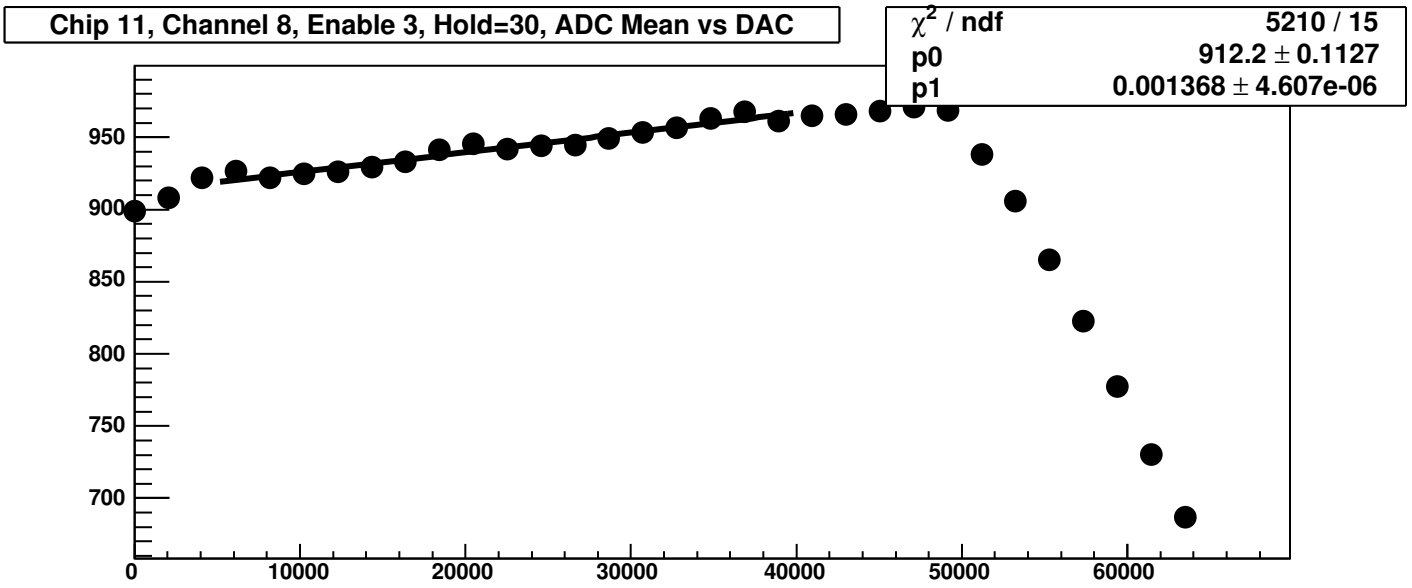

Chip 11, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
5.2
0
10000
20000
30000
40000
50000
60000

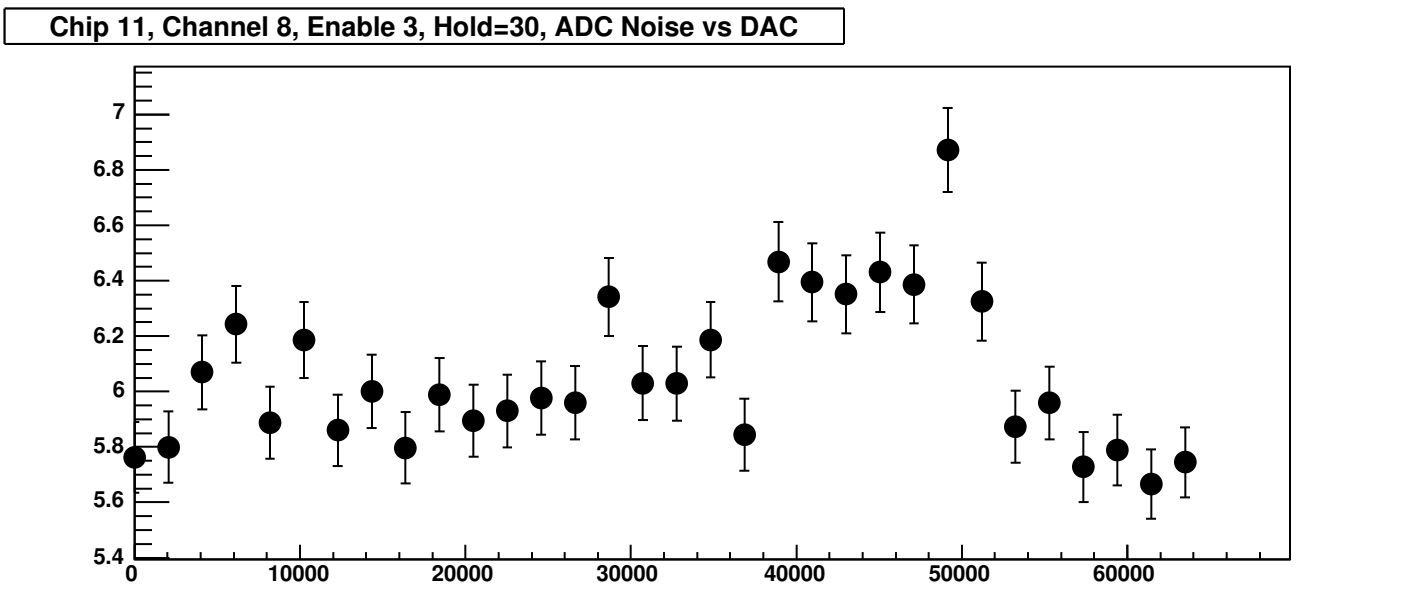

Chip 11, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

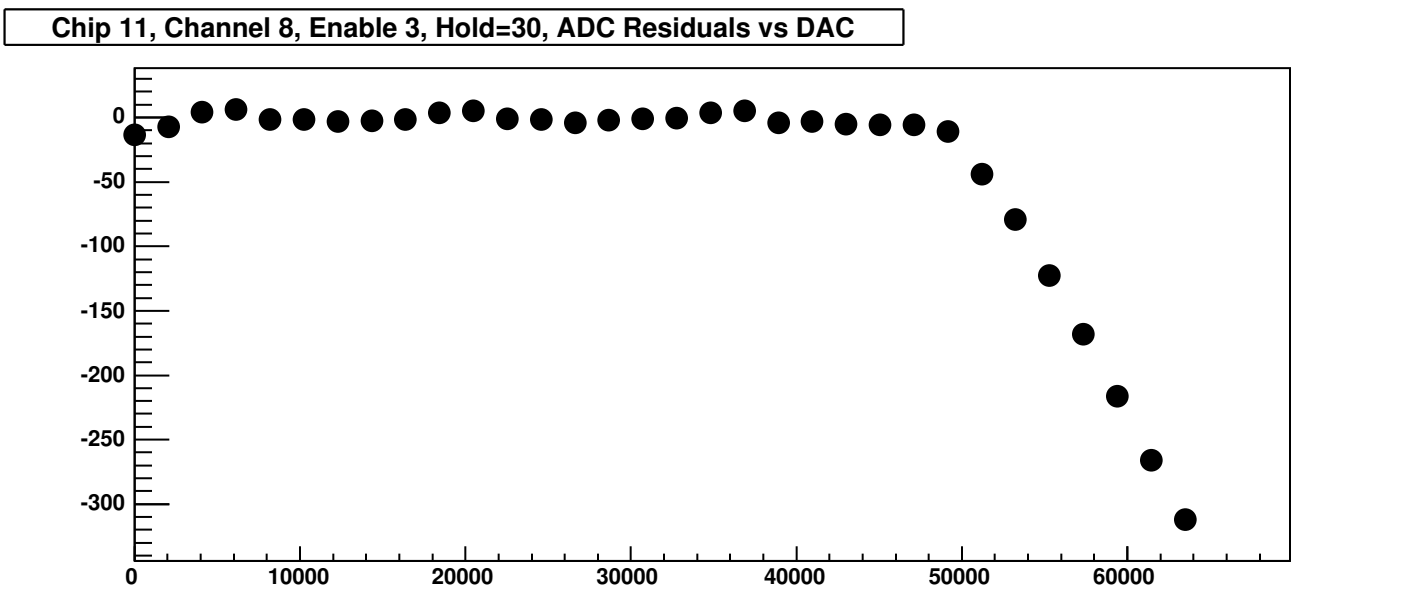

Chip 11, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4288 / 15
p0 908.4 ± 0.1131
p1 0.00188 ± 4.702e-06
1000
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000

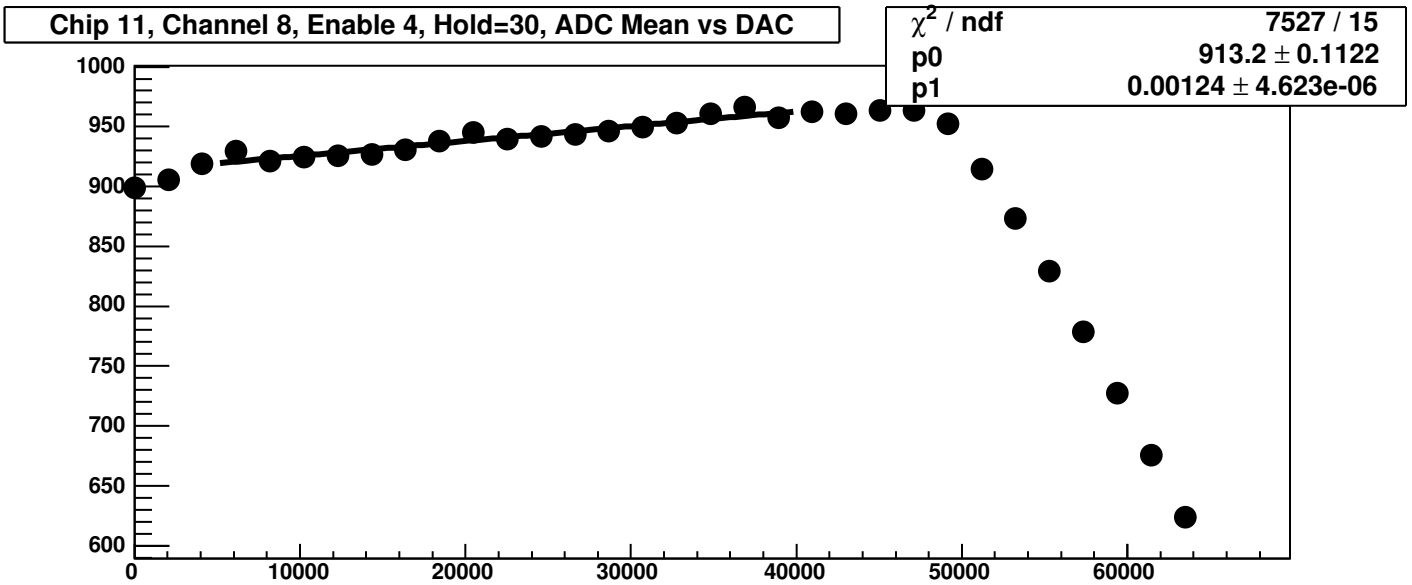

Chip 11, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

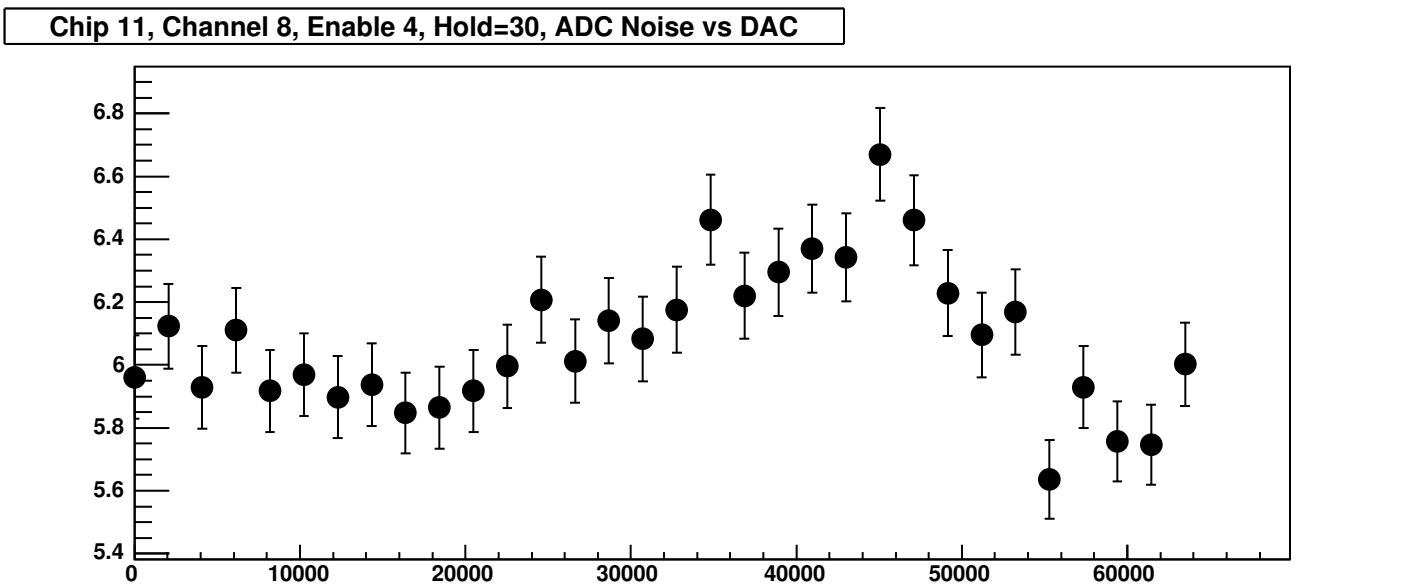

Chip 11, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

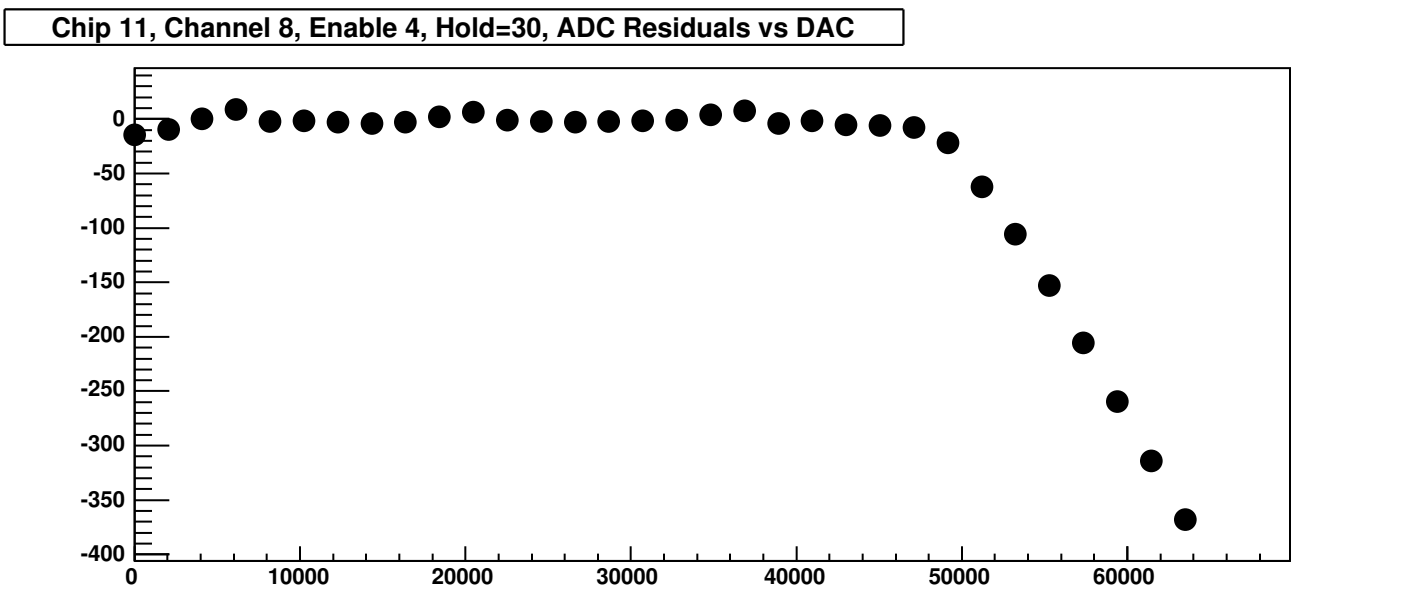

Chip 11, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5210 / 15
p0 912.2 ± 0.1127
p1 0.001368 ± 4.607e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

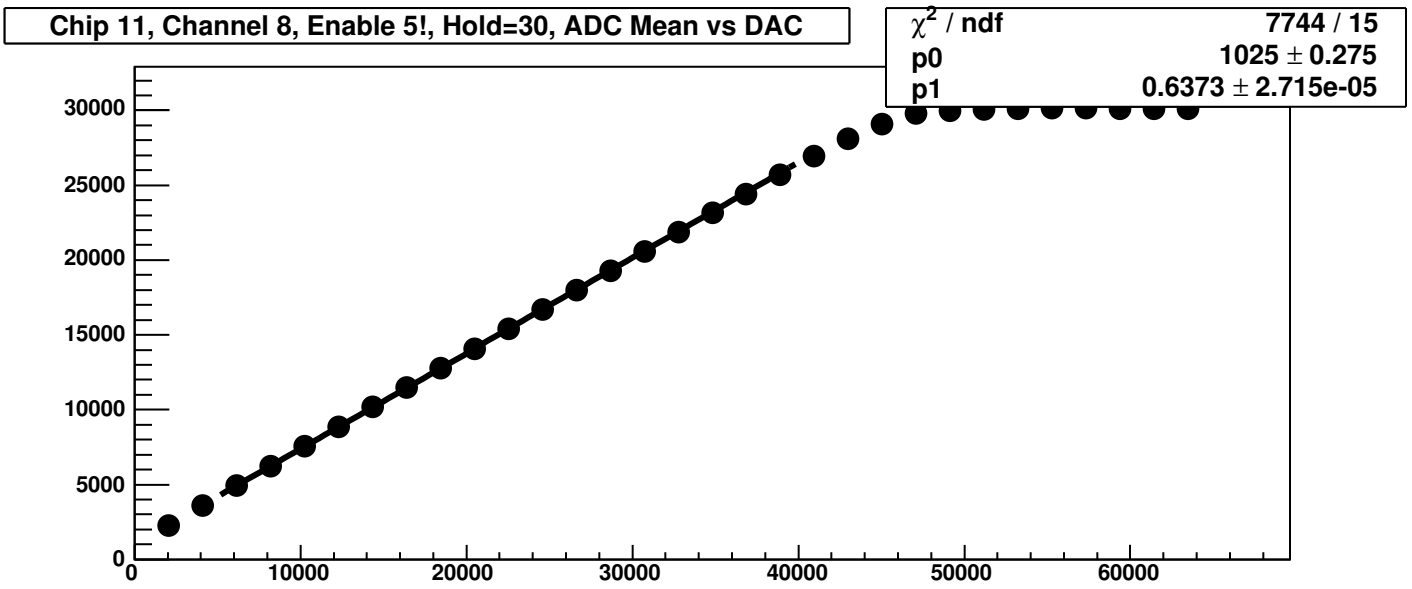

Chip 11, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

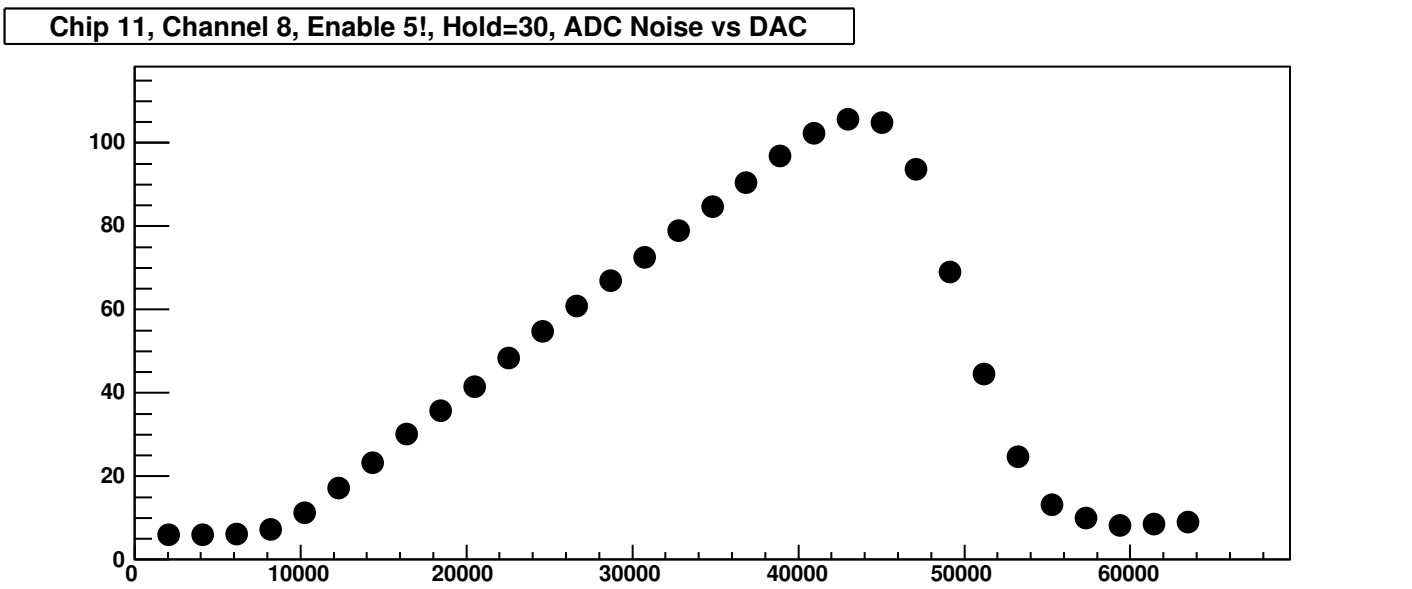

Chip 11, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

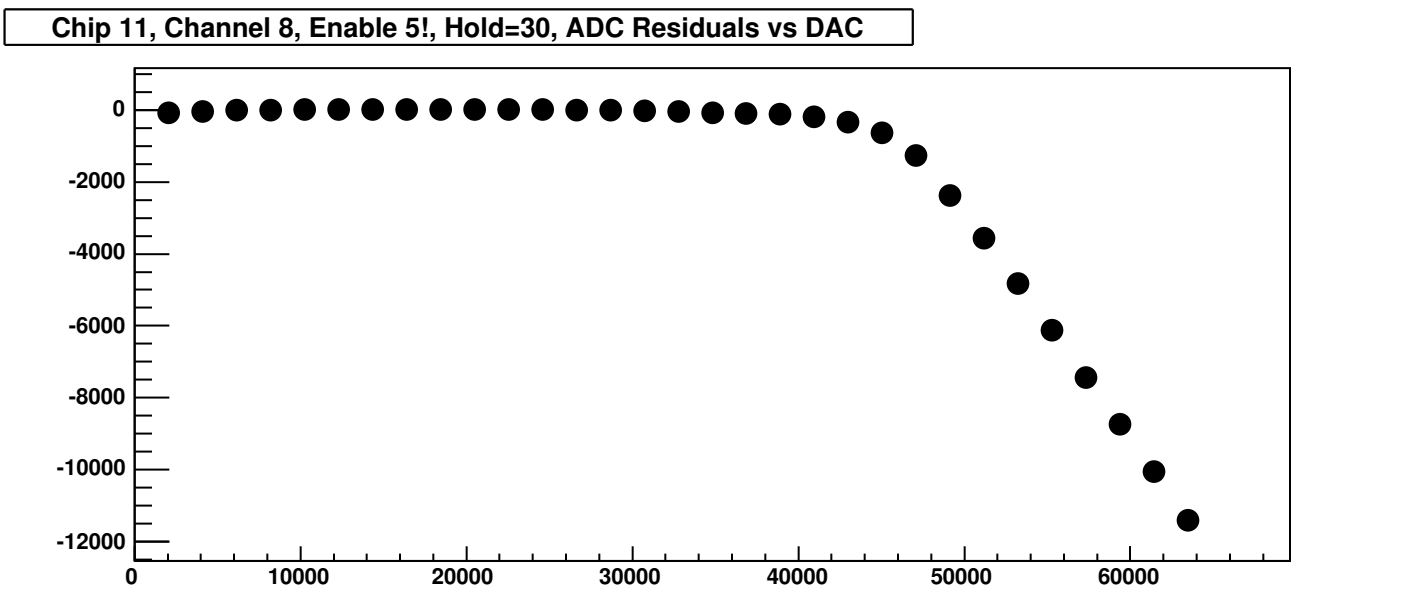

Chip 11, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7527 / 15
p0 913.2 ± 0.1122
p1 0.00124 ± 4.623e-06
1000
950
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

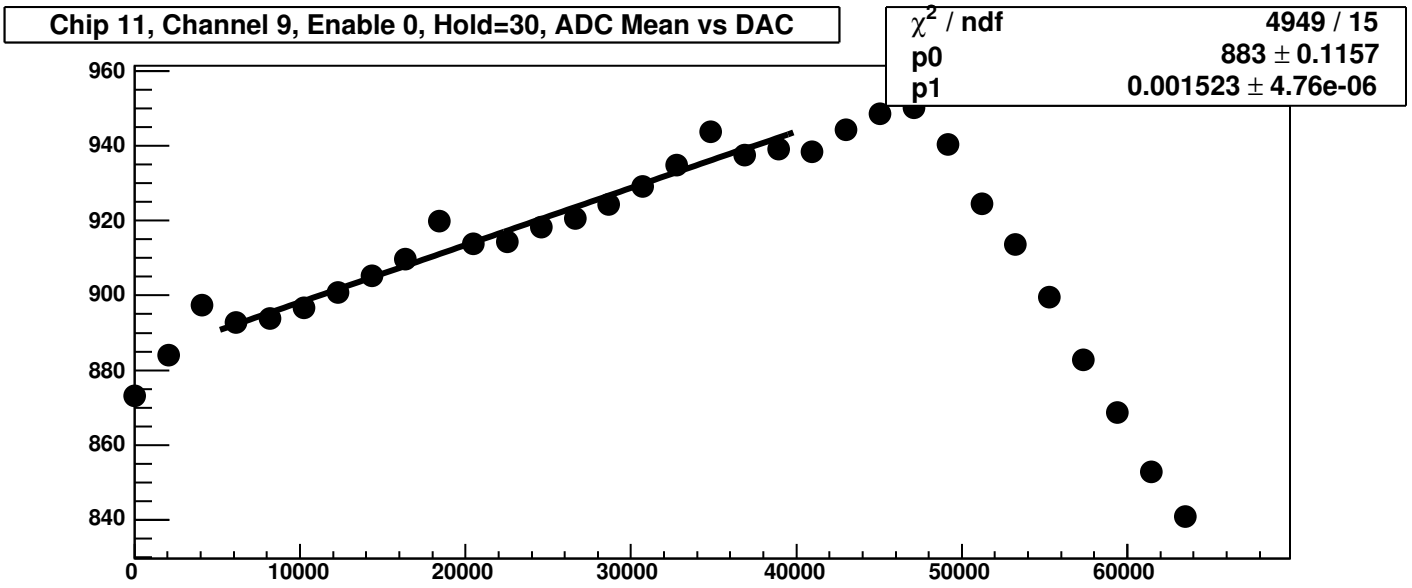

Chip 11, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

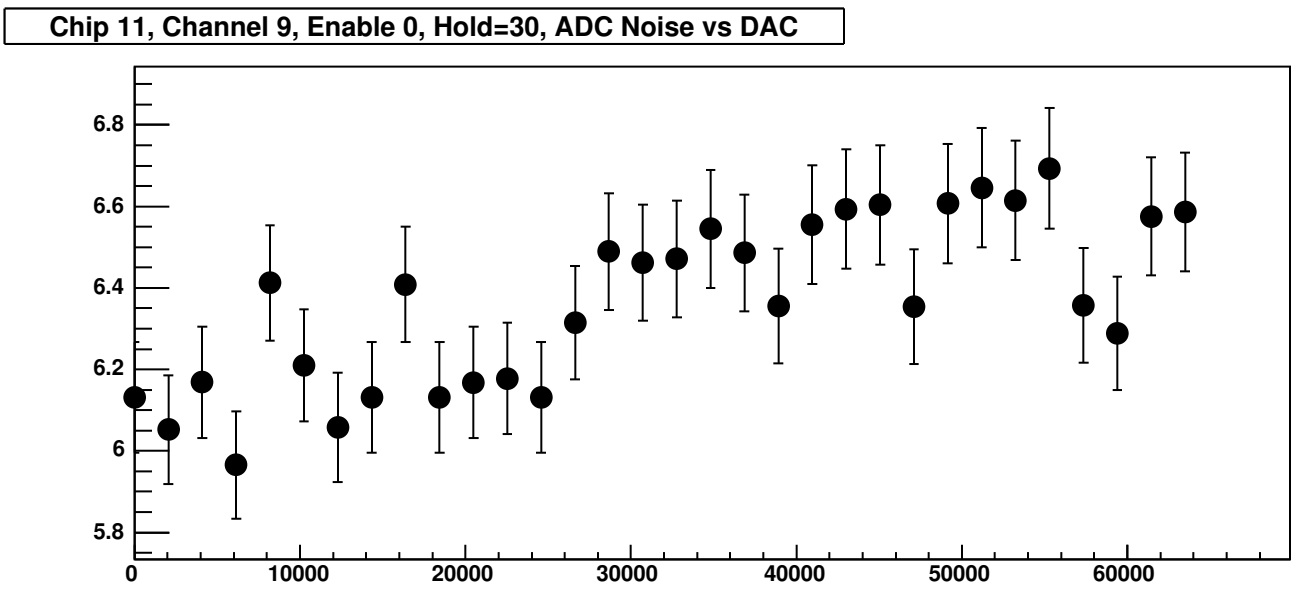

Chip 11, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

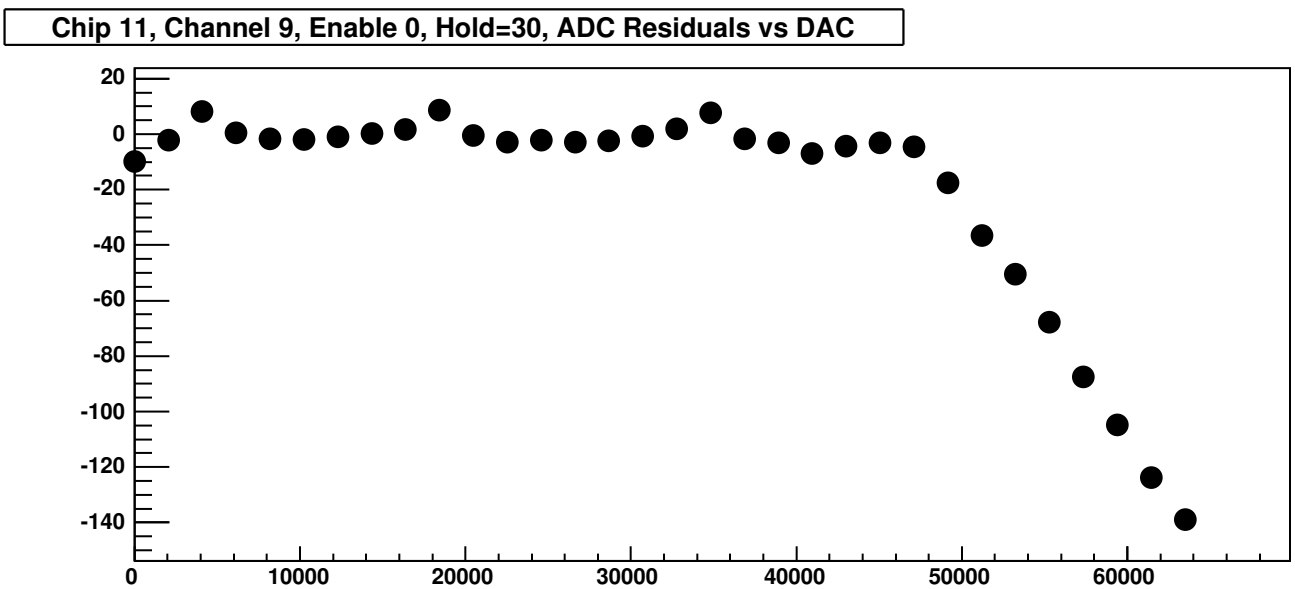

Chip 11, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7744 / 15
p0 1025 ± 0.275
p1 0.6373 ± 2.715e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

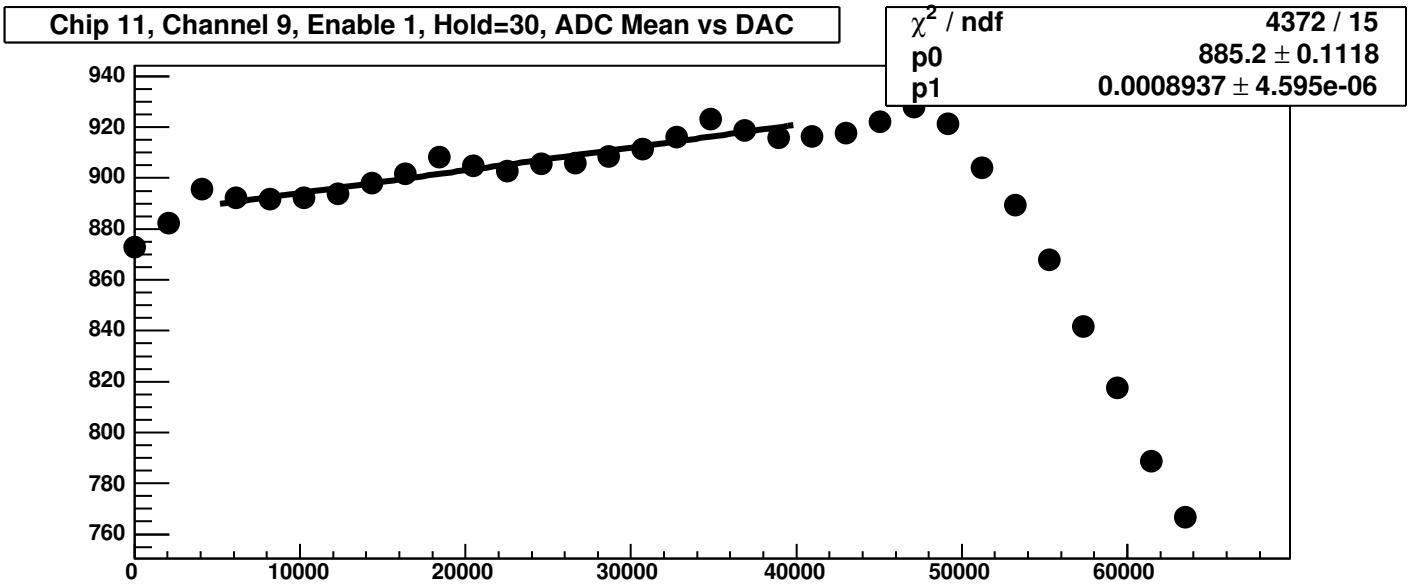

Chip 11, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

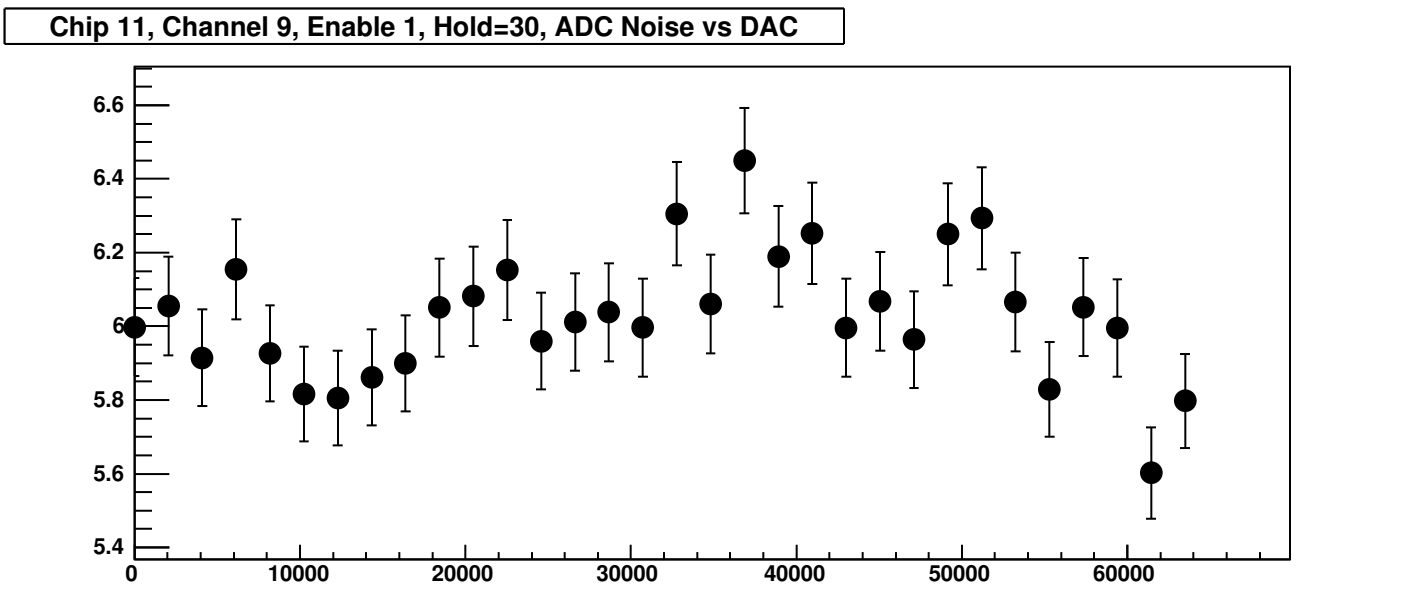

Chip 11, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

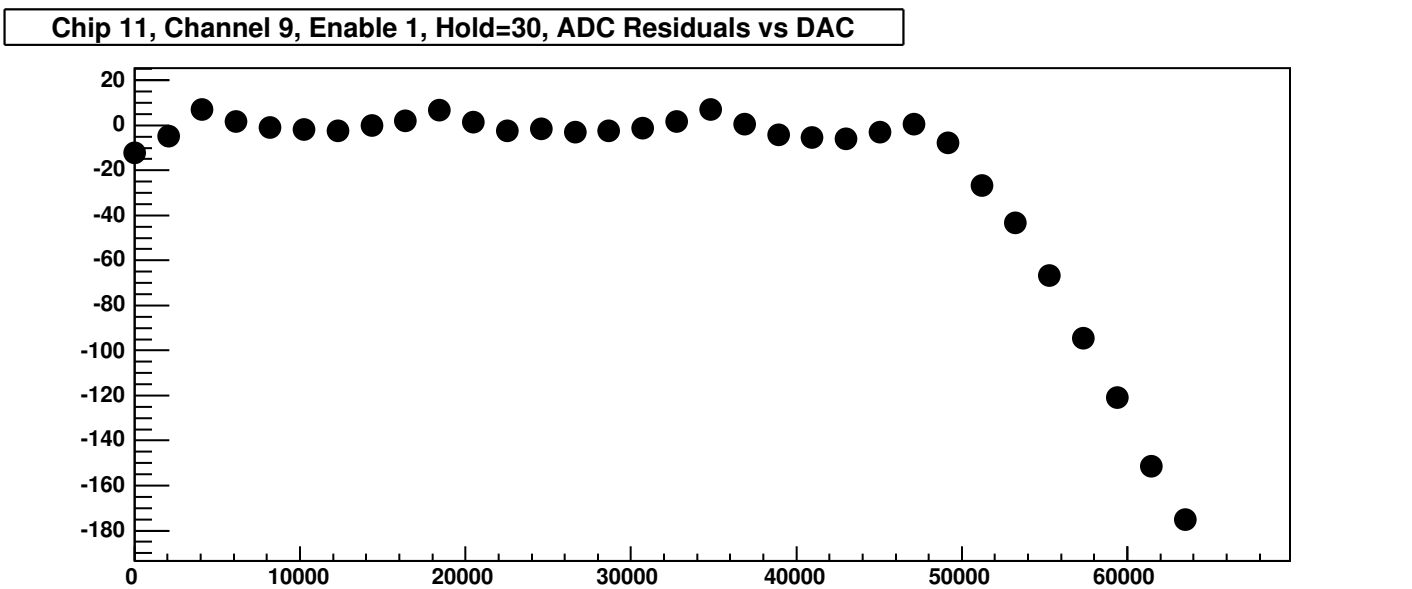

Chip 11, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4949 / 15
p0 883 ± 0.1157
p1 0.001523 ± 4.76e-06
960
940
920
900
880
860
840
0
10000
20000
30000
40000
50000
60000

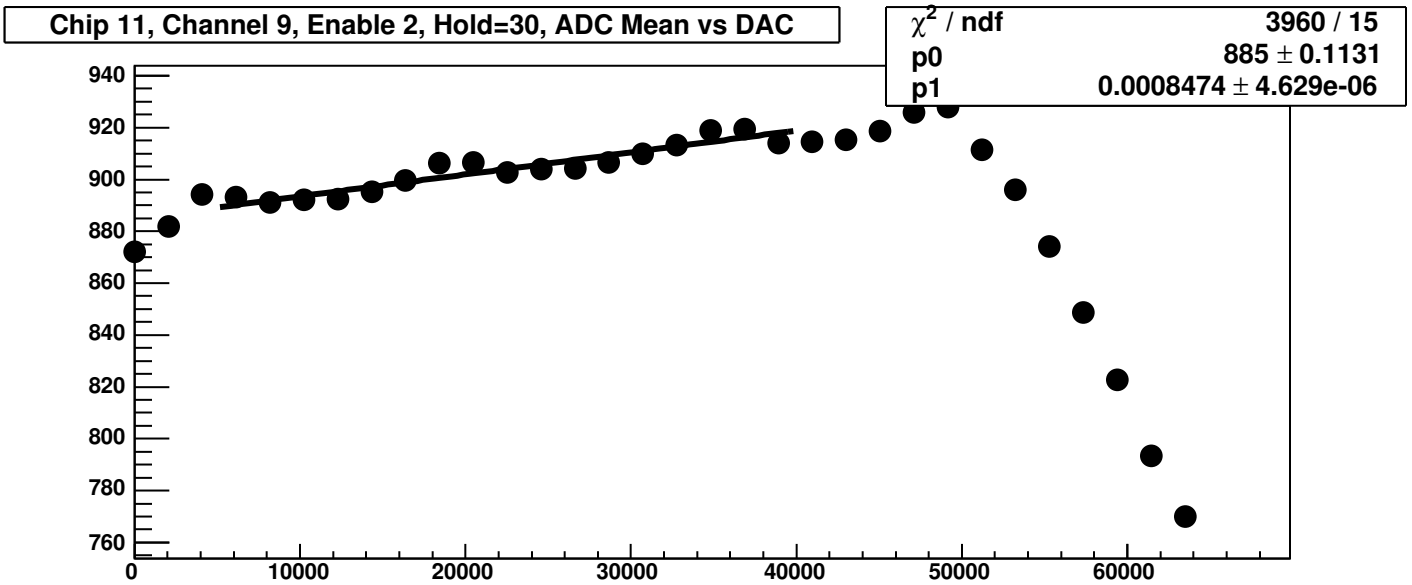

Chip 11, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

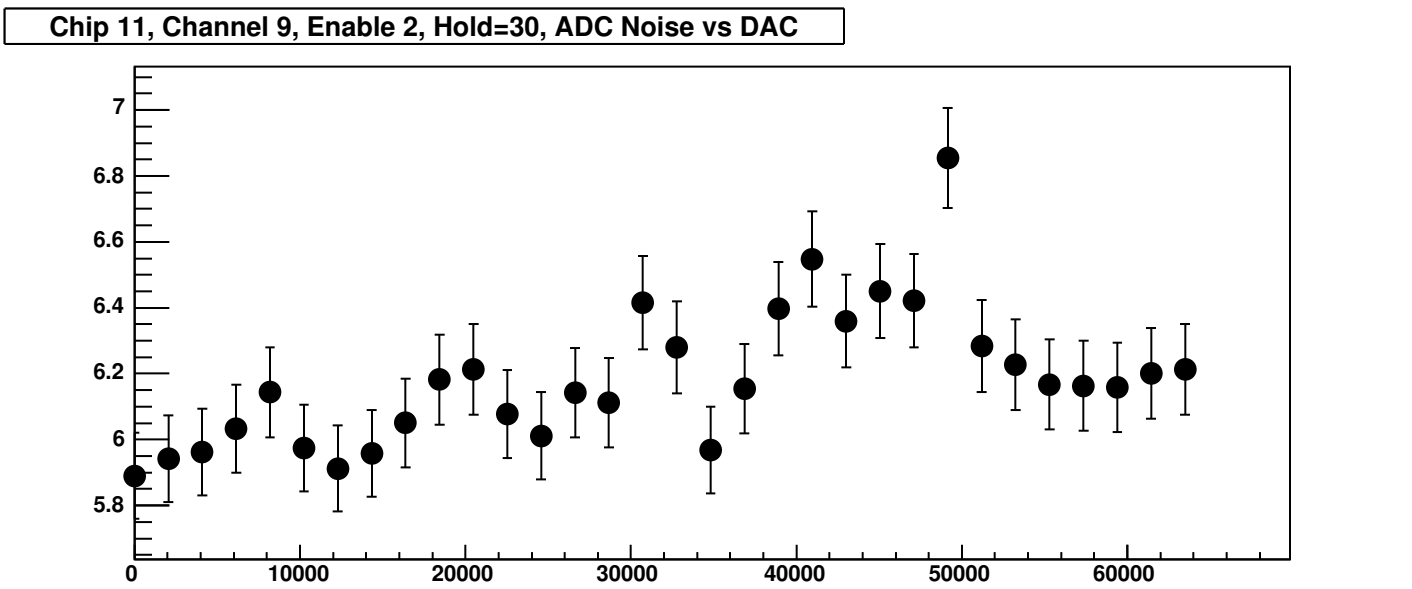

Chip 11, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

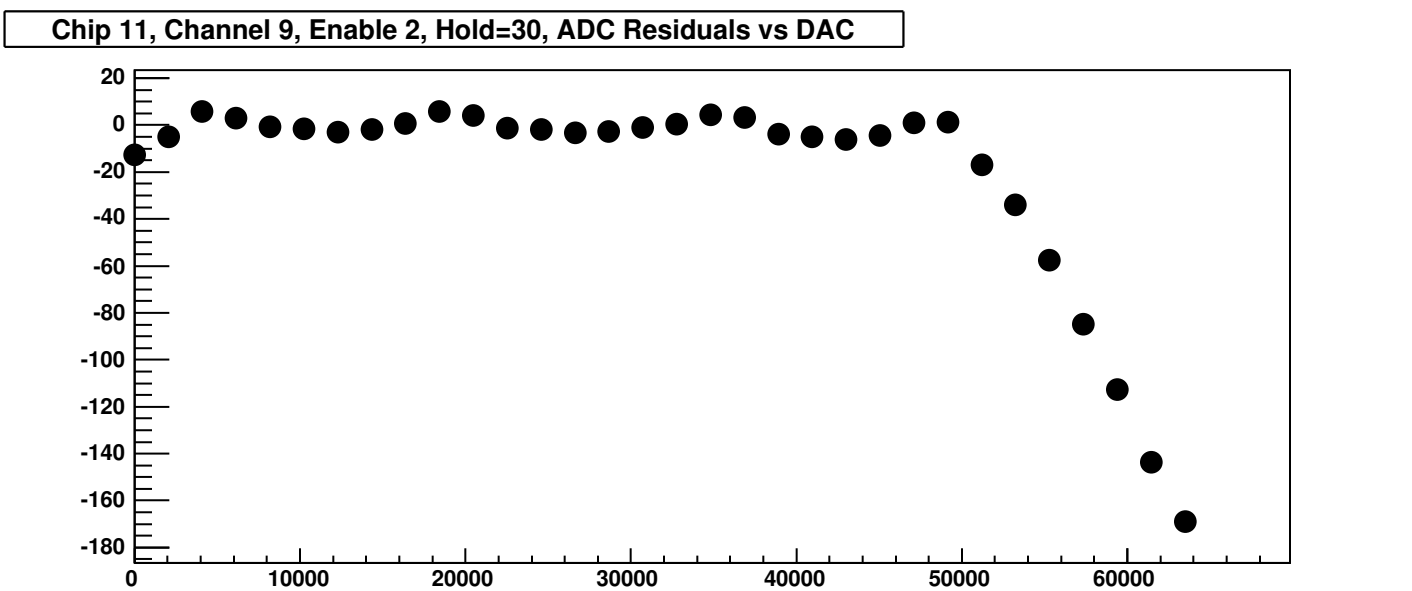

Chip 11, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4372 / 15
p0 885.2 ± 0.1118
p1 0.0008937 ± 4.595e-06
940
920
900
880
860
840
820
800
780
760
0
10000
20000
30000
40000
50000
60000

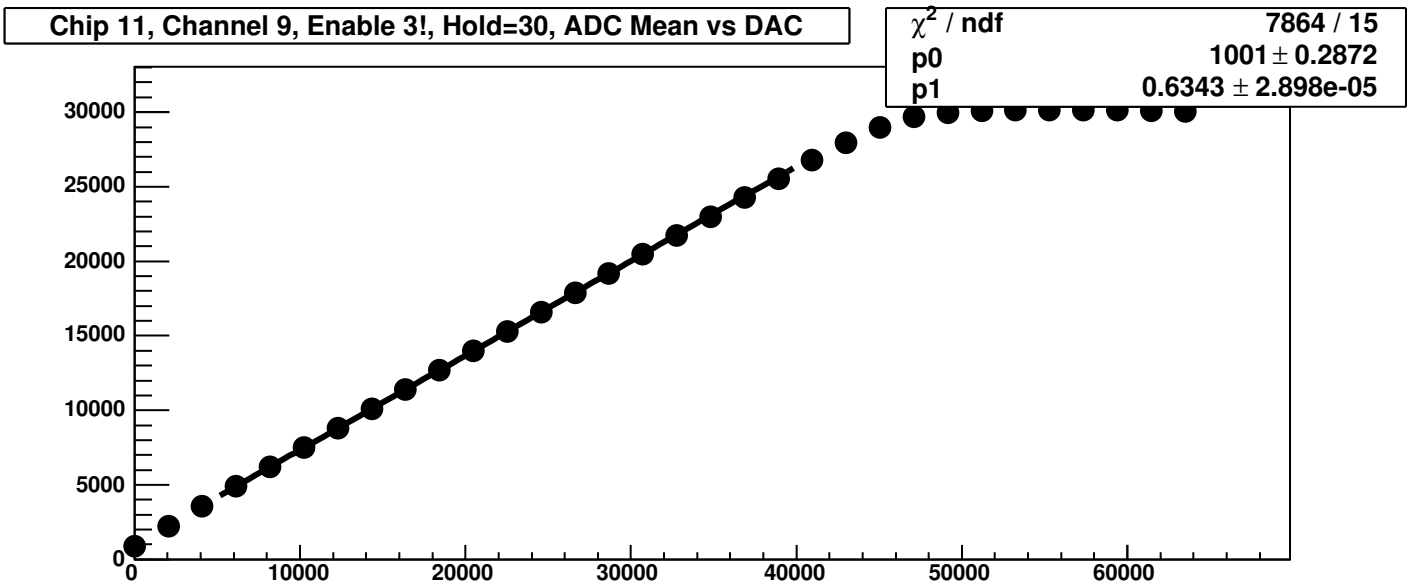

Chip 11, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

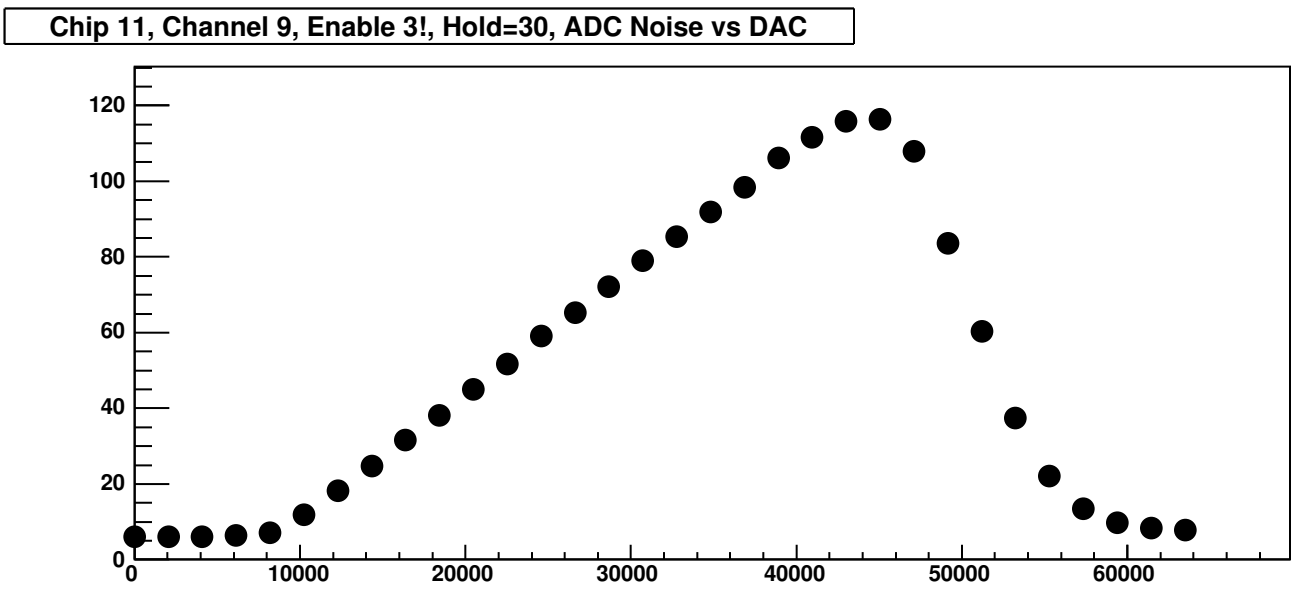

Chip 11, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

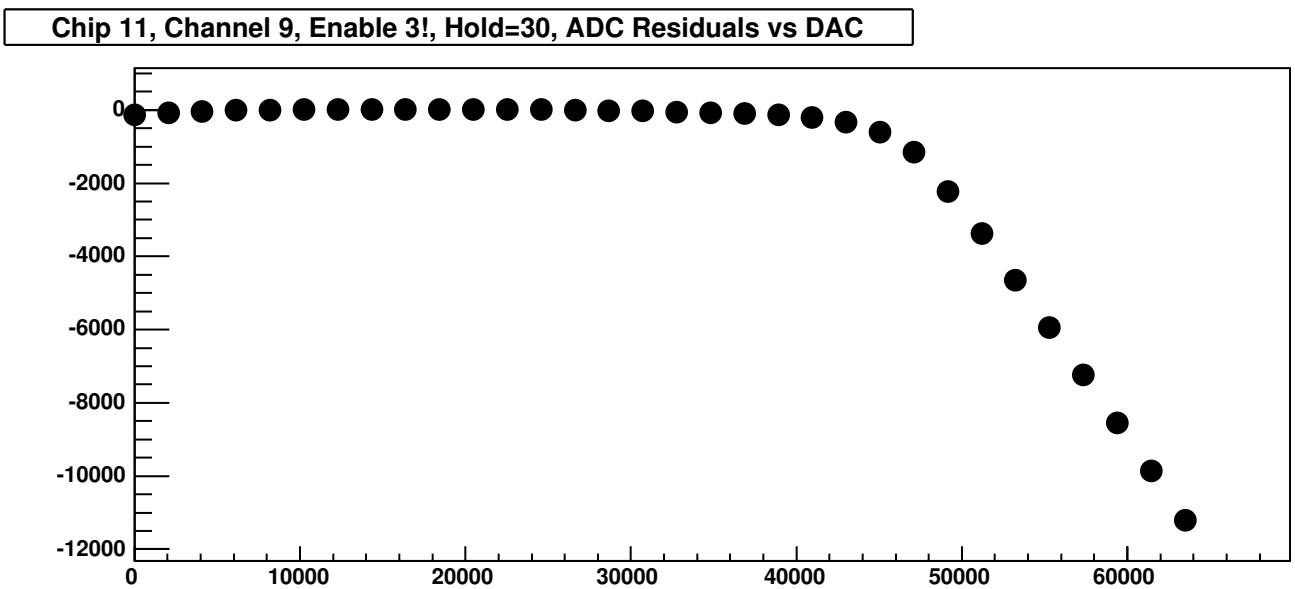

Chip 11, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3960 / 15
p0 885 ± 0.1131
p1 0.0008474 ± 4.629e-06
940
920
900
880
860
840
820
800
780
760
0
10000
20000
30000
40000
50000
60000

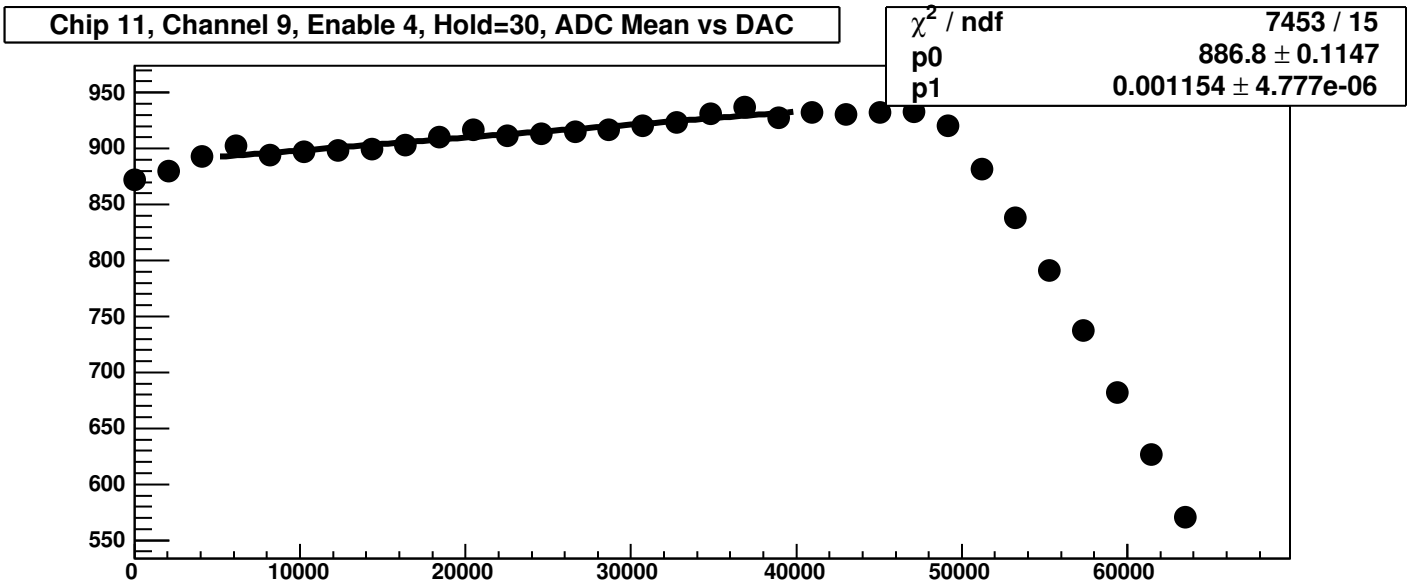

Chip 11, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

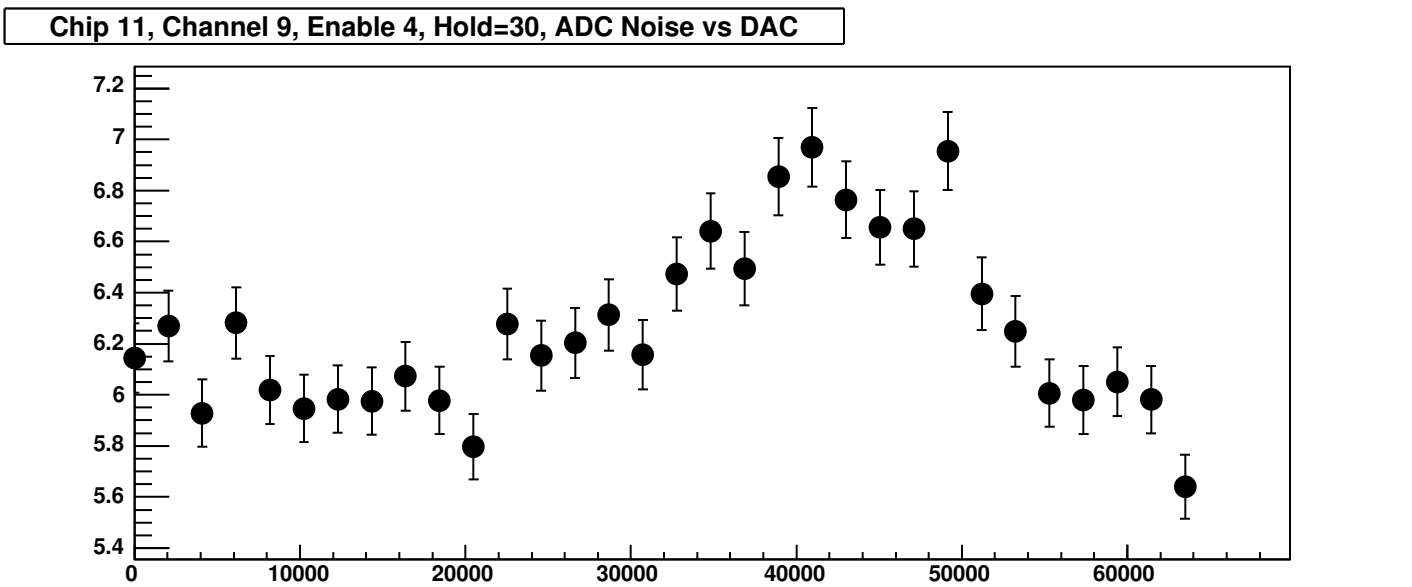

Chip 11, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

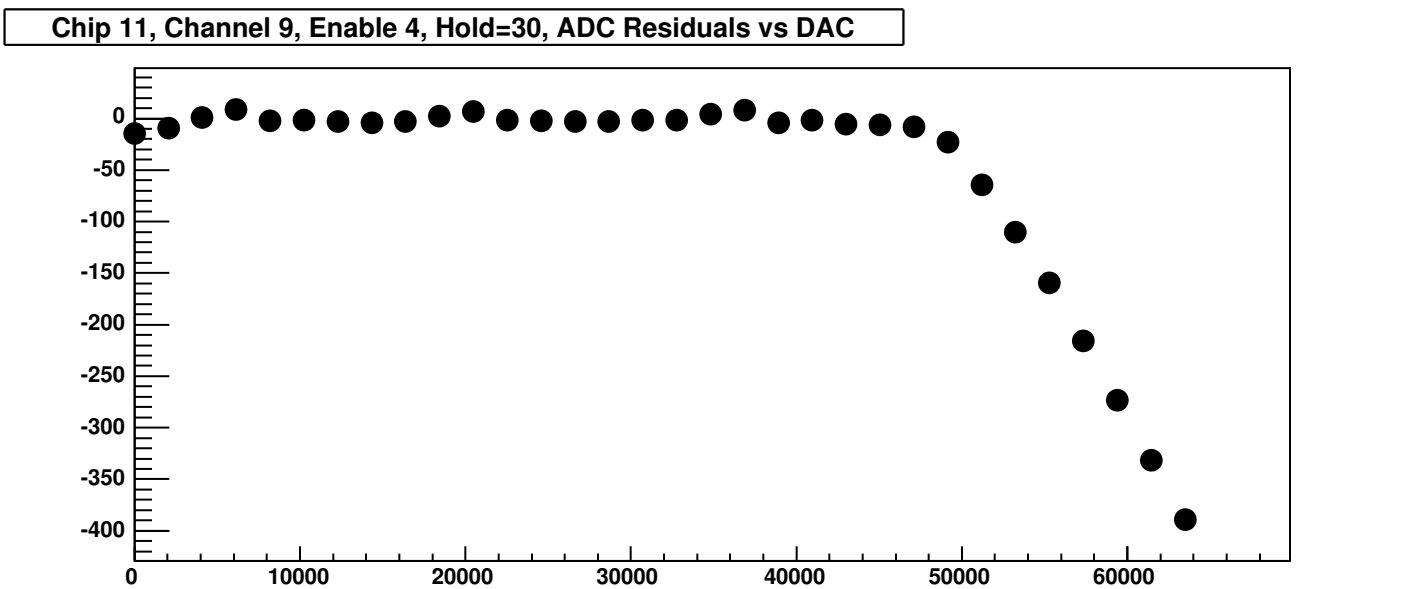

Chip 11, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 7864 / 15
p0 1001 ± 0.2872
p1 0.6343 ± 2.898e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

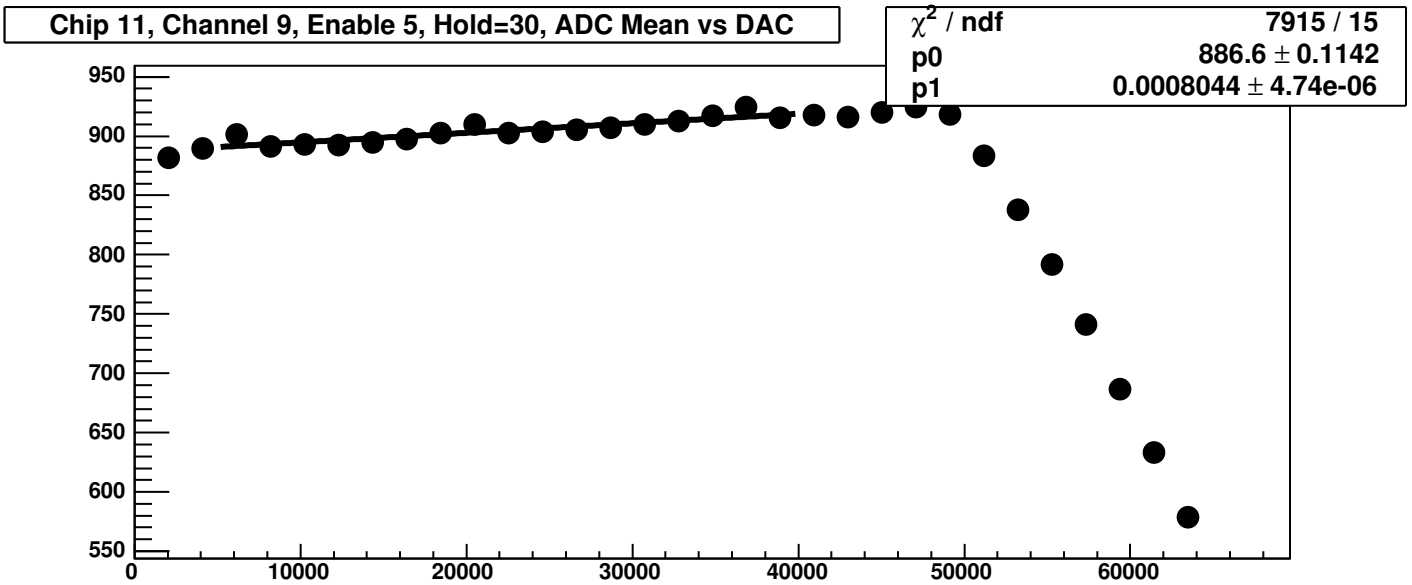

Chip 11, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

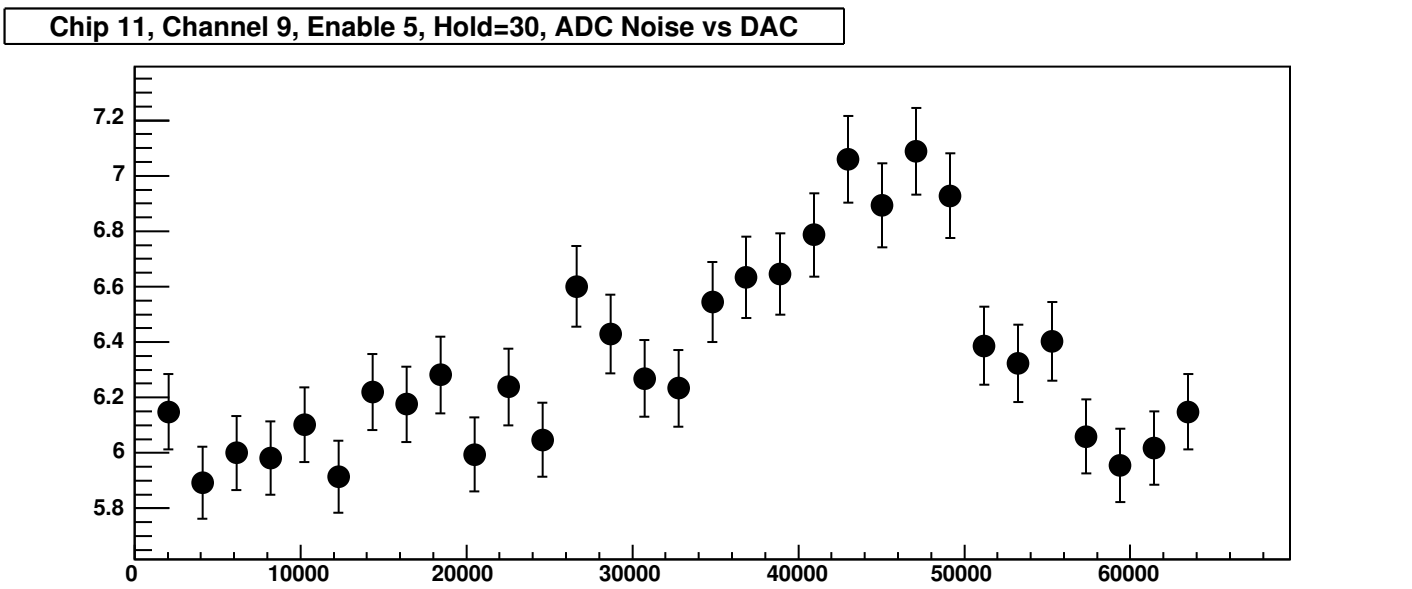

Chip 11, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

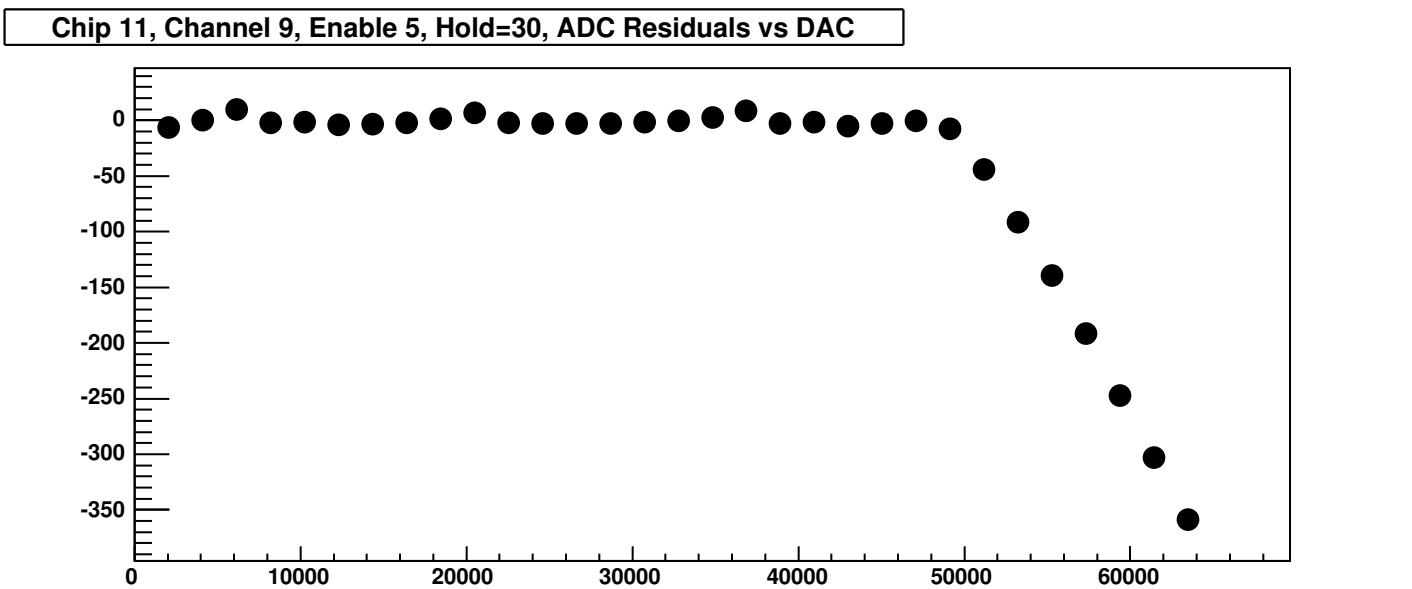

Chip 11, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7453 / 15
p0 886.8 ± 0.1147
p1 0.001154 ± 4.777e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

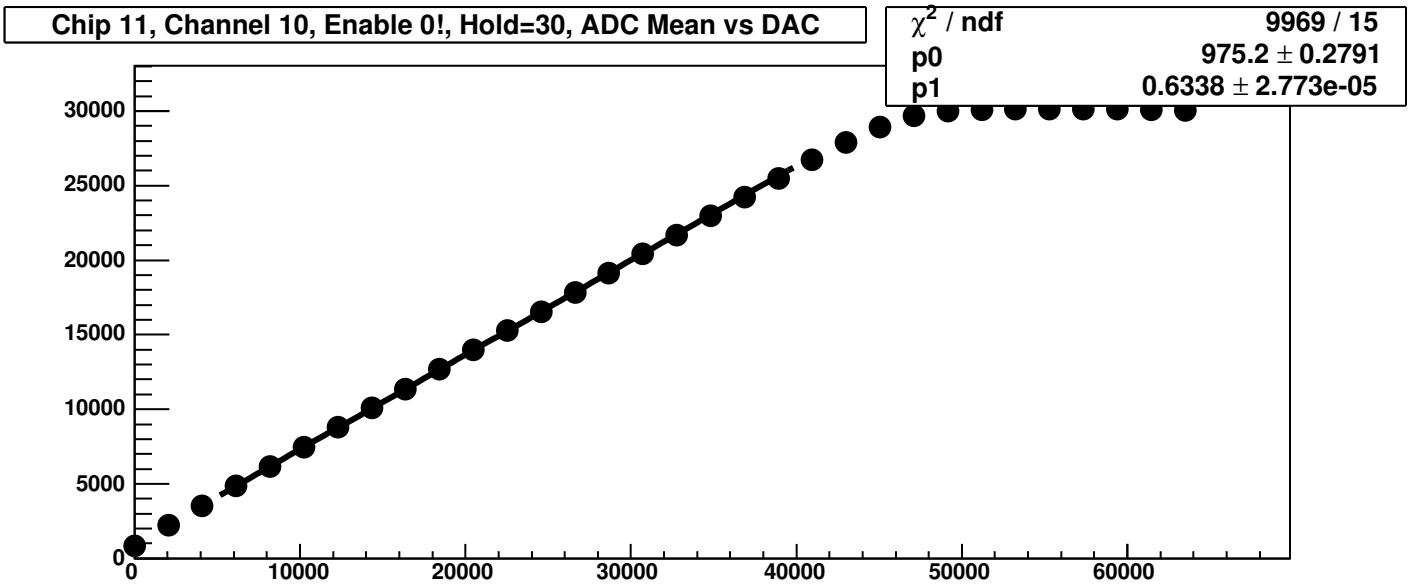

Chip 11, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

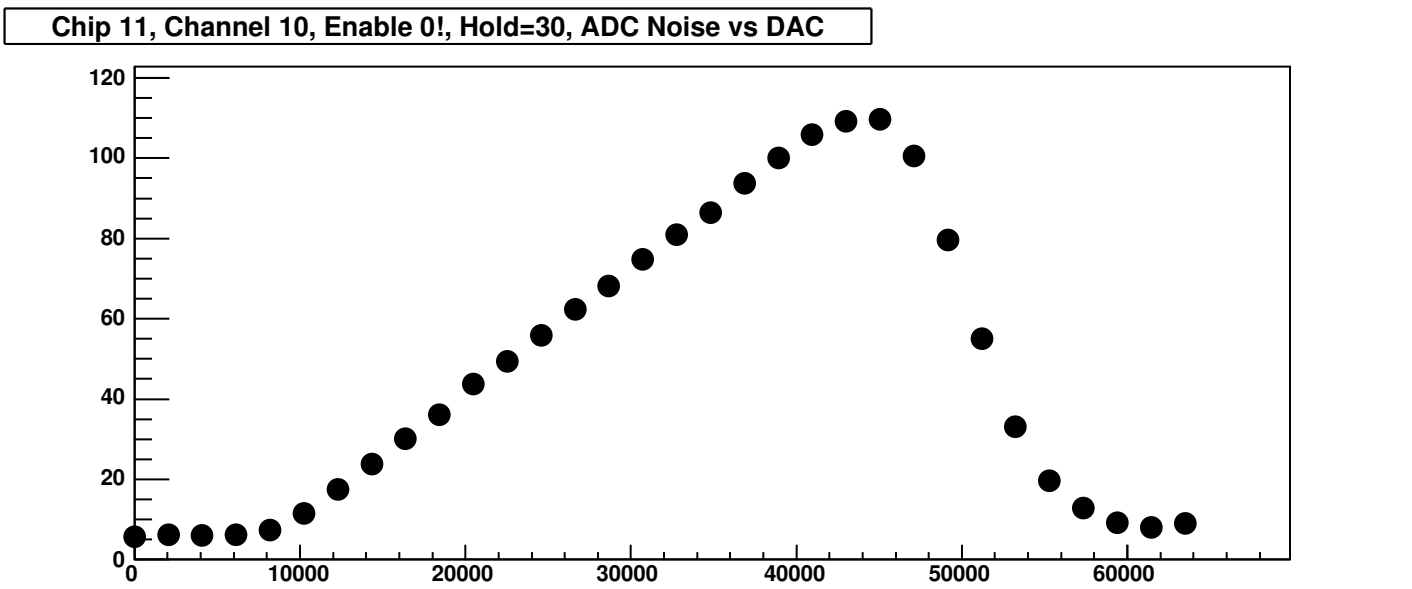

Chip 11, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

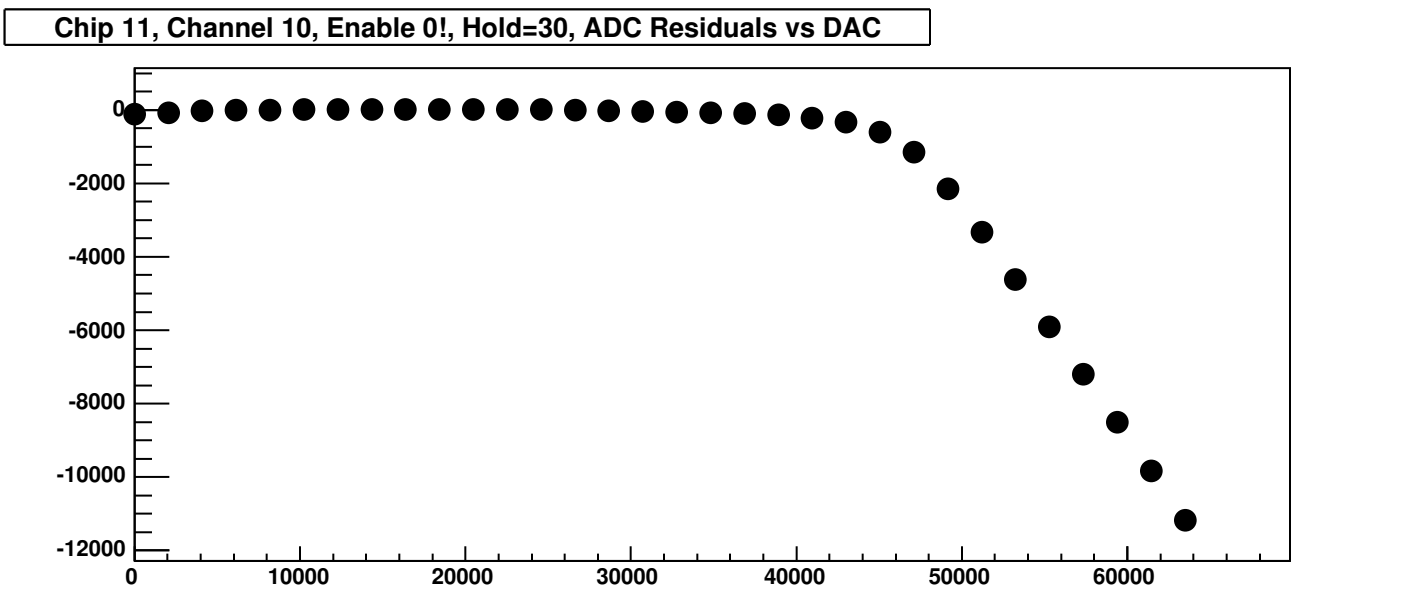

Chip 11, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7915 / 15
p0 886.6 ± 0.1142
p1 0.0008044 ± 4.74e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

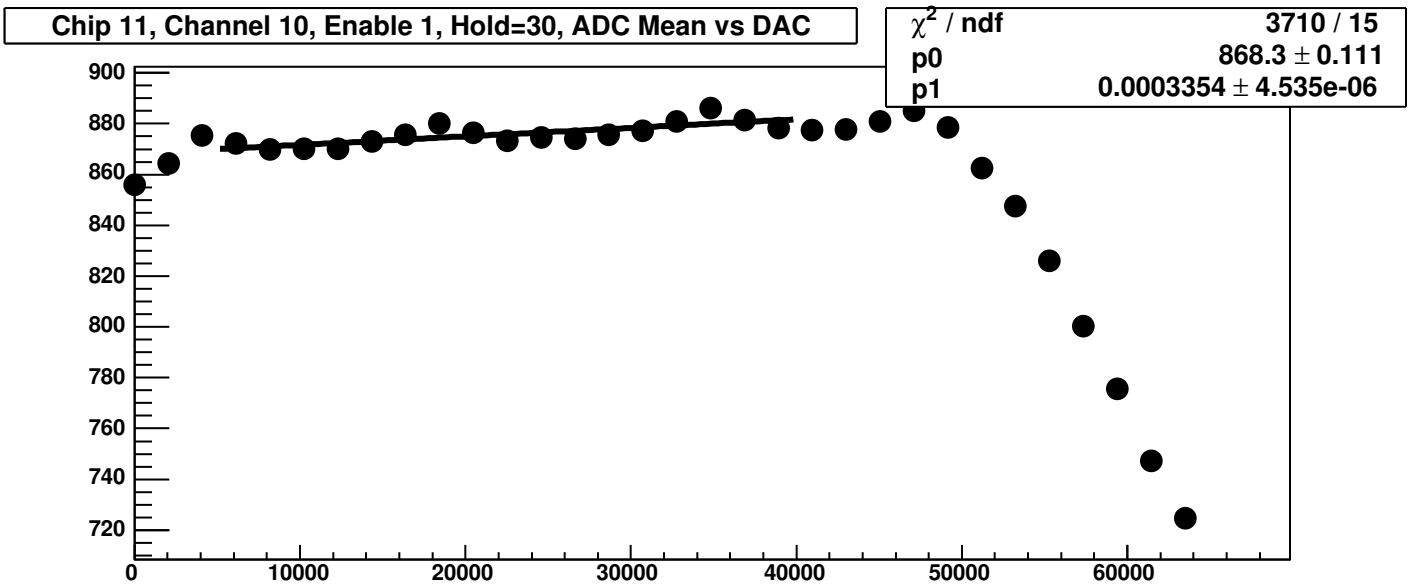

Chip 11, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

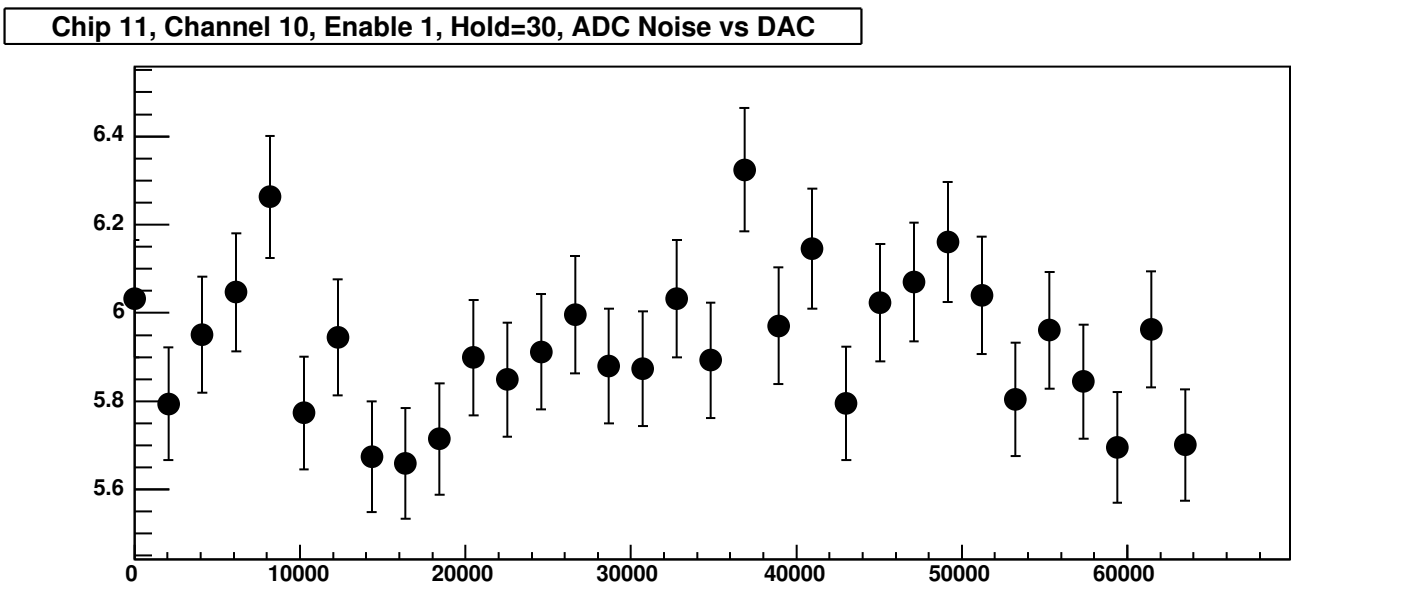

Chip 11, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

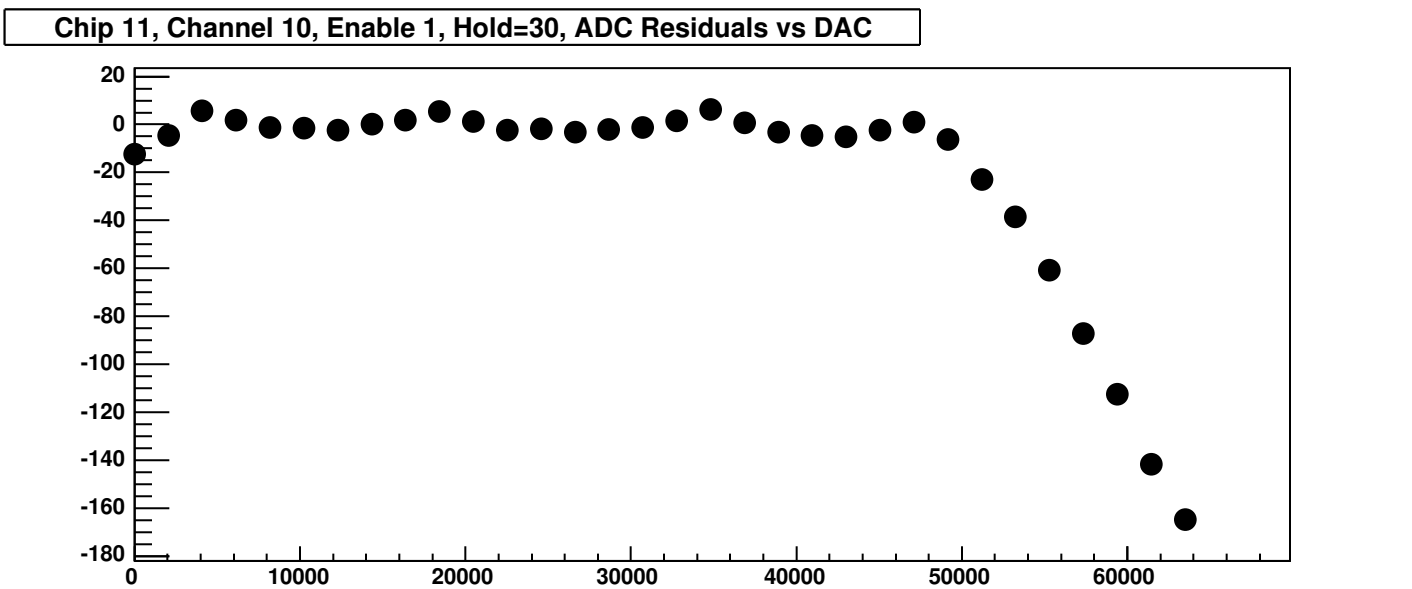

Chip 11, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 9969 / 15
p0 975.2 ± 0.2791
p1 0.6338 ± 2.773e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

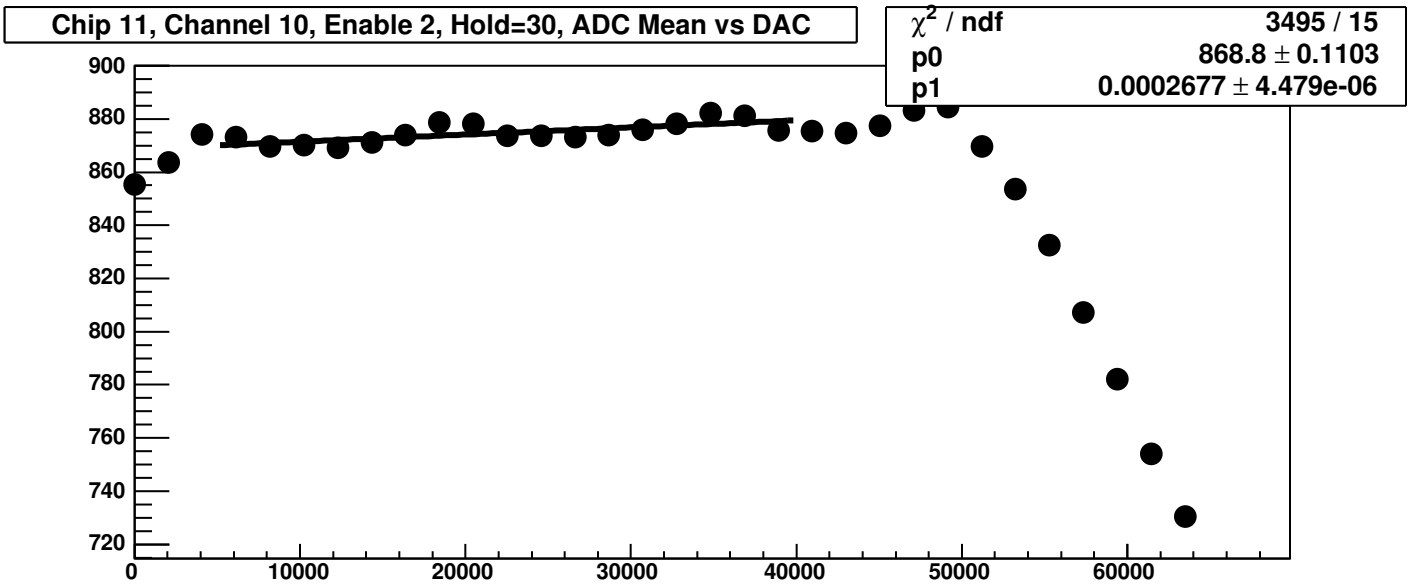

Chip 11, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

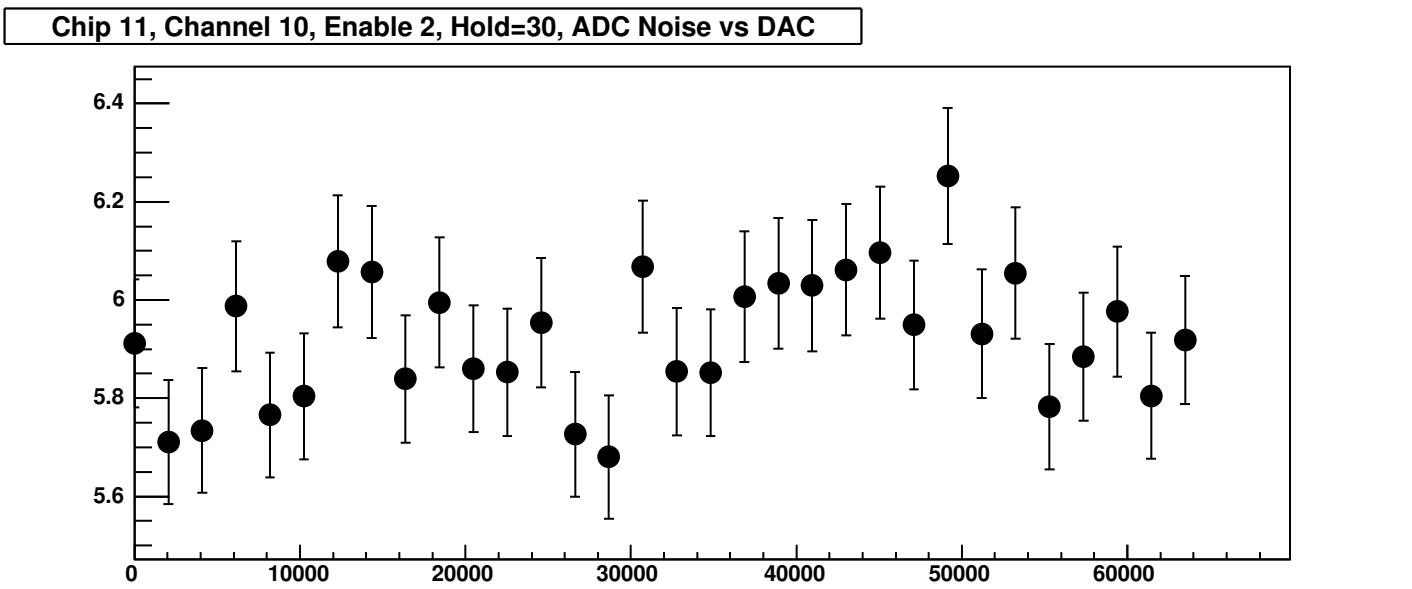

Chip 11, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

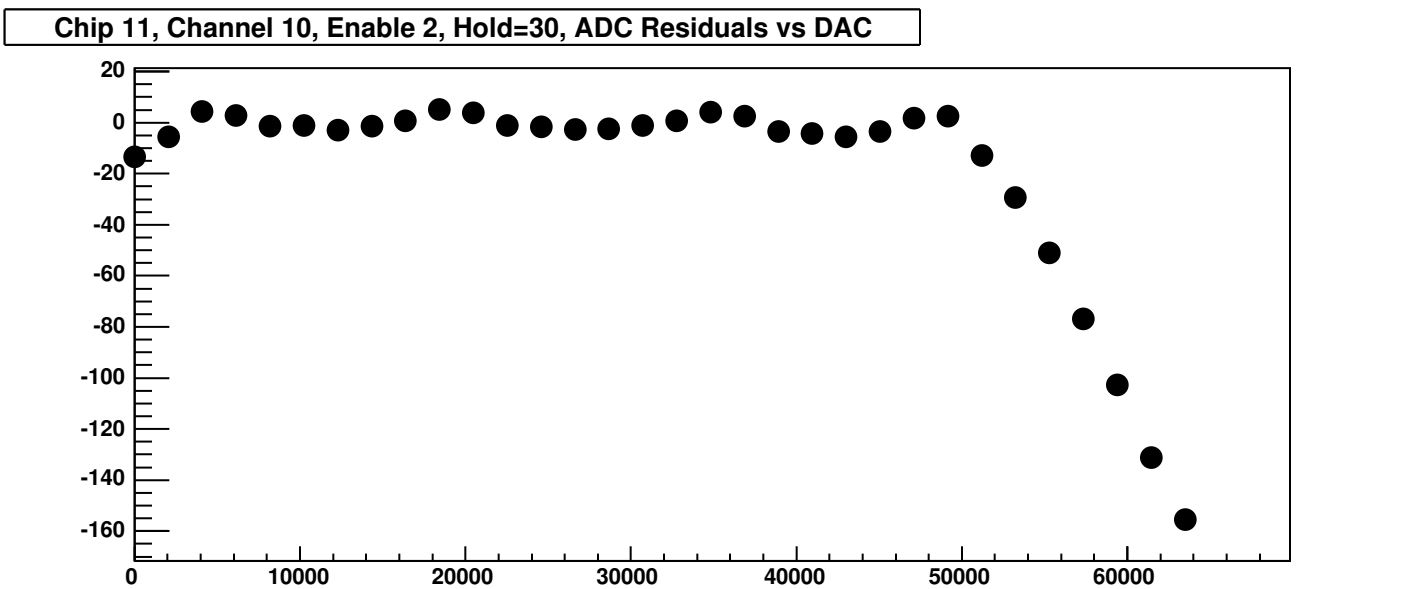

Chip 11, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3710 / 15
p0 868.3 ± 0.111
p1 0.0003354 ± 4.535e-06
900
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

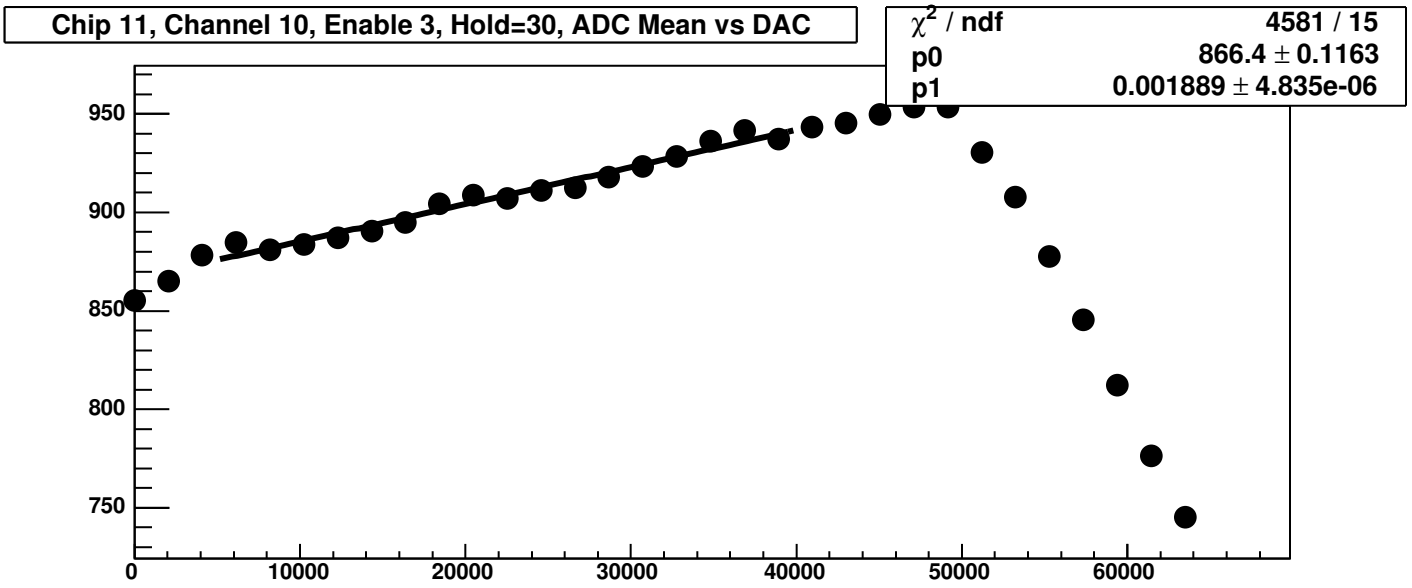

Chip 11, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

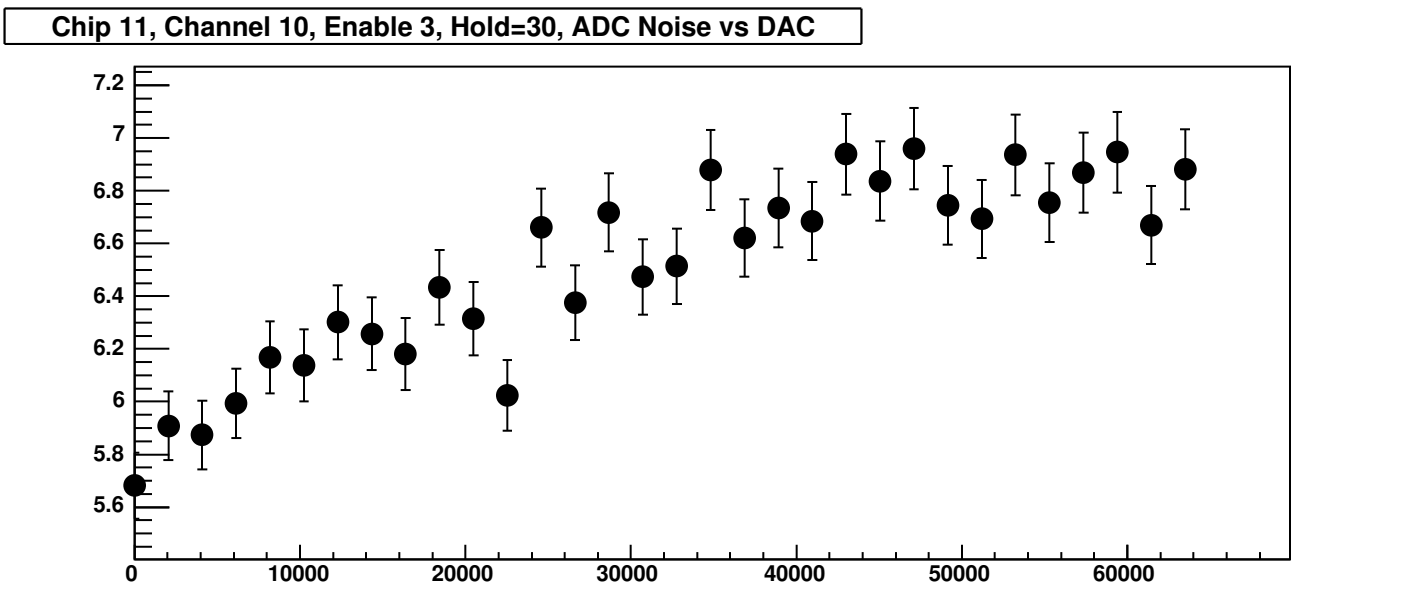

Chip 11, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

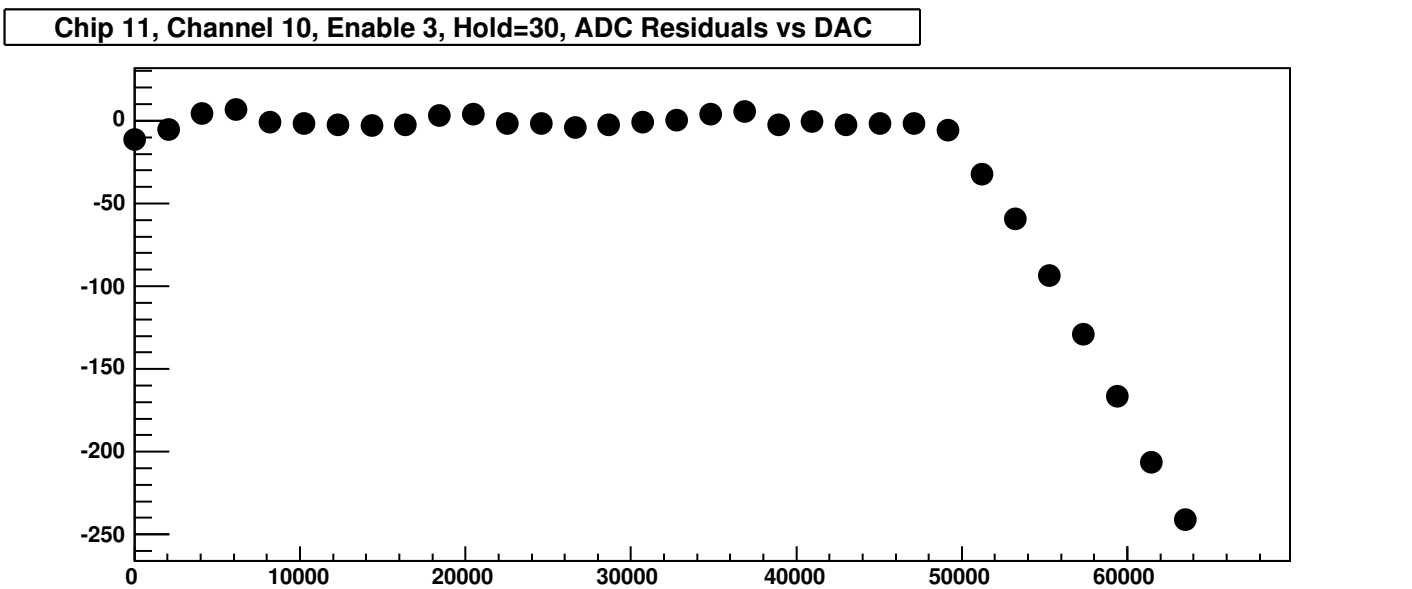

Chip 11, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3495 / 15
p0 868.8 ± 0.1103
p1 0.0002677 ± 4.479e-06
900
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

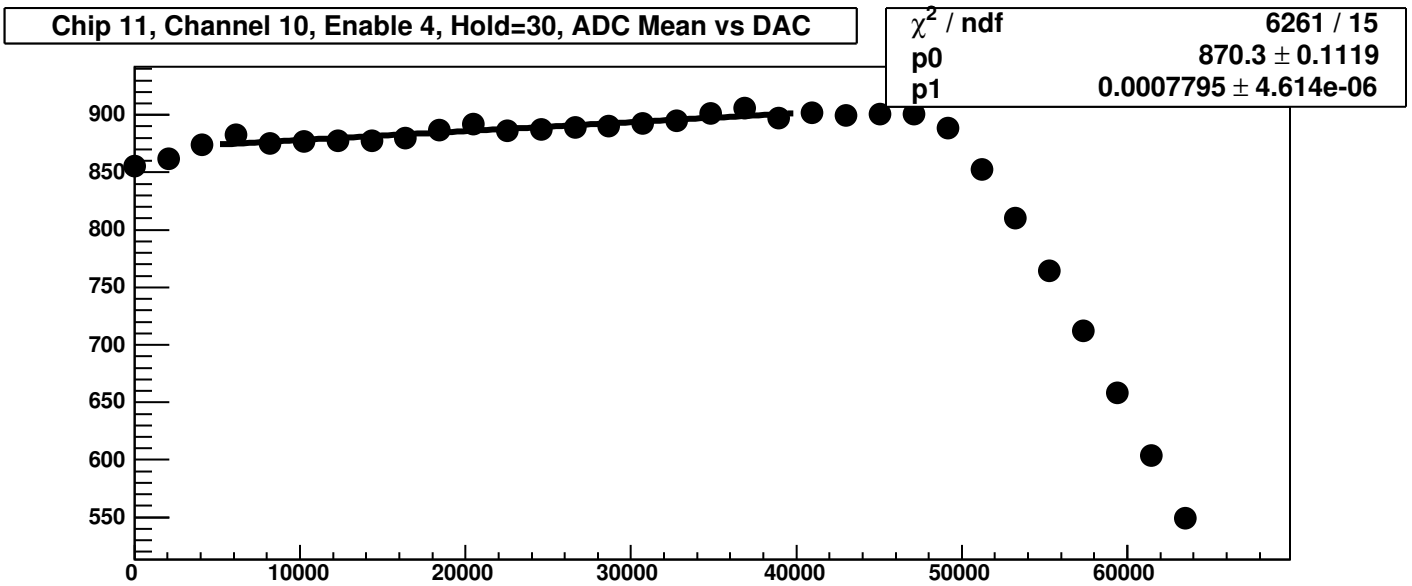

Chip 11, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

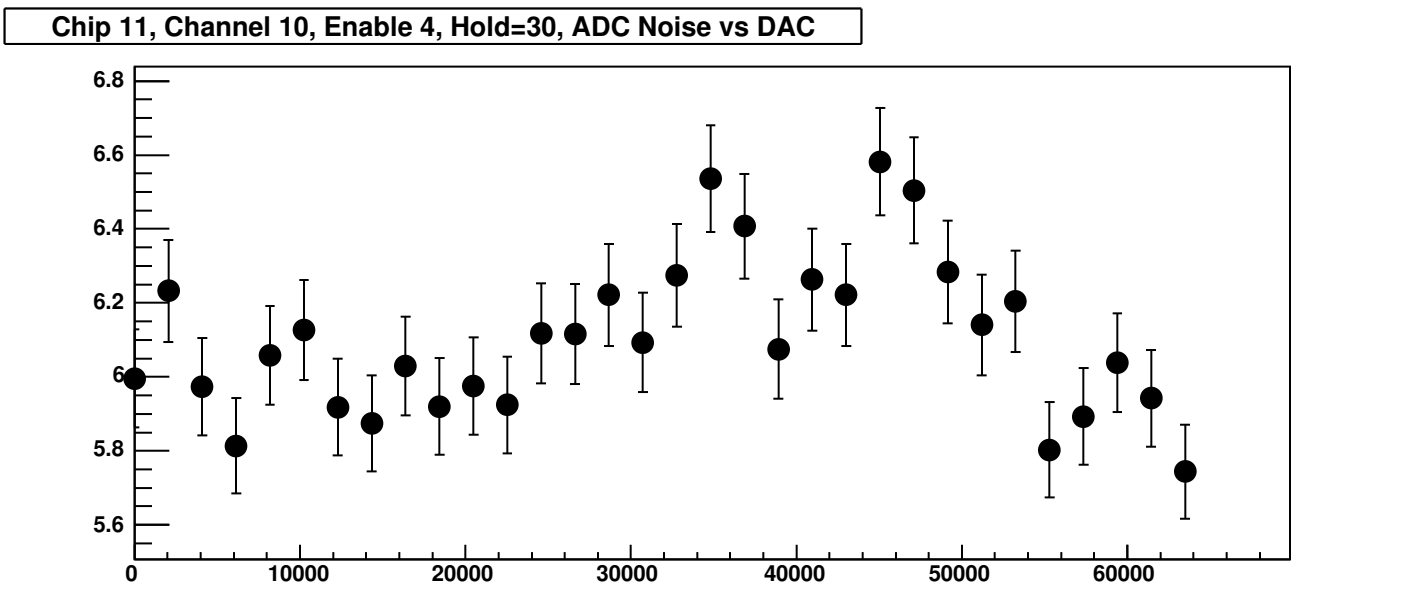

Chip 11, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

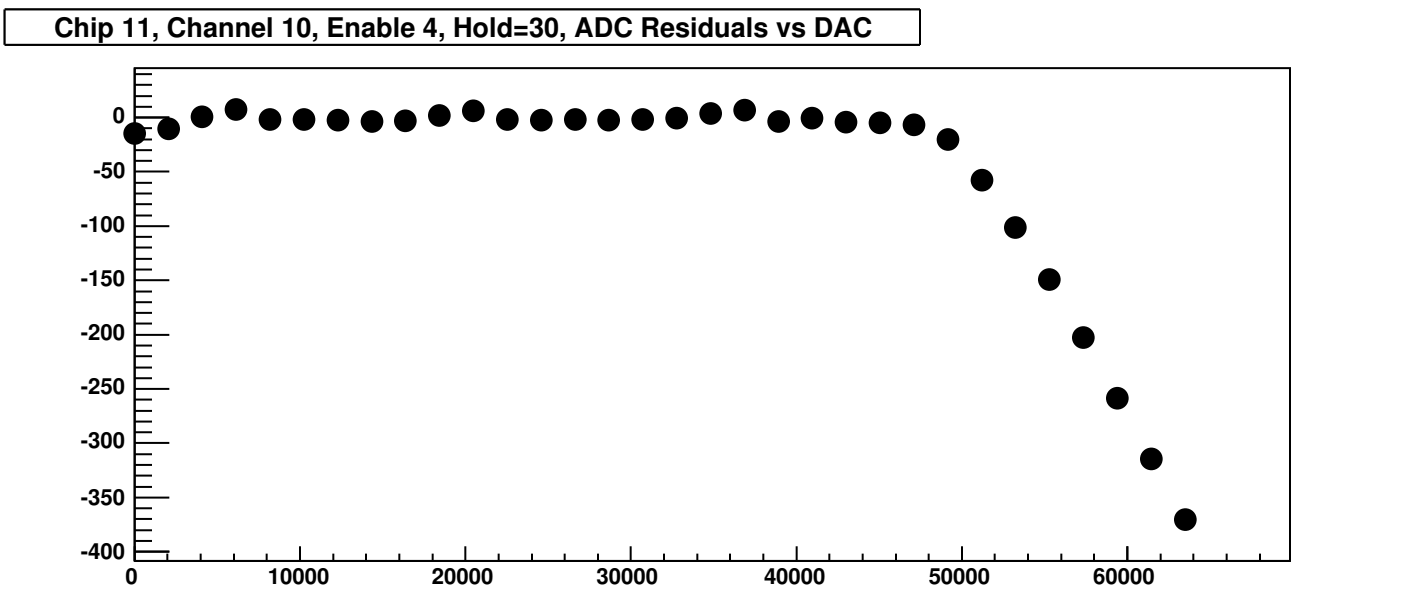

Chip 11, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4581 / 15
p0 866.4 ± 0.1163
p1 0.001889 ± 4.835e-06
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

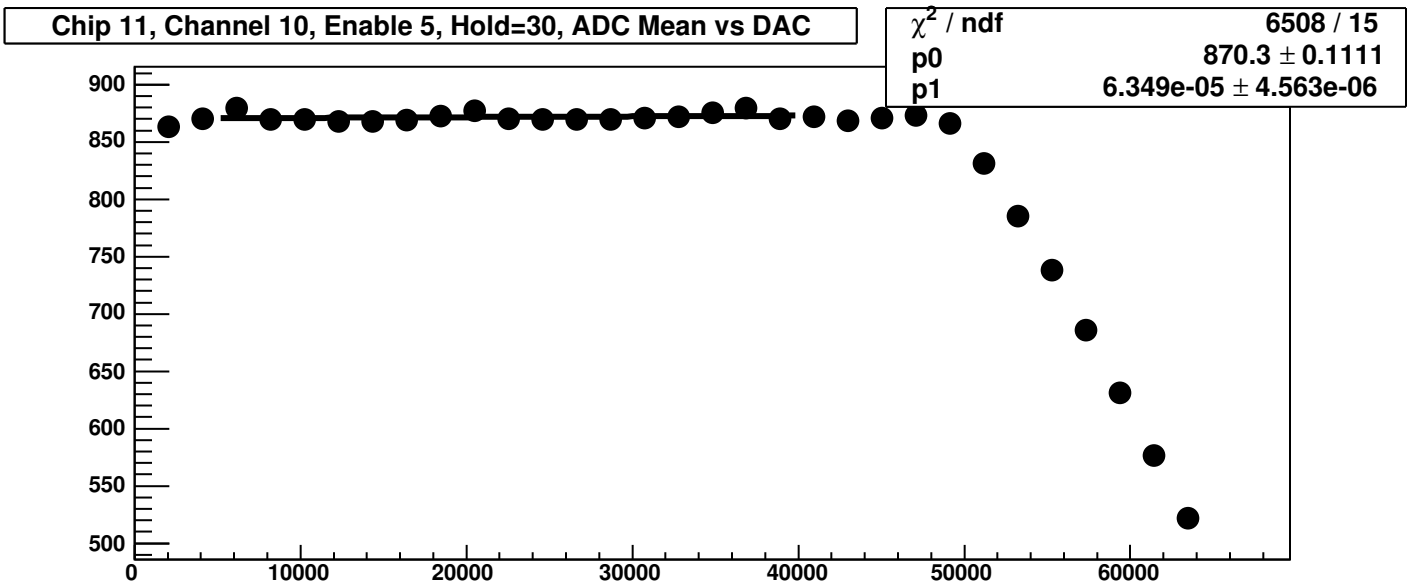

Chip 11, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

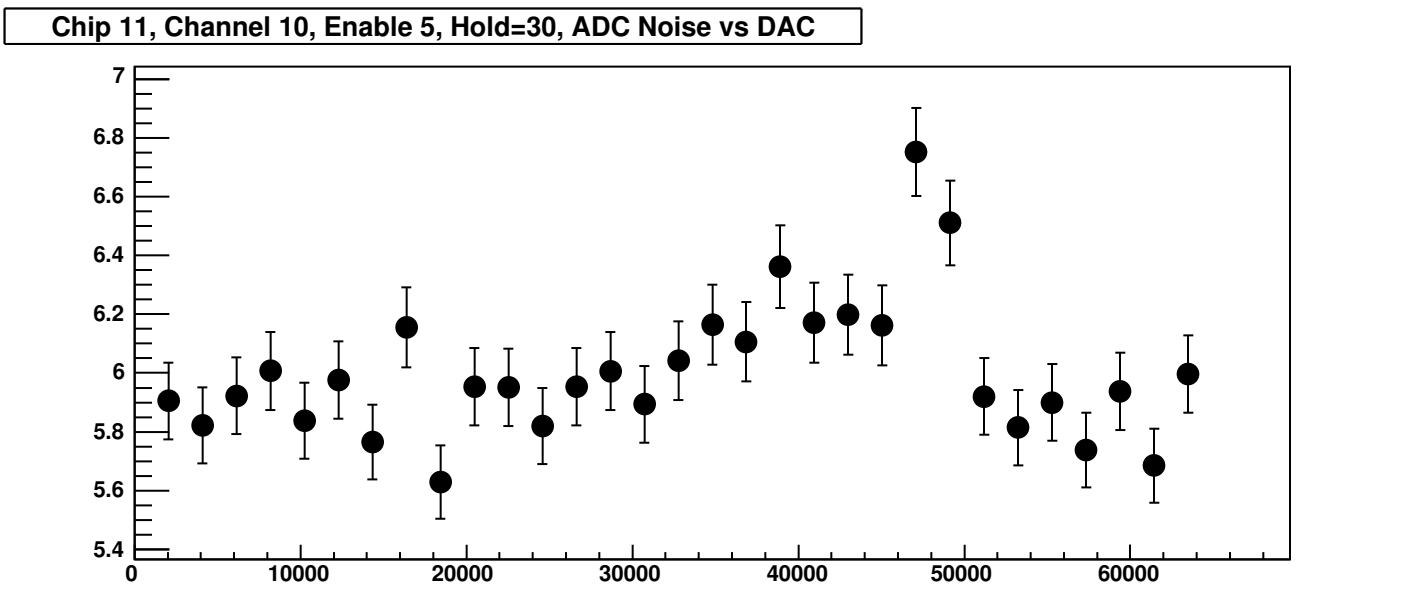

Chip 11, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

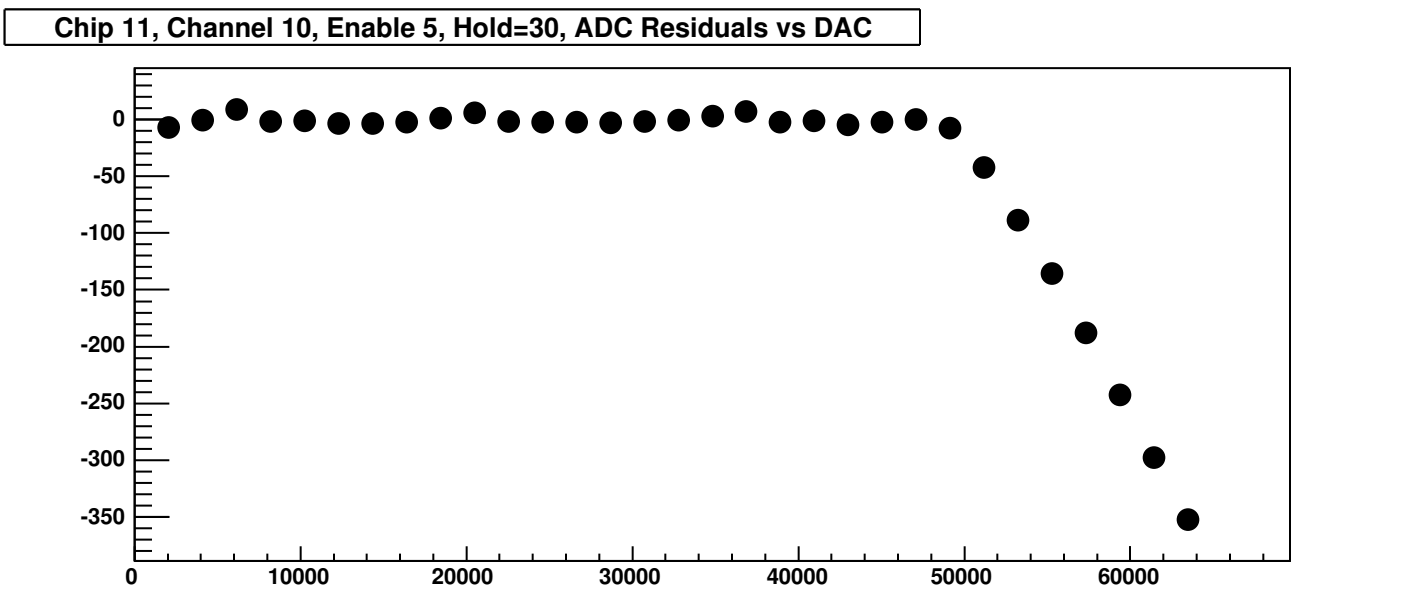

Chip 11, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6261 / 15
p0 870.3 ± 0.1119
p1 0.0007795 ± 4.614e-06
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

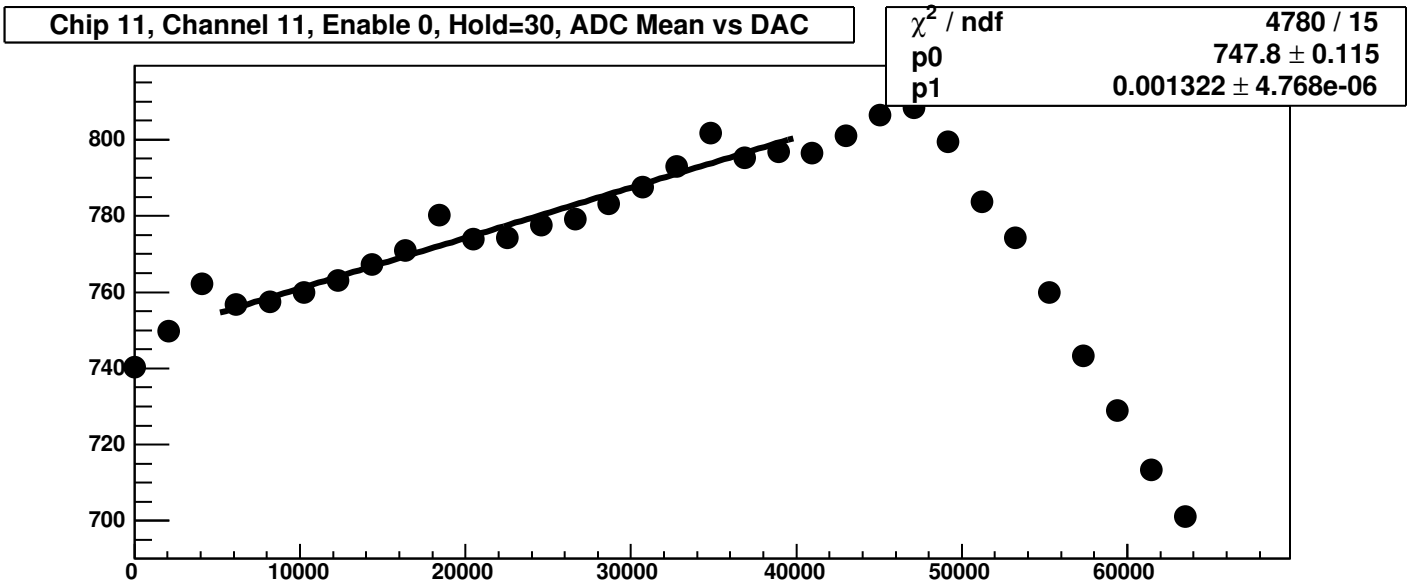

Chip 11, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

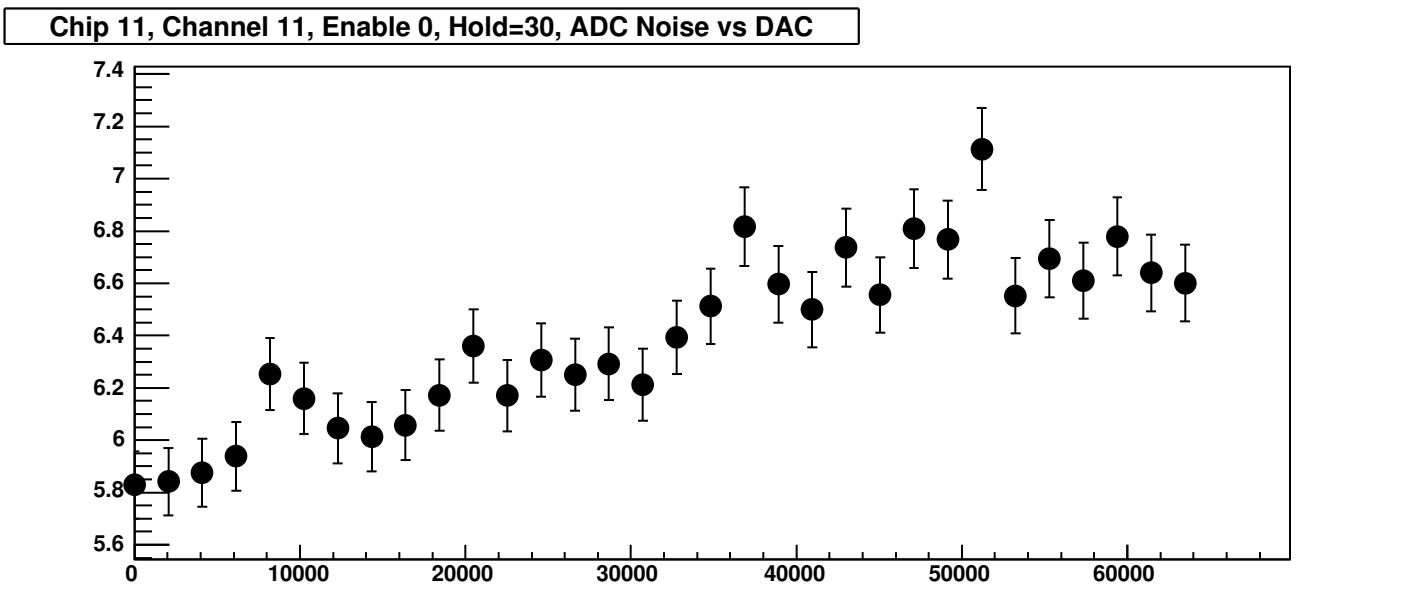

Chip 11, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

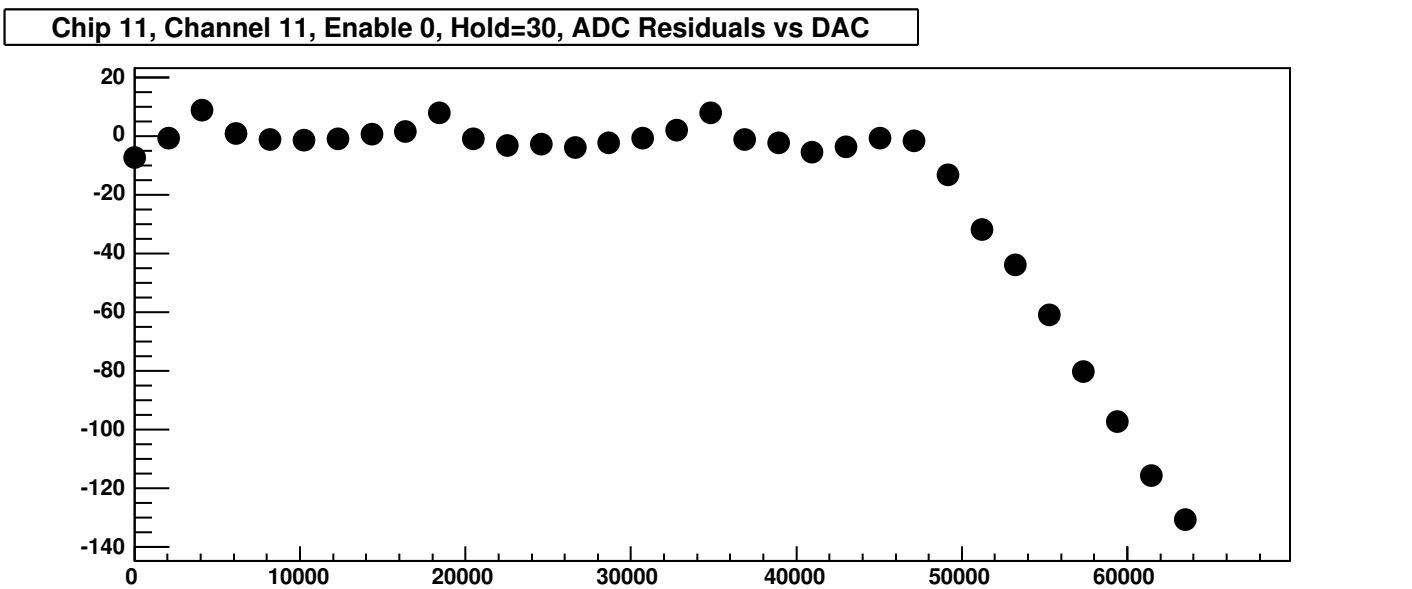

Chip 11, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6508 / 15
p0 870.3 ± 0.1111
p1 6.349e-05 ± 4.563e-06
900
850
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

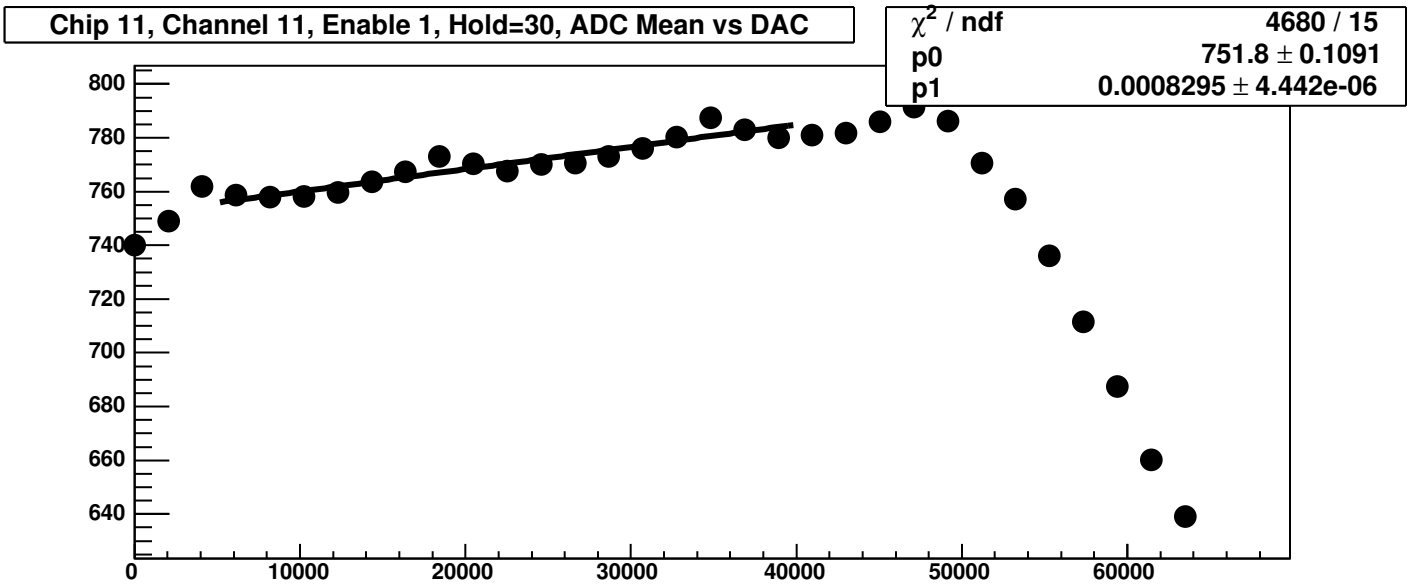

Chip 11, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

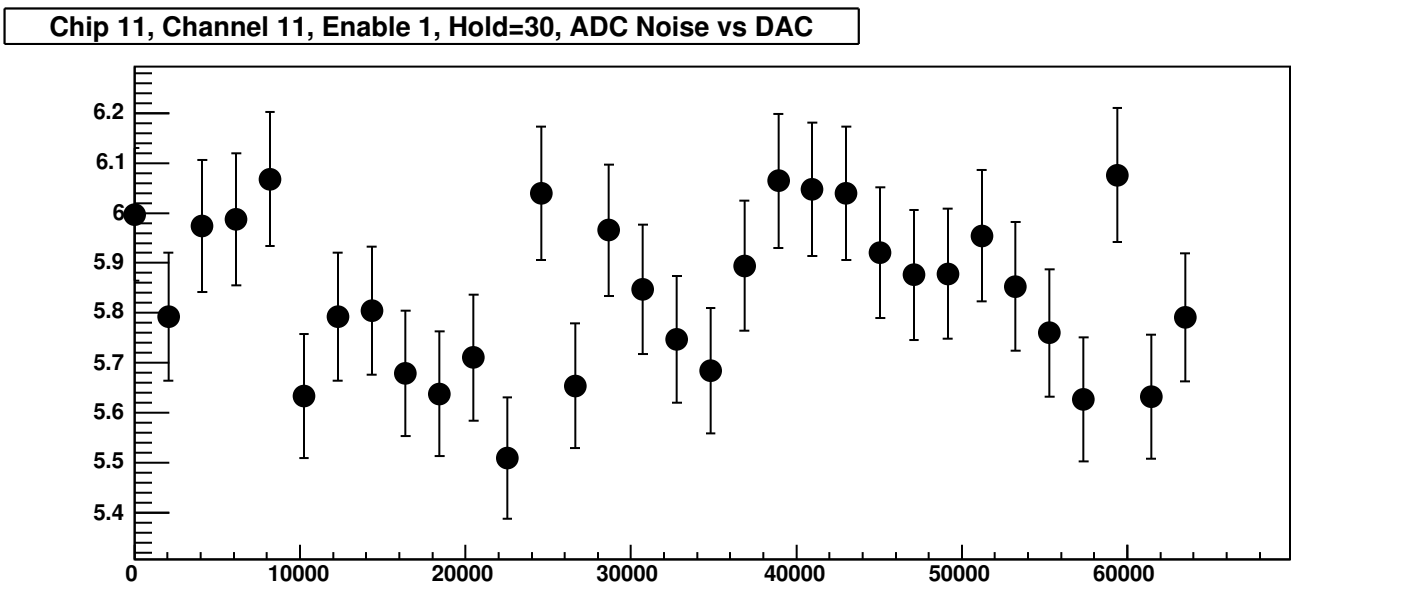

Chip 11, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

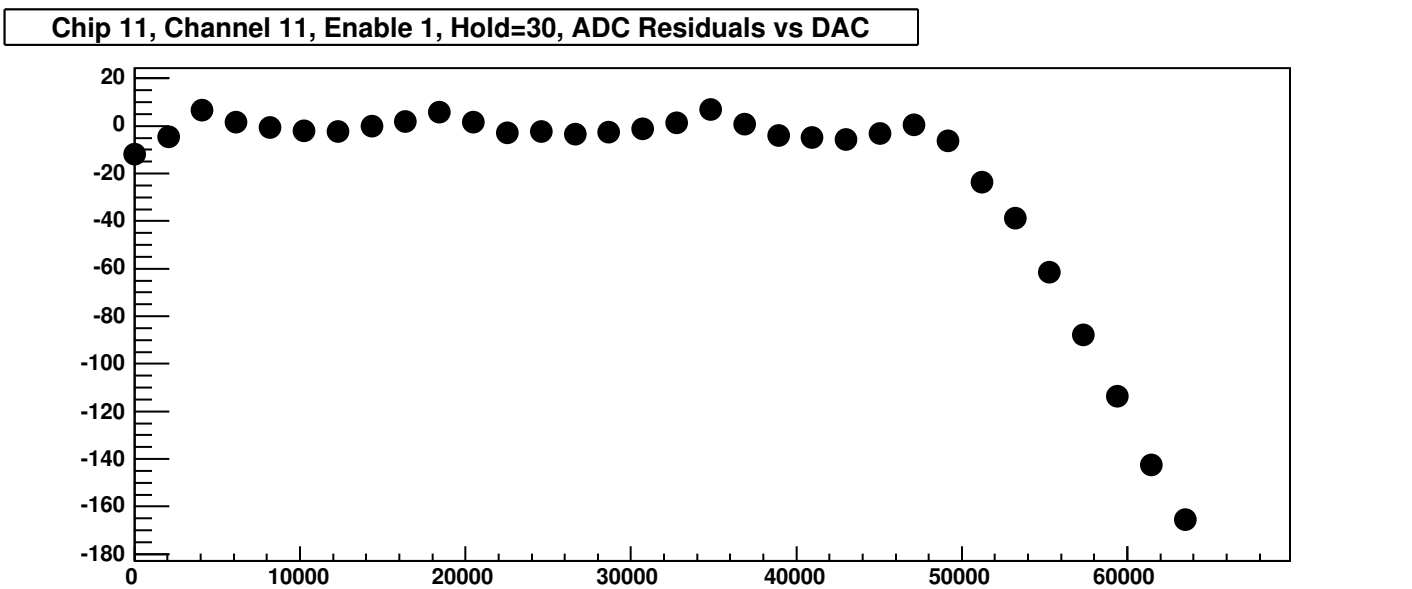

Chip 11, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4780 / 15
p0 747.8 ± 0.115
p1 0.001322 ± 4.768e-06
800
780
760
740
720
700
0
10000
20000
30000
40000
50000
60000

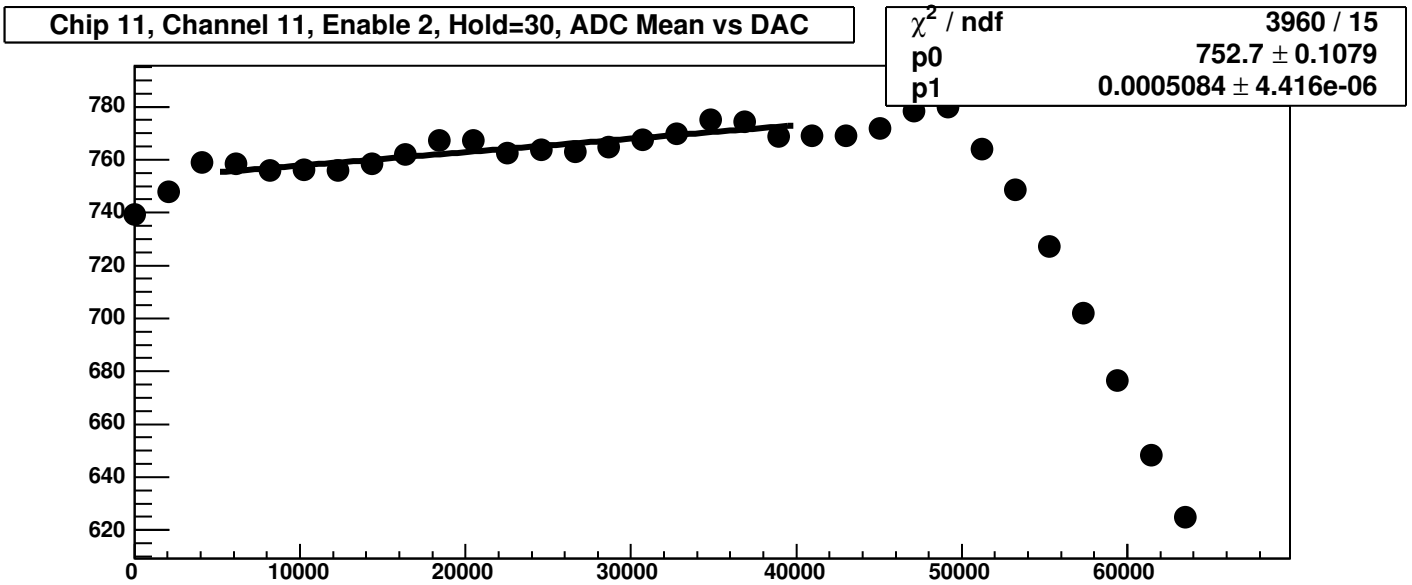

Chip 11, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

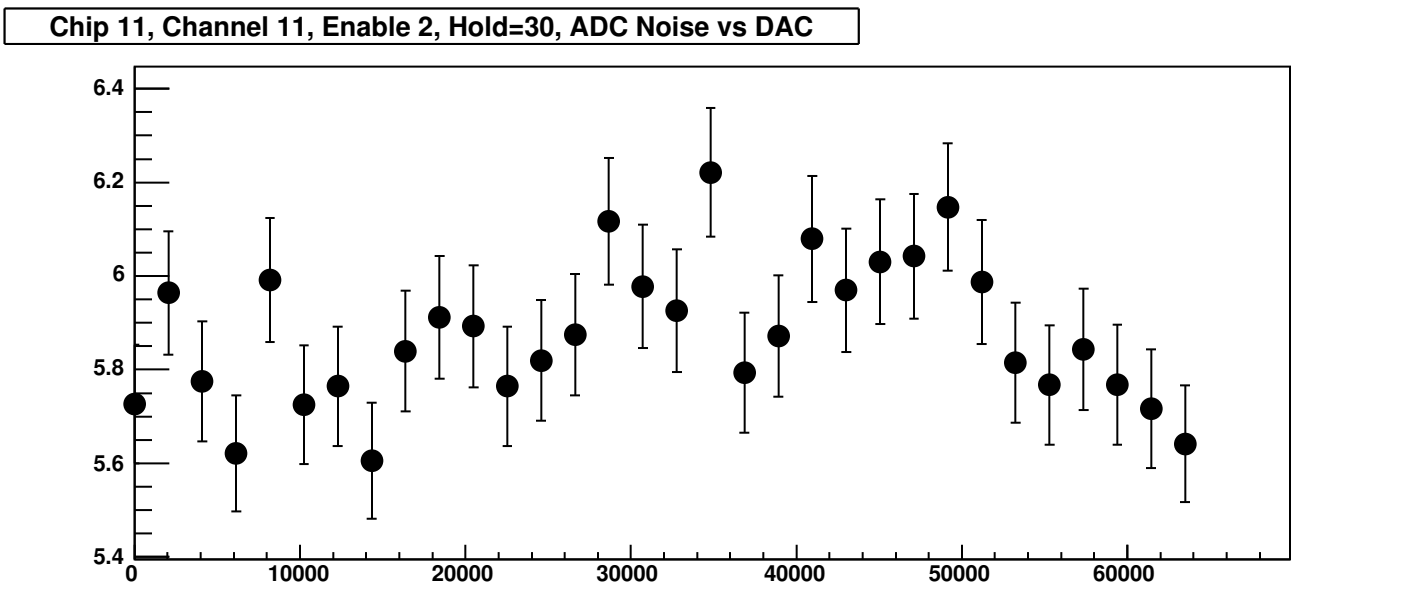

Chip 11, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

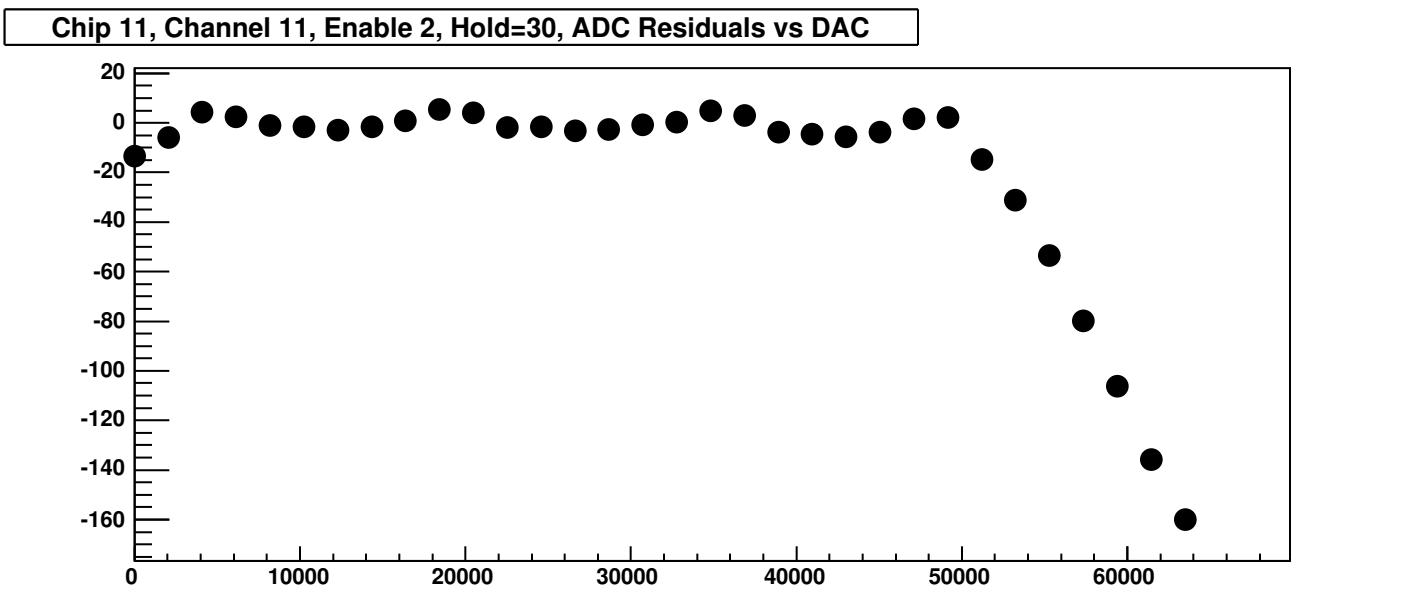

Chip 11, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4680 / 15
p0 751.8 ± 0.1091
p1 0.0008295 ± 4.442e-06
800
780
760
740
720
700
680
660
640
0
10000
20000
30000
40000
50000
60000

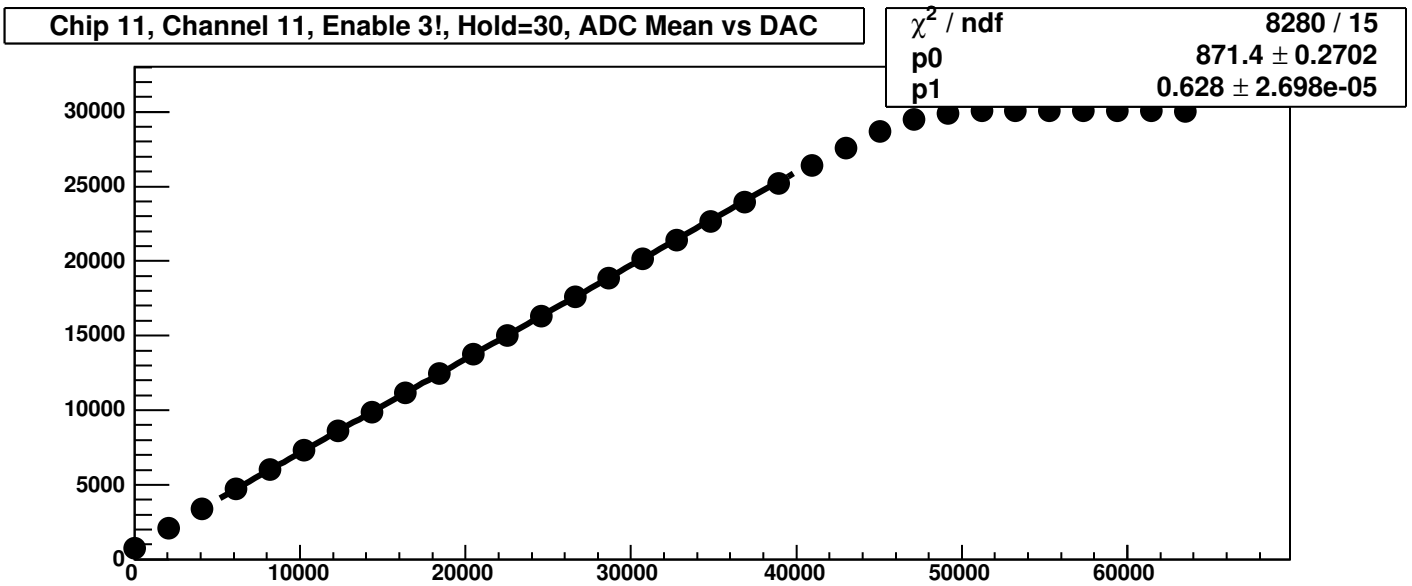

Chip 11, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
5.4
0
10000
20000
30000
40000
50000
60000

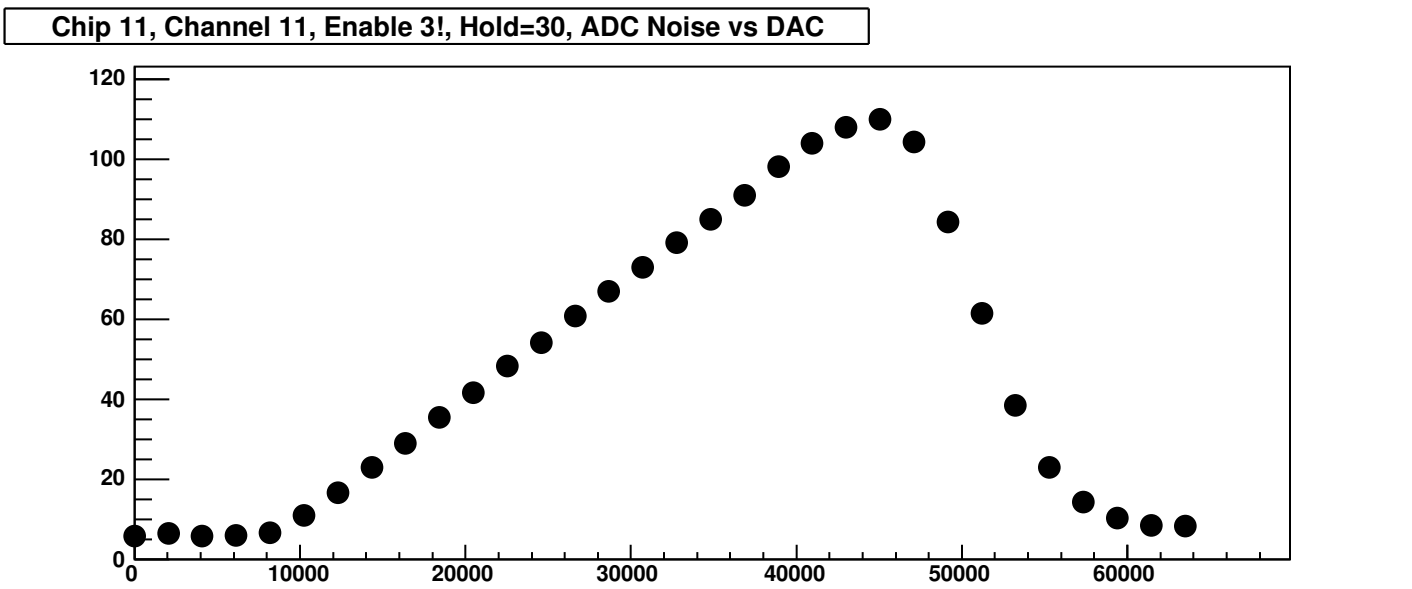

Chip 11, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

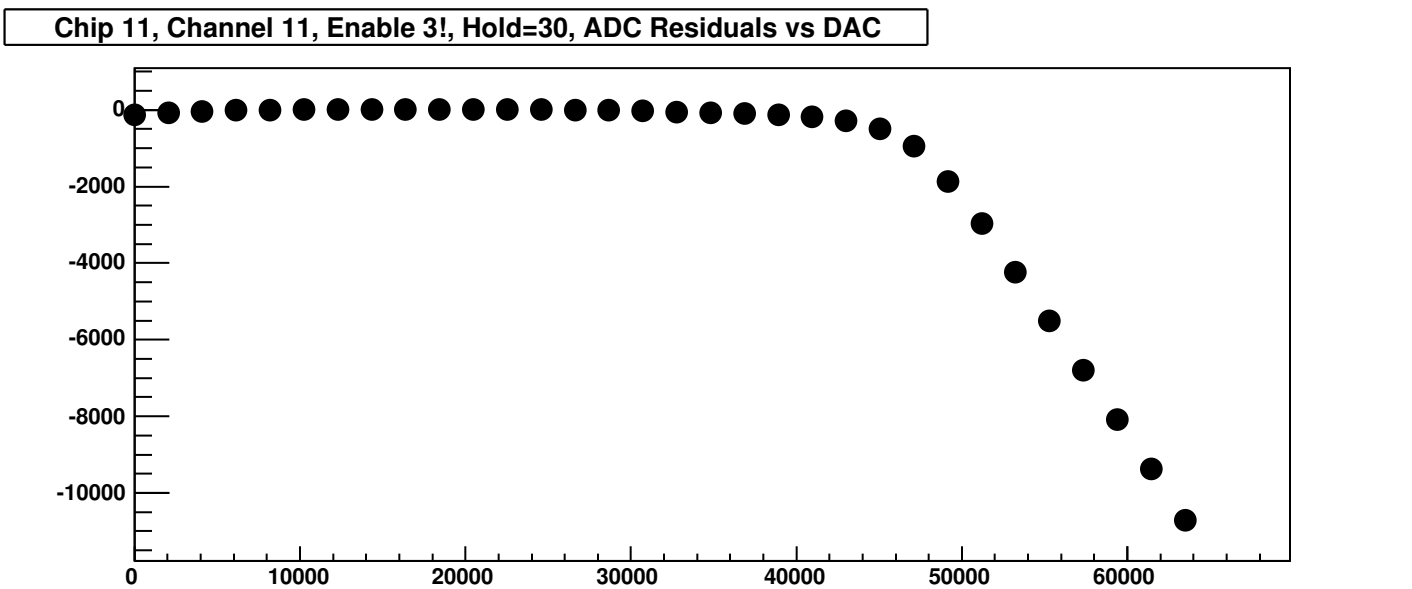

Chip 11, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3960 / 15
p0 752.7 ± 0.1079
p1 0.0005084 ± 4.416e-06
780
760
740
720
700
680
660
640
620
0
10000
20000
30000
40000
50000
60000

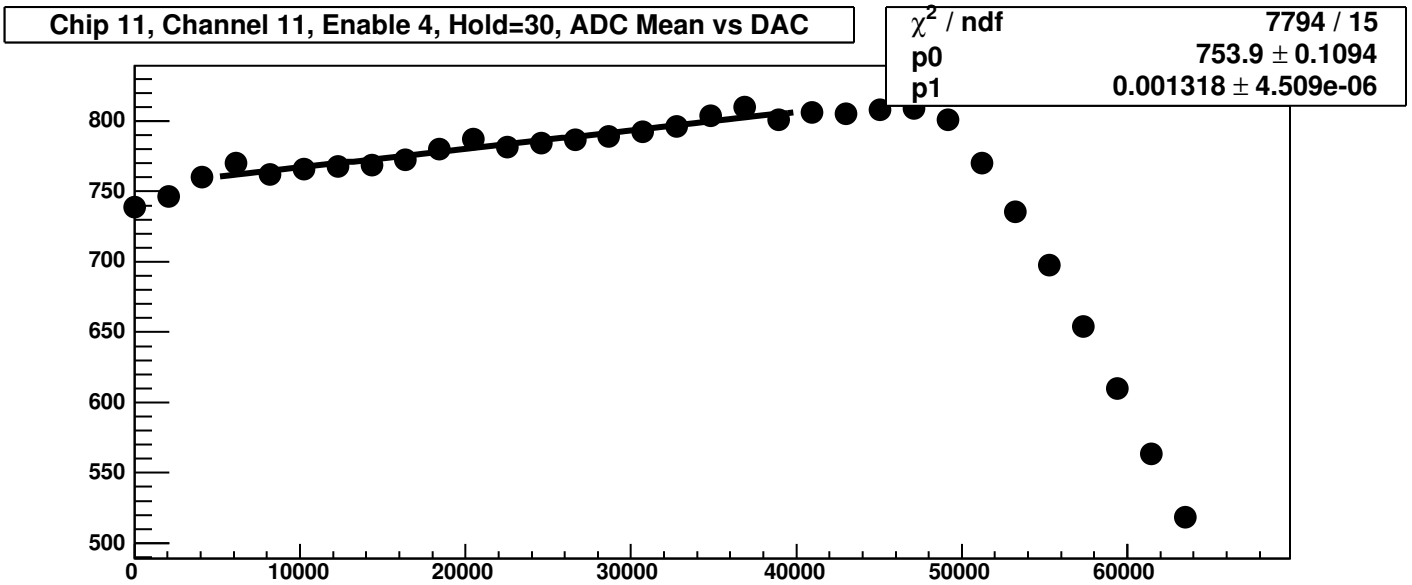

Chip 11, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

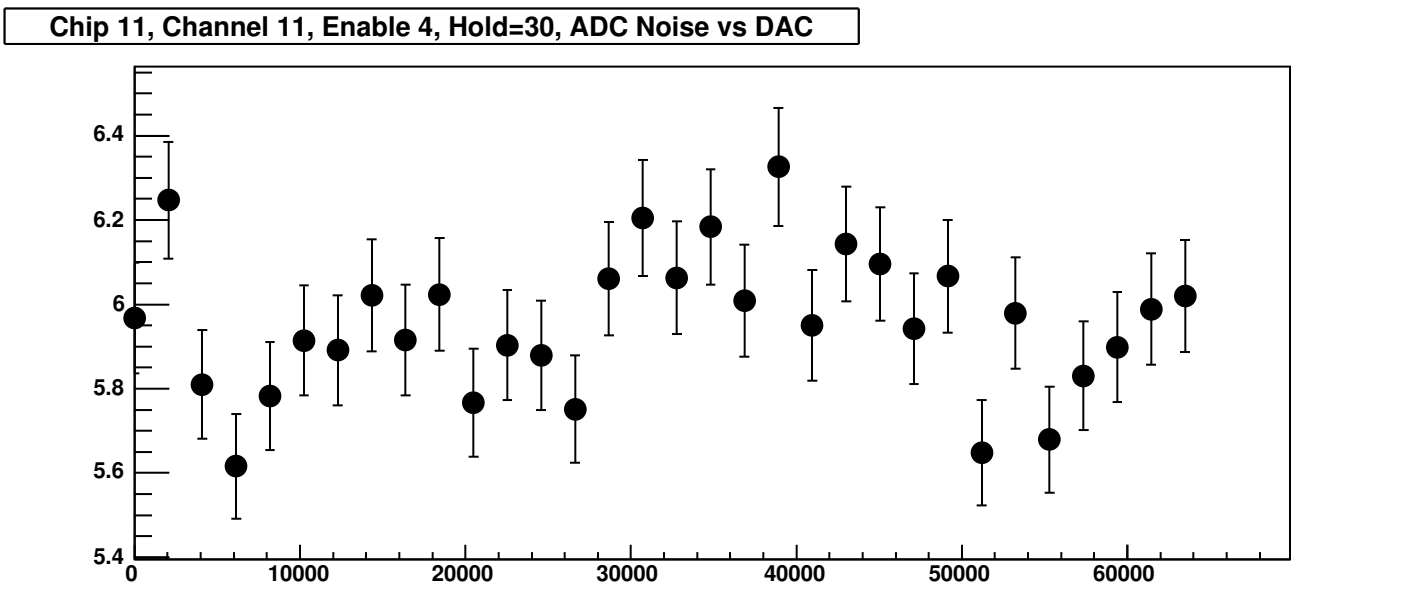

Chip 11, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

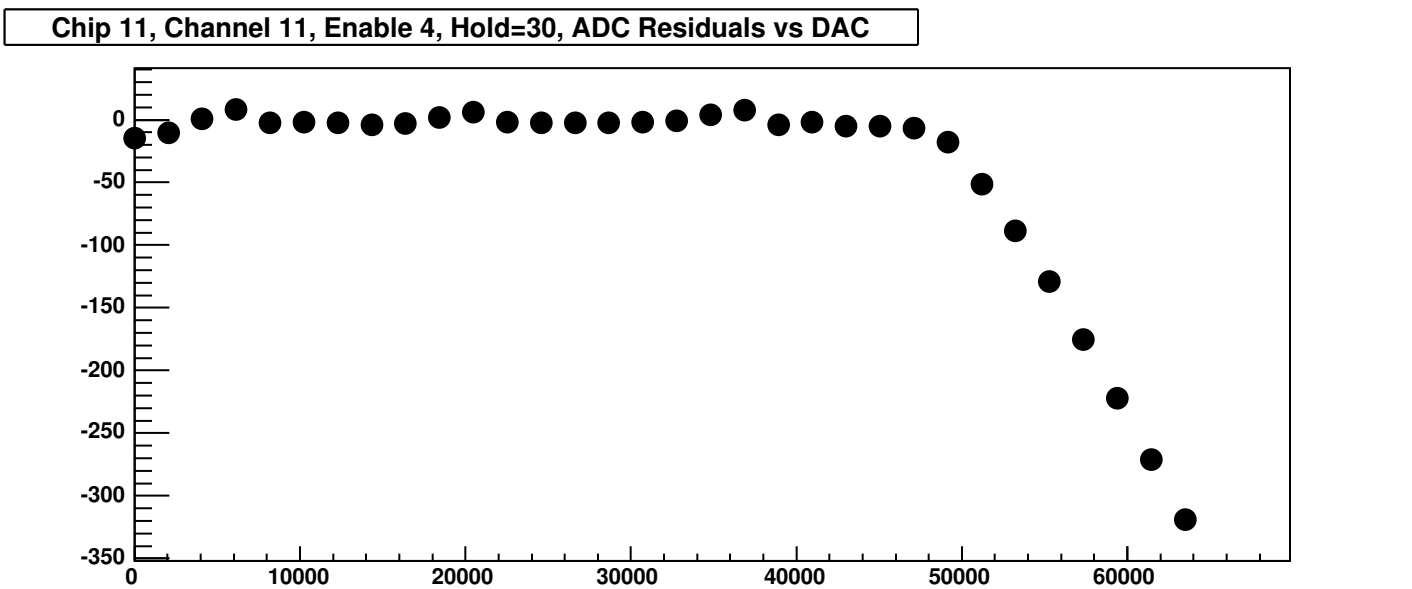

Chip 11, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 8280 / 15
p0 871.4 ± 0.2702
p1 0.628 ± 2.698e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

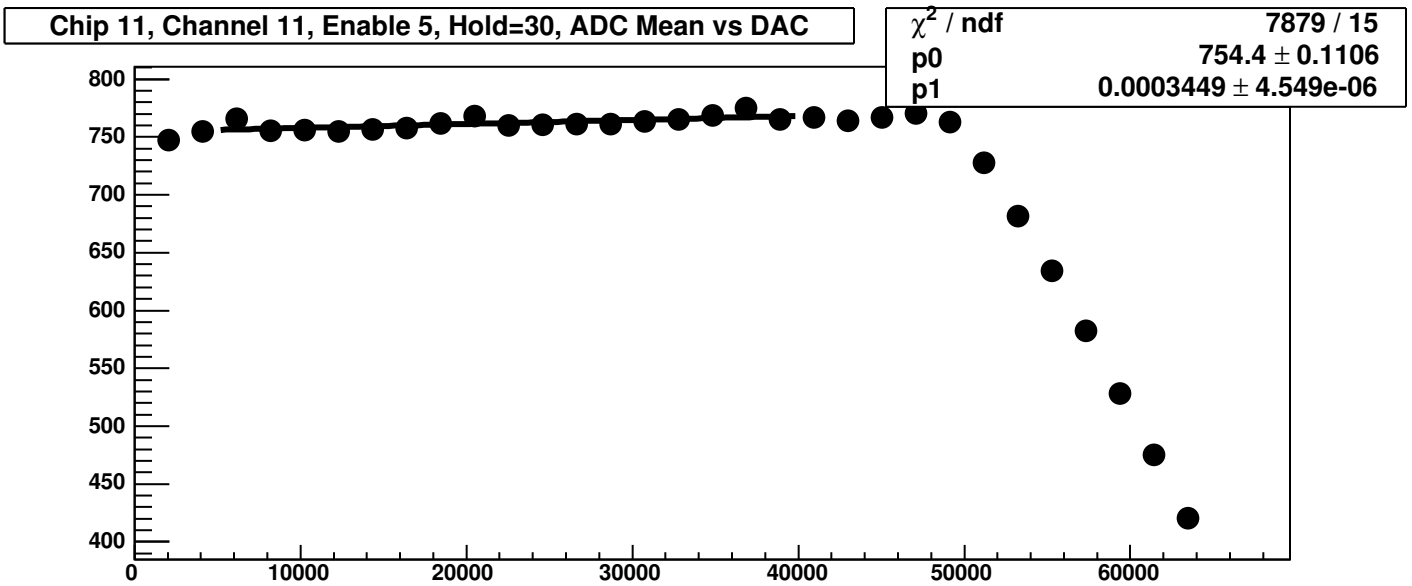

Chip 11, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

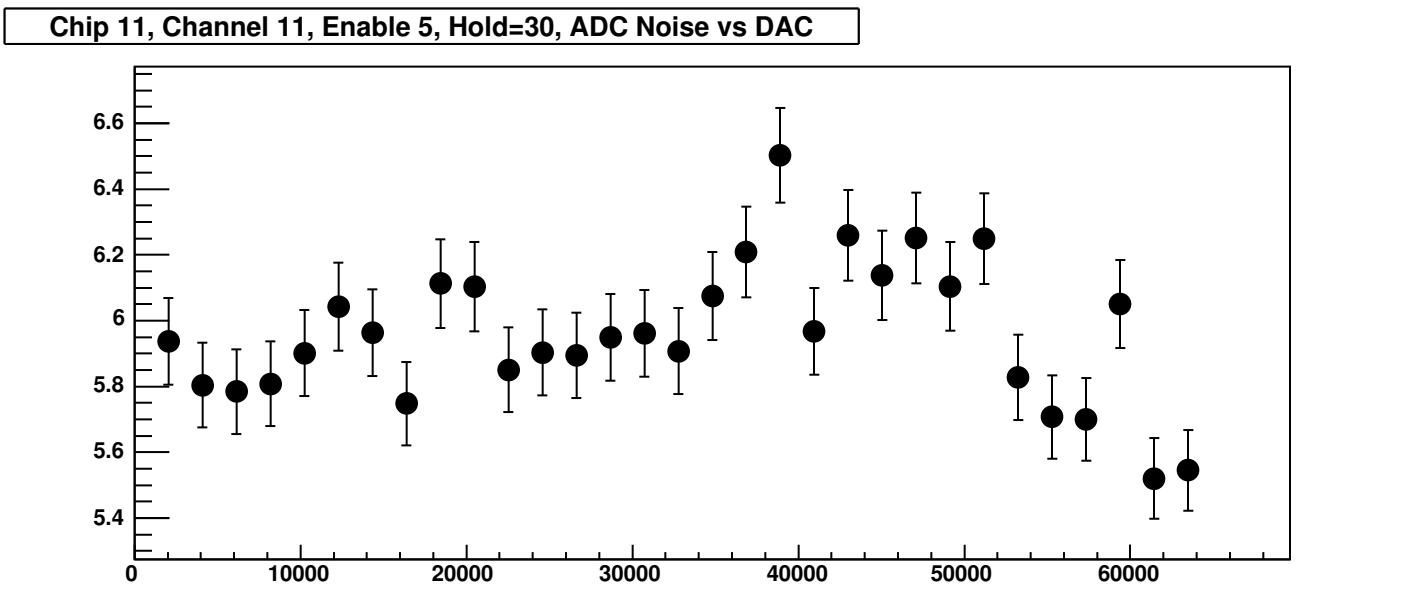

Chip 11, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

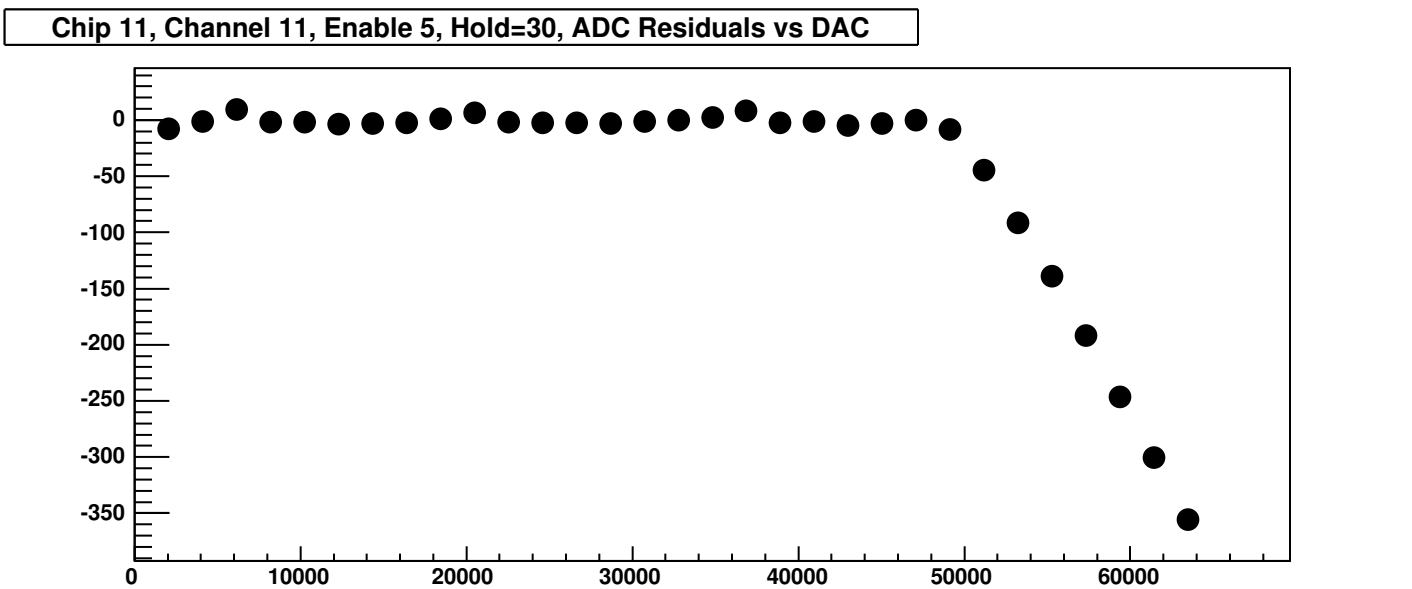

Chip 11, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7794 / 15
p0 753.9 ± 0.1094
p1 0.001318 ± 4.509e-06
800
750
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000

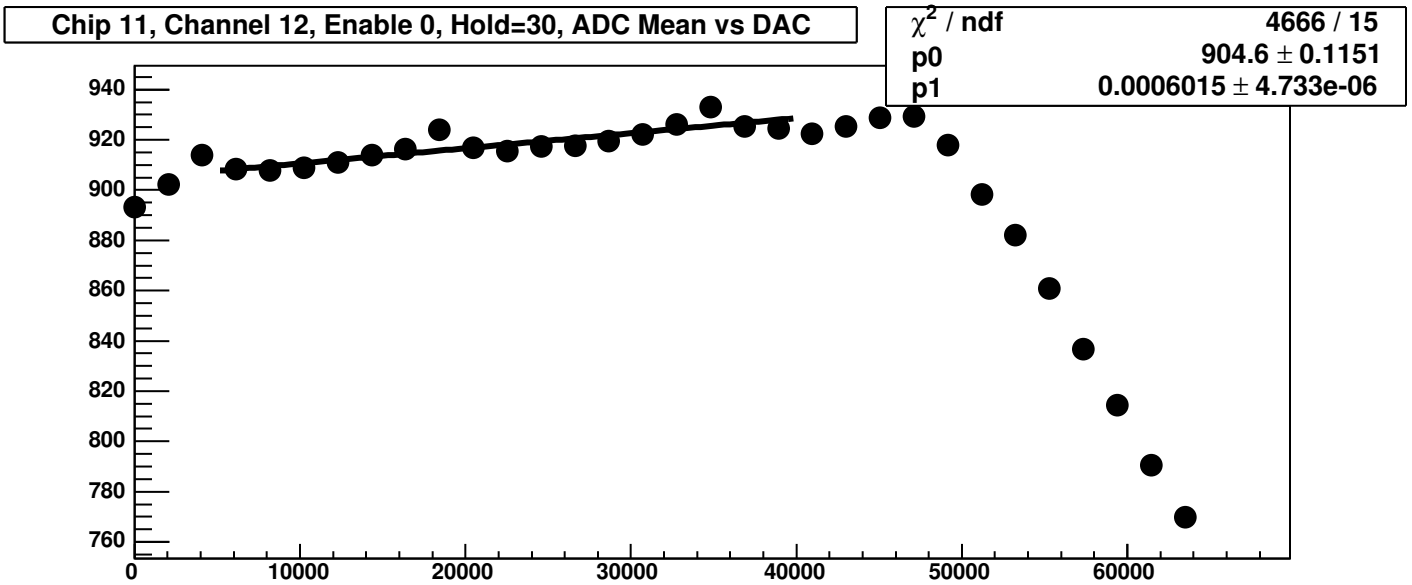

Chip 11, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

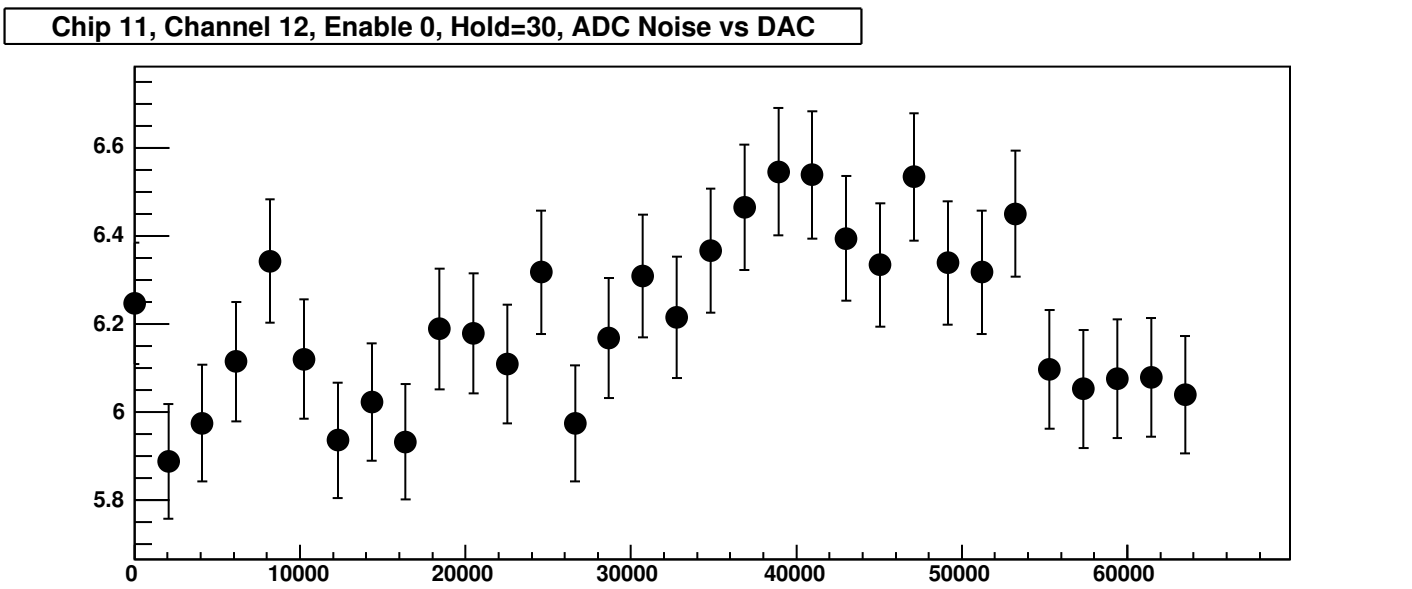

Chip 11, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

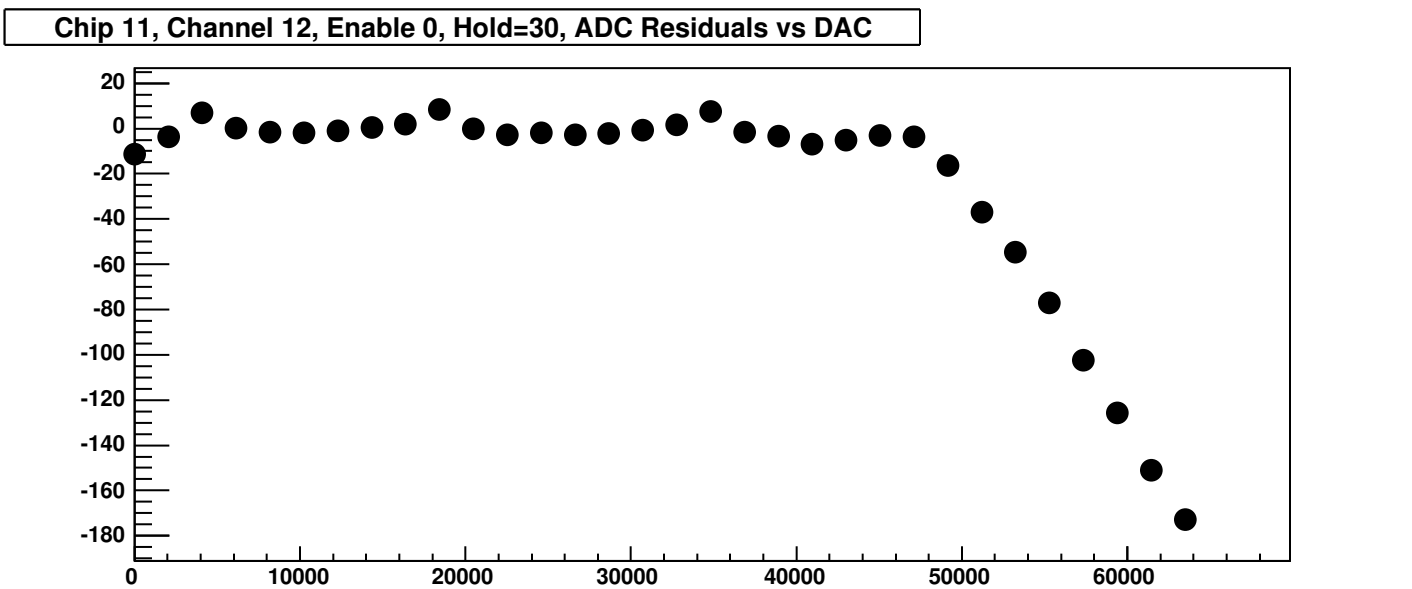

Chip 11, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7879 / 15
p0 754.4 ± 0.1106
p1 0.0003449 ± 4.549e-06
800
750
700
650
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000

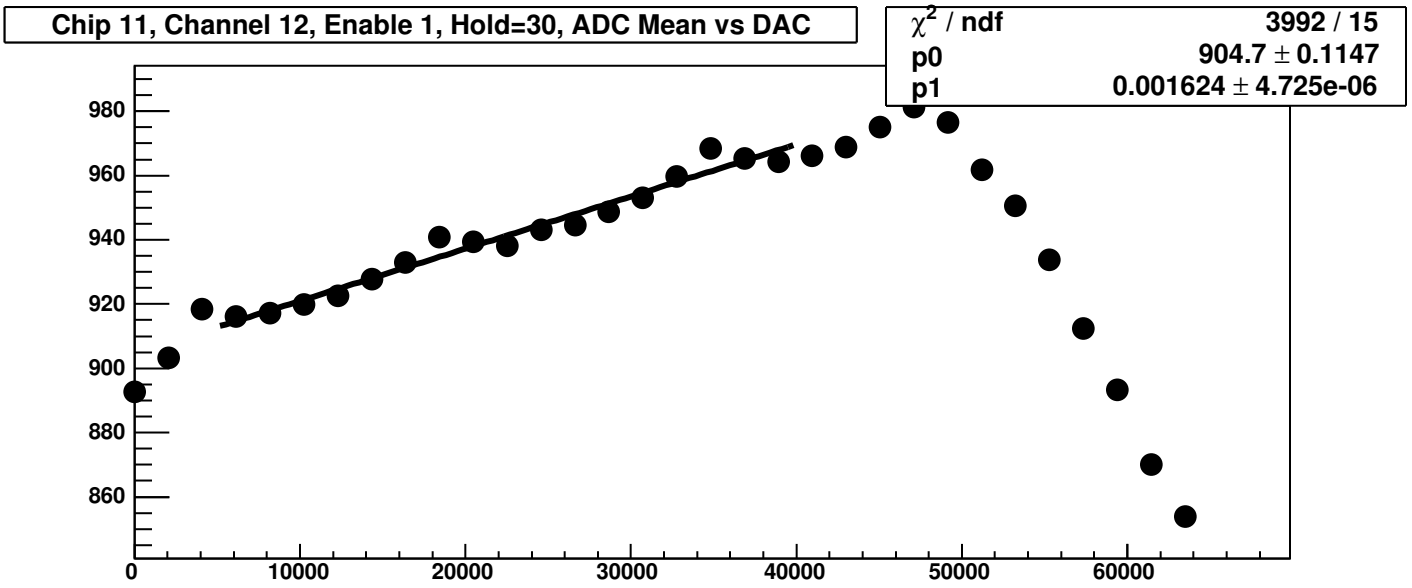

Chip 11, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

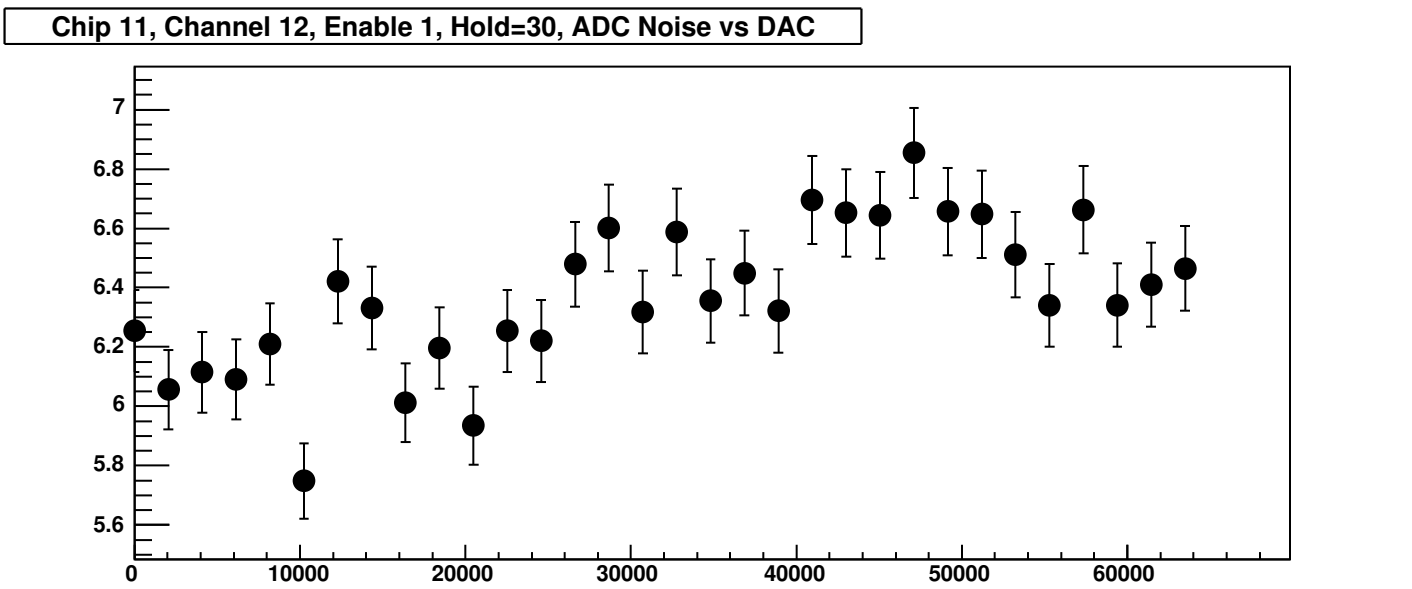

Chip 11, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
0
10000
20000
30000
40000
50000
60000

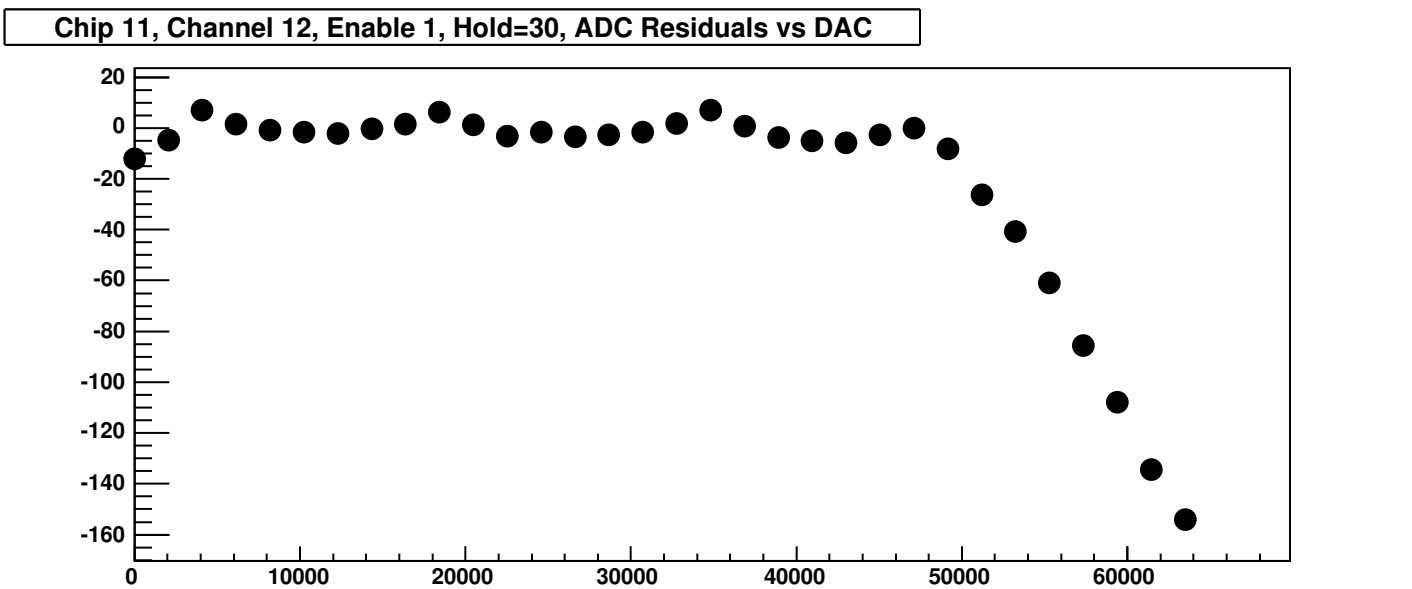

Chip 11, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4666 / 15
p0 904.6 ± 0.1151
p1 0.0006015 ± 4.733e-06
940
920
900
880
860
840
820
800
780
760
0
10000
20000
30000
40000
50000
60000

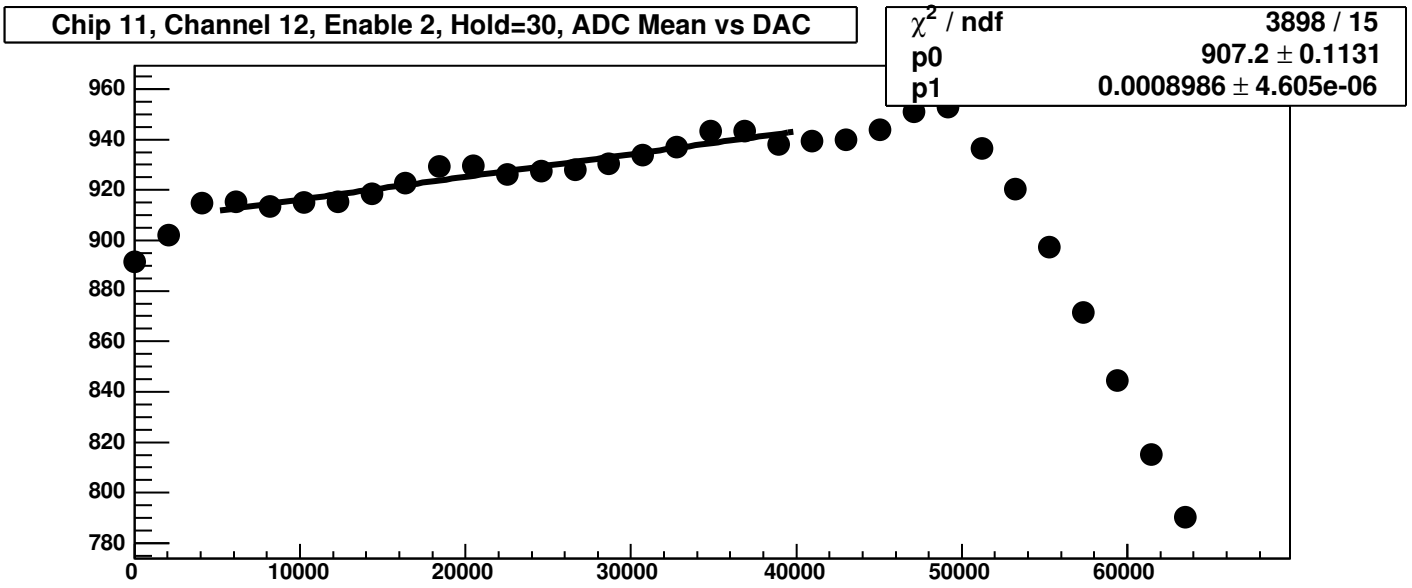

Chip 11, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

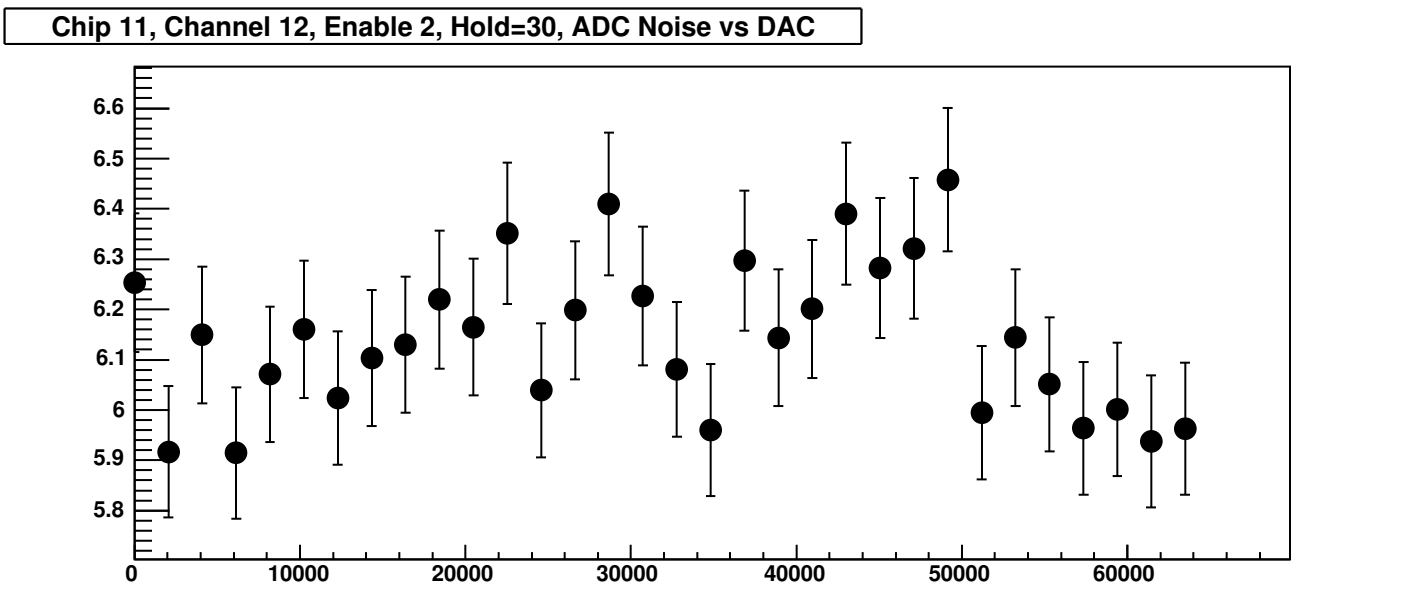

Chip 11, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

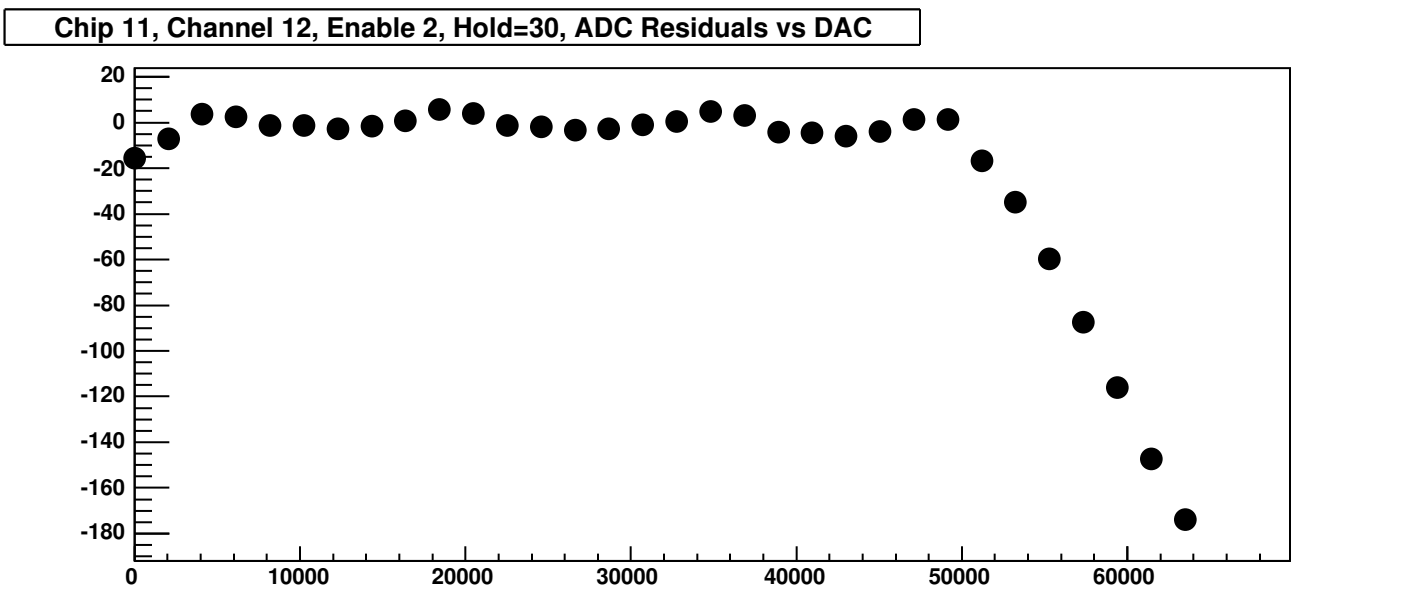

Chip 11, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3992 / 15
p0 904.7 ± 0.1147
p1 0.001624 ± 4.725e-06
980
960
940
920
900
880
860
0
10000
20000
30000
40000
50000
60000

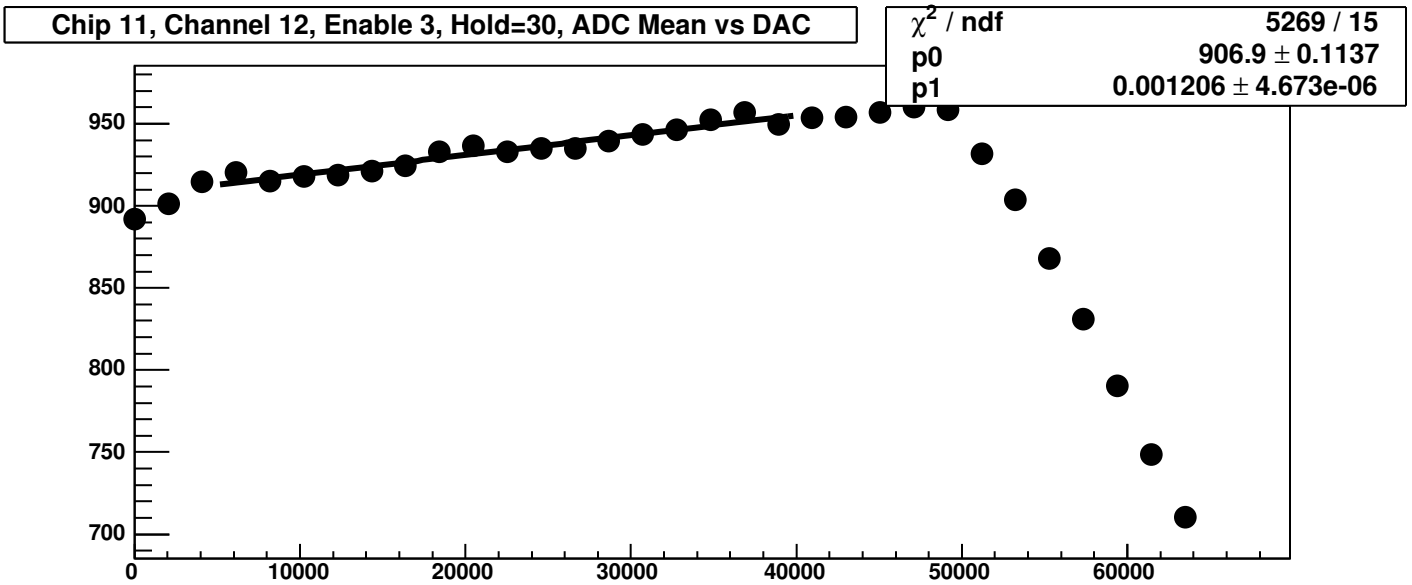

Chip 11, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

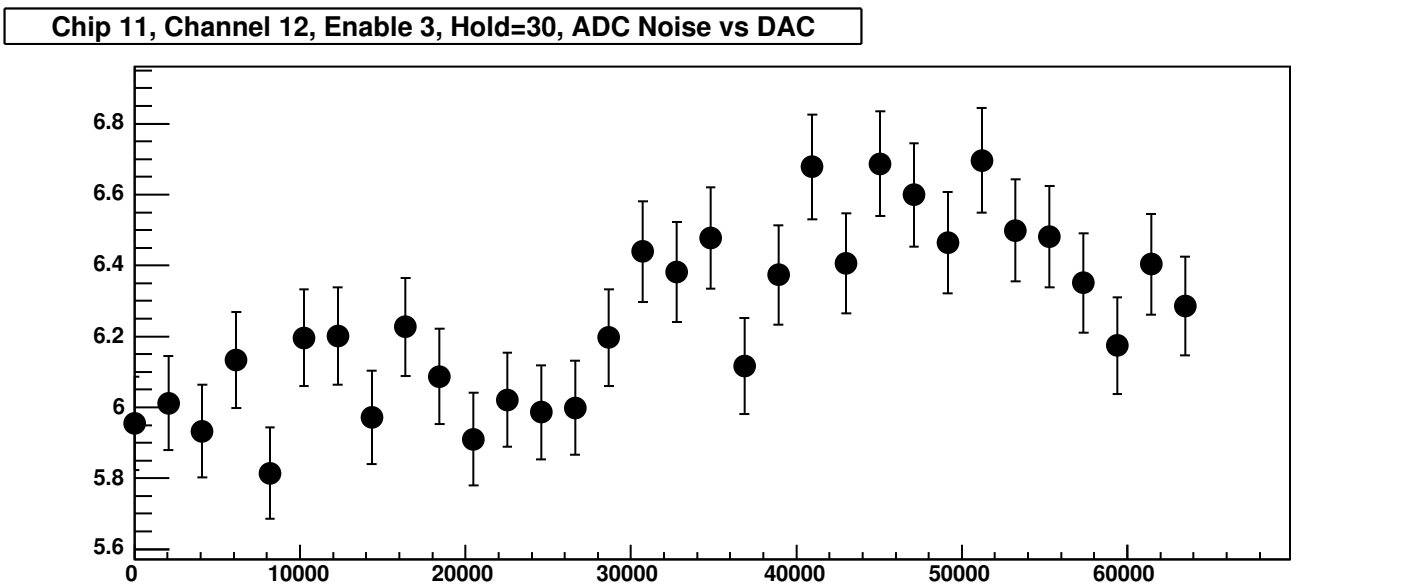

Chip 11, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

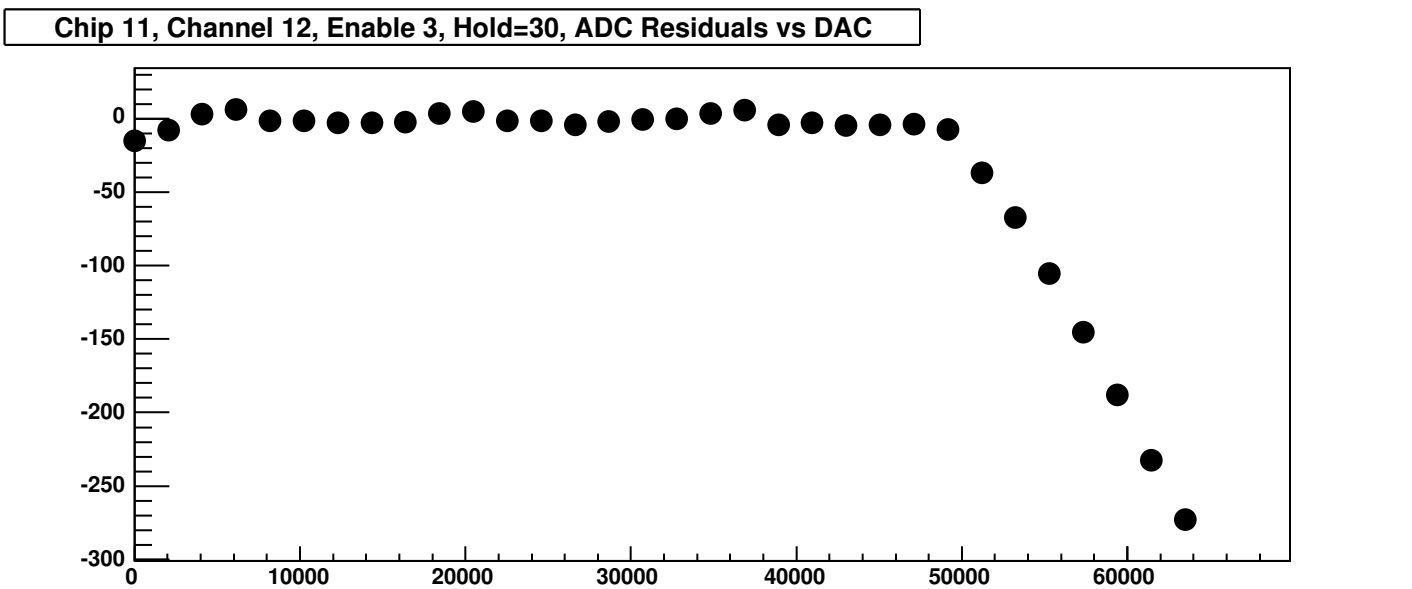

Chip 11, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3898 / 15
p0 907.2 ± 0.1131
p1 0.0008986 ± 4.605e-06
960
940
920
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

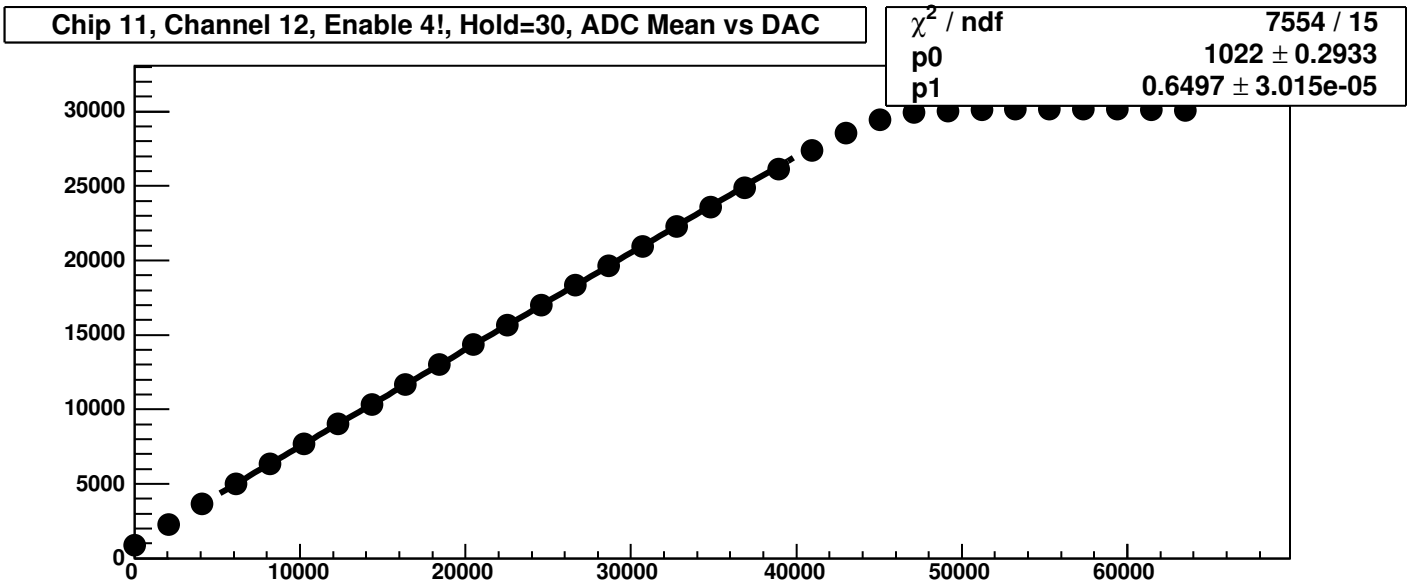

Chip 11, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
6.6
6.5
6.4
6.3
6.2
6.1
6
5.9
5.8
0
10000
20000
30000
40000
50000
60000

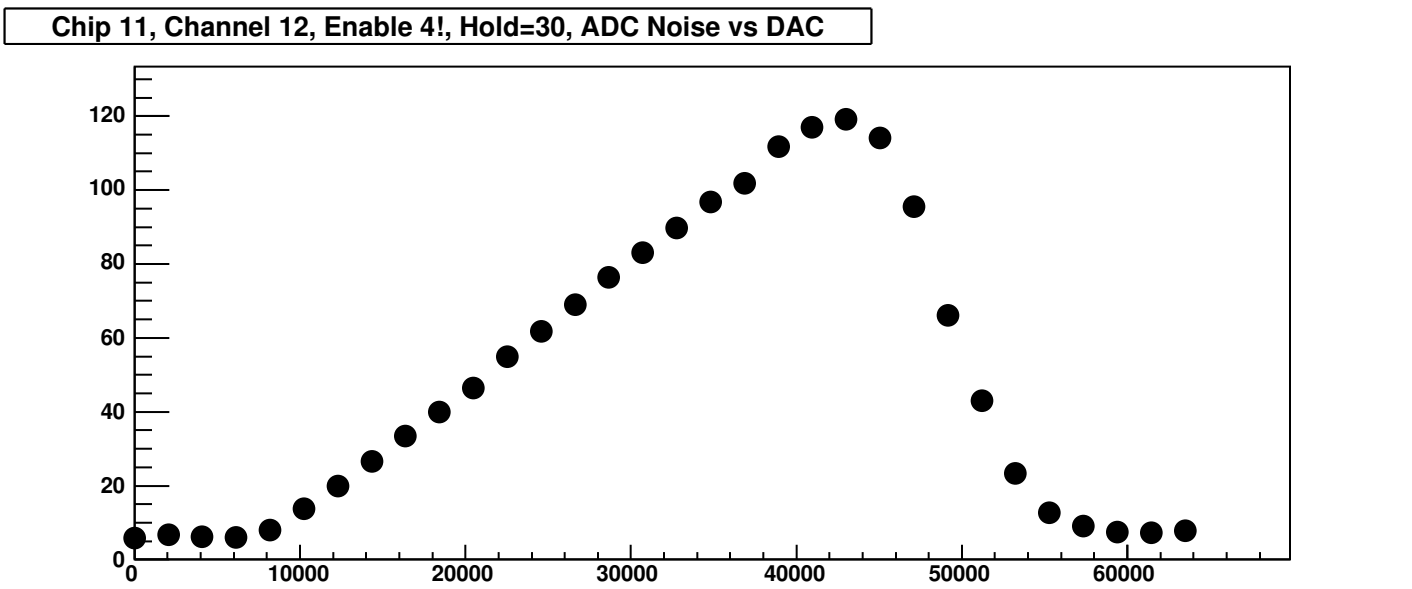

Chip 11, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

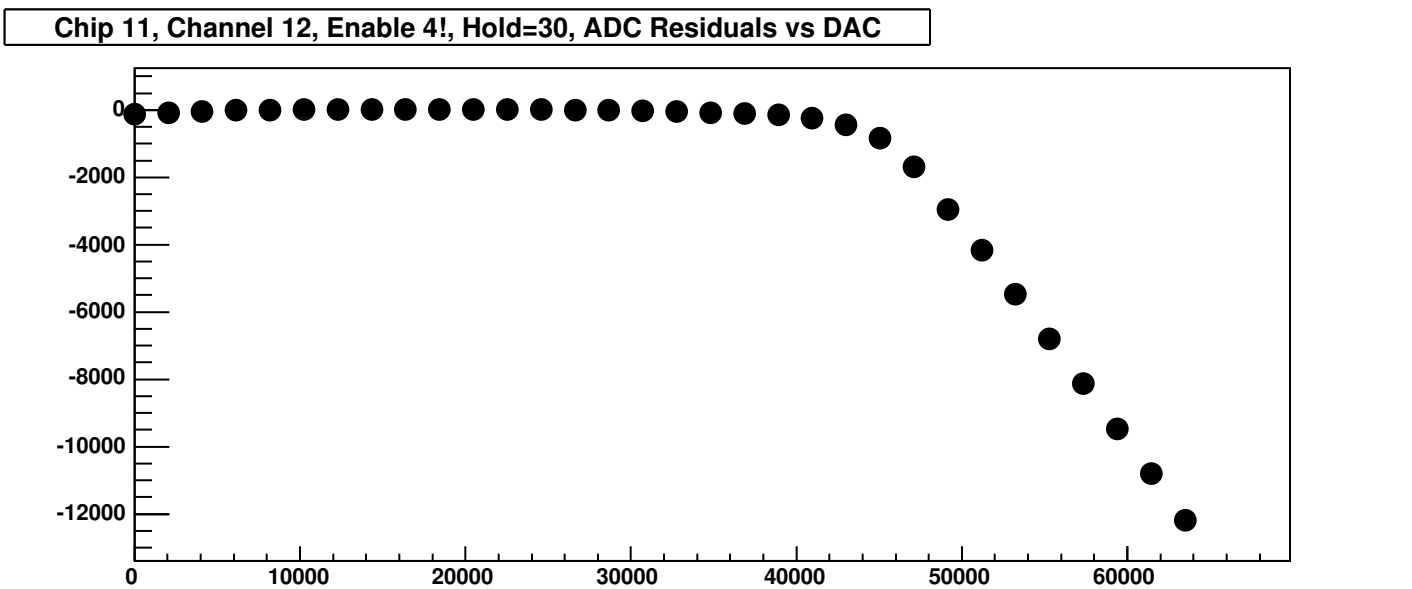

Chip 11, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5269 / 15
p0 906.9 ± 0.1137
p1 0.001206 ± 4.673e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

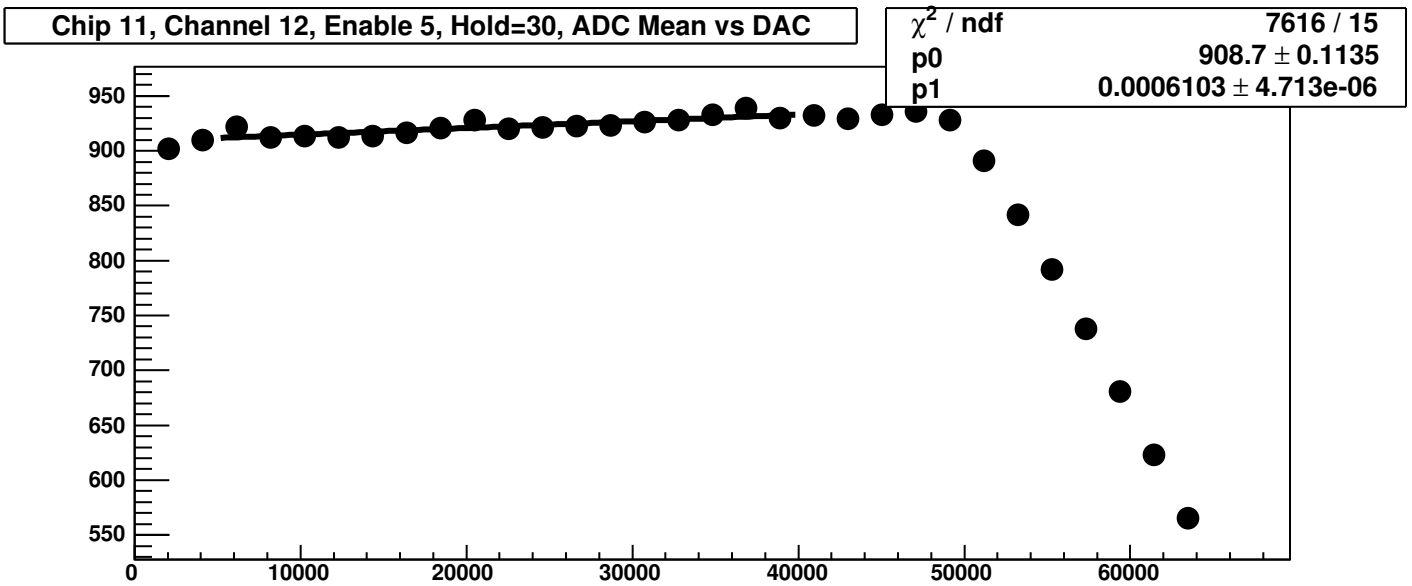

Chip 11, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

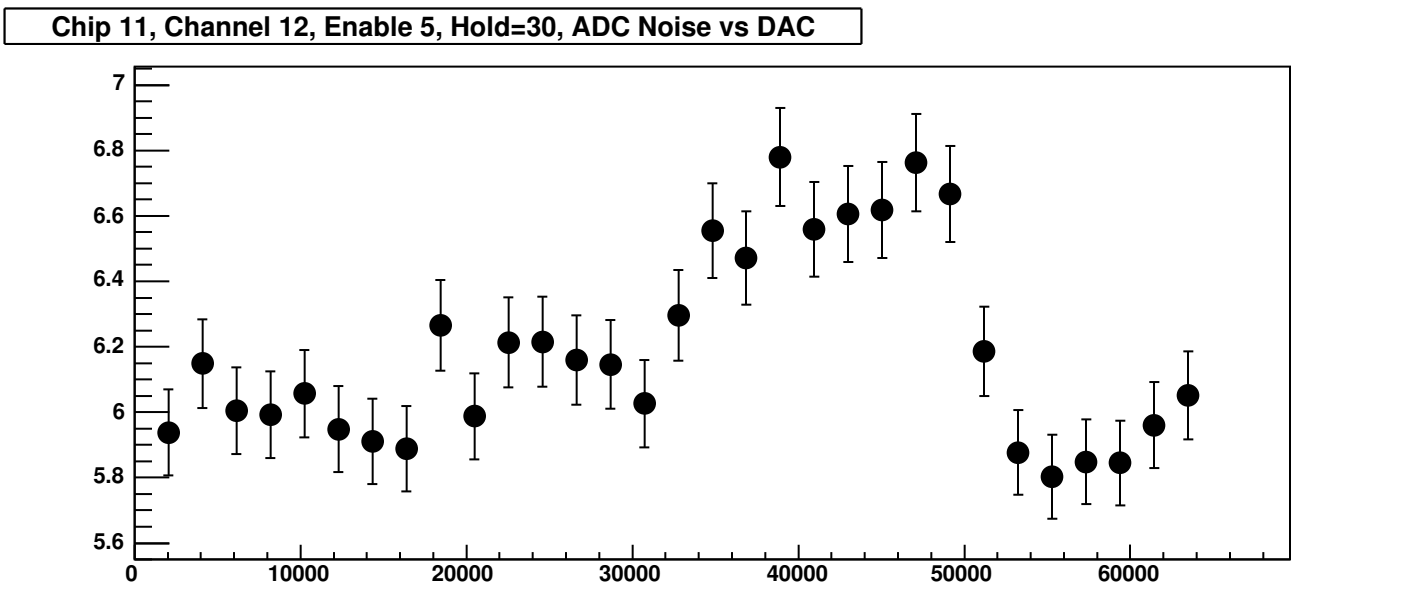

Chip 11, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

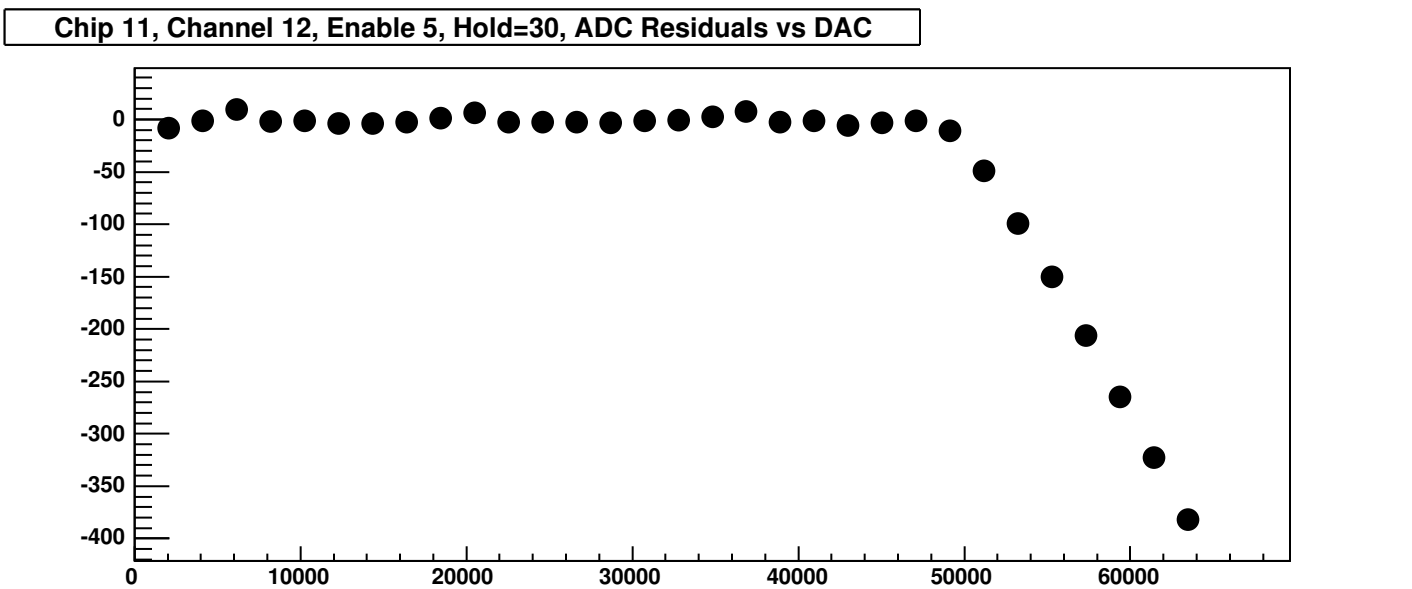

Chip 11, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 7554 / 15
p0 1022 ± 0.2933
p1 0.6497 ± 3.015e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

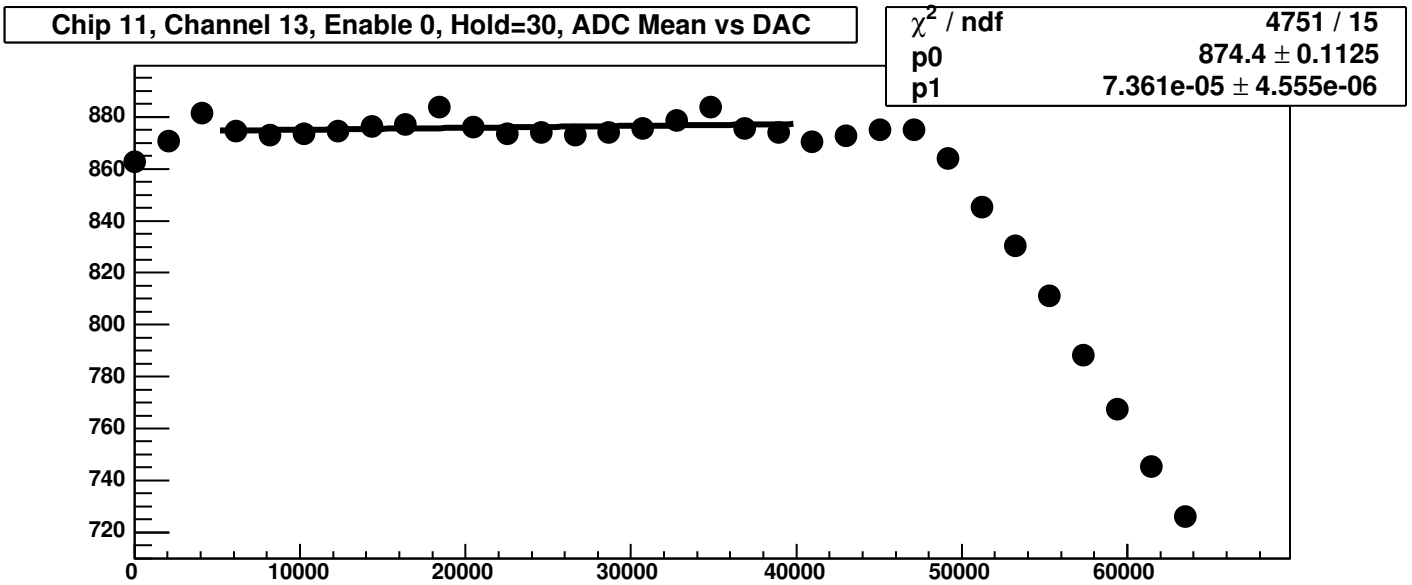

Chip 11, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

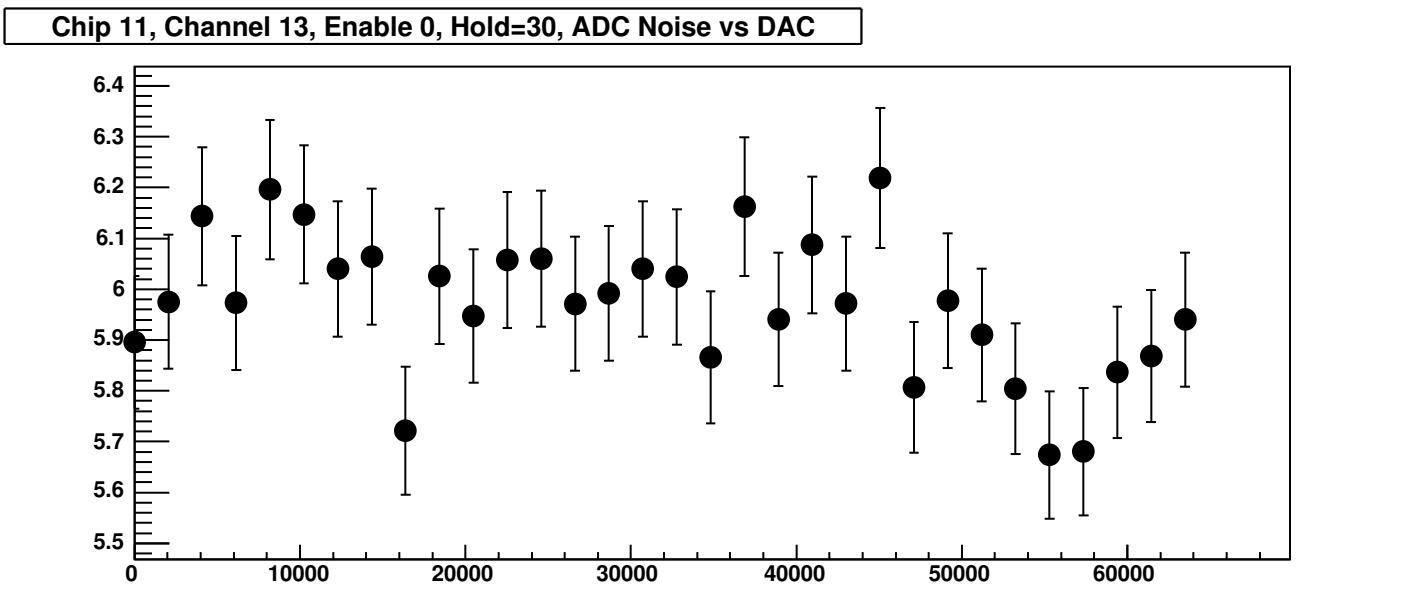

Chip 11, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

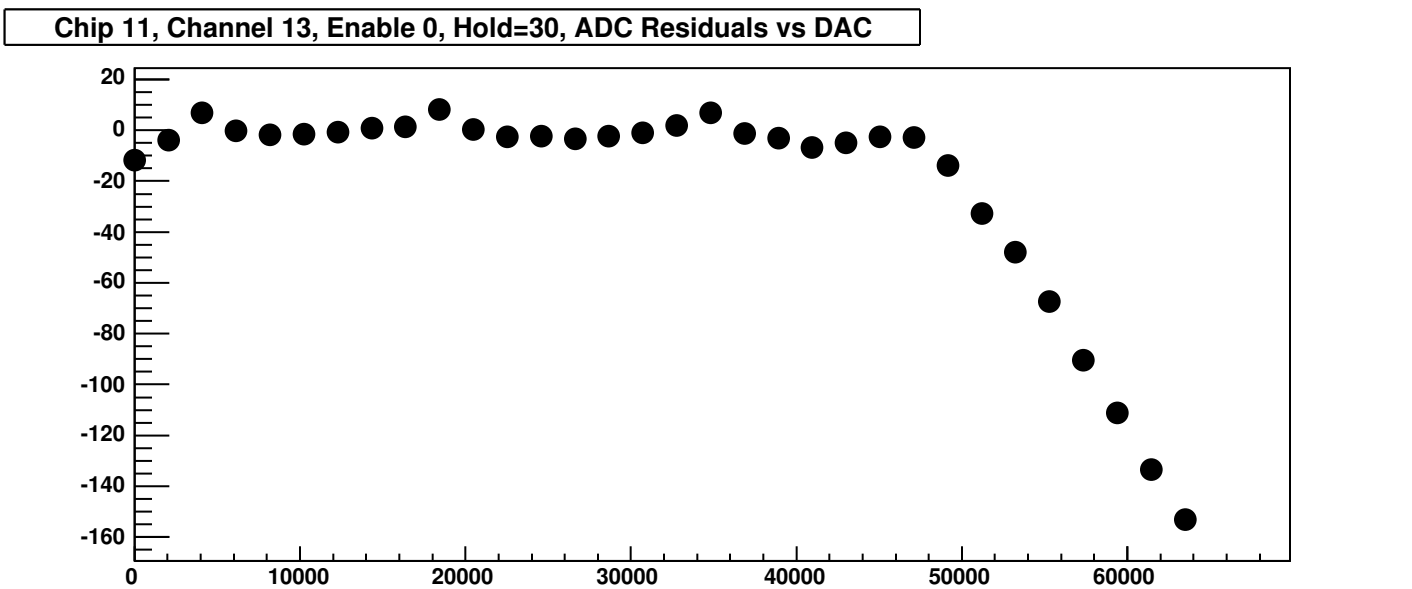

Chip 11, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7616 / 15
p0 908.7 ± 0.1135
p1 0.0006103 ± 4.713e-06
950
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

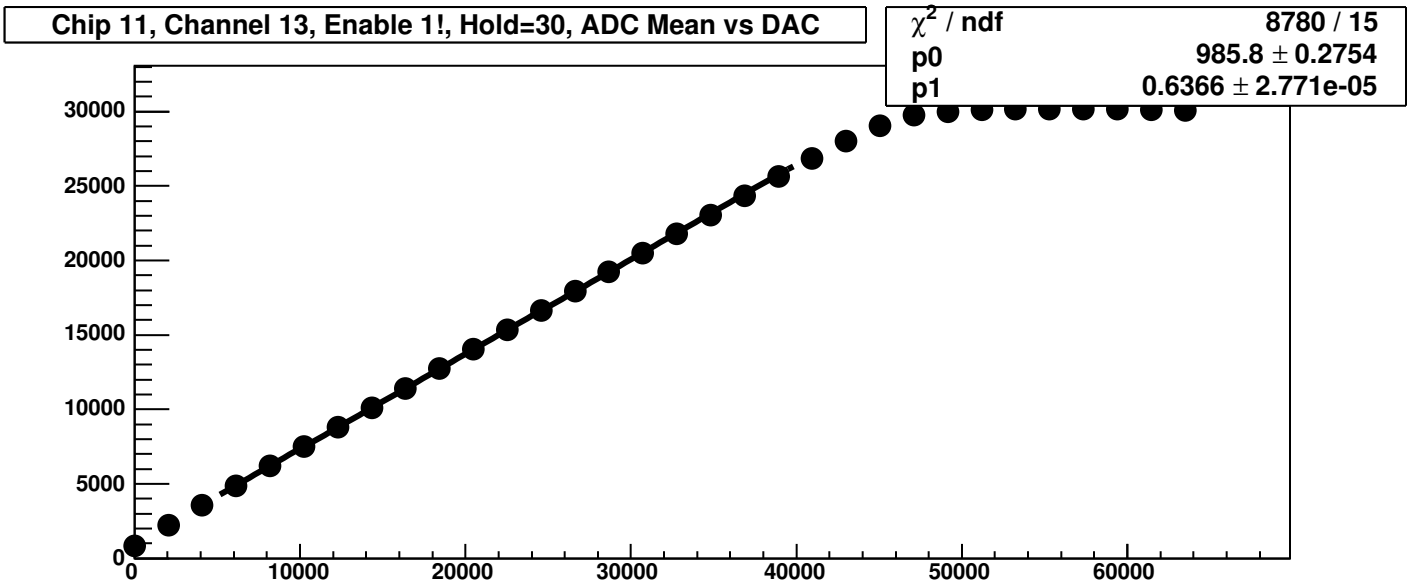

Chip 11, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

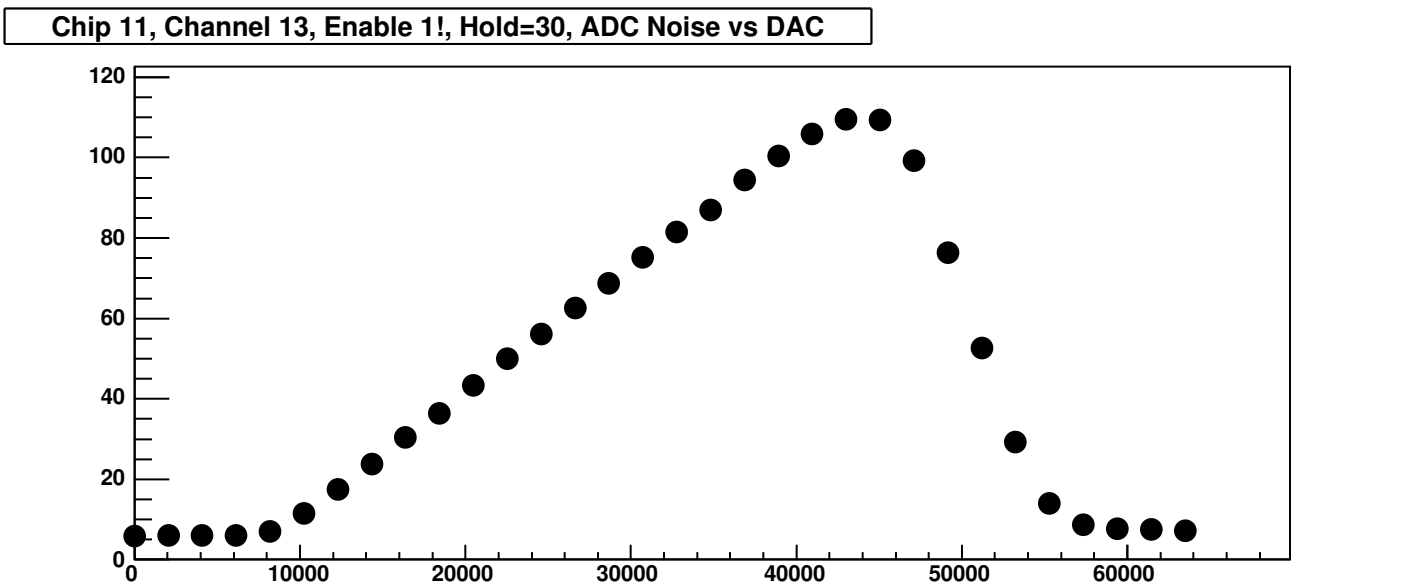

Chip 11, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

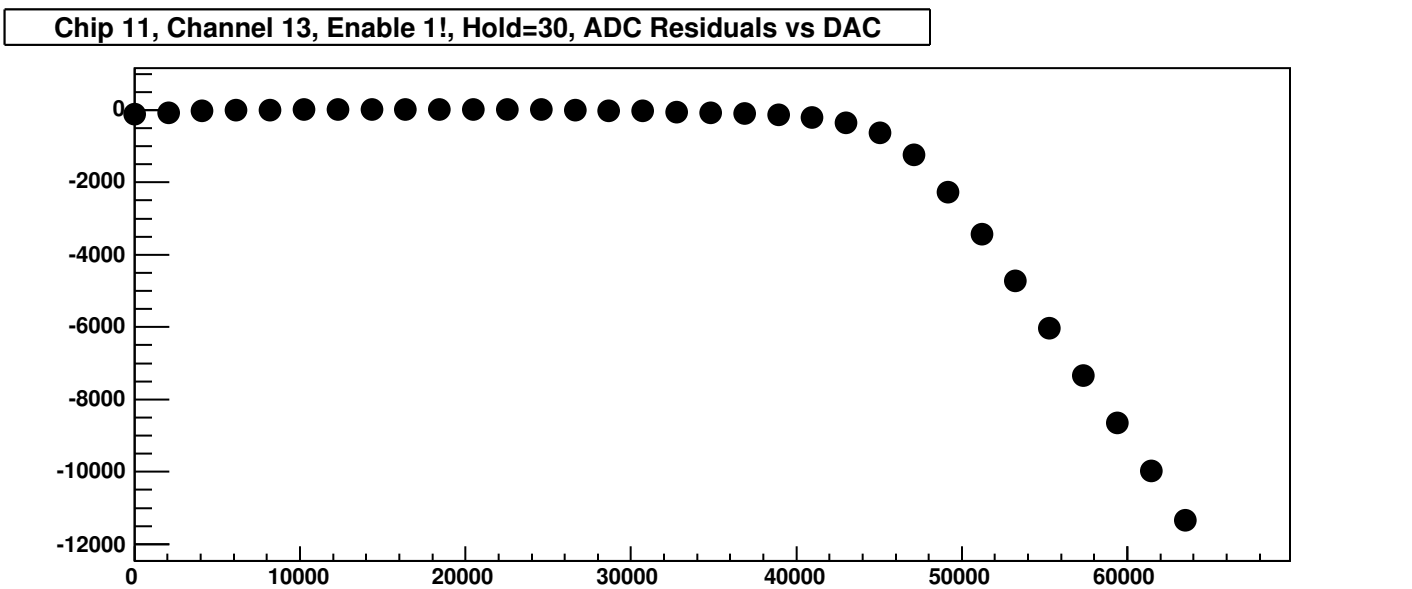

Chip 11, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4751 / 15
p0 874.4 ± 0.1125
p1 7.361e-05 ± 4.555e-06
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

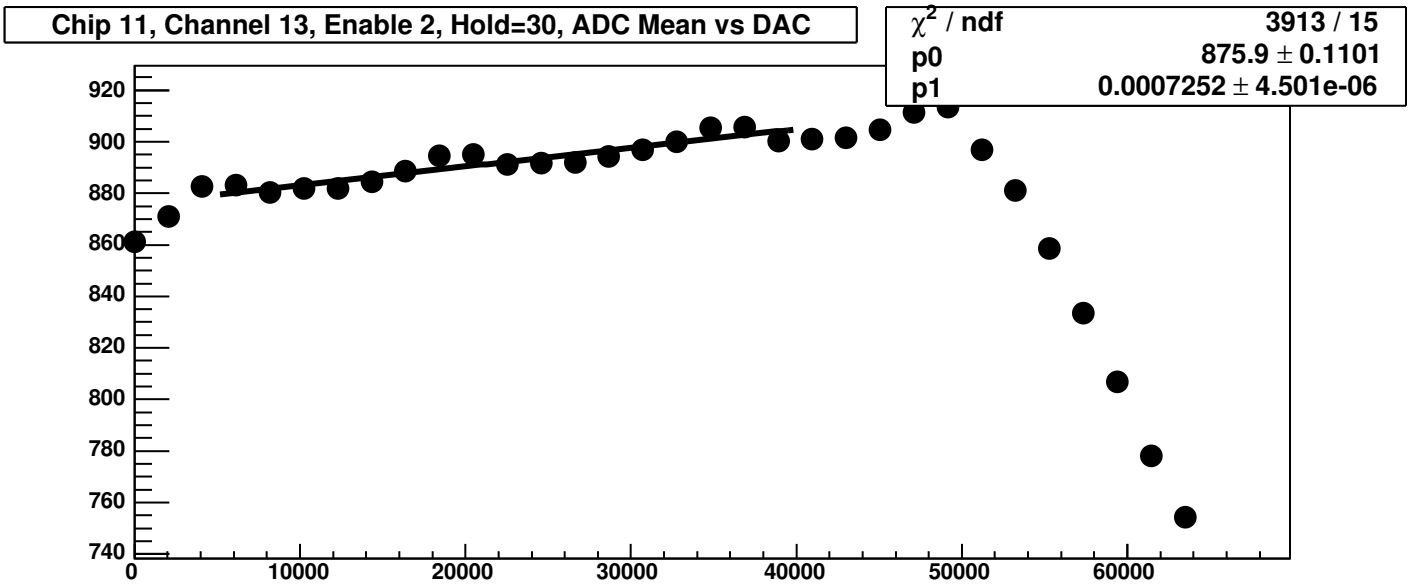

Chip 11, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

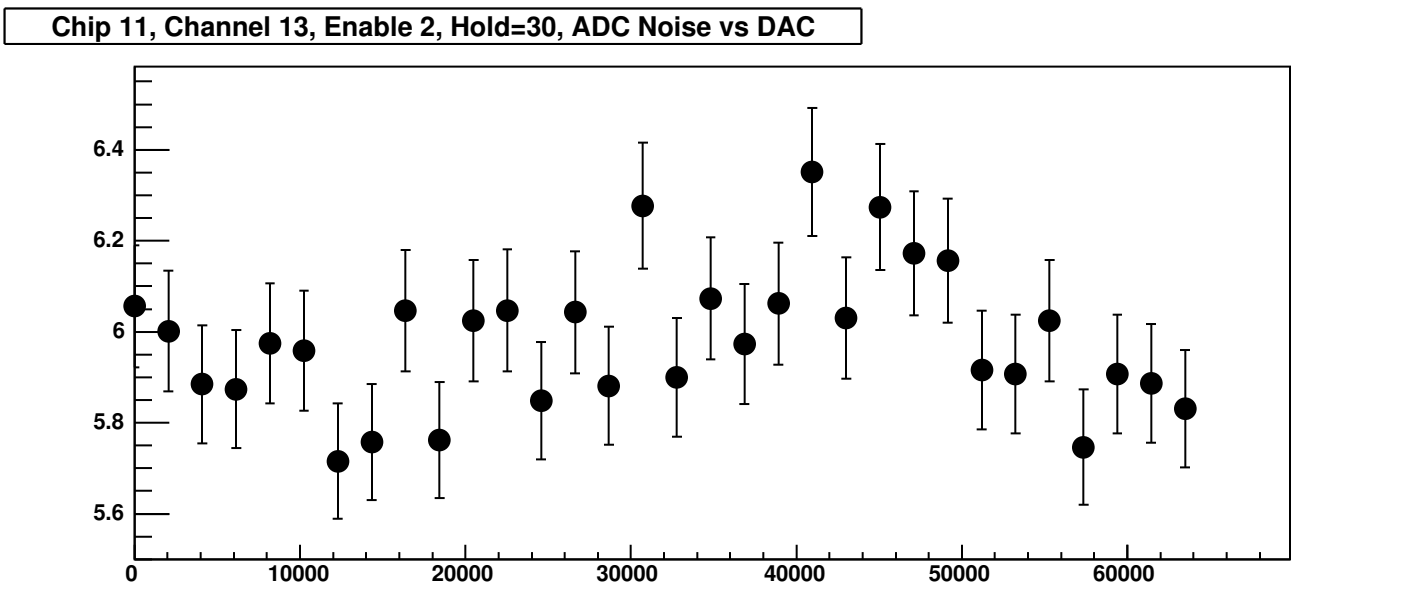

Chip 11, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

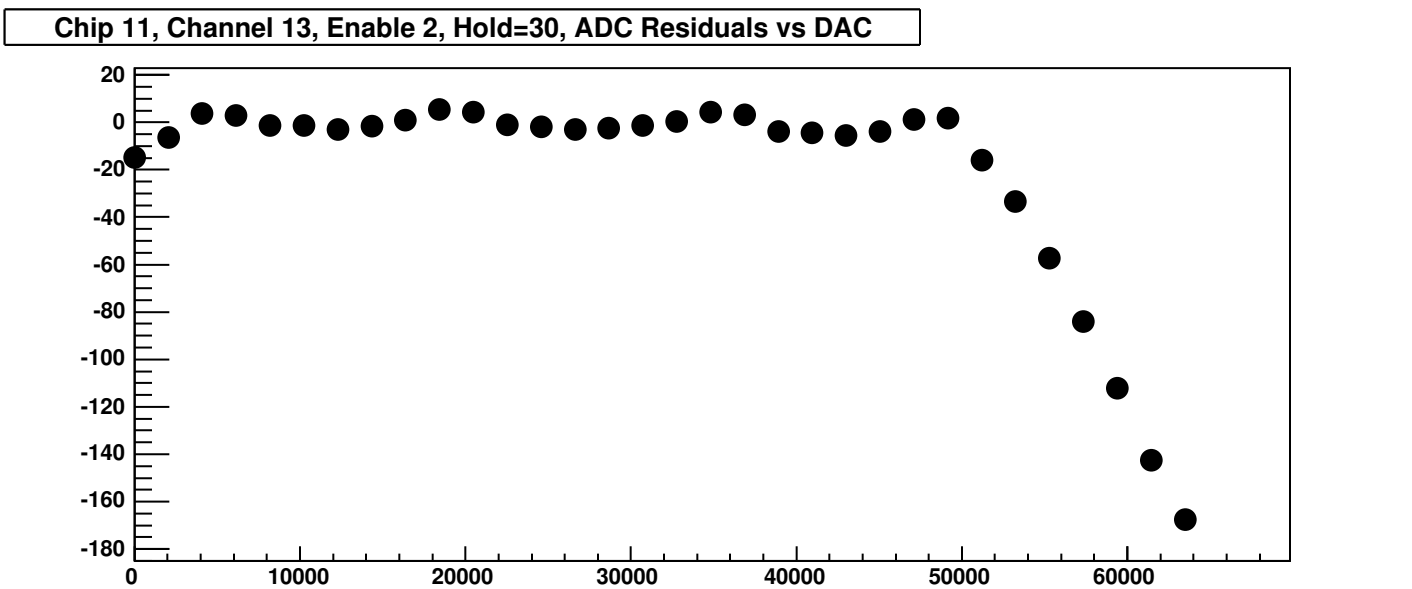

Chip 11, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 8780 / 15
p0 985.8 ± 0.2754
p1 0.6366 ± 2.771e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

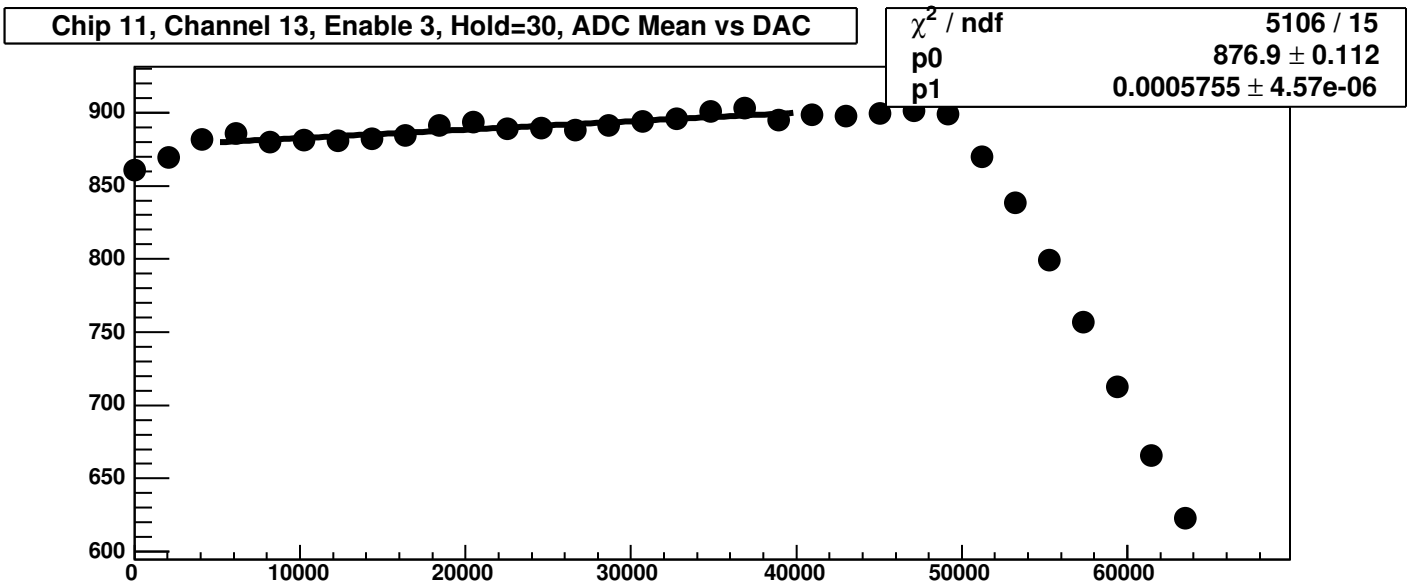

Chip 11, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

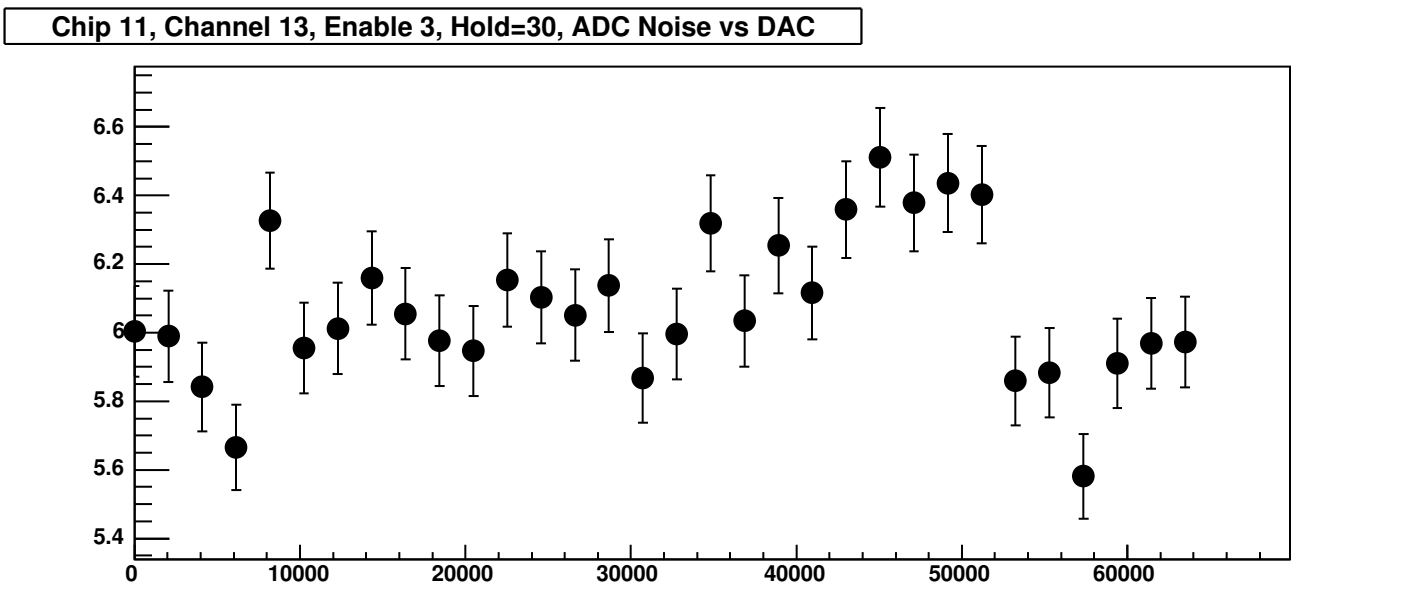

Chip 11, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

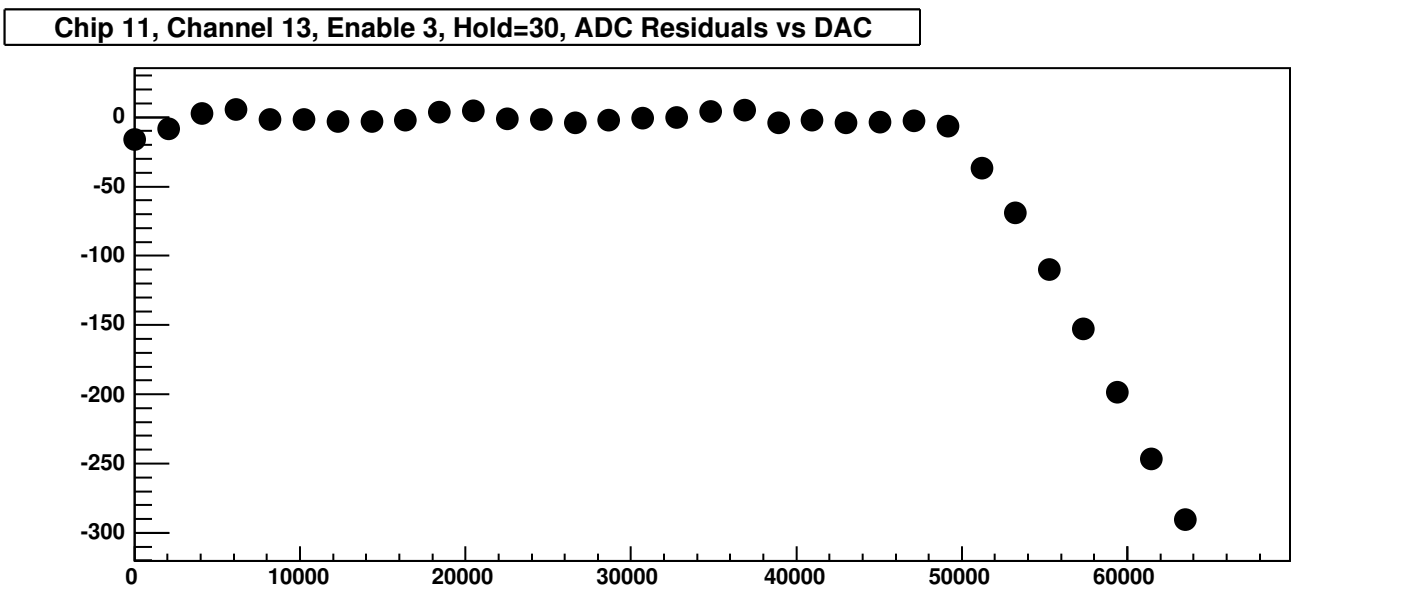

Chip 11, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3913 / 15
p0 875.9 ± 0.1101
p1 0.0007252 ± 4.501e-06
920
900
880
860
840
820
800
780
760
740
0
10000
20000
30000
40000
50000
60000

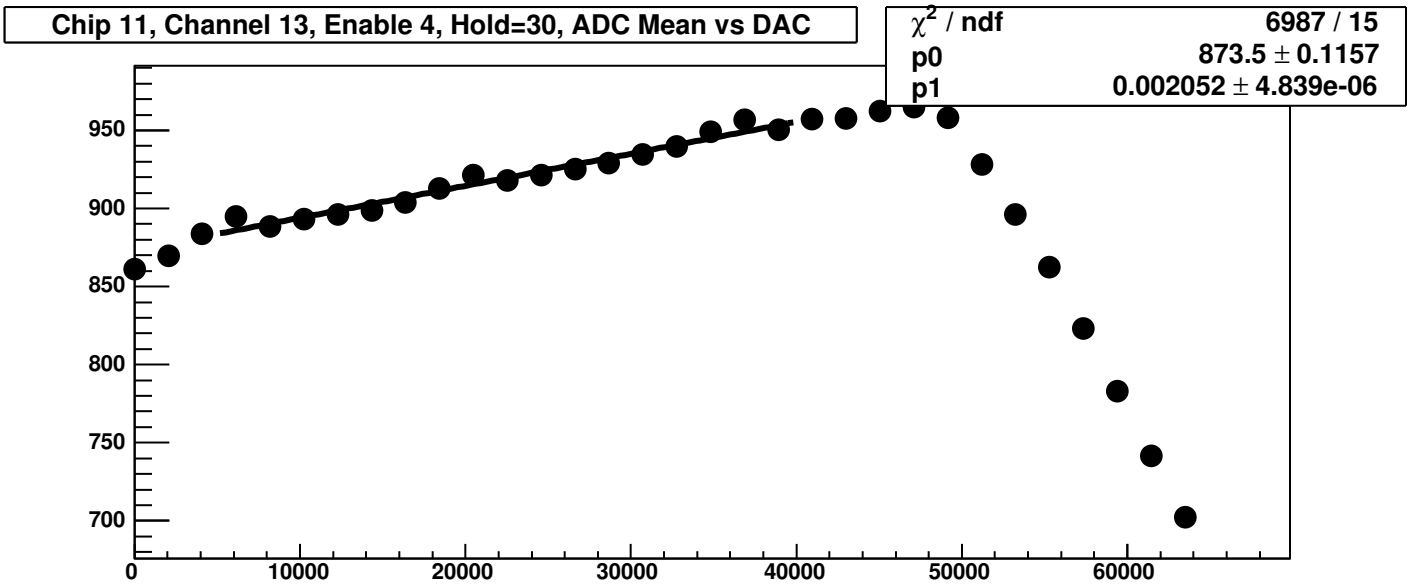

Chip 11, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

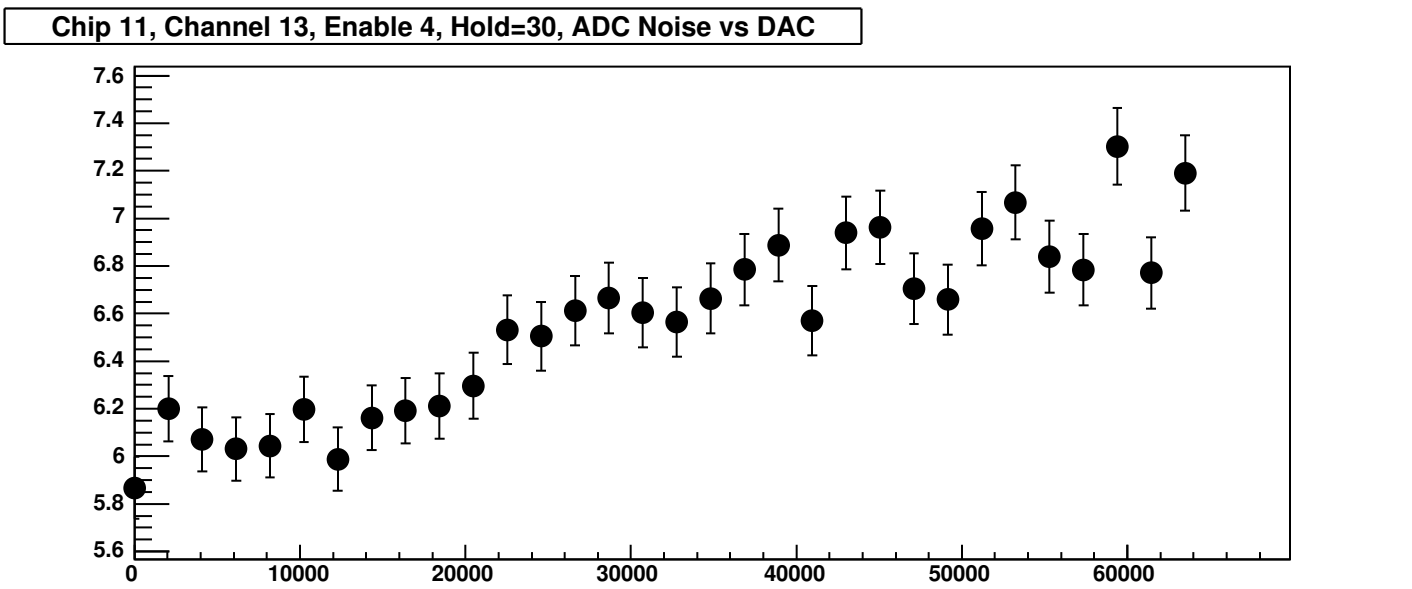

Chip 11, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

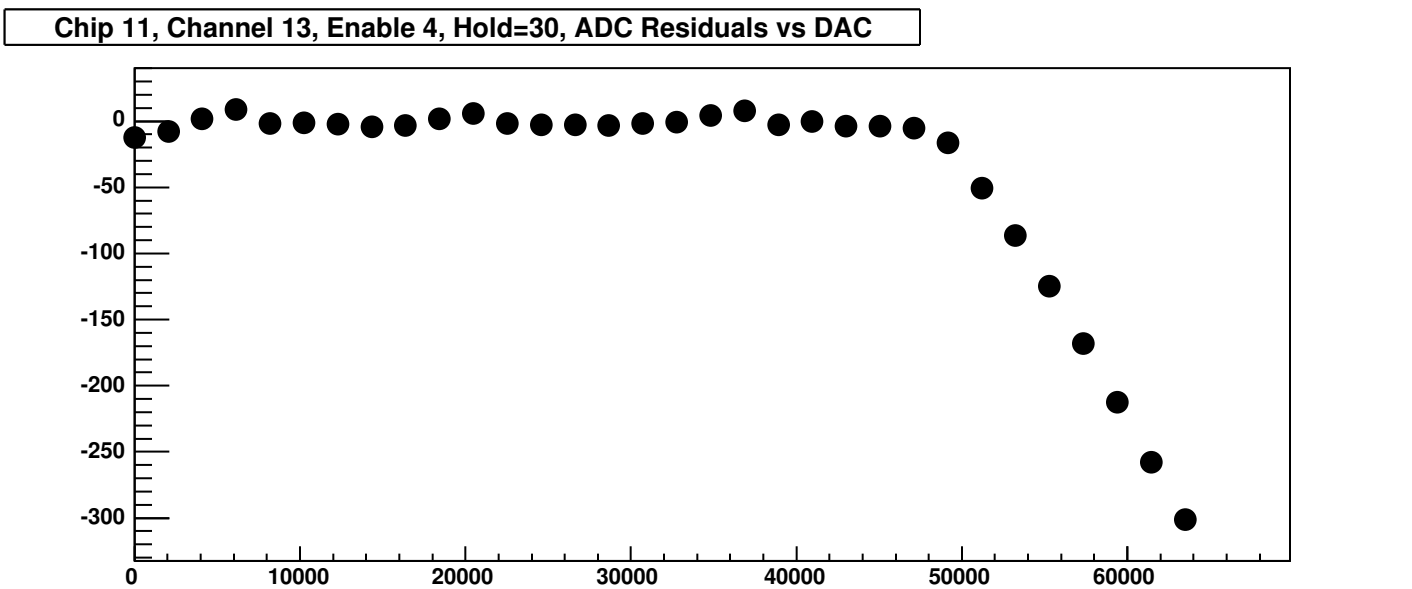

Chip 11, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5106 / 15
p0 876.9 ± 0.112
p1 0.0005755 ± 4.57e-06
900
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

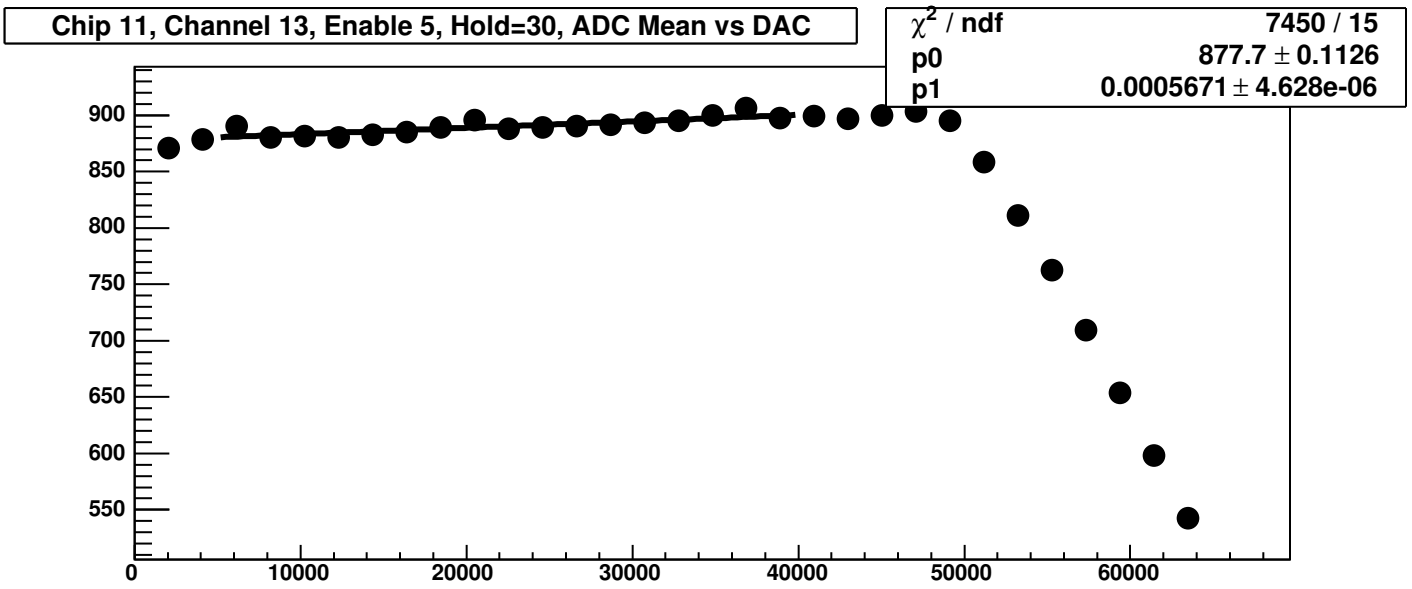

Chip 11, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

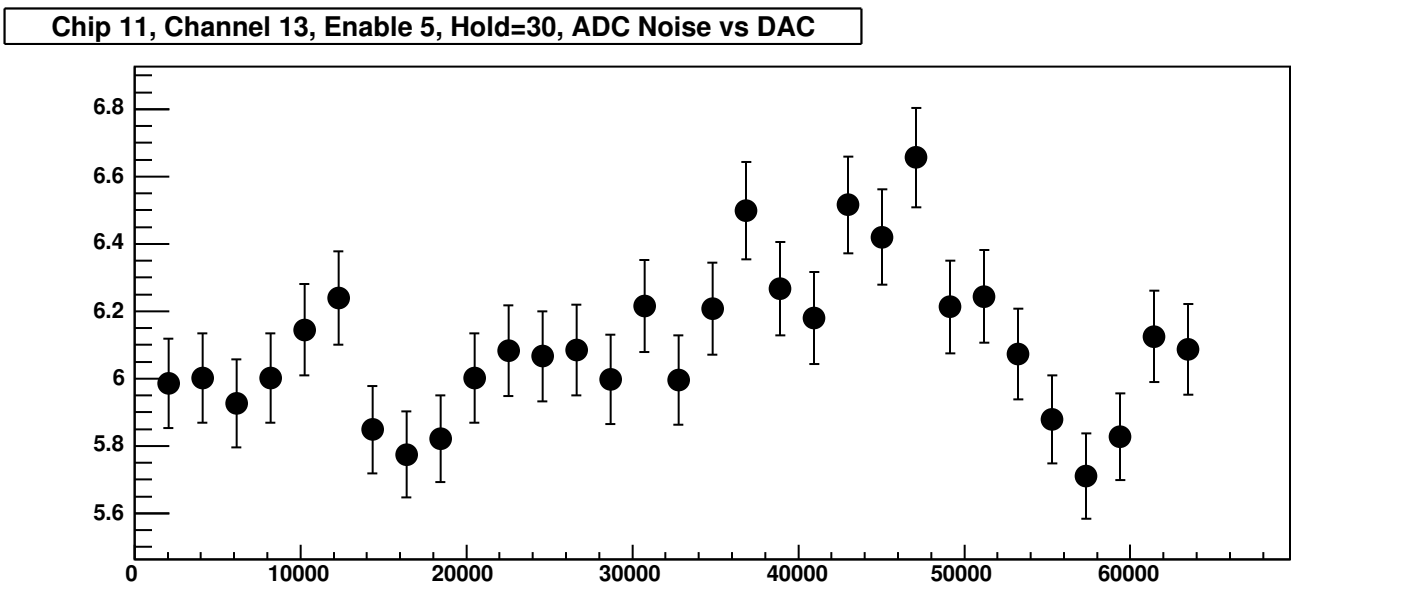

Chip 11, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

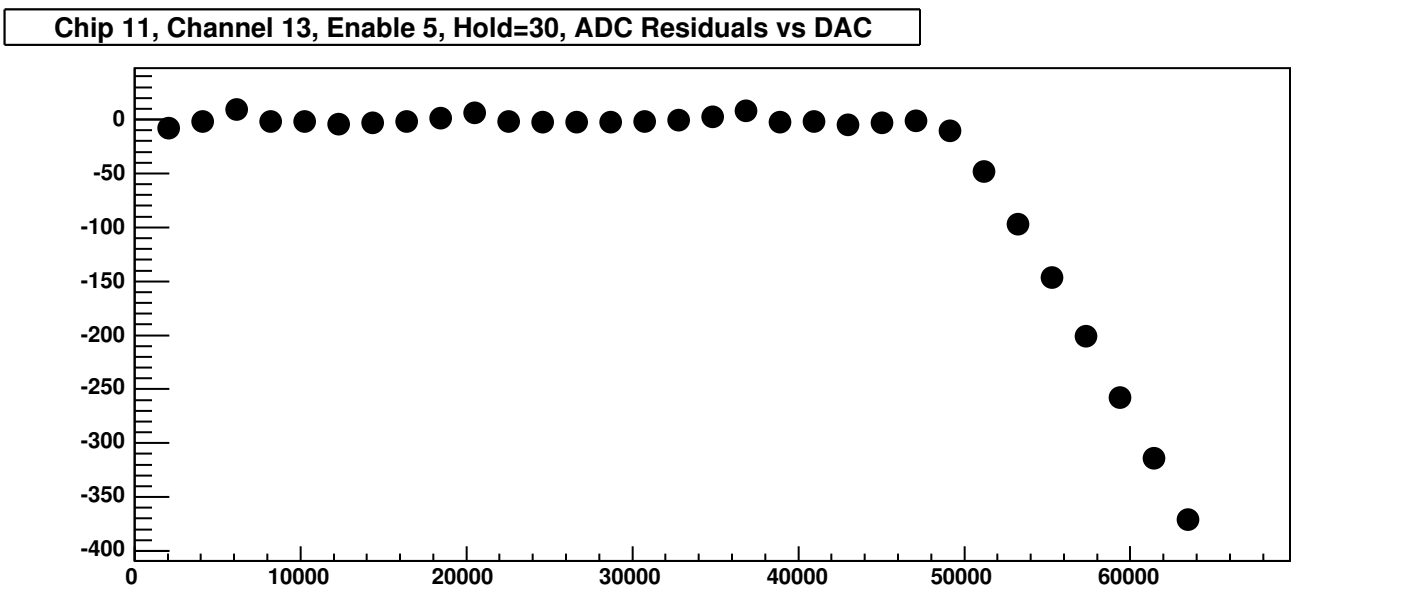

Chip 11, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6987 / 15
p0 873.5 ± 0.1157
p1 0.002052 ± 4.839e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

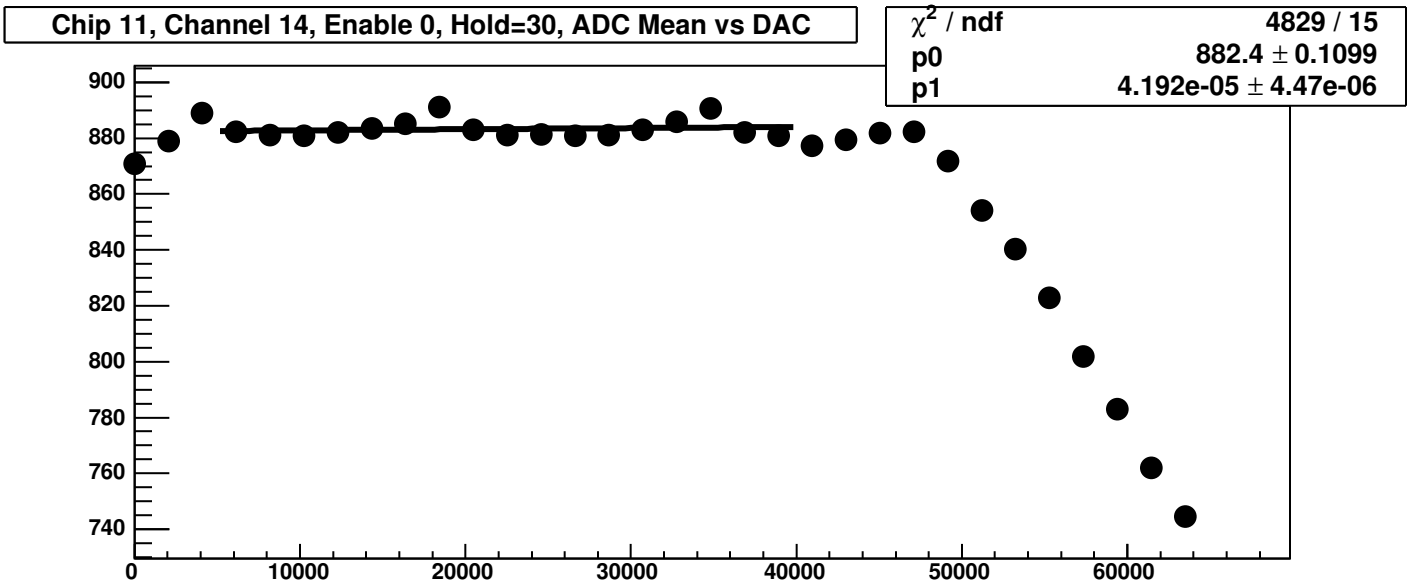

Chip 11, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

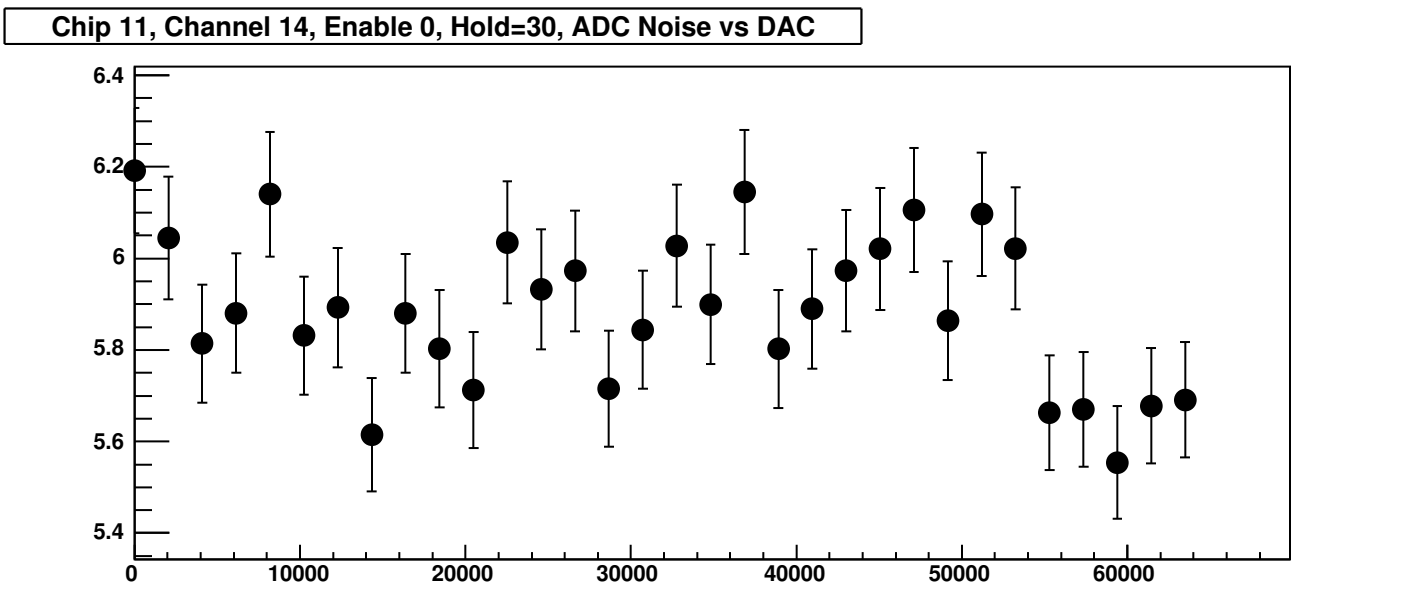

Chip 11, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

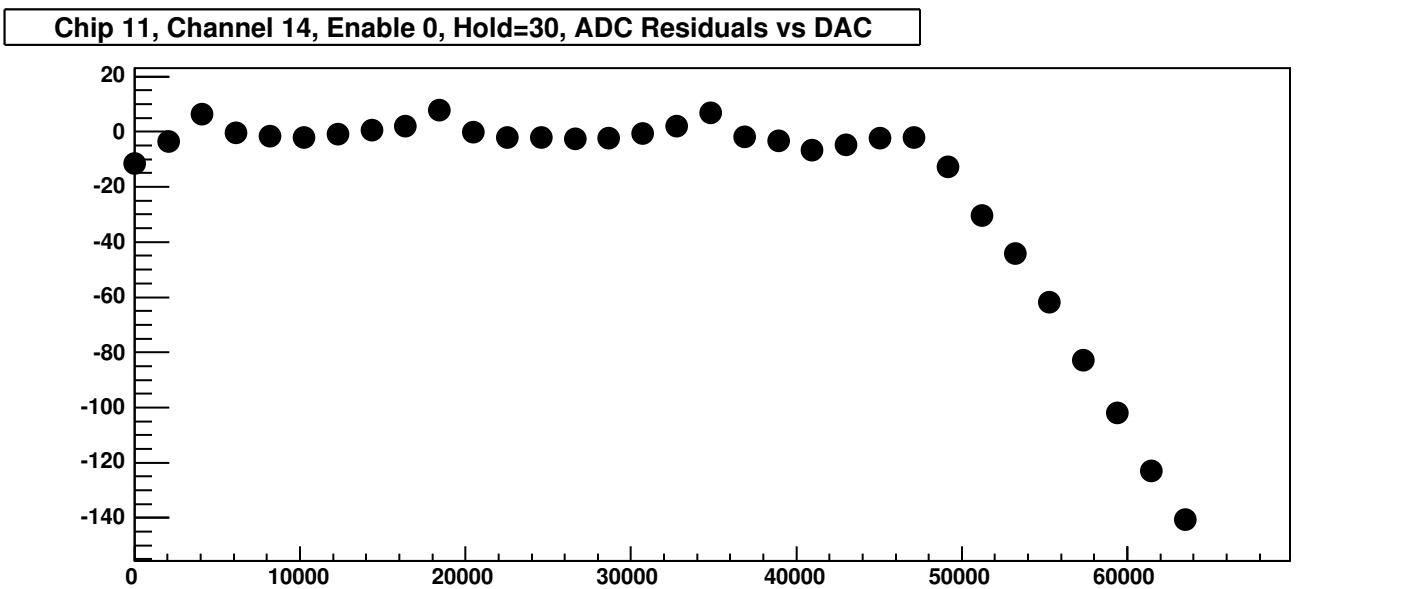

Chip 11, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7450 / 15
p0 877.7 ± 0.1126
p1 0.0005671 ± 4.628e-06
900
850
800
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

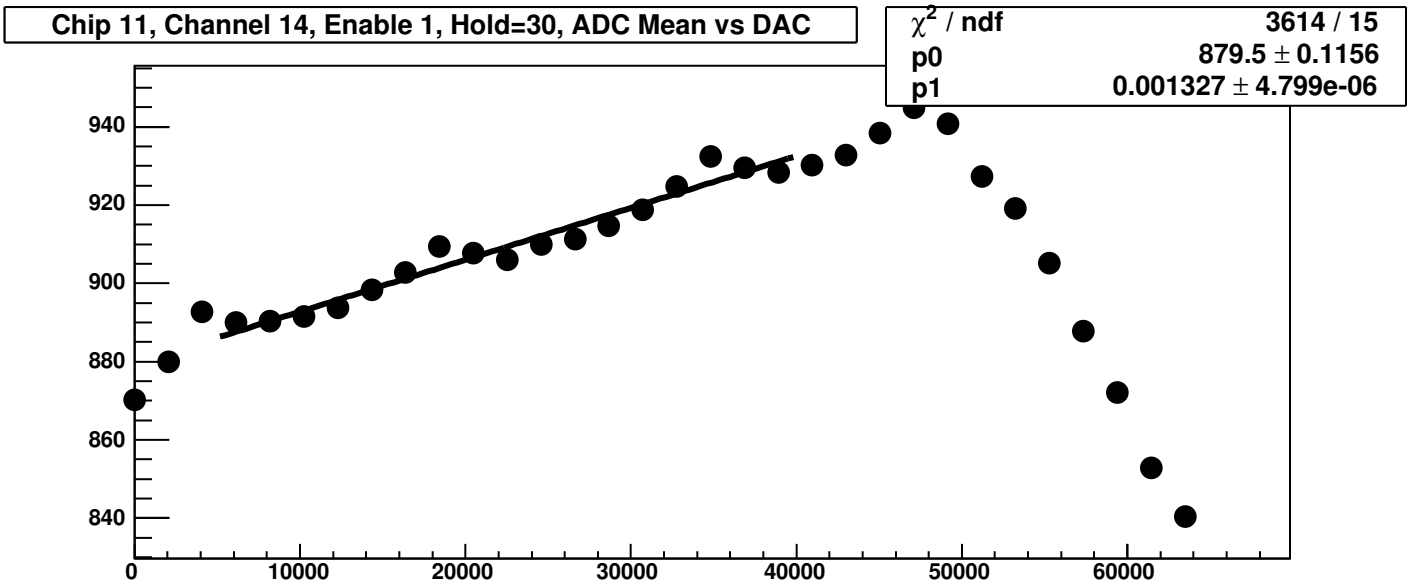

Chip 11, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

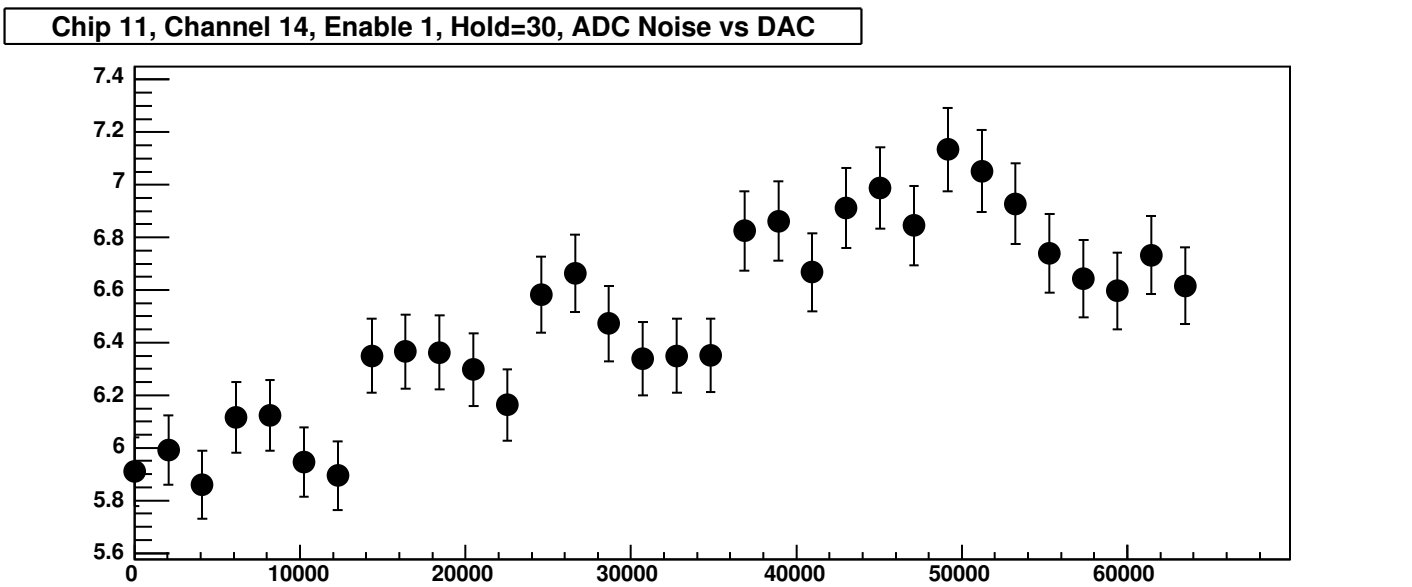

Chip 11, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
-350
-400
0
10000
20000
30000
40000
50000
60000

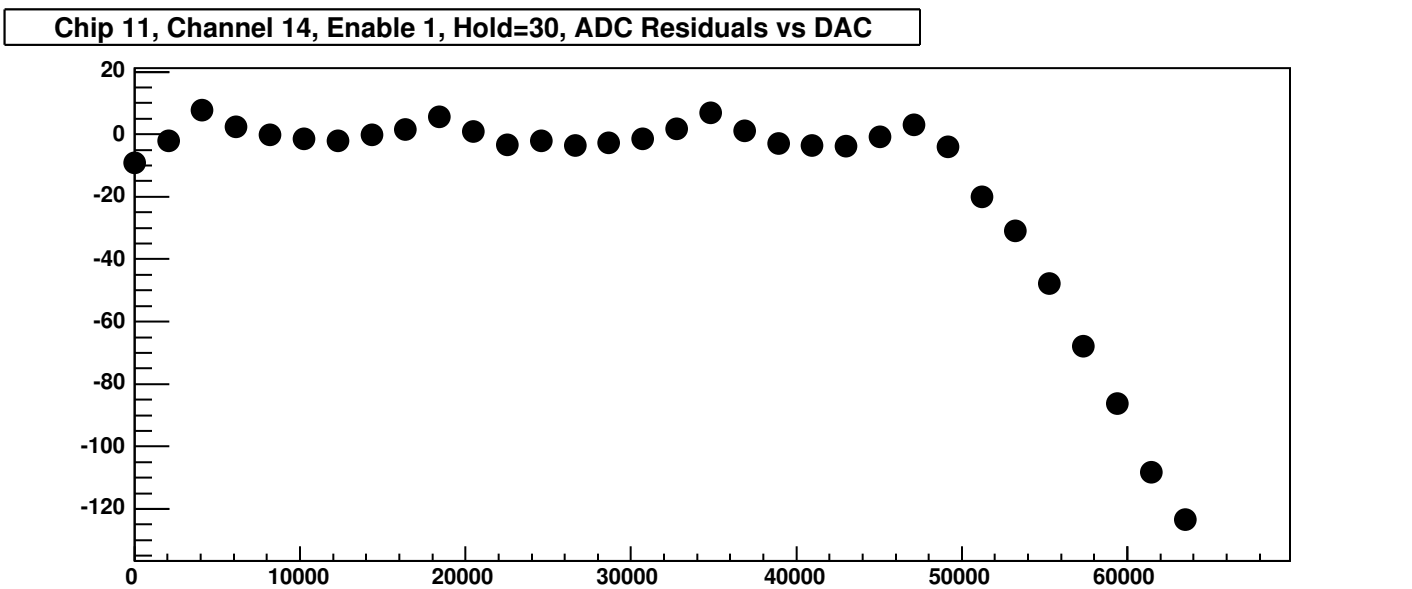

Chip 11, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4829 / 15
p0 882.4 ± 0.1099
p1 4.192e-05 ± 4.47e-06
900
880
860
840
820
800
780
760
740
0
10000
20000
30000
40000
50000
60000

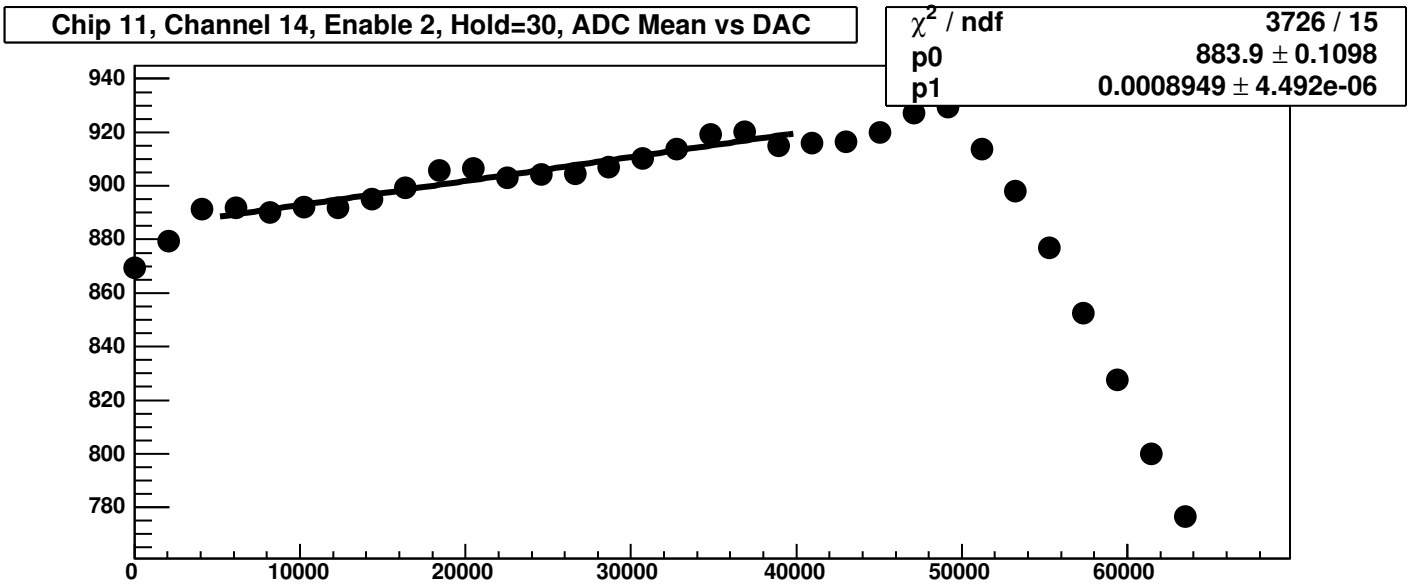

Chip 11, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

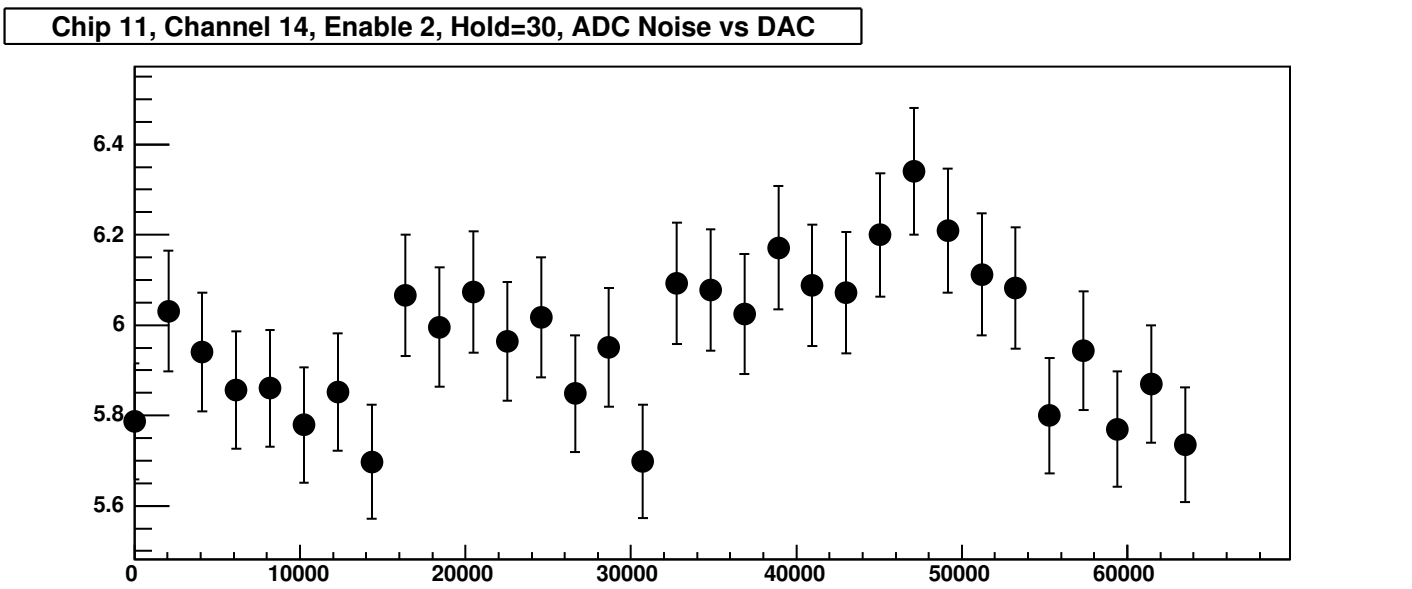

Chip 11, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

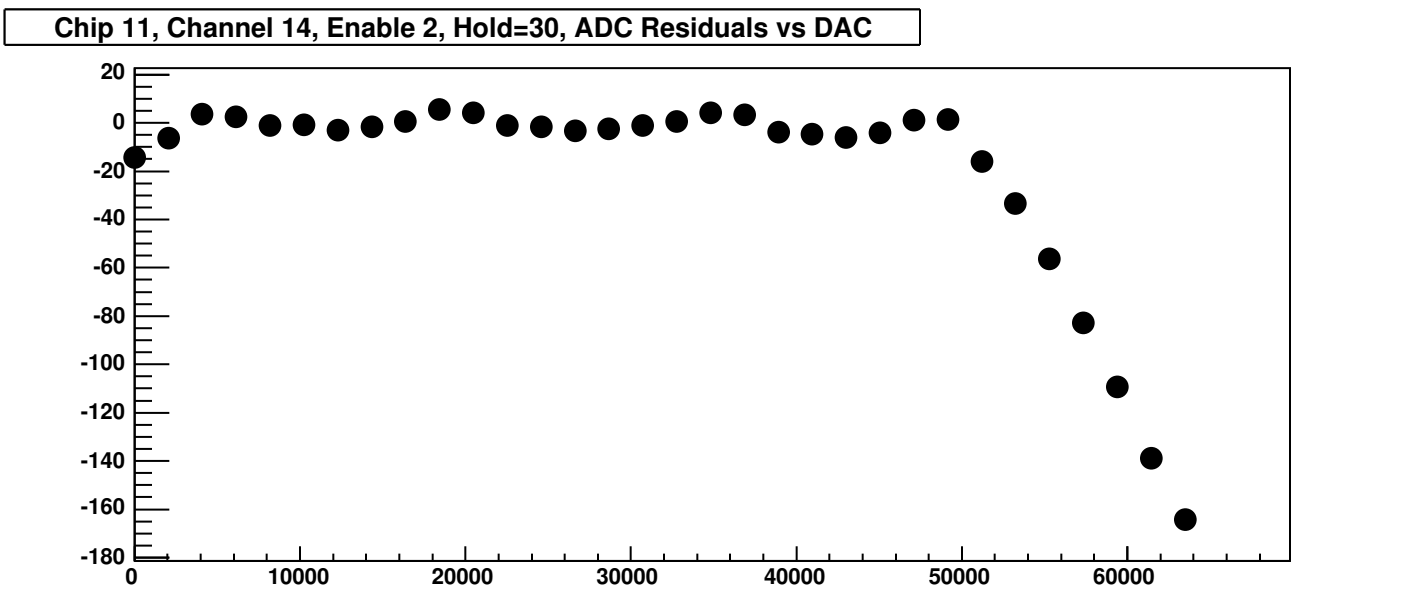

Chip 11, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3614 / 15
p0 879.5 ± 0.1156
p1 0.001327 ± 4.799e-06
940
920
900
880
860
840
0
10000
20000
30000
40000
50000
60000

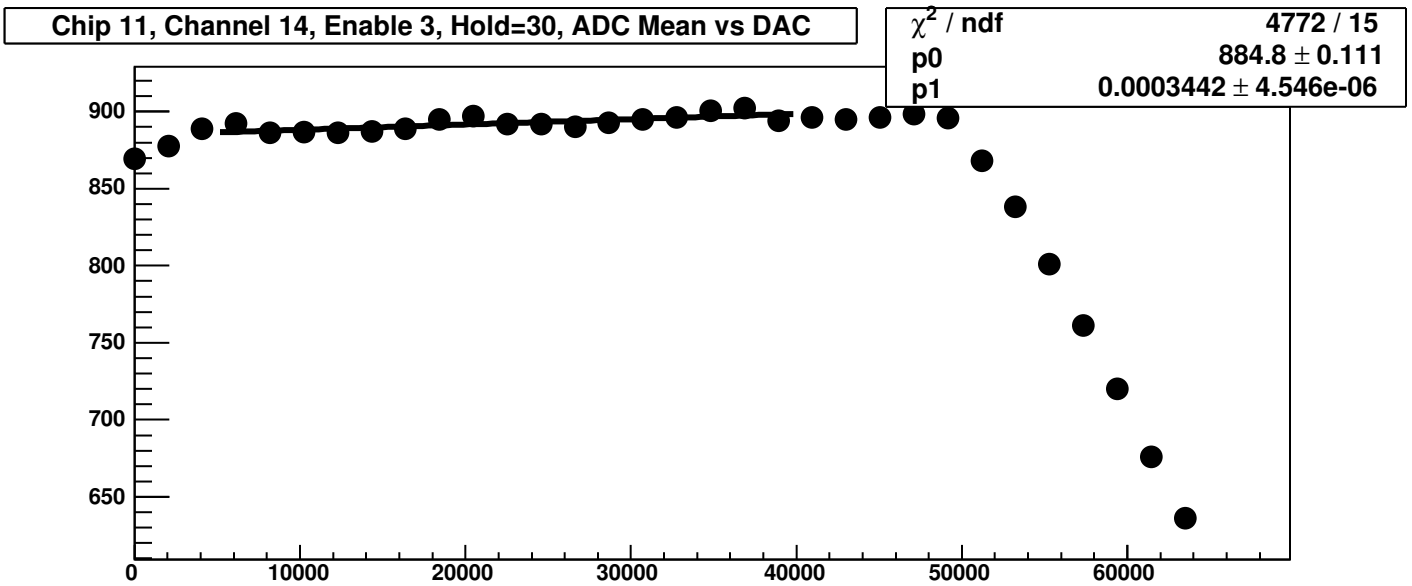

Chip 11, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

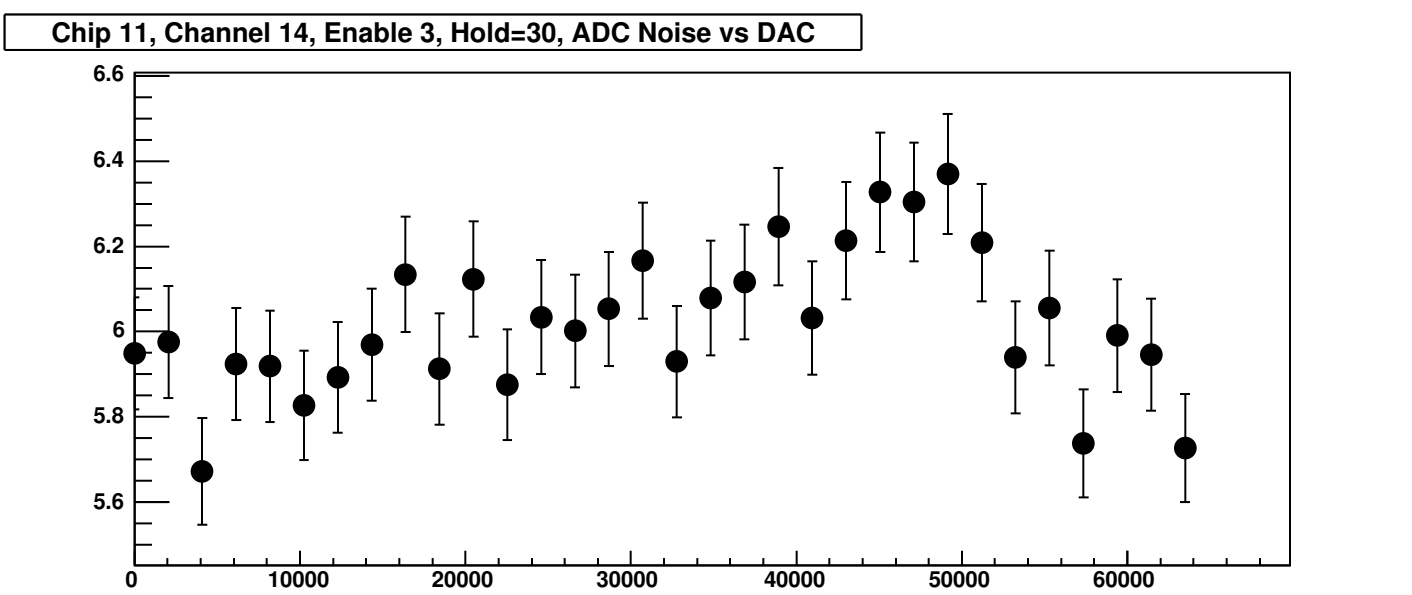

Chip 11, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

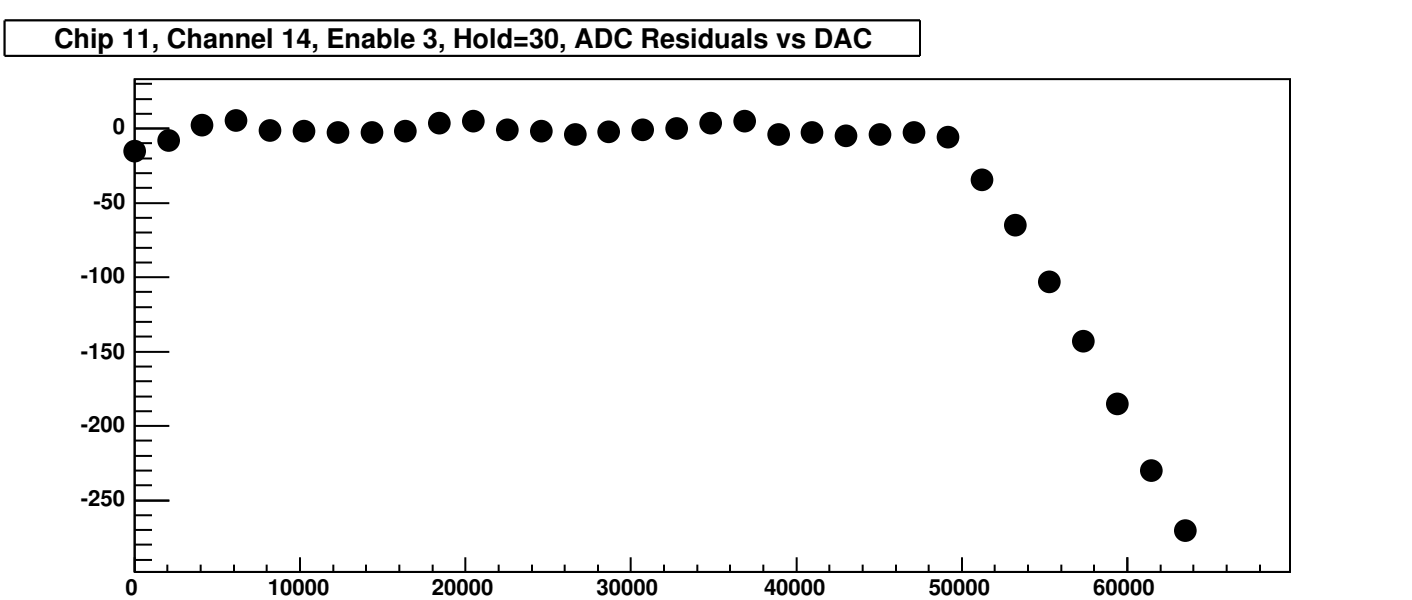

Chip 11, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3726 / 15
p0 883.9 ± 0.1098
p1 0.0008949 ± 4.492e-06
940
920
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

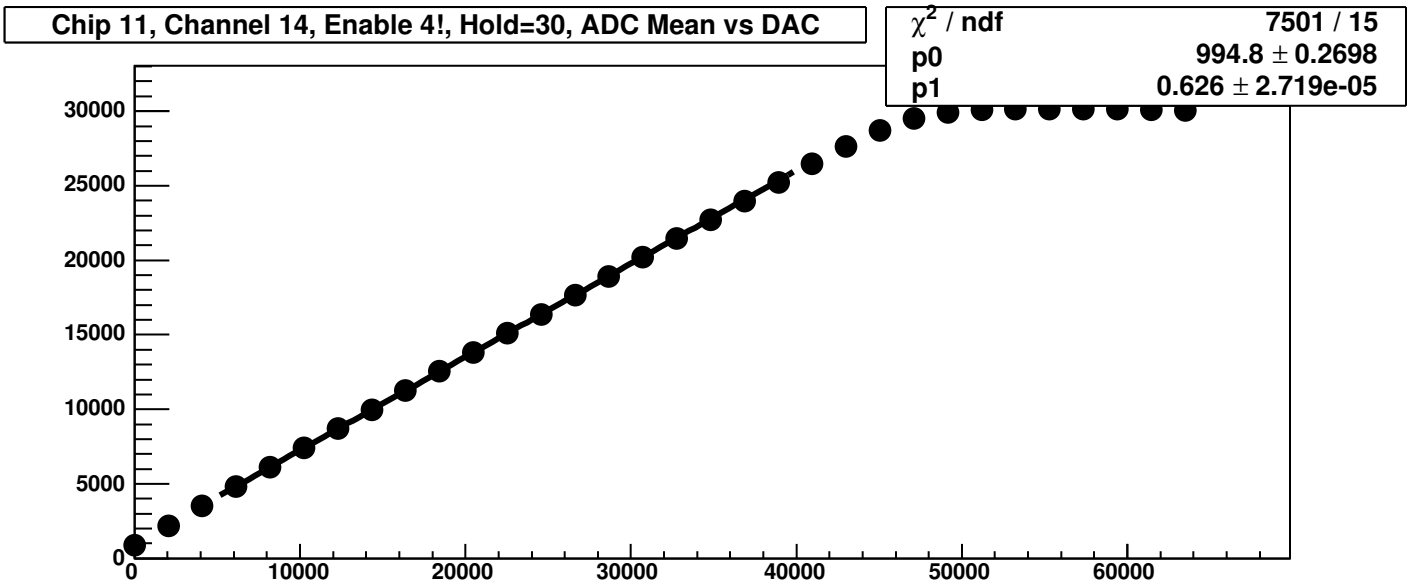

Chip 11, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

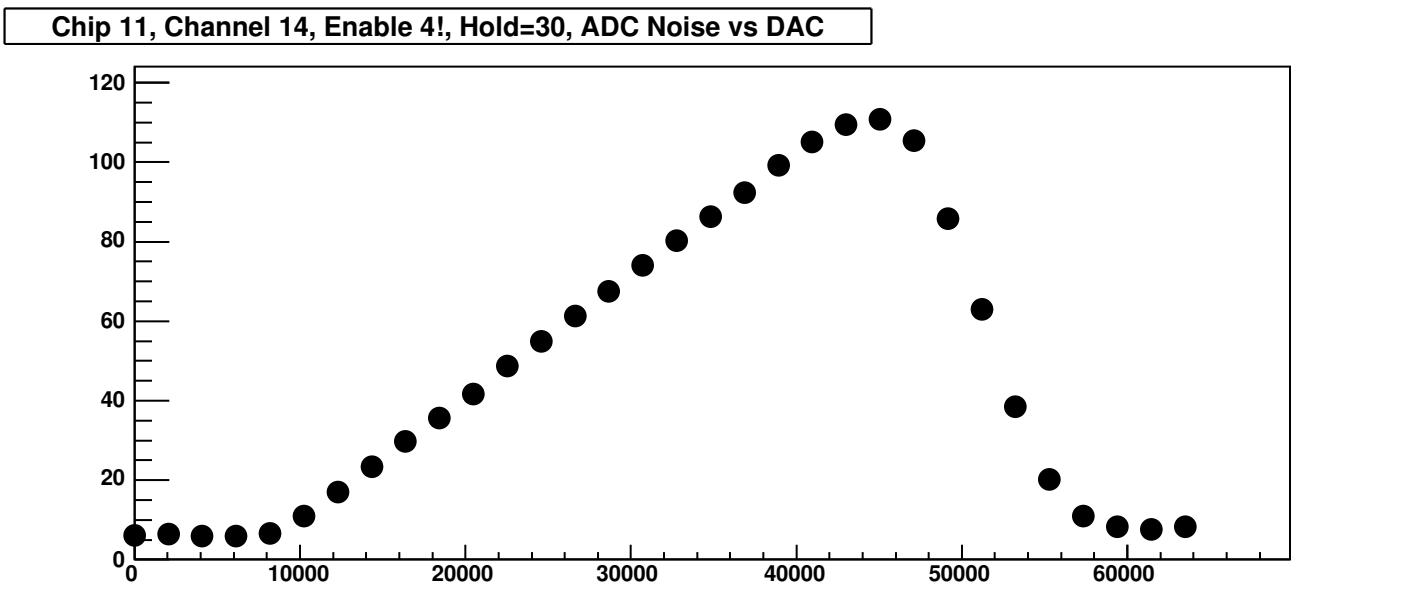

Chip 11, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
10000
20000
30000
40000
50000
60000

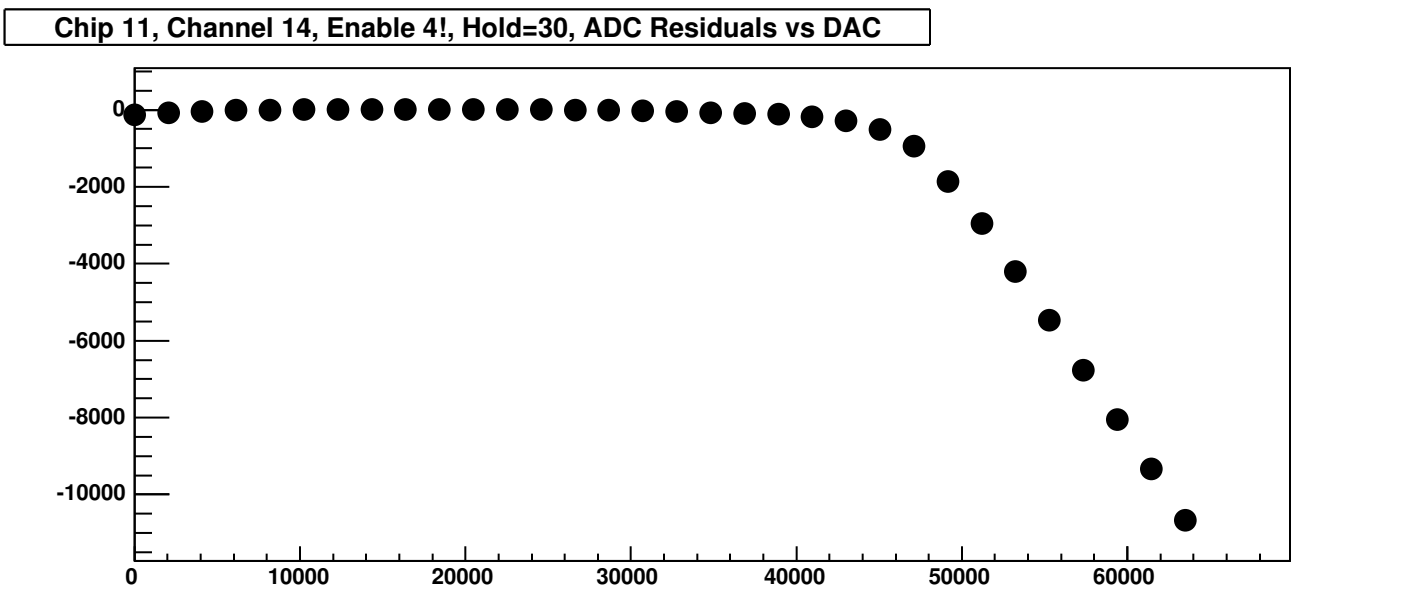

Chip 11, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4772 / 15
p0 884.8 ± 0.111
p1 0.0003442 ± 4.546e-06
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

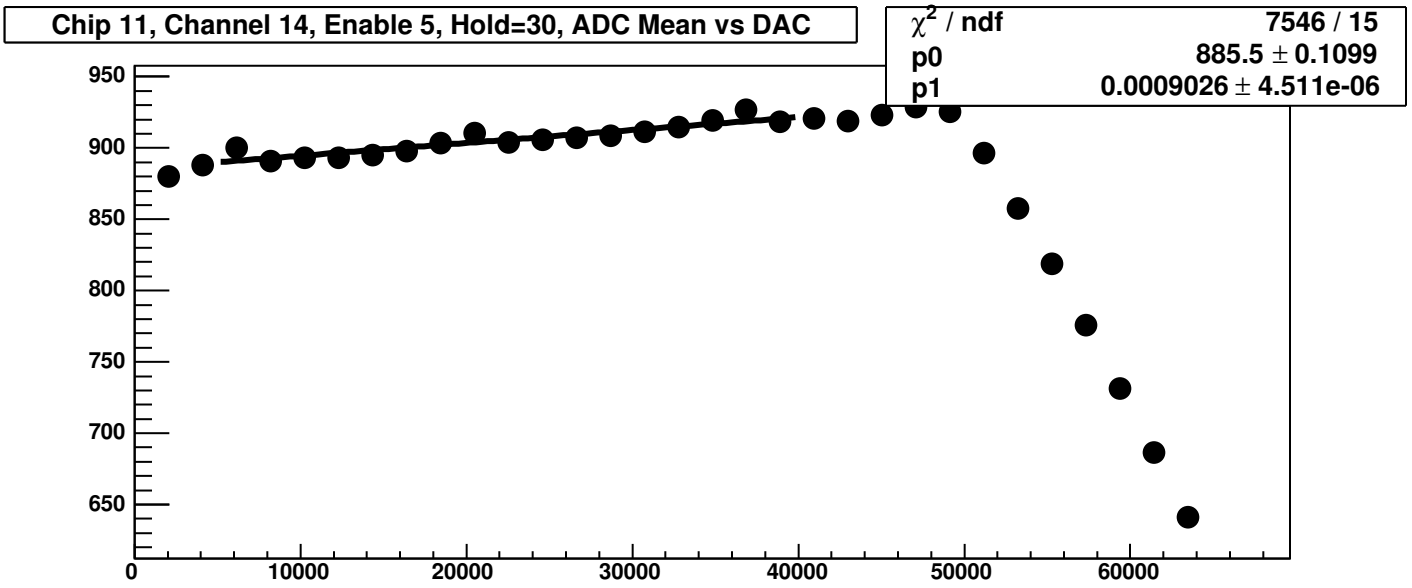

Chip 11, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

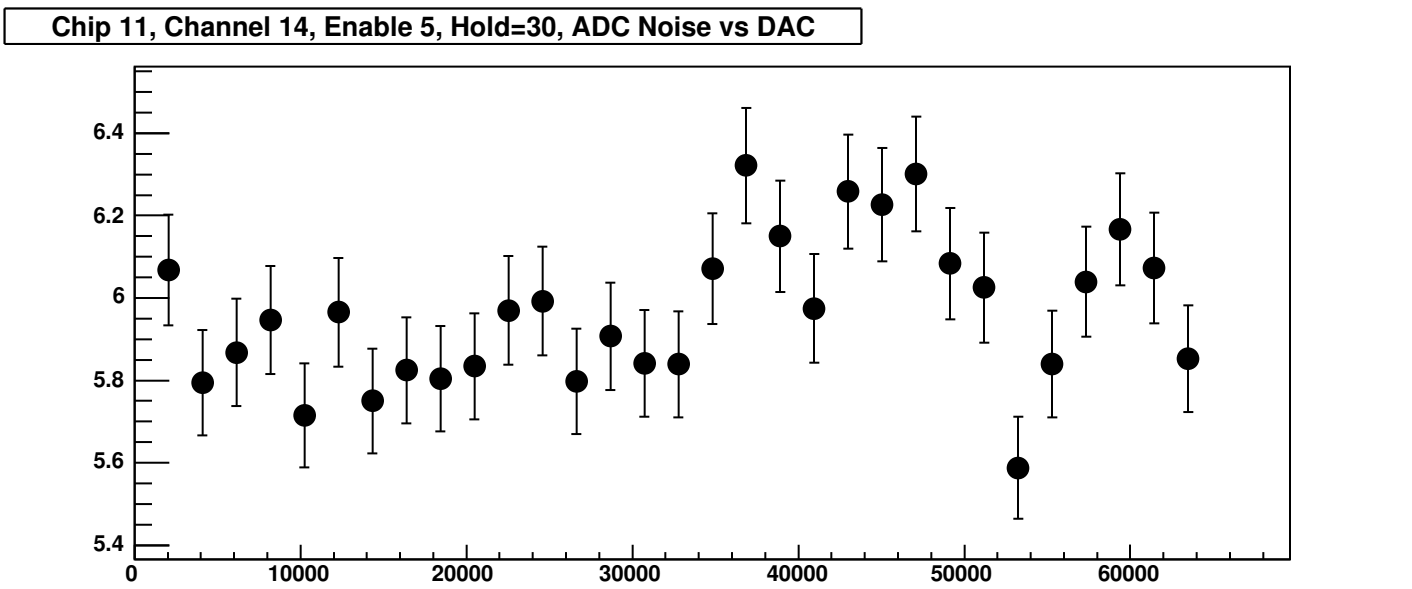

Chip 11, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

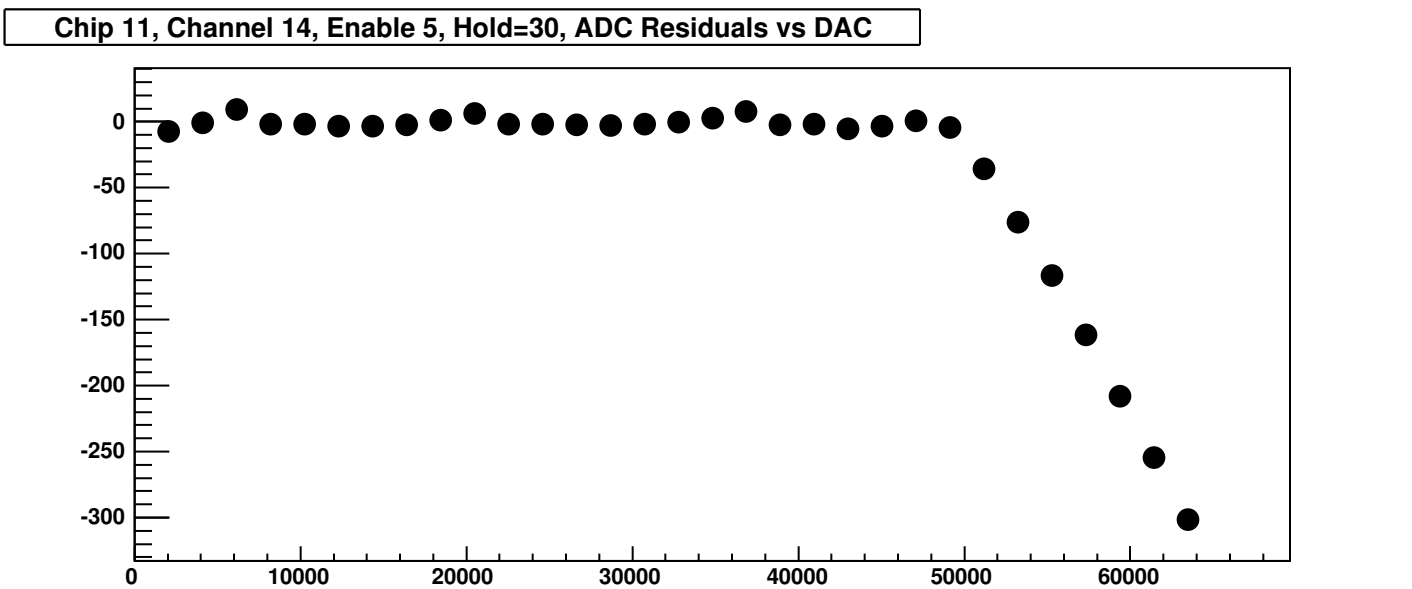

Chip 11, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 7501 / 15
p0 994.8 ± 0.2698
p1 0.626 ± 2.719e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

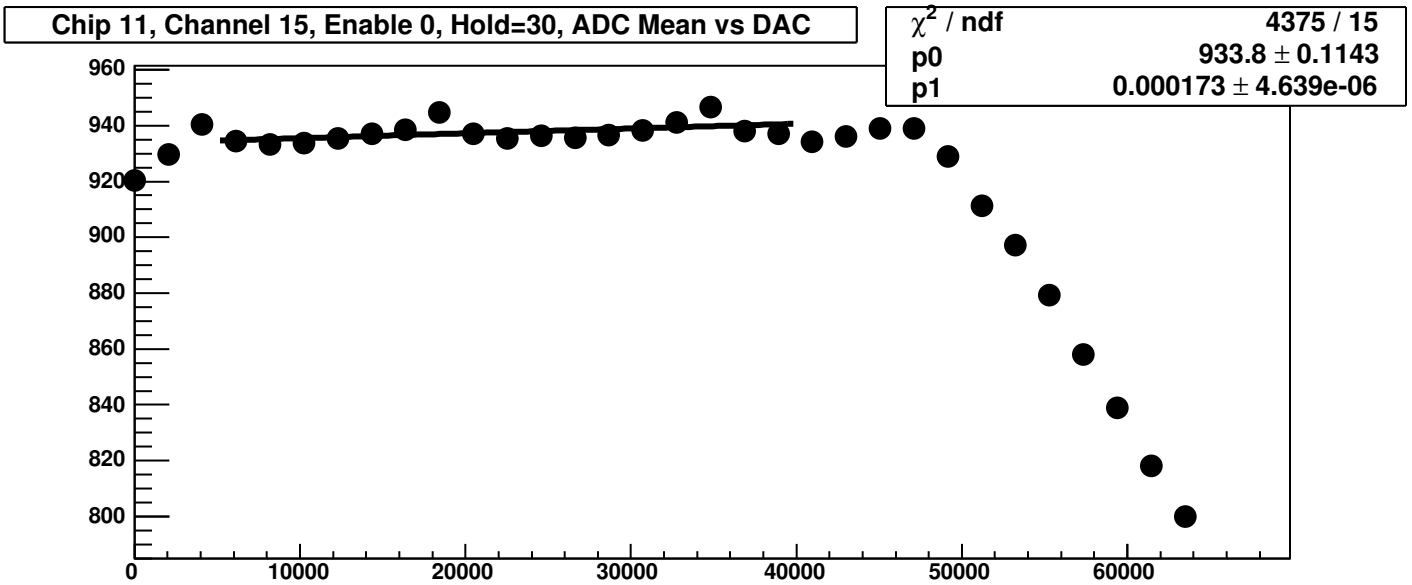

Chip 11, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

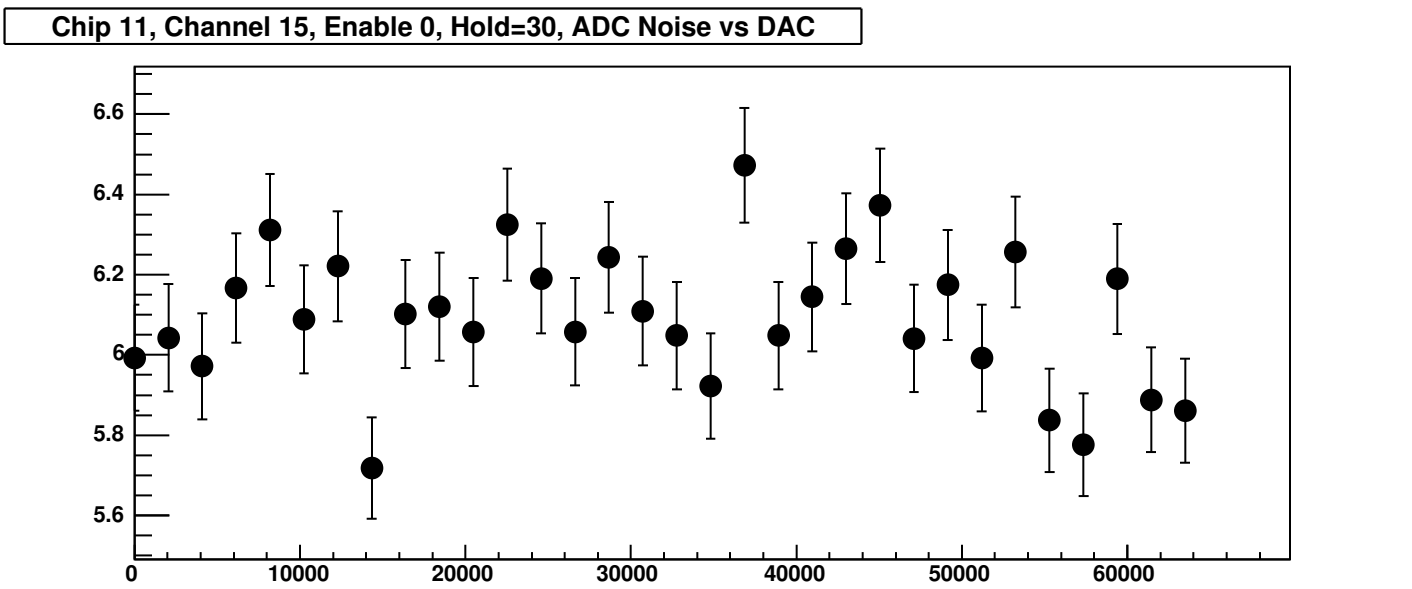

Chip 11, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000

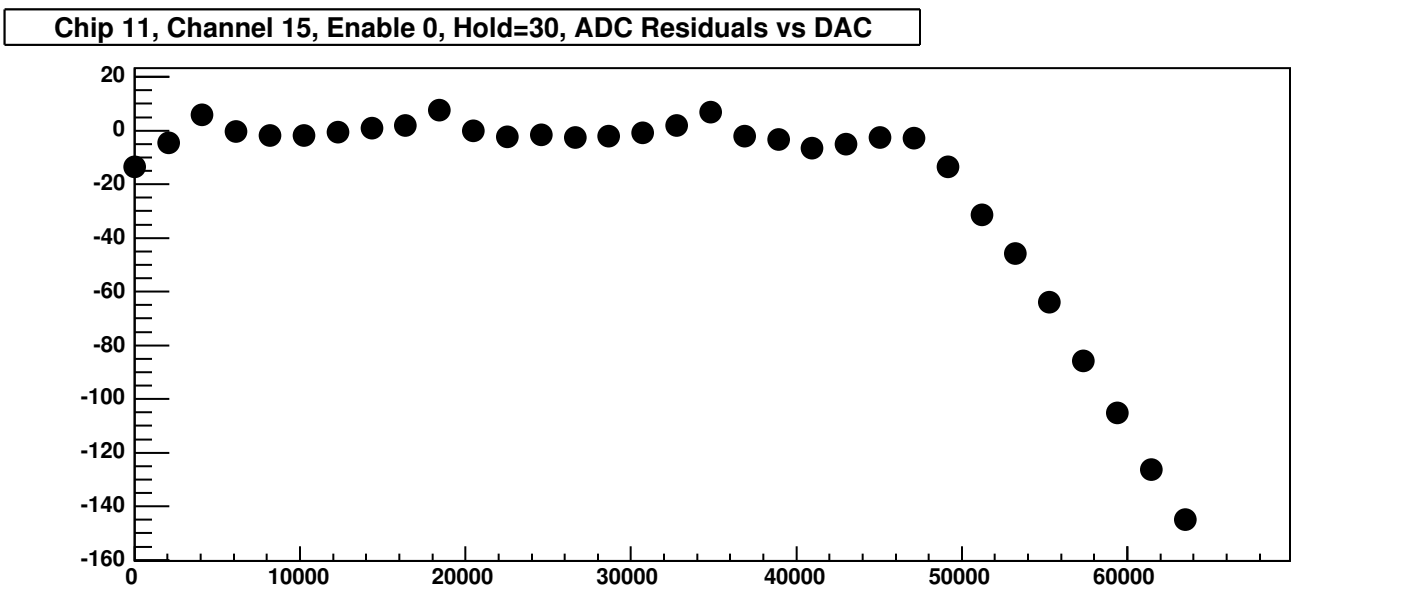

Chip 11, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7546 / 15
p0 885.5 ± 0.1099
p1 0.0009026 ± 4.511e-06
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

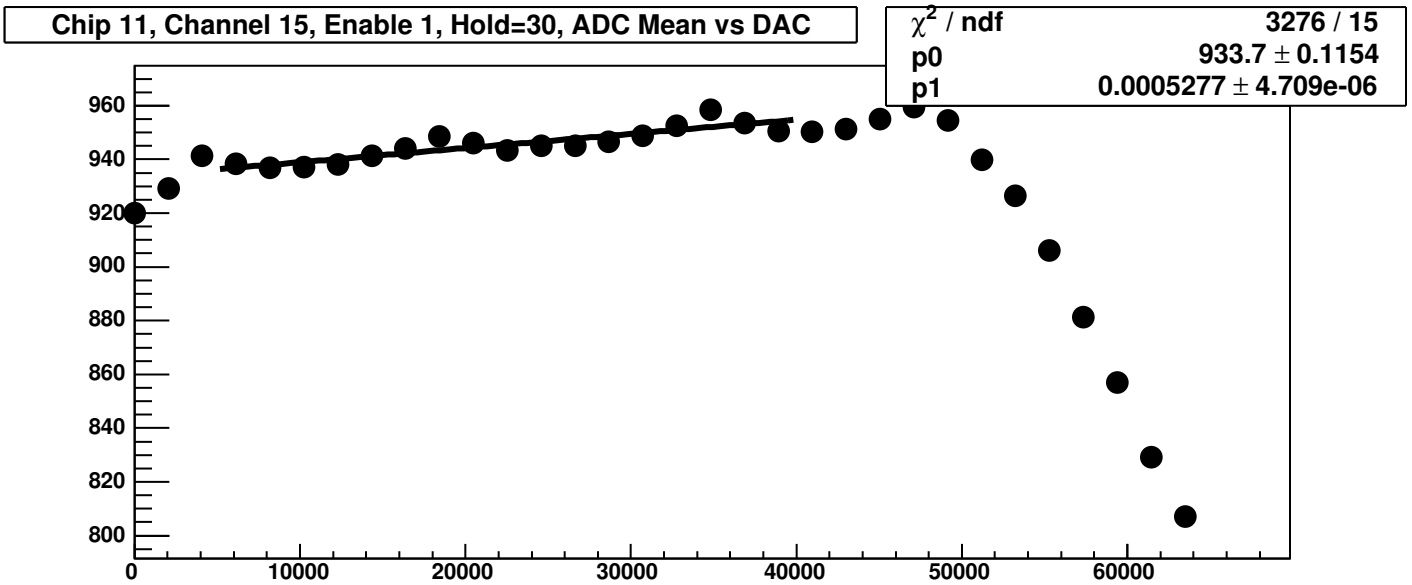

Chip 11, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

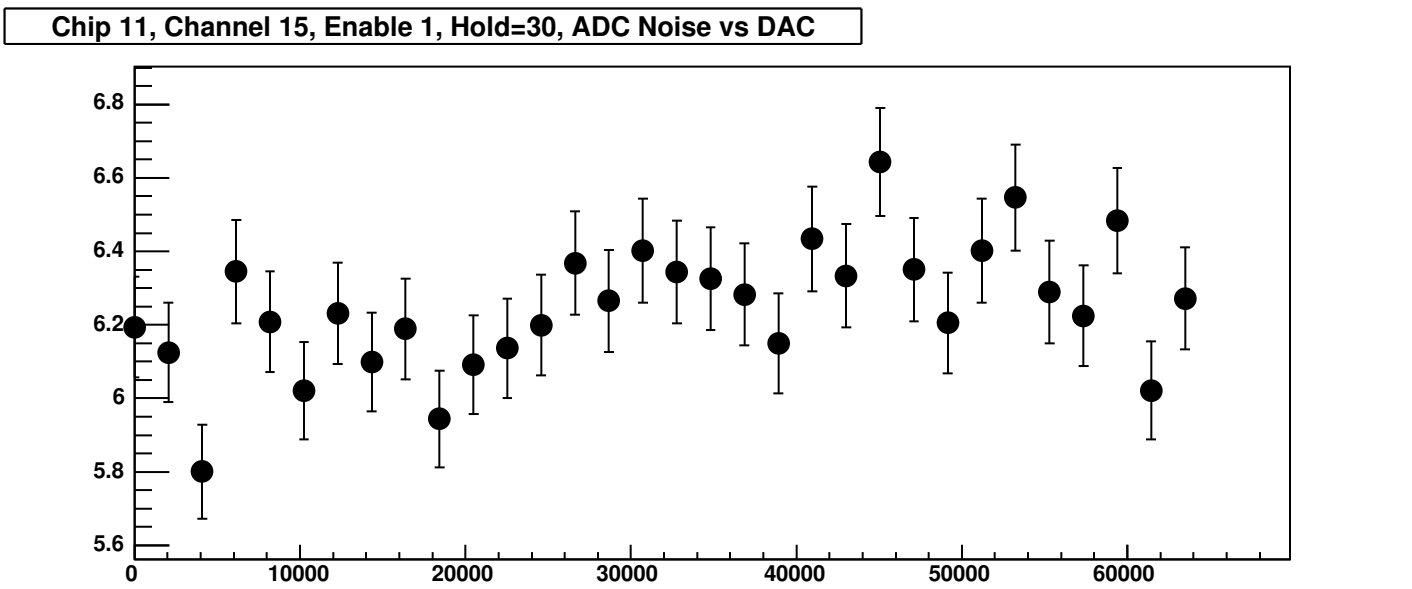

Chip 11, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

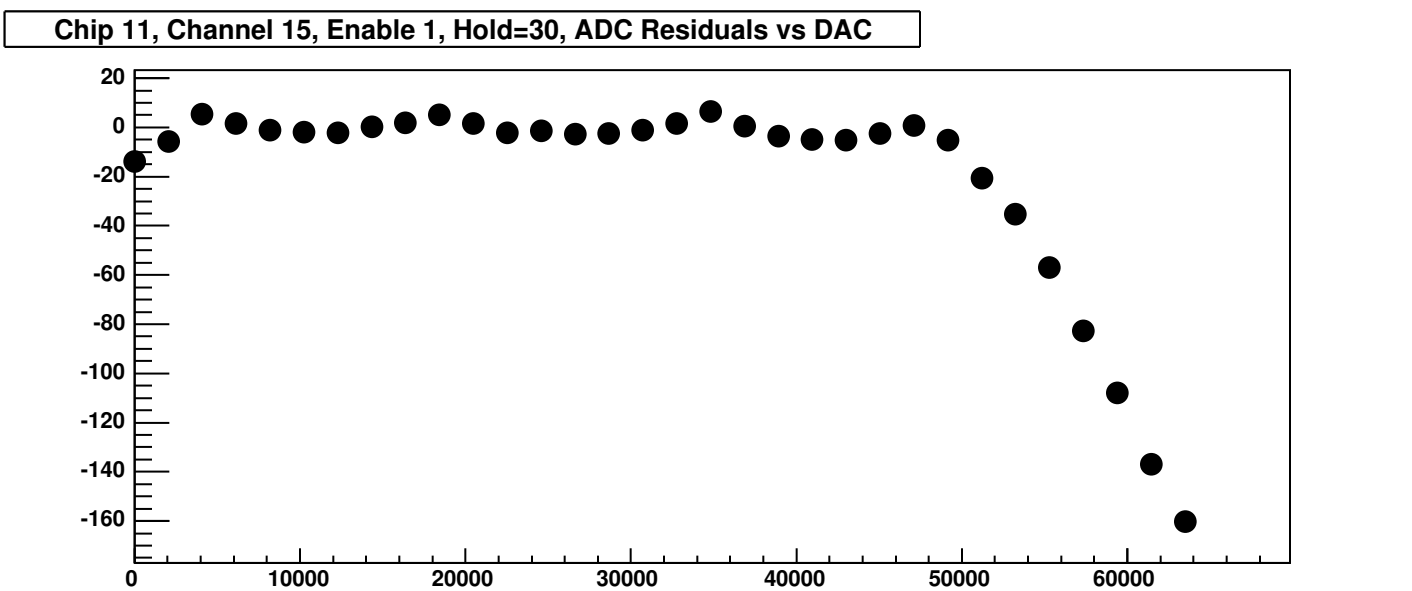

Chip 11, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4375 / 15
p0 933.8 ± 0.1143
p1 0.000173 ± 4.639e-06
960
940
920
900
880
860
840
820
800
0
10000
20000
30000
40000
50000
60000

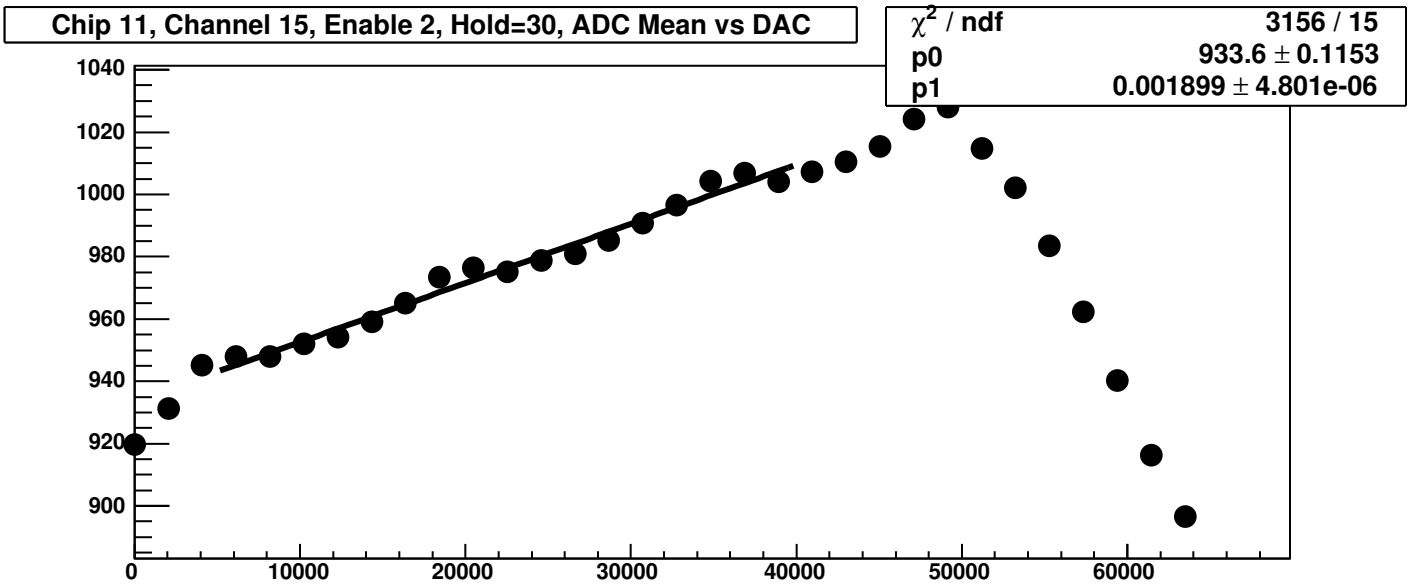

Chip 11, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

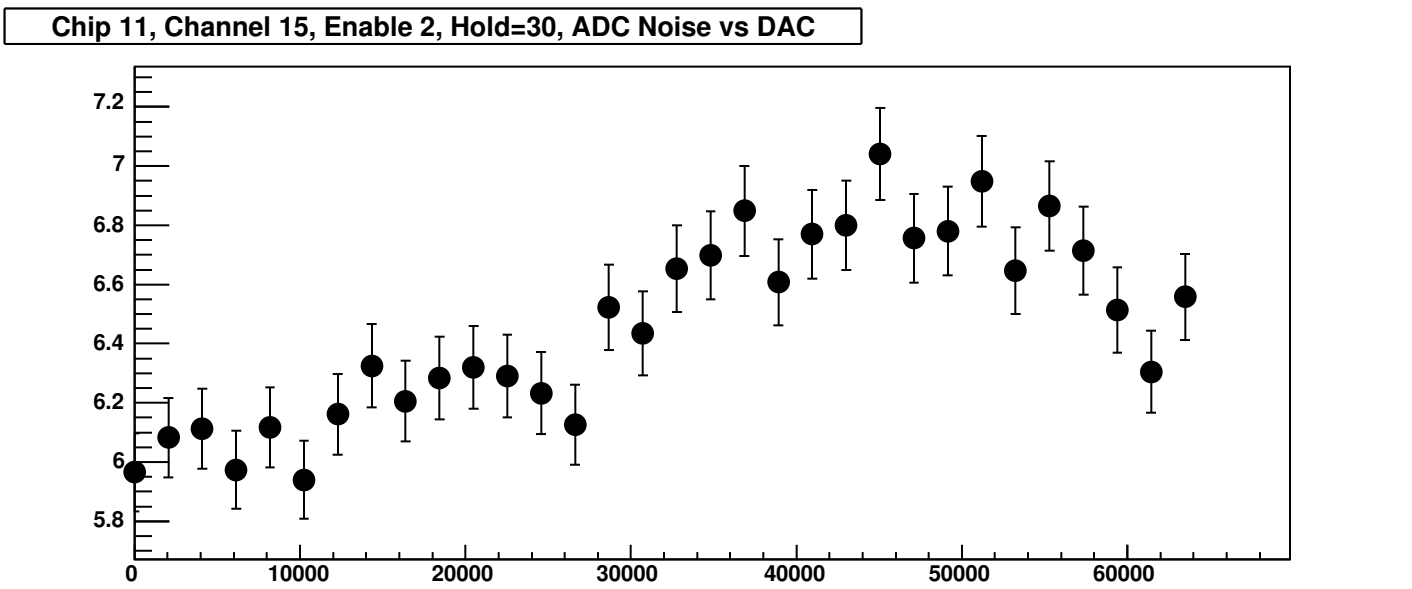

Chip 11, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

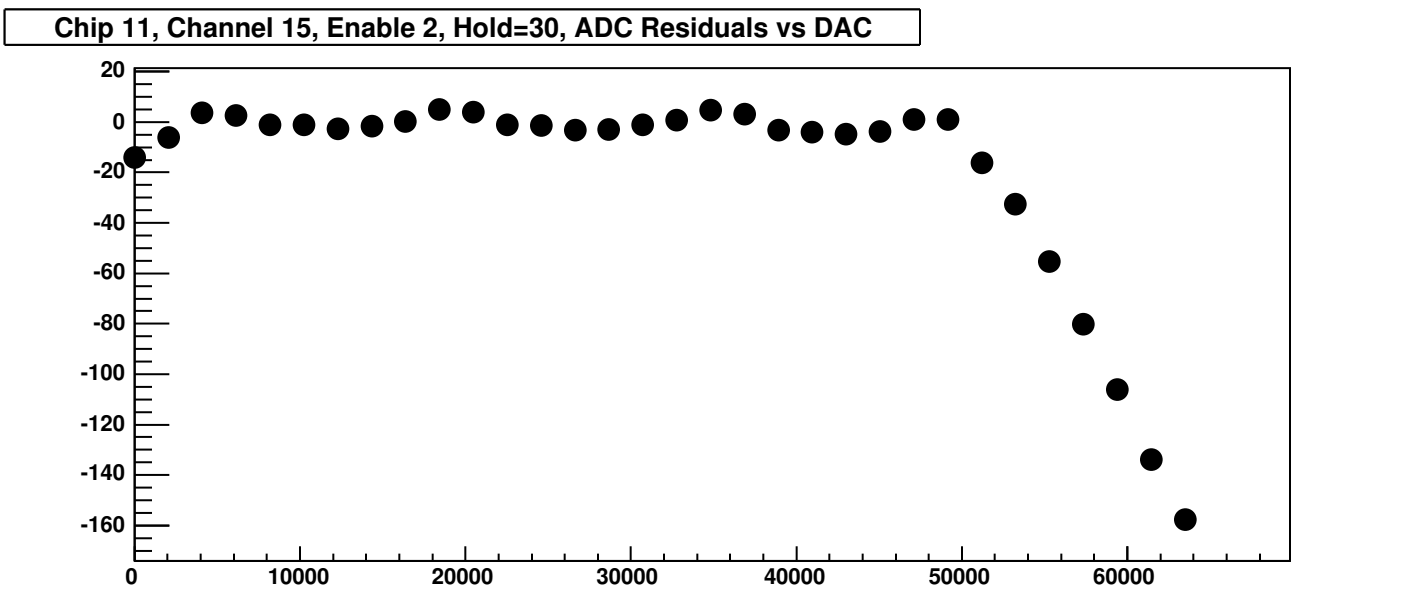

Chip 11, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3276 / 15
p0 933.7 ± 0.1154
p1 0.0005277 ± 4.709e-06
960
940
920
900
880
860
840
820
800
0
10000
20000
30000
40000
50000
60000

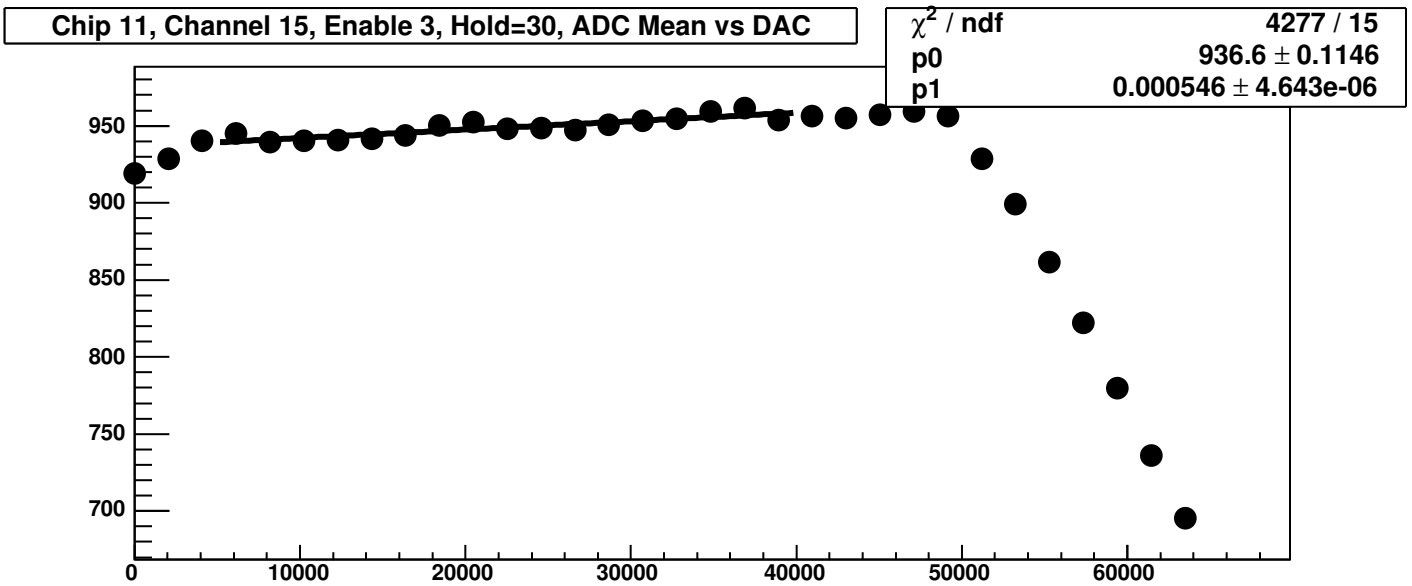

Chip 11, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

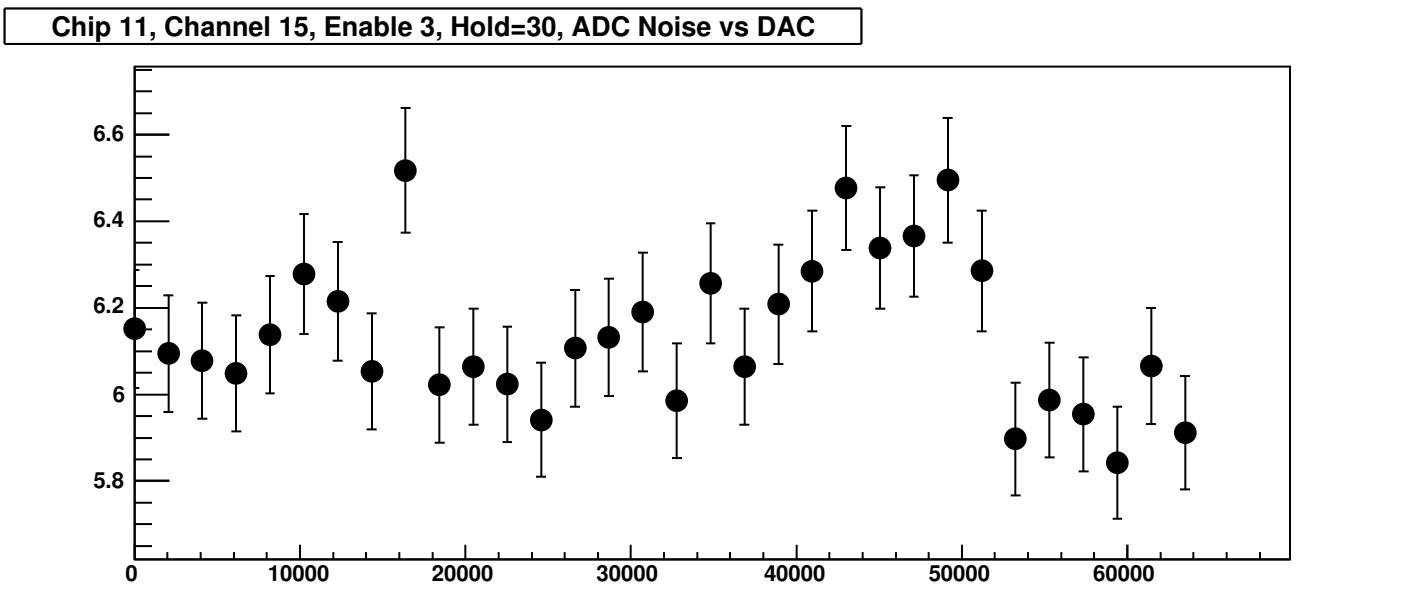

Chip 11, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

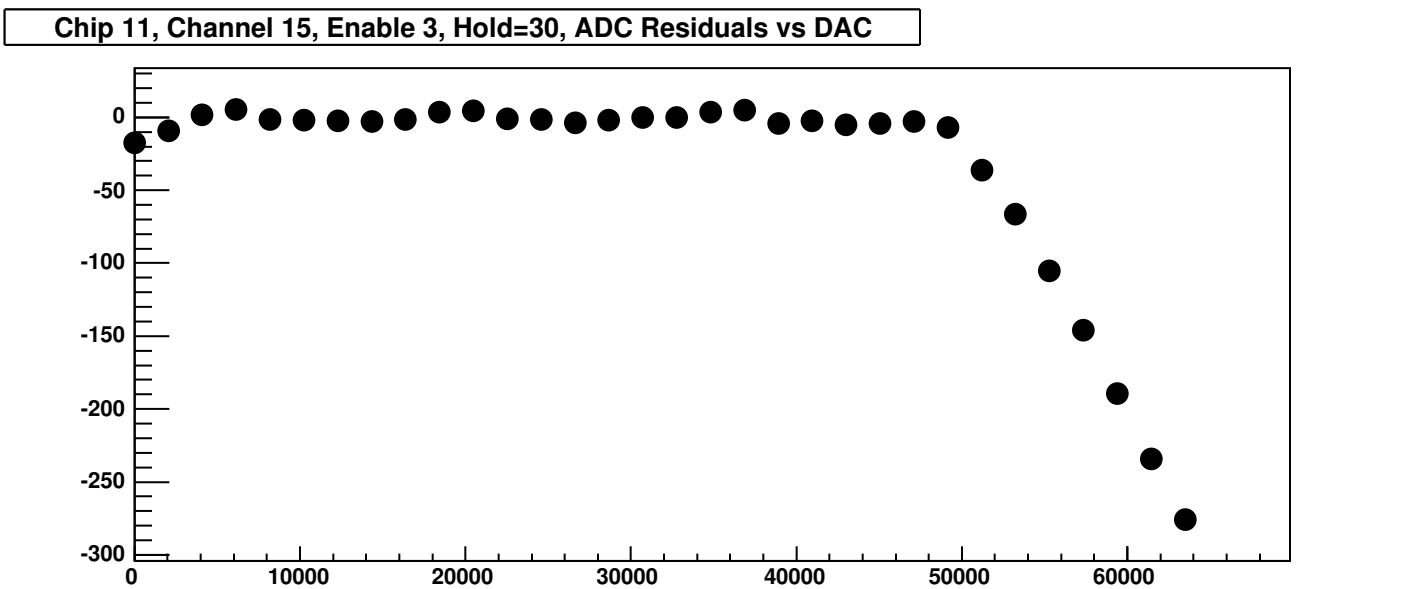

Chip 11, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3156 / 15
p0 933.6 ± 0.1153
p1 0.001899 ± 4.801e-06
1040
1020
1000
980
960
940
920
900
0
10000
20000
30000
40000
50000
60000

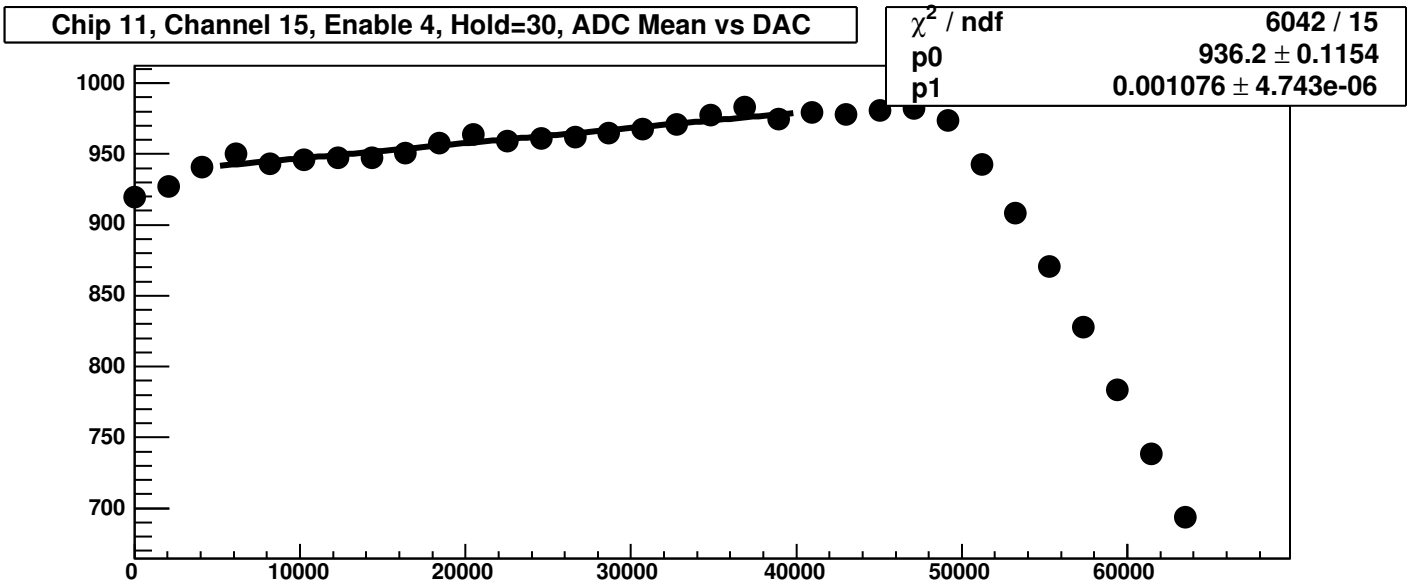

Chip 11, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

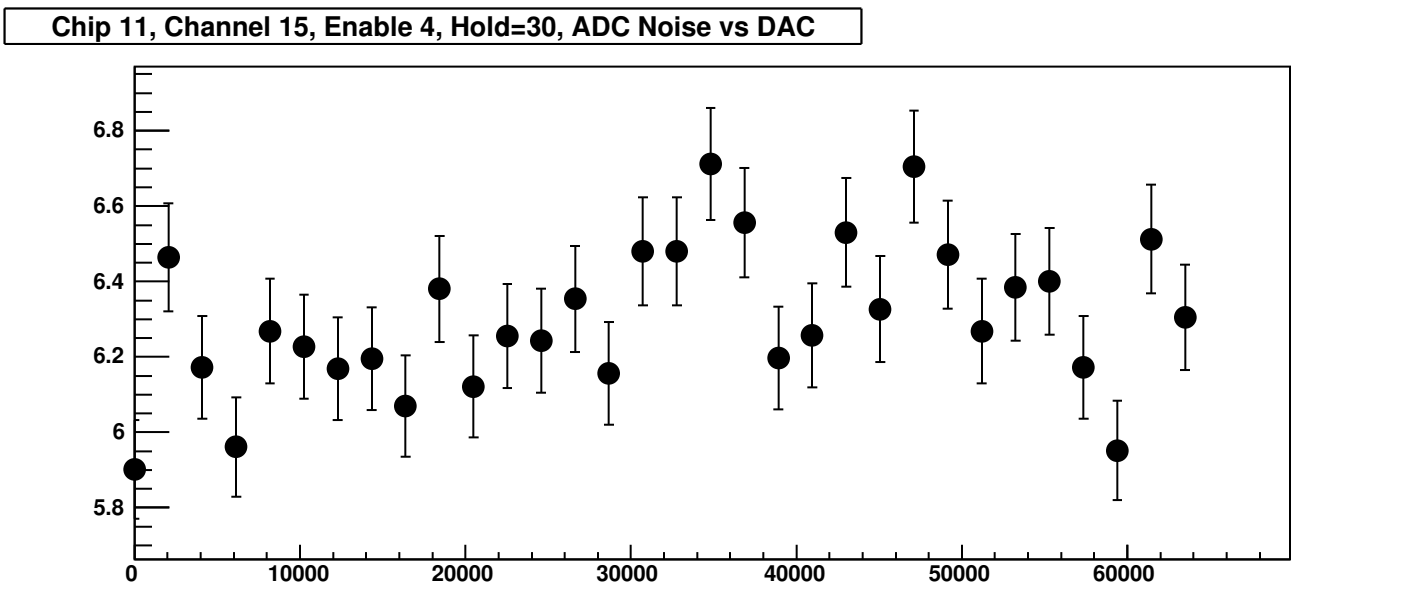

Chip 11, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

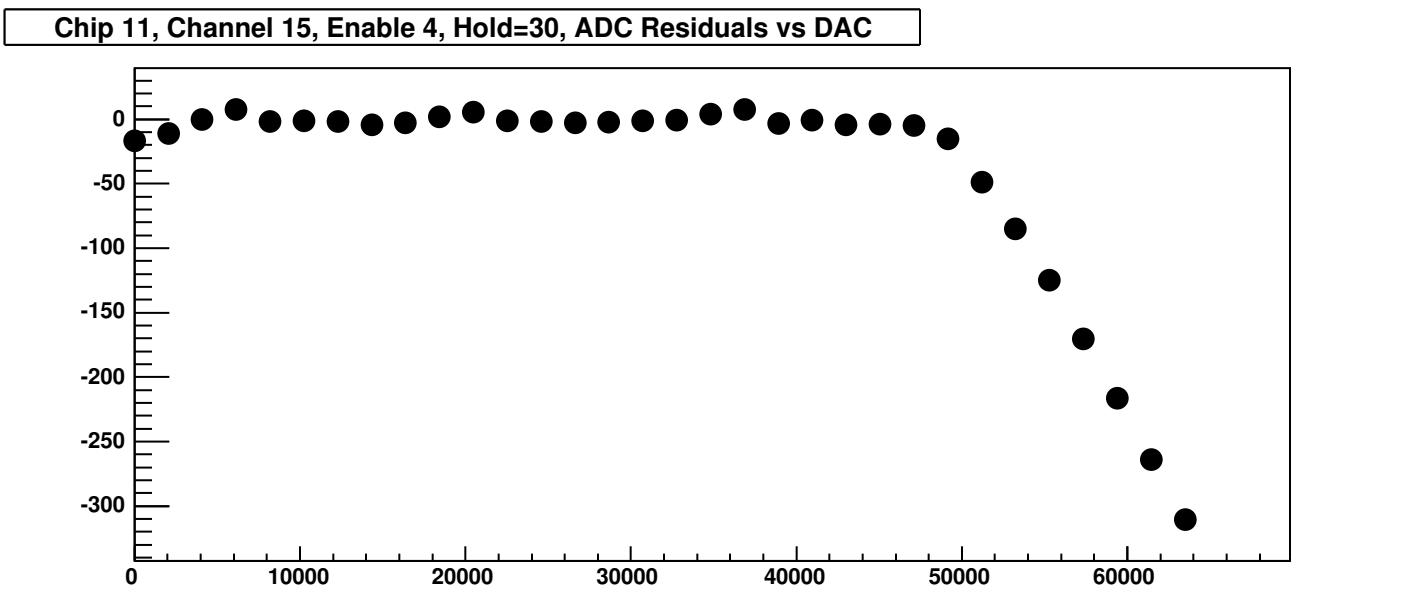

Chip 11, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4277 / 15
p0 936.6 ± 0.1146
p1 0.000546 ± 4.643e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

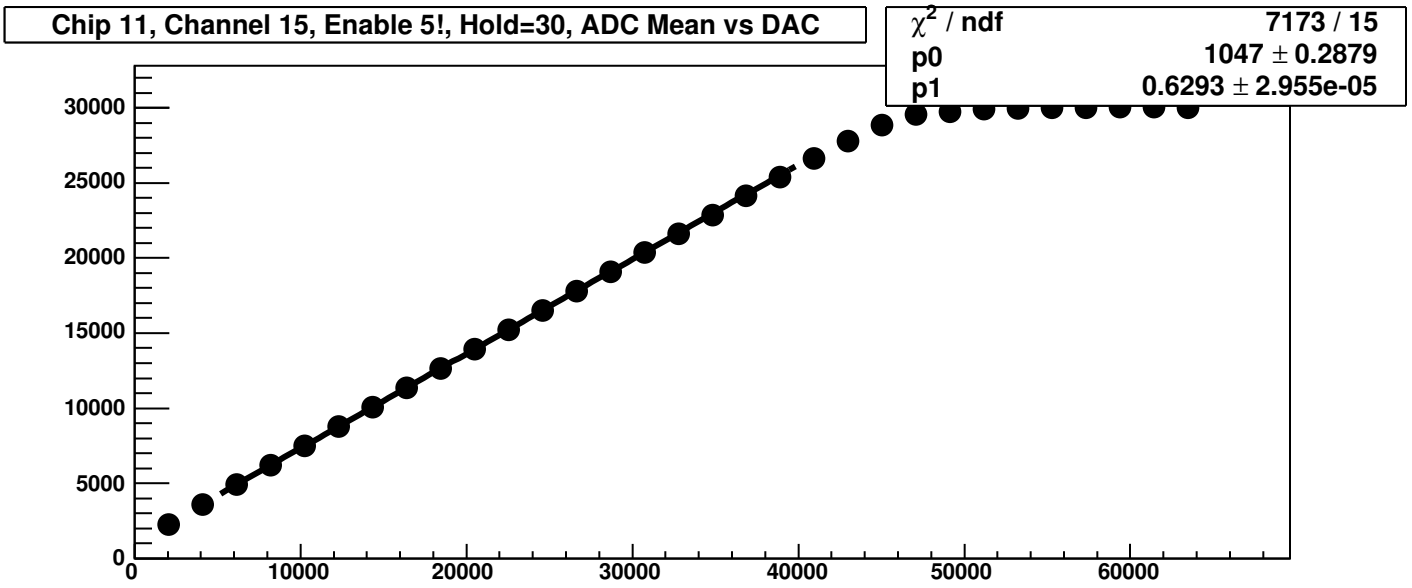

Chip 11, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

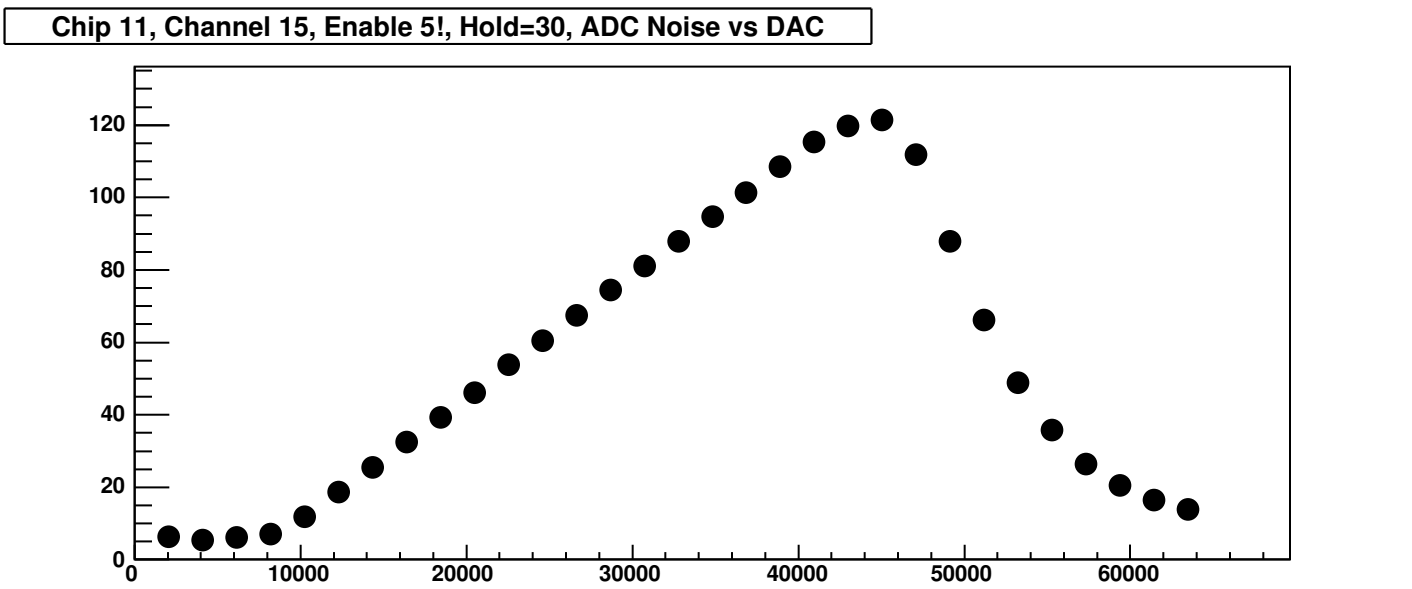

Chip 11, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

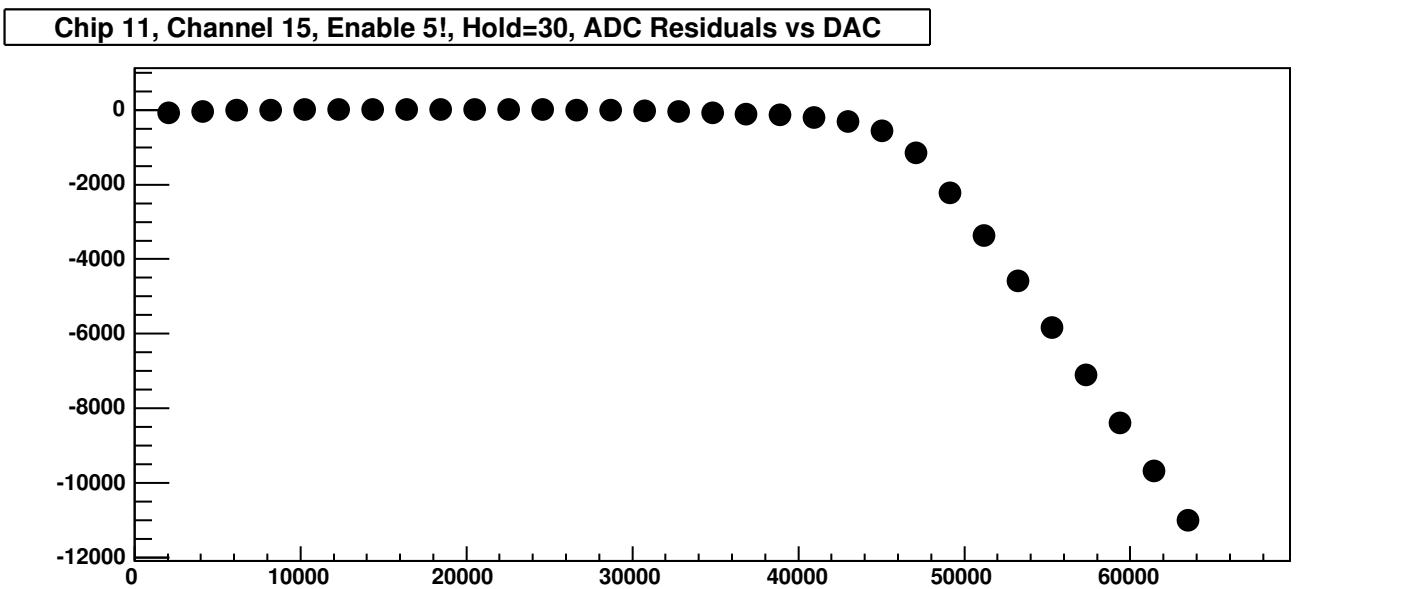

Chip 11, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6042 / 15
p0 936.2 ± 0.1154
p1 0.001076 ± 4.743e-06
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

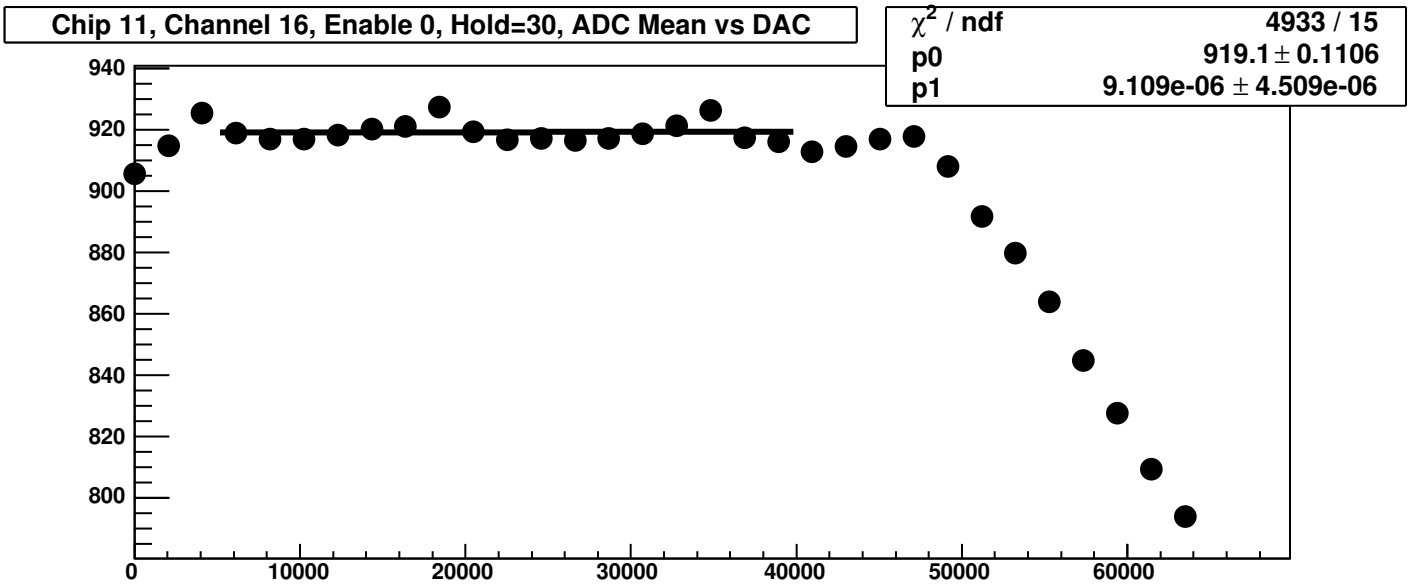

Chip 11, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

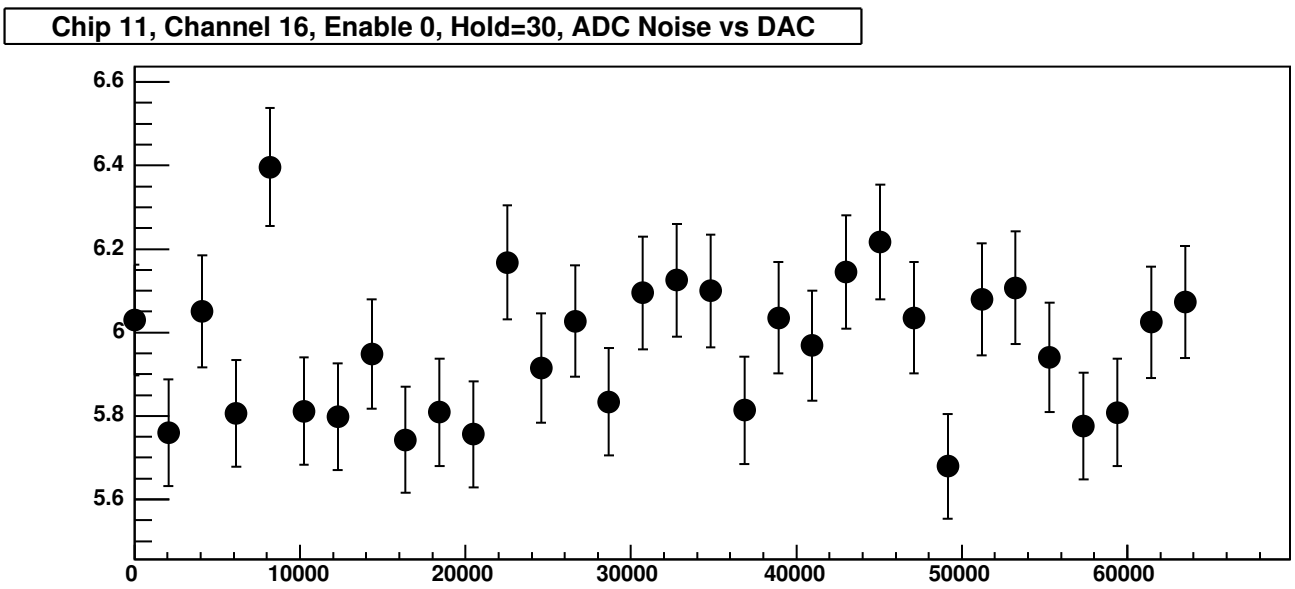

Chip 11, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

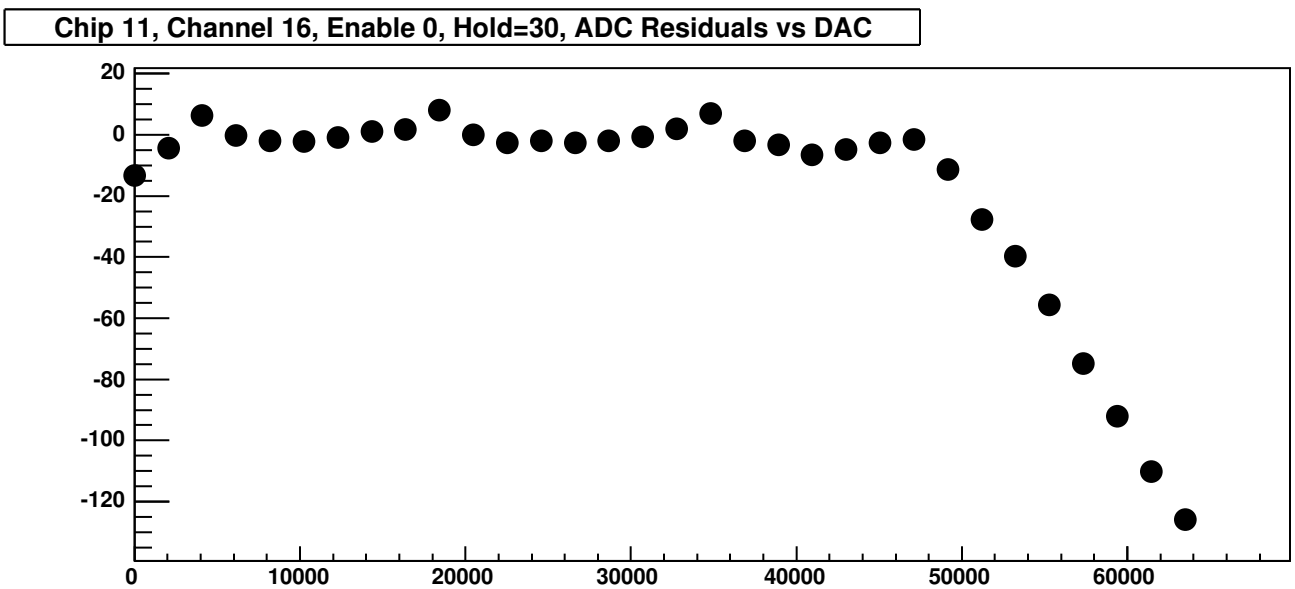

Chip 11, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 7173 / 15
p0 1047 ± 0.2879
p1 0.6293 ± 2.955e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

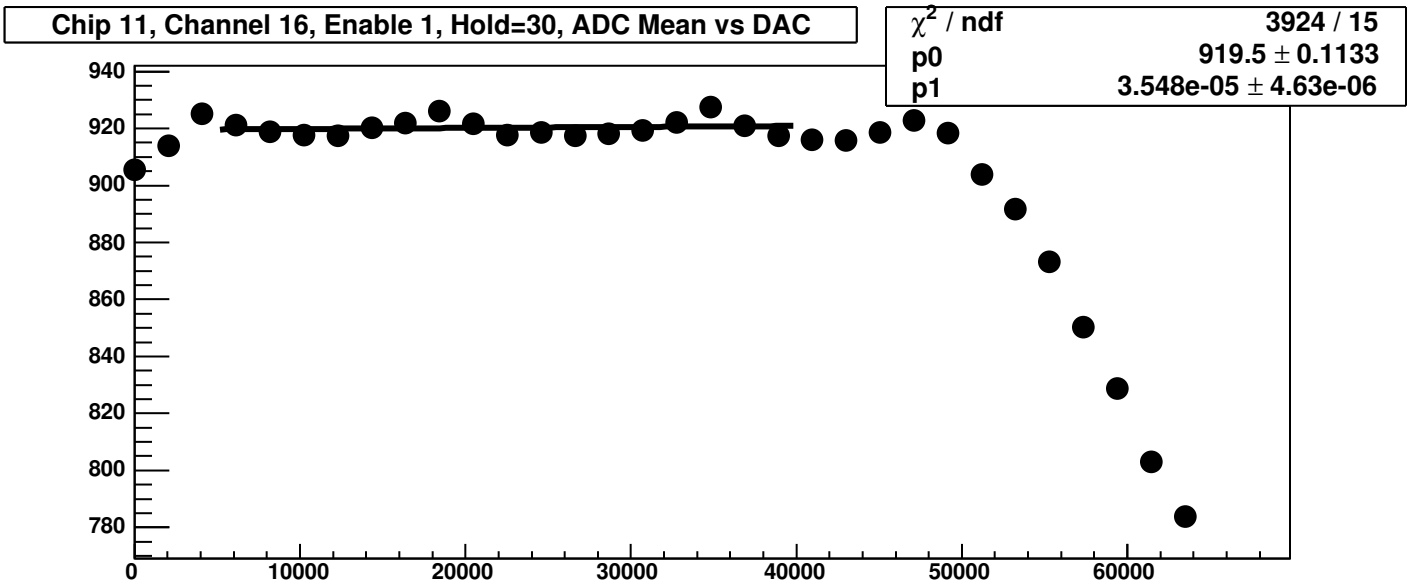

Chip 11, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

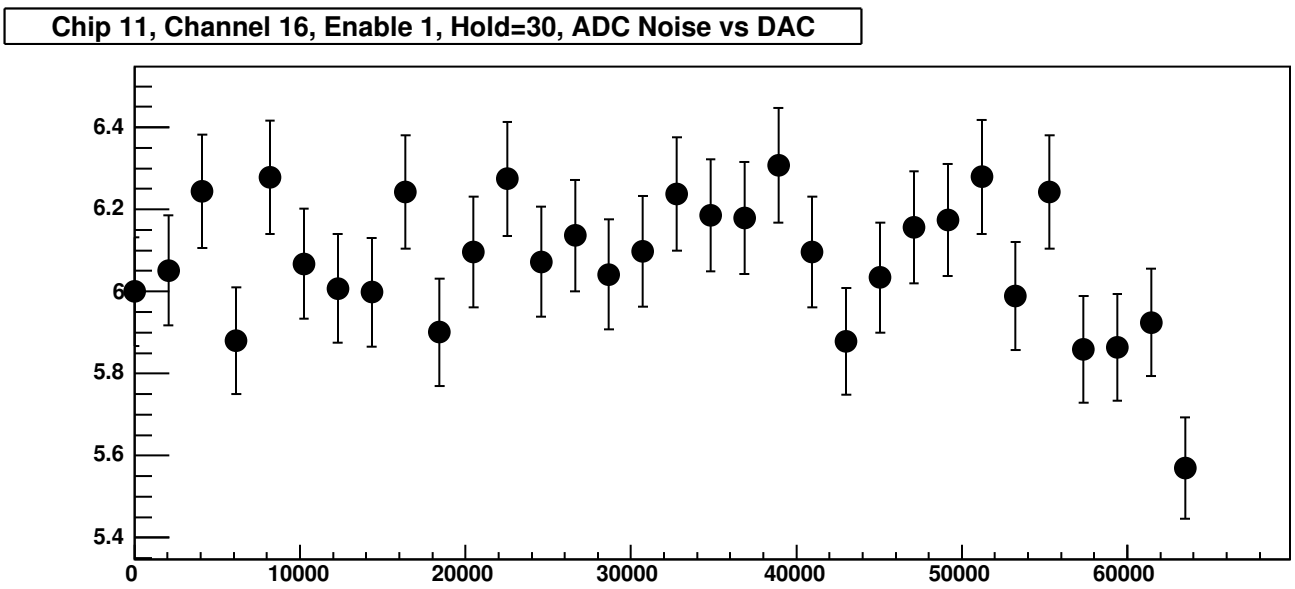

Chip 11, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
10000
20000
30000
40000
50000
60000

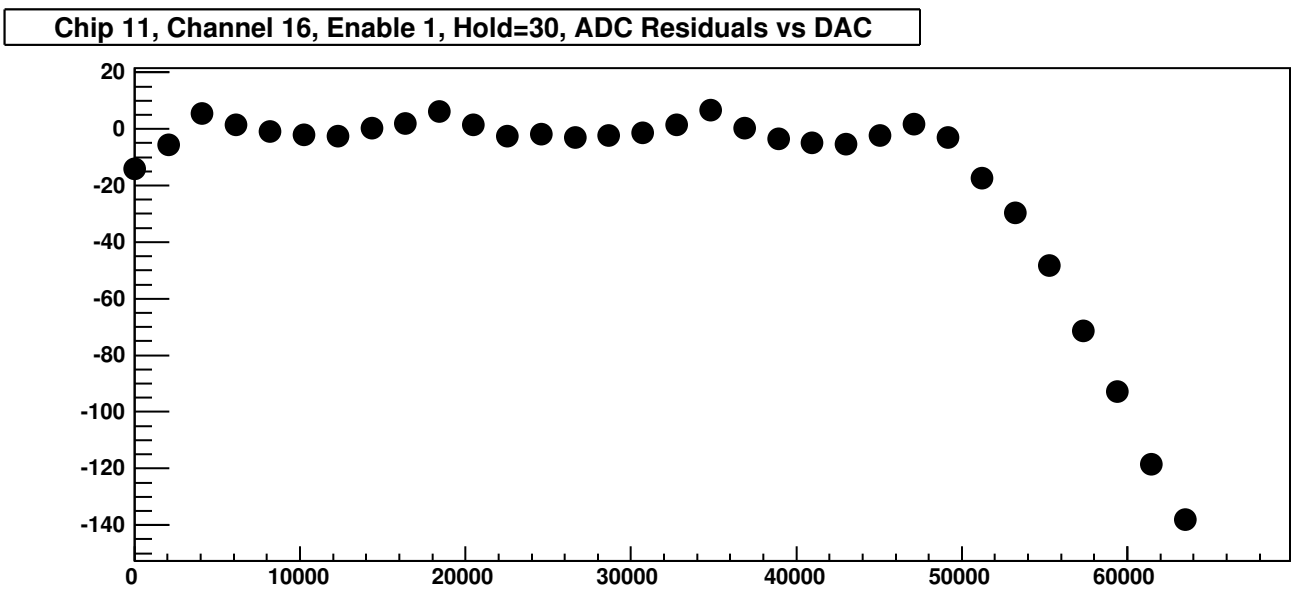

Chip 11, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4933 / 15
p0 919.1 ± 0.1106
p1 9.109e-06 ± 4.509e-06
940
920
900
880
860
840
820
800
0
10000
20000
30000
40000
50000
60000

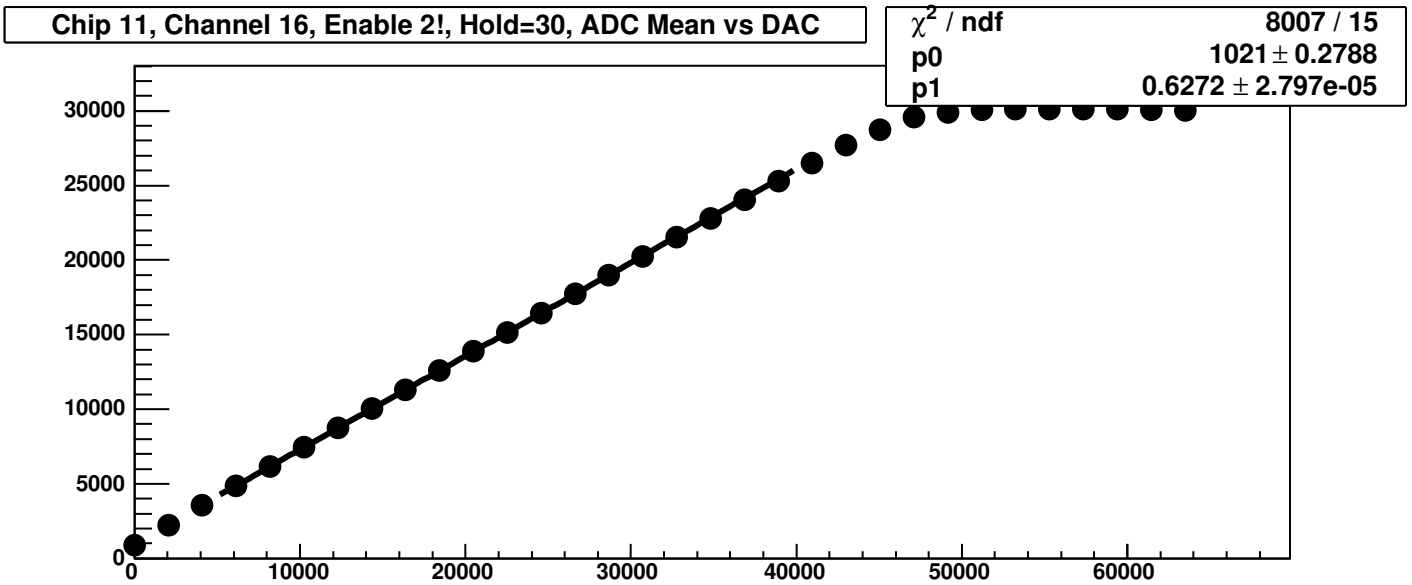

Chip 11, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000

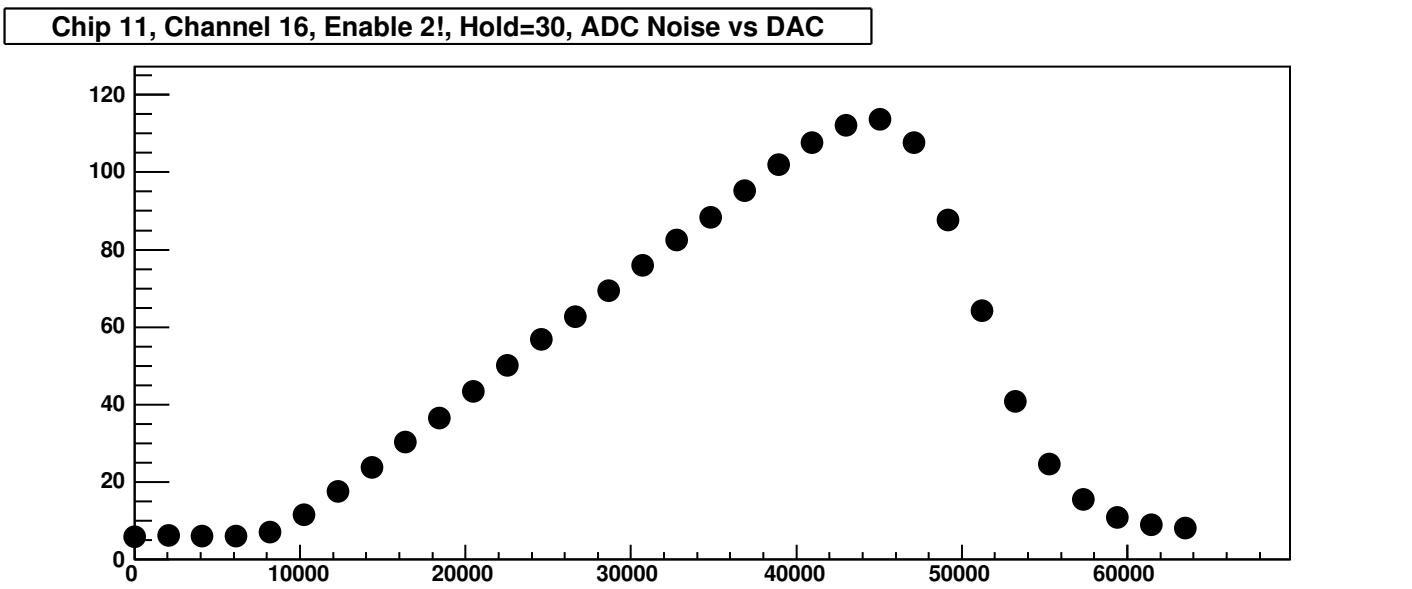

Chip 11, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

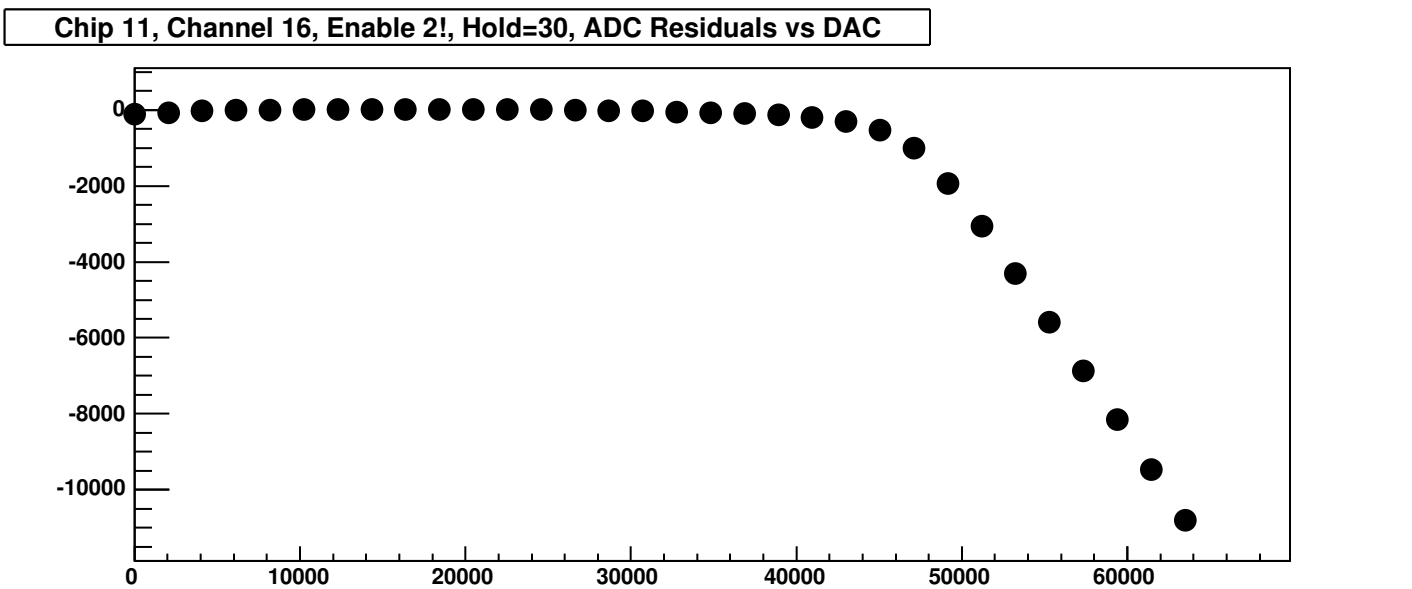

Chip 11, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3924 / 15
p0 919.5 ± 0.1133
p1 3.548e-05 ± 4.63e-06
940
920
900
880
860
840
820
800
780
0
10000
20000
30000
40000
50000
60000

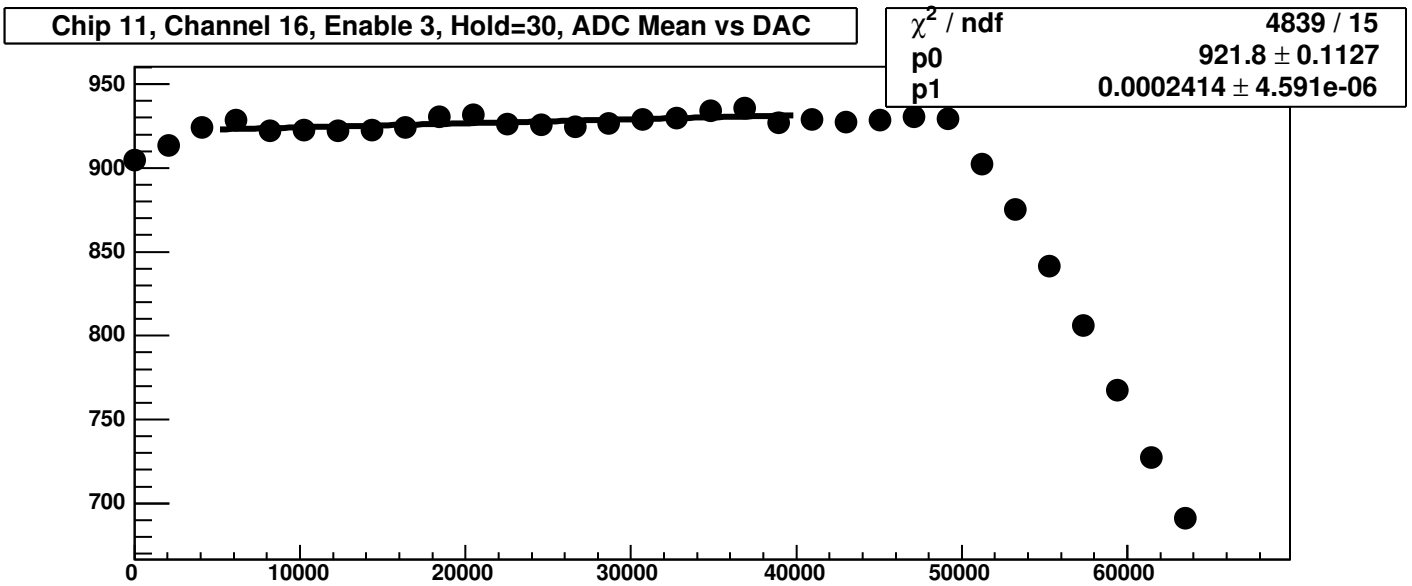

Chip 11, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

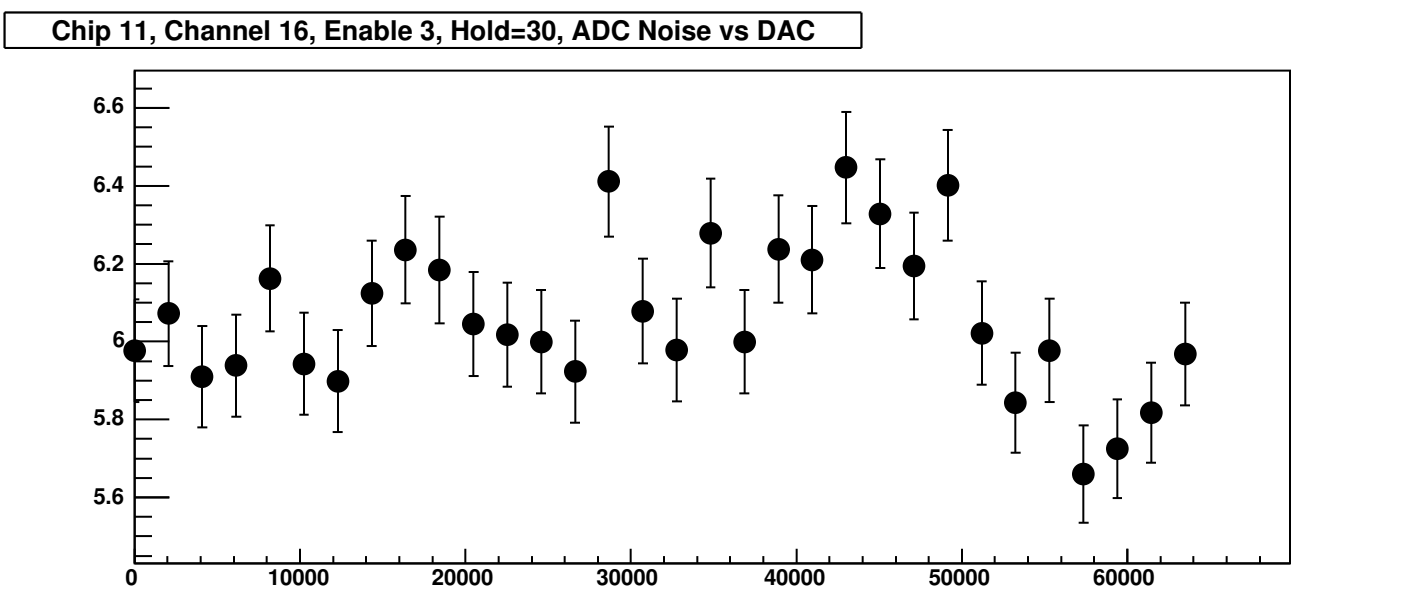

Chip 11, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

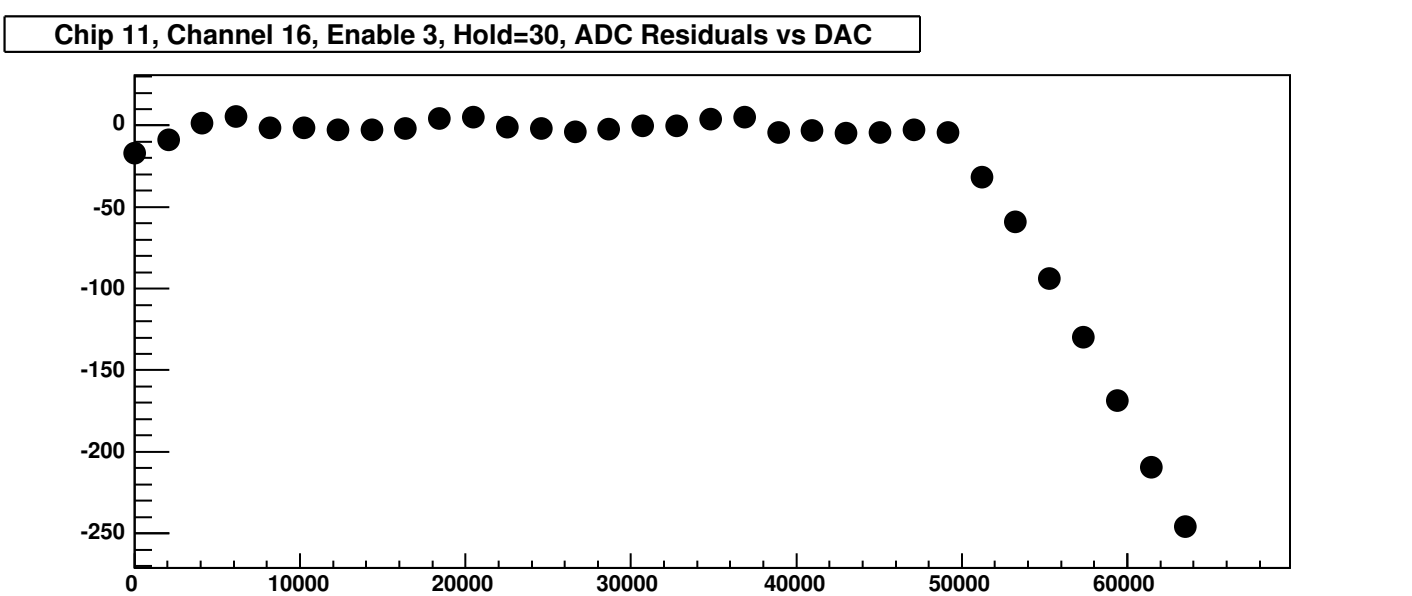

Chip 11, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 8007 / 15
p0 1021 ± 0.2788
p1 0.6272 ± 2.797e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

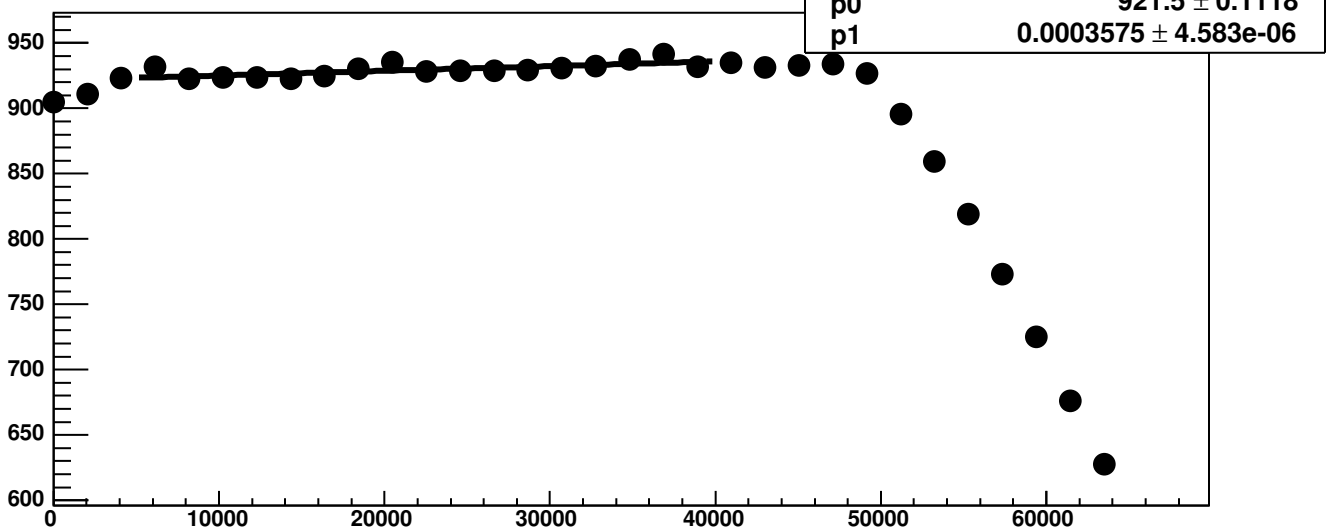

Chip 11, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4839 / 15
p0 921.8 ± 0.1127
p1 0.0002414 ± 4.591e-06
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

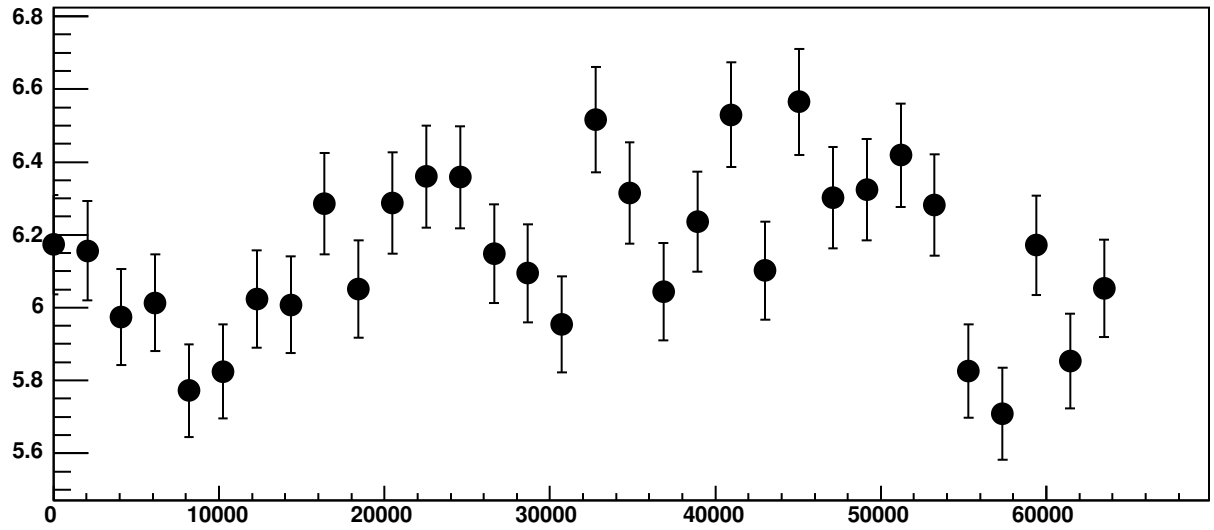

Chip 11, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

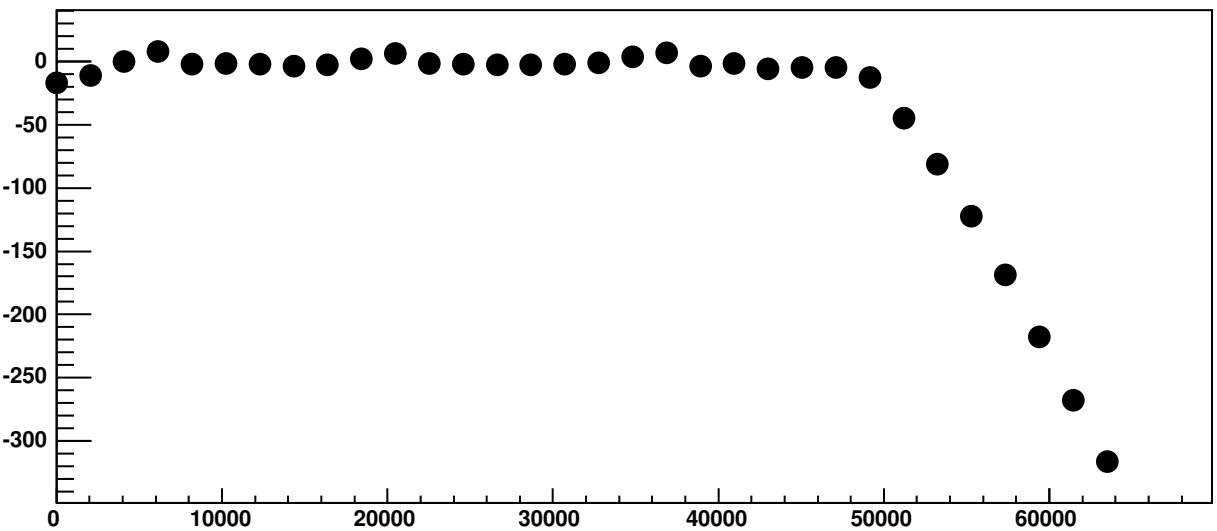
Chip 11, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

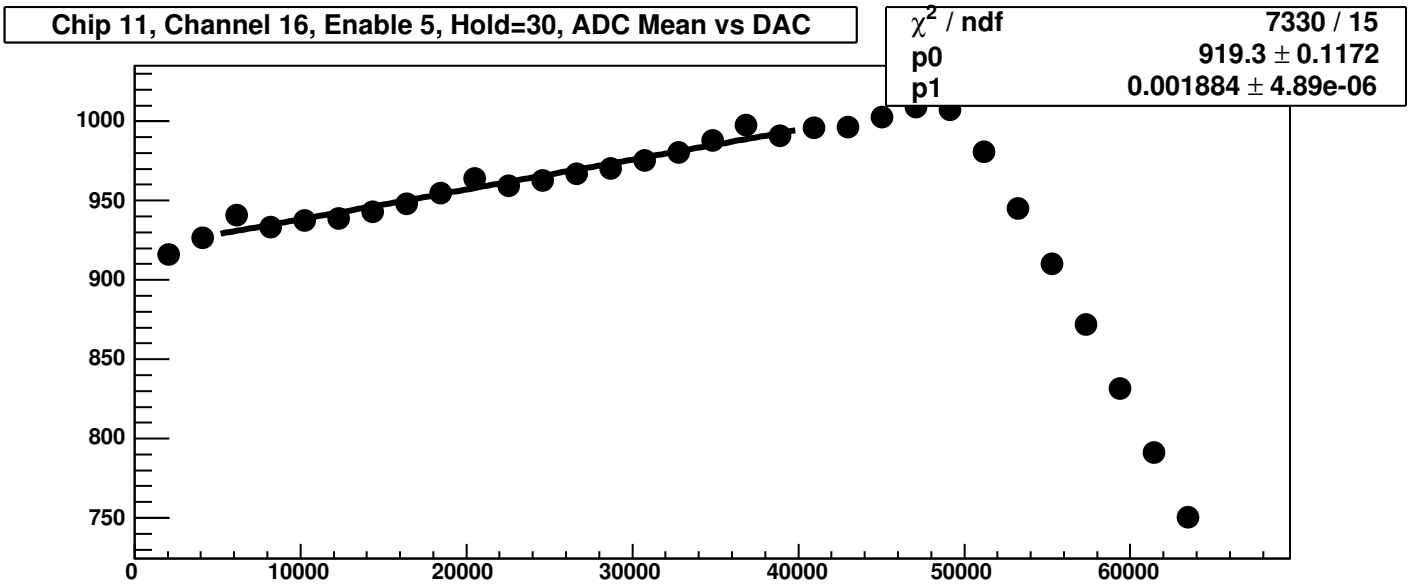


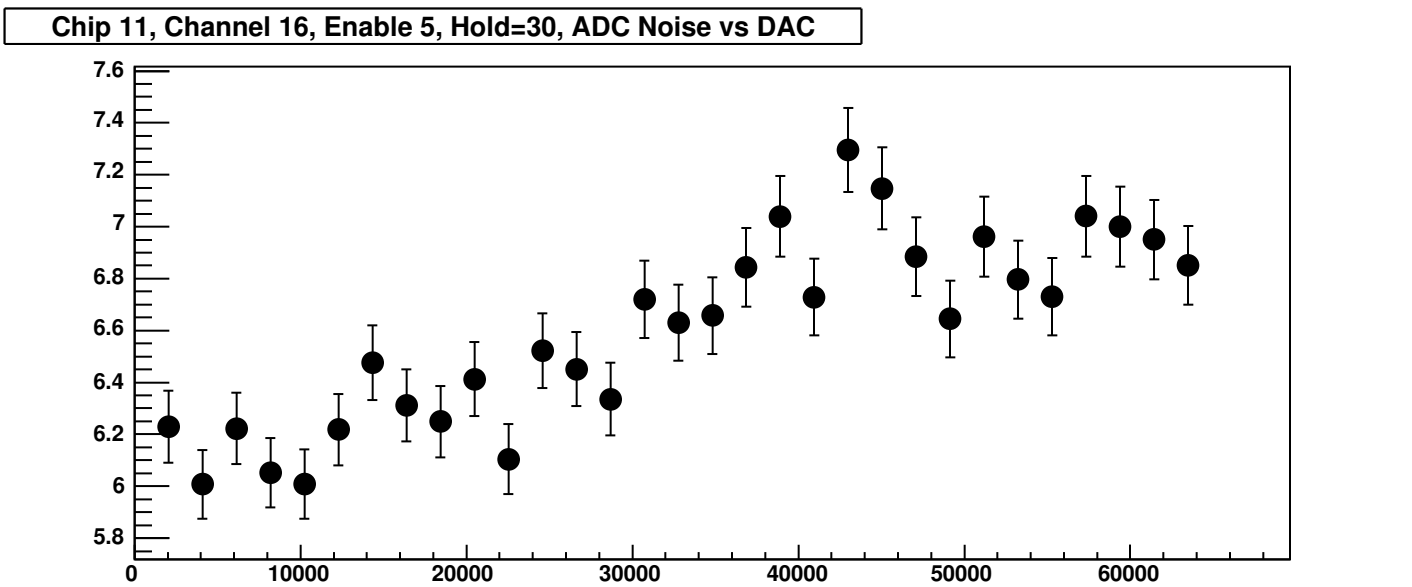
Chip 11, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC

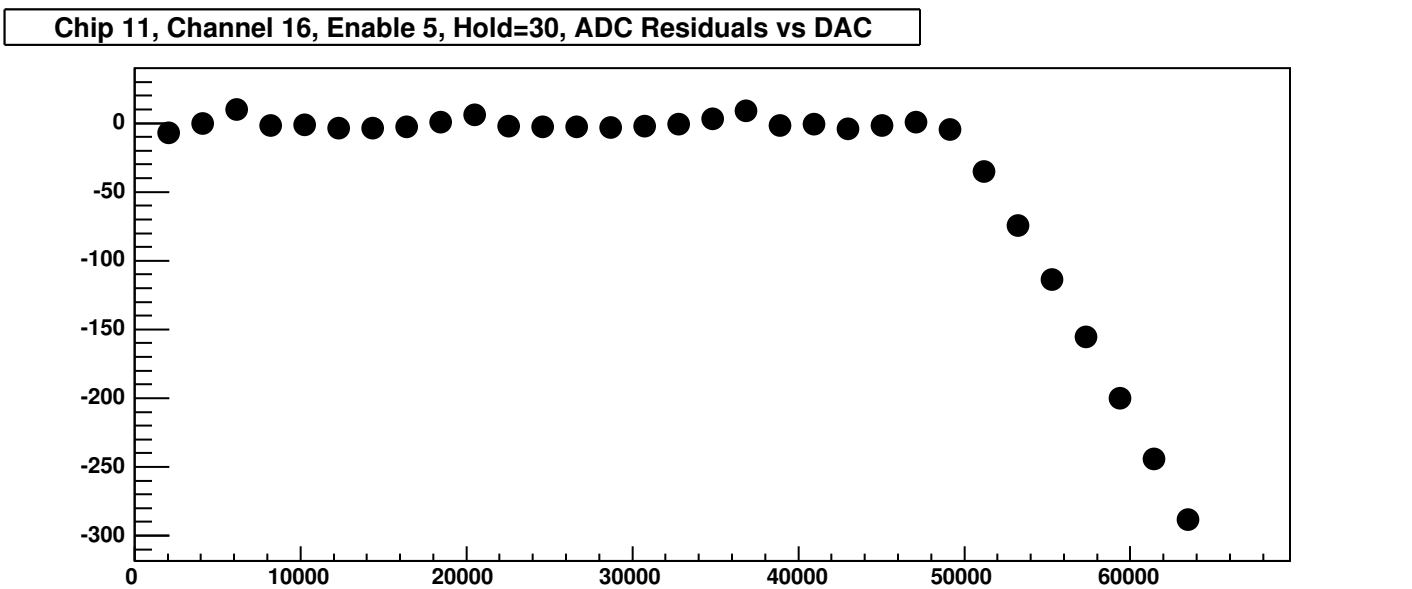


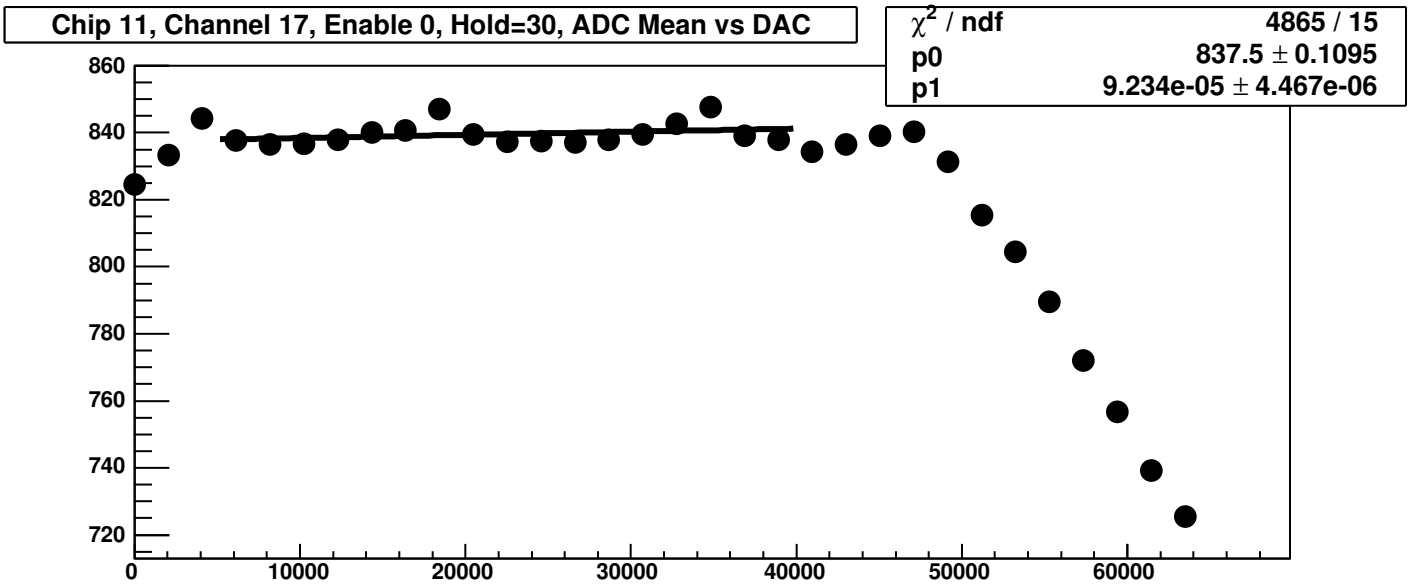
Chip 11, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC

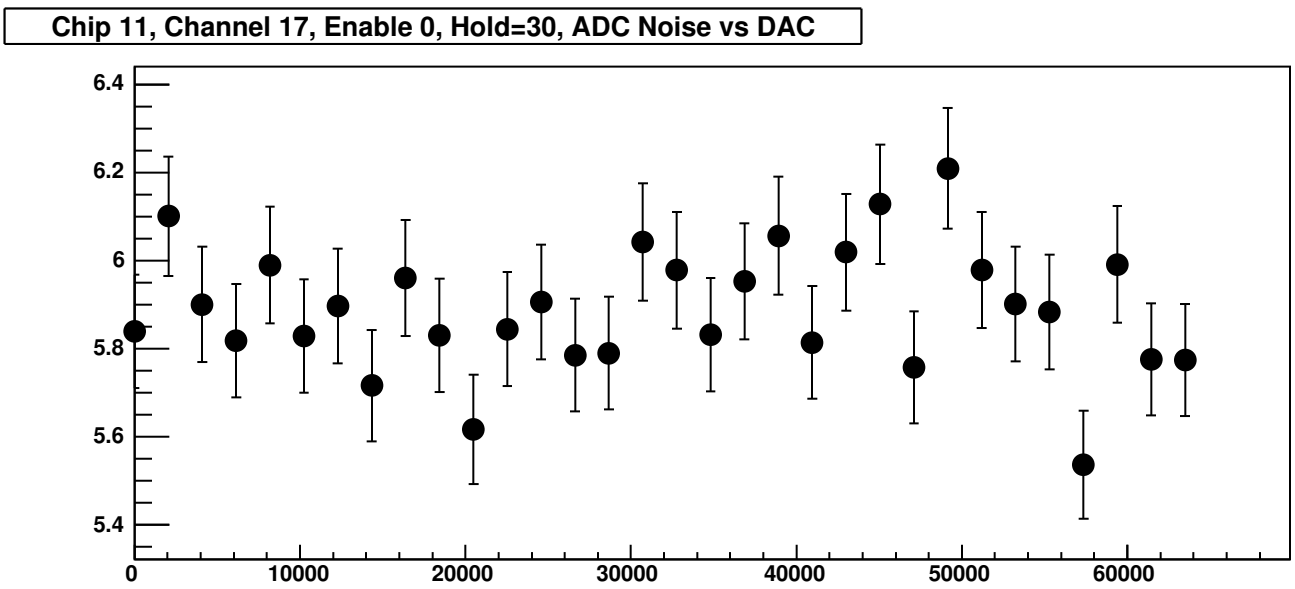


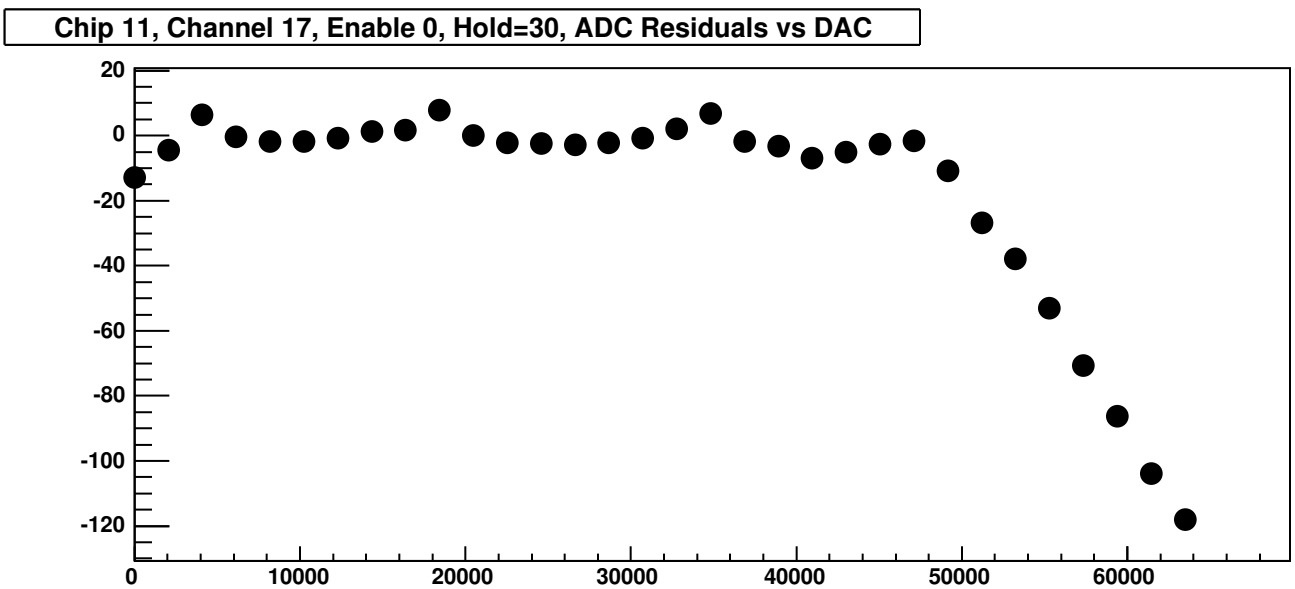

Chip 11, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7330 / 15
p0 919.3 ± 0.1172
p1 0.001884 ± 4.89e-06
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000

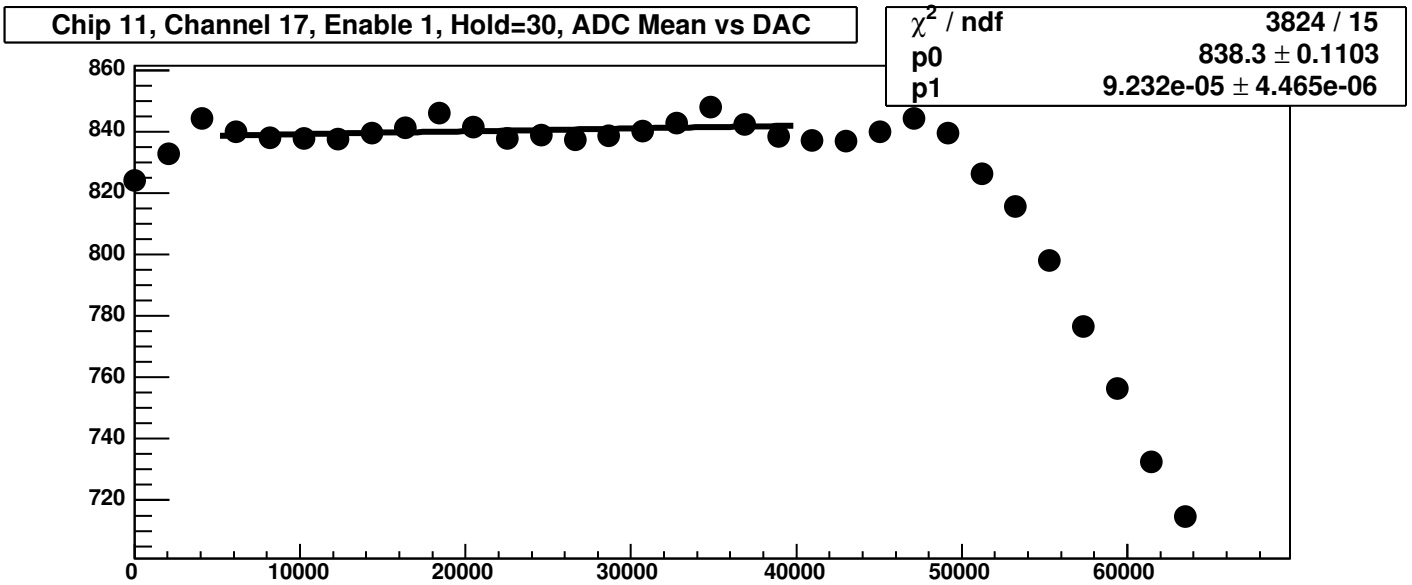

Chip 11, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC
7.6
7.4
7.2
7
6.8
6.6
6.4
6.2
6
5.8
0
10000
20000
30000
40000
50000
60000

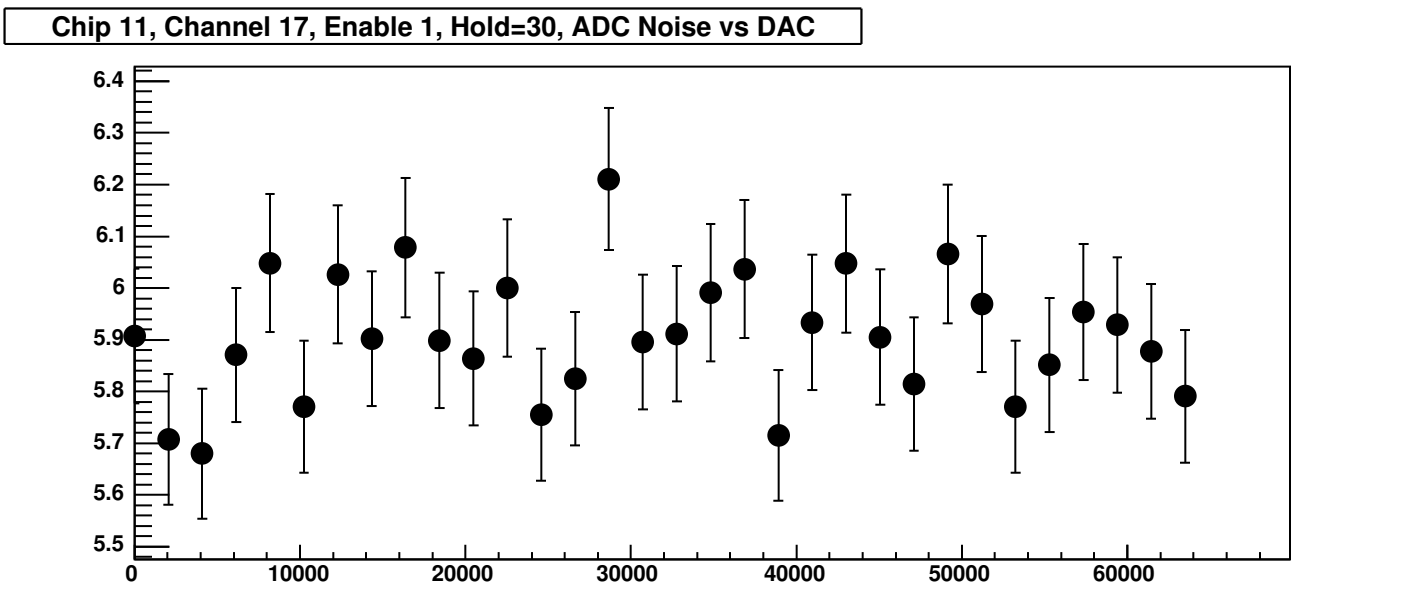

Chip 11, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
-300
0
10000
20000
30000
40000
50000
60000

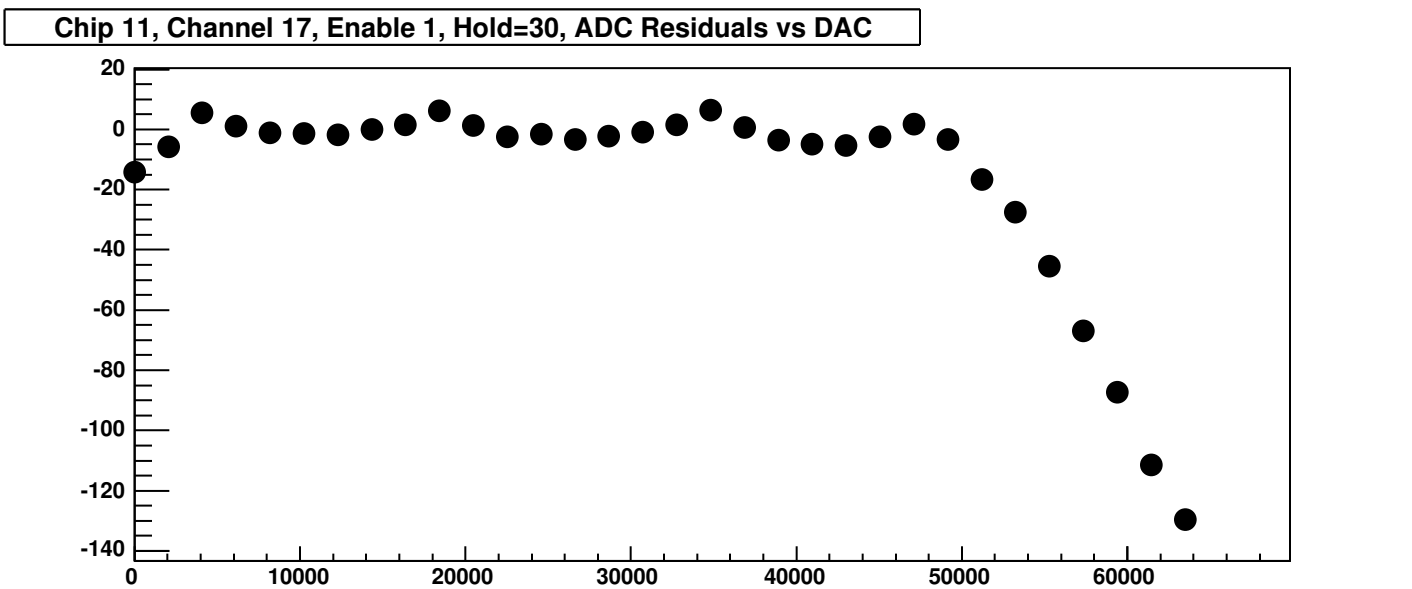

Chip 11, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 4865 / 15
p0 837.5 ± 0.1095
p1 9.234e-05 ± 4.467e-06
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

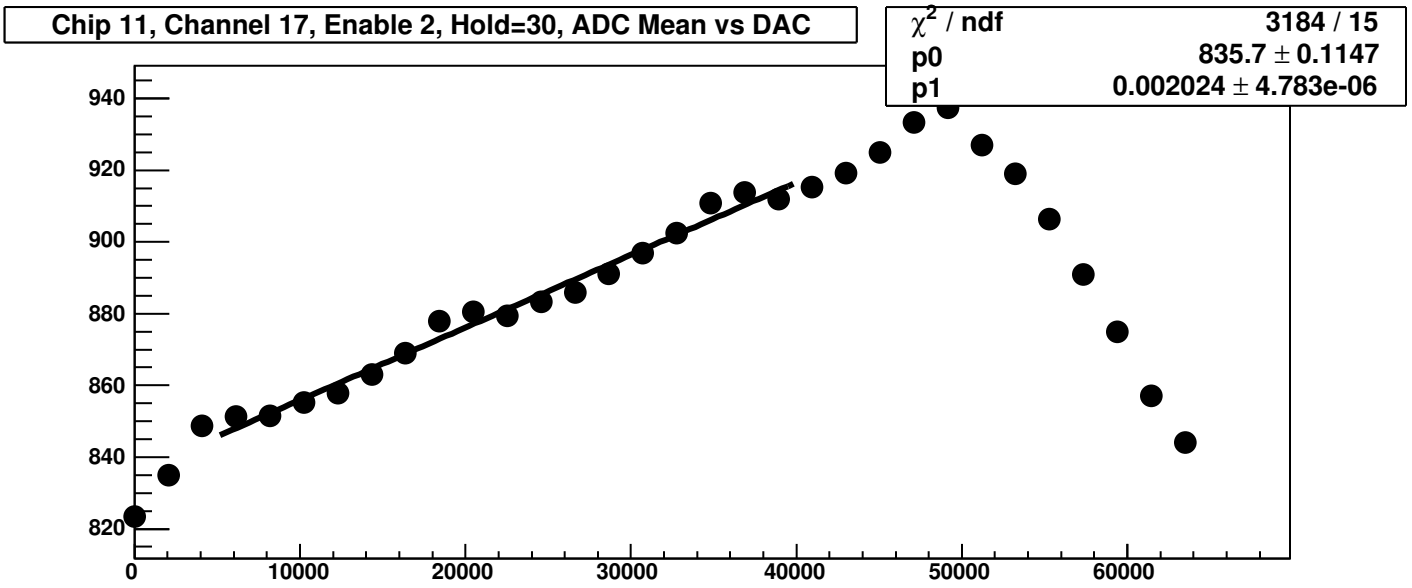

Chip 11, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000

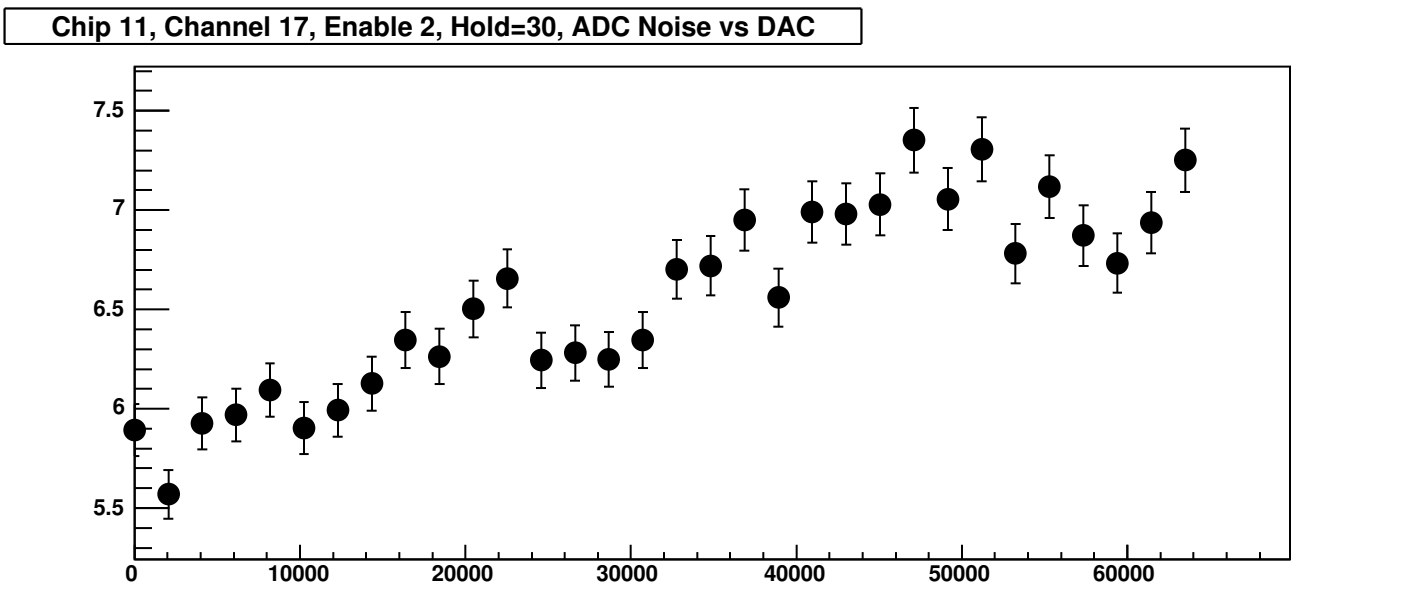

Chip 11, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000

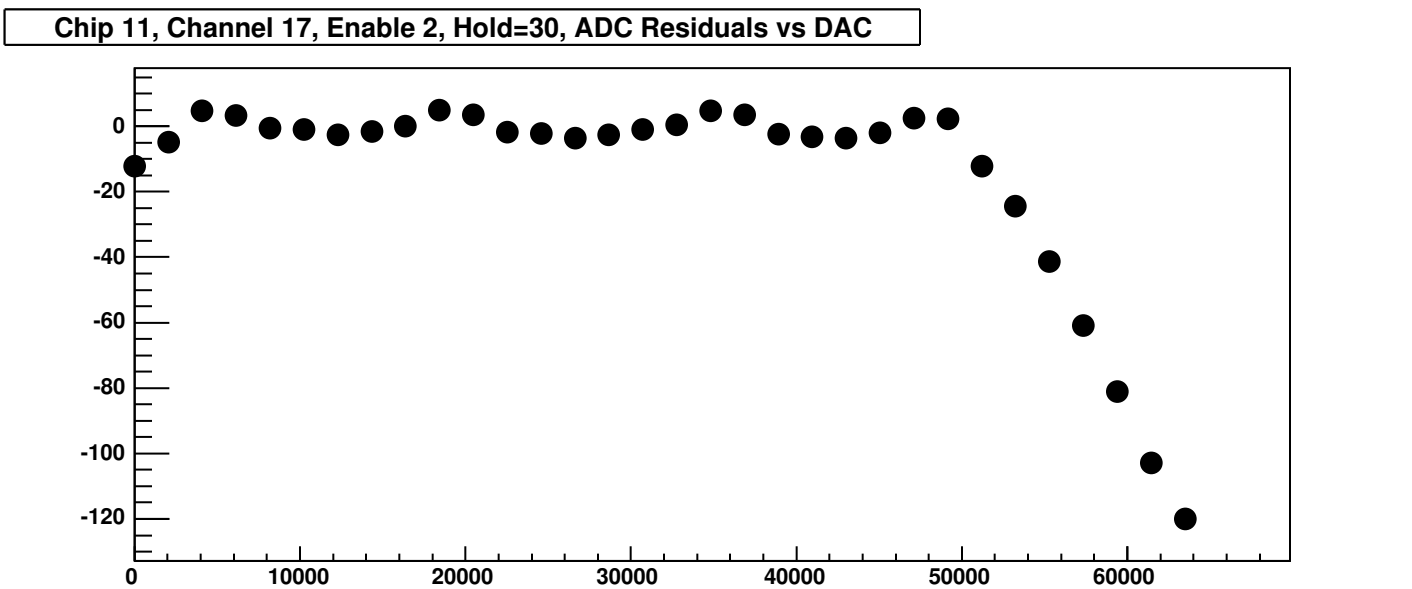

Chip 11, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3824 / 15
p0 838.3 ± 0.1103
p1 9.232e-05 ± 4.465e-06
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

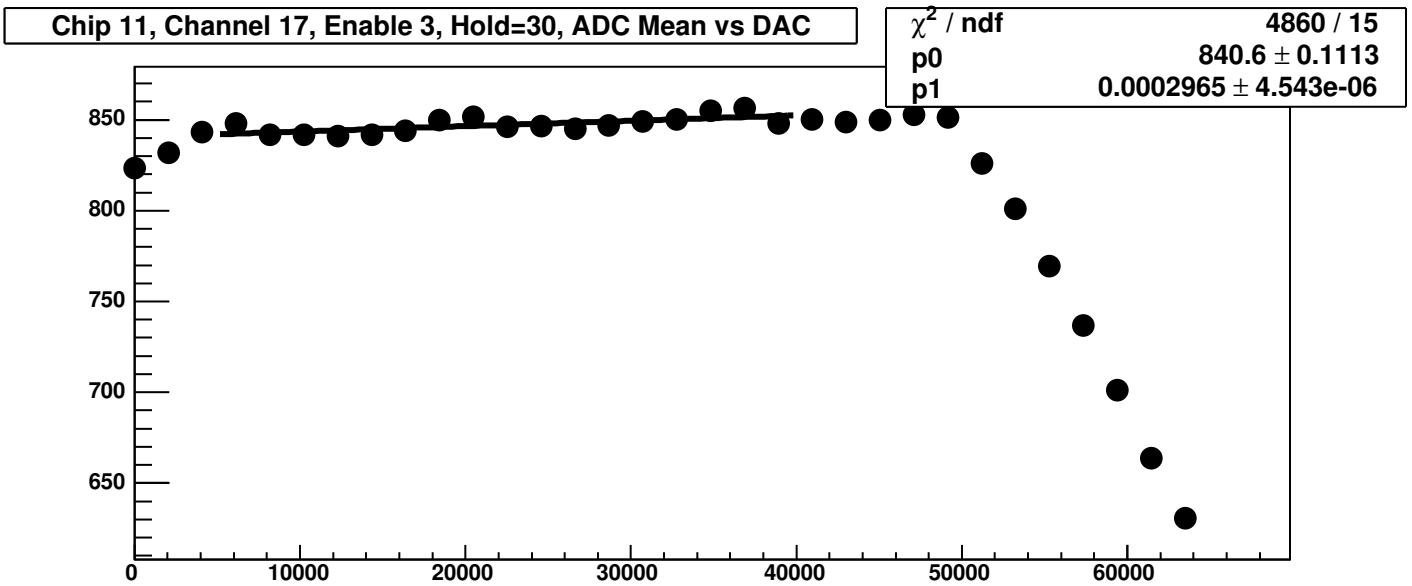

Chip 11, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
6.4
6.3
6.2
6.1
6
5.9
5.8
5.7
5.6
5.5
0
10000
20000
30000
40000
50000
60000

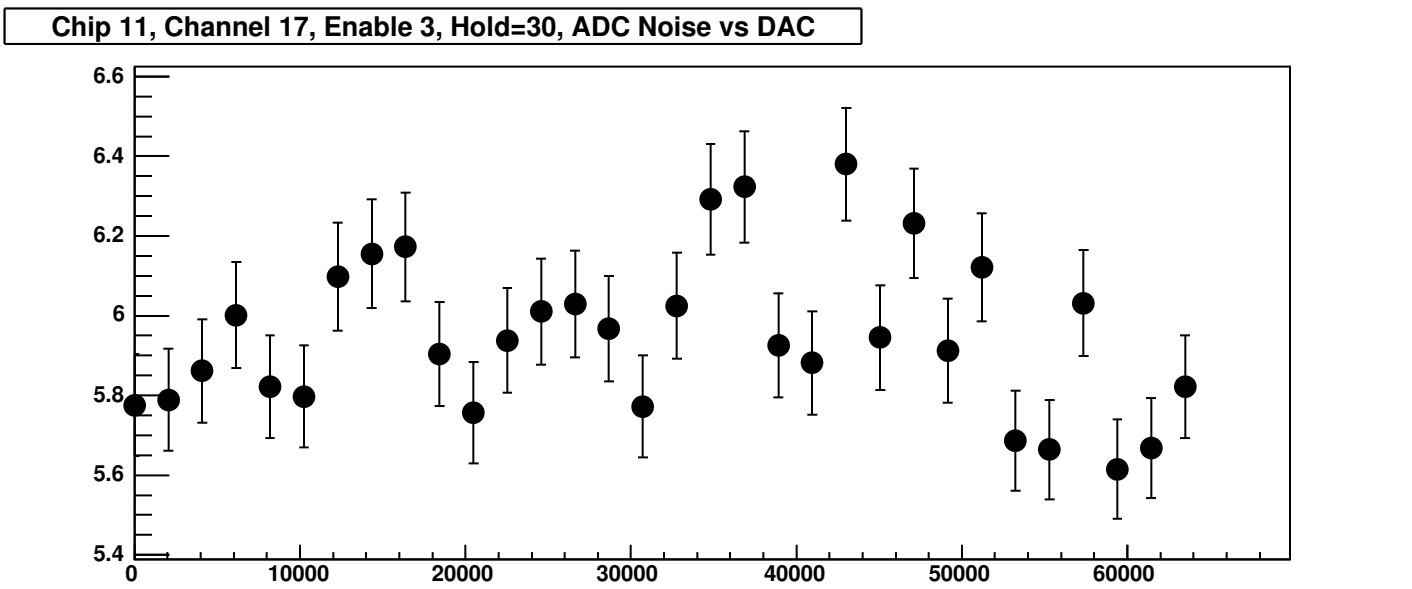

Chip 11, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
10000
20000
30000
40000
50000
60000

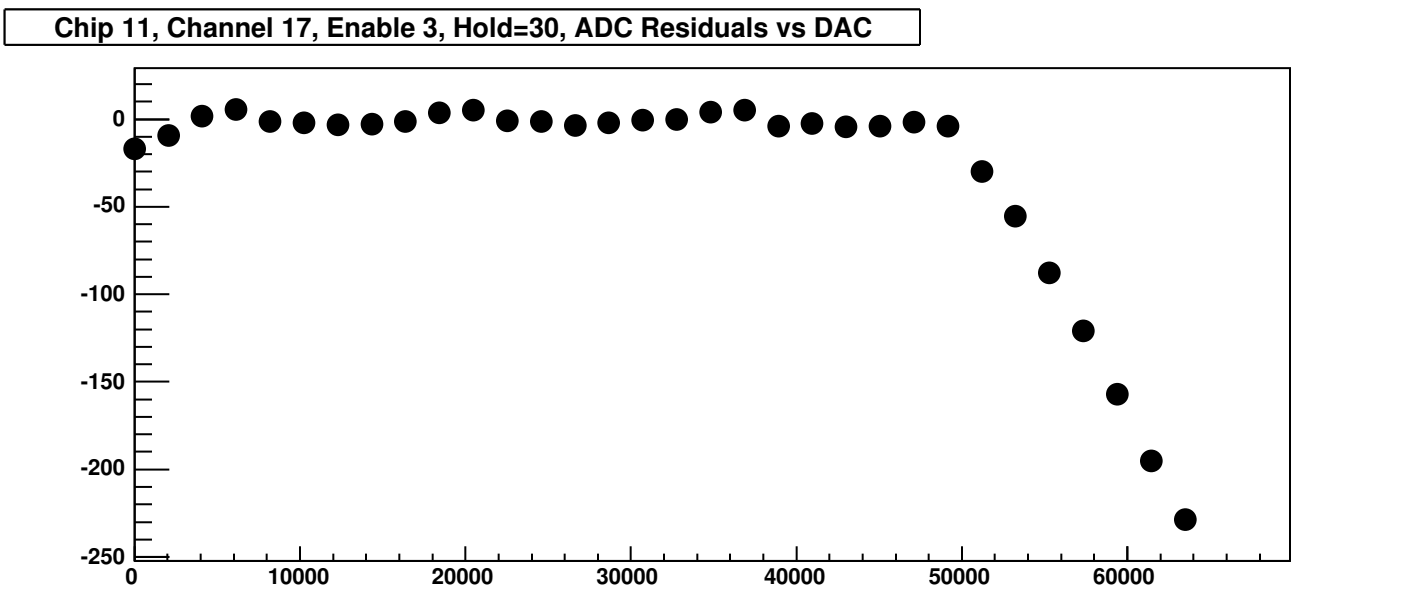

Chip 11, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3184 / 15
p0 835.7 ± 0.1147
p1 0.002024 ± 4.783e-06
940
920
900
880
860
840
820
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC
7.5
7
6.5
6
5.5
0
10000
20000
30000
40000
50000
60000

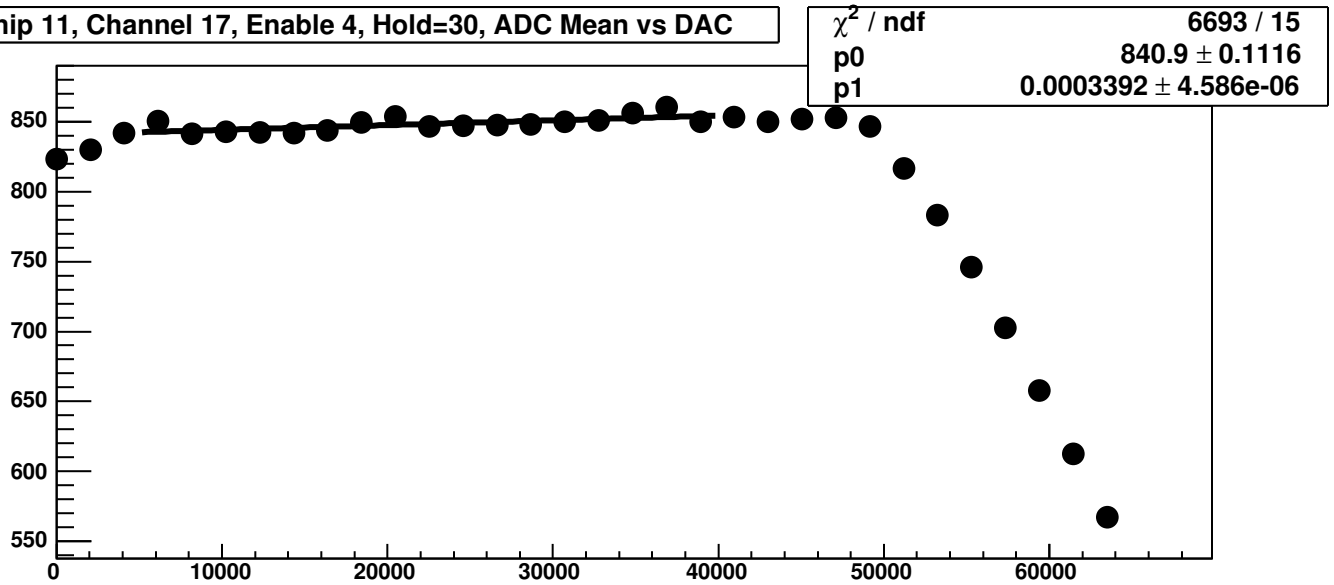

Chip 11, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4860 / 15
p0 840.6 ± 0.1113
p1 0.0002965 ± 4.543e-06
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

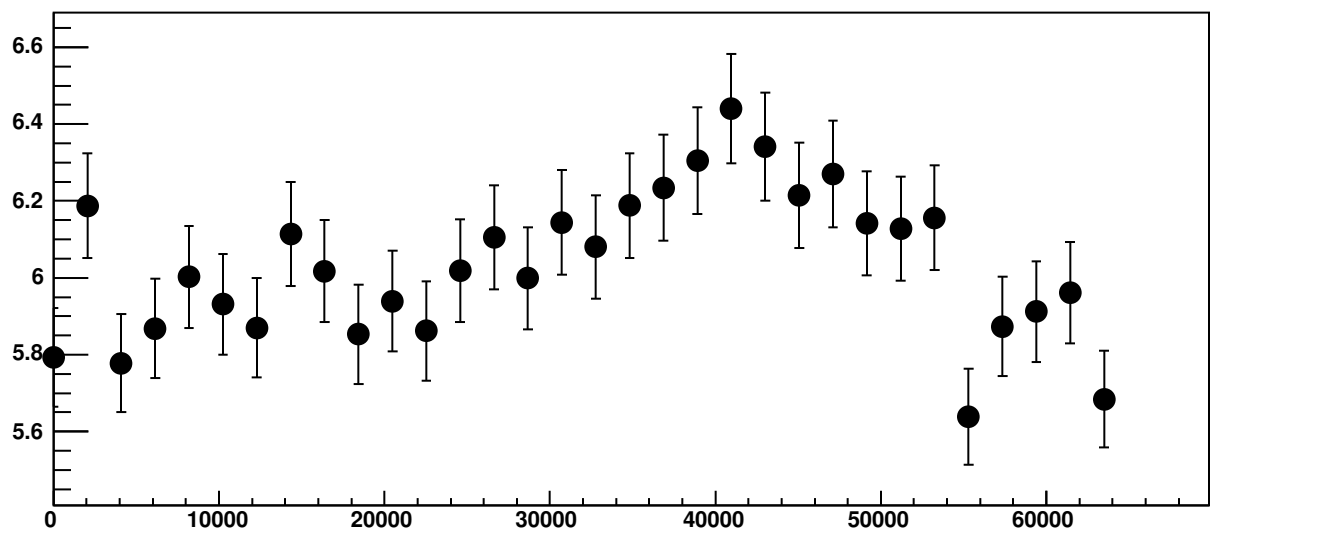
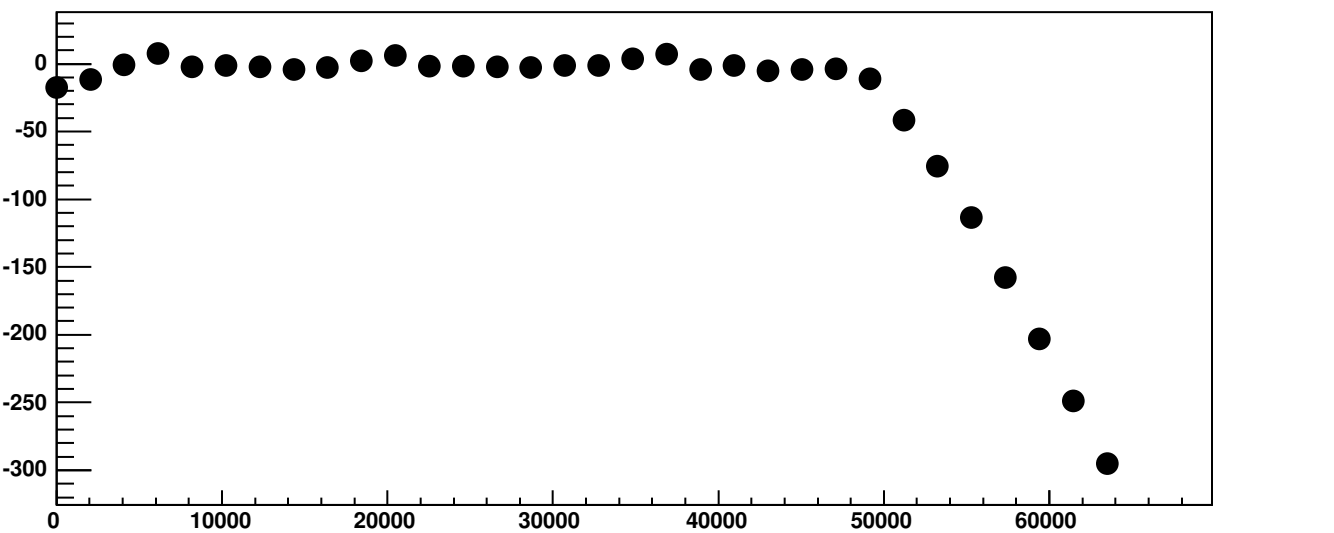

Chip 11, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
6.6
6.4
6.2
6
5.8
5.6
5.4
0
10000
20000
30000
40000
50000
60000


Chip 11, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000

Chip 11, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC



Chip 11, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



Chip 11, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC